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# Electronic Design23 

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Cover: Photo by Stan Stansfield, courtesy of Cherry Electrical Products Corp., Waukegan, III.

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Figure 2


Figure 3

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## A paean to the muon from an early believer

What a pleasant surprise to see in News Scope that Dr. Richard C. Arnold had developed muon communications (see "Nuclear Communications Proposed as Competitive," ED 17, Aug. 17, 1972, p. 27). As an Australian with the Packard Bell Space \& Systems Div., I was in a group trying to figure ways of communicating through "impenetrable" Vietnam jungle with a manpack device. All my 50odd approaches failed dismally till I got to muons and antineutrinos.

No encouraging feedback has ever penetrated the thick security screens since nine years ago, when I submitted the theory.

My proposals to eliminate all cables, outside electromagnetic antennas and complex data transmission systems for such problems as flight-deck automatic checkout, spacecraft telephone (through walls and reactors), submarine communications or through-the-earth TV, unspoiled by solar filters or satellite failures, must have been too far out. To replace such complex radiator systems with small nuclear generators and detectors inside the control rooms must have appeared absurd.

One consequential prediction was that optical data transmission via fiber-optics or lasers would be overshadowed by the far more reliable, and possibly cheaper, freespace nuclear communicationswhether to the moon or as a linkless interconnection through a computer.

So sincere congratulations to Dr. Arnold on opening up a whole new sophisticated field-and maybe revealing a new pollution.

Patrick Howden Commonwealth Savings Bank of Australia Aldwych Branch London, England

## More comments on CAD article

I would like to comment on Ralph D. Taylor's article, "Speed Computer-Aided Circuit Design" (ED 11, May 25, 1972, p. 54). Several implied generalizations in the paper are very misleading and some, to the best of my knowledge, are simply not true. The randomnumber method proposed is indeed a brute-force method, but it is anything but a panacea. Let me make the following points:

1. Consider the statement, "While Fletcher-Powell's results will be accurate to eight decimal places, accuracy to four decimal places should be expected when random numbers are used." This is not generally true, since one can always relax the termination criterion in any iterative process, thereby reducing computer time at the expense of less accuracy. A tradeoff between accuracy and computer time can almost always be made for most iterative minimization techniques.
2. It is true that a lot more effort is required to program a Fletcher-Powell type of routine than a simple random-number routine. However, if the former is written in a general form, it may be used over and over in conjunction with other circuit designs. In this case the effort is worthwhile. Furthermore many minimization
(continued on p. 10)

[^1]

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[^3]ACROSS THE DESK

## (continued from p. 7)

algorithms are available as packaged subroutines (see the System 360 scientific subroutine package).
3. For any iterative minimization scheme, the better the initial "guess" at the design parameters, the quicker convergence will be attained. In the logical extreme, if the initial guess corresponds to an optimal solution, the process will terminate after a single iteration. With the random-number technique, one rarely knows how close he is to an optimal solution.
4. In many situations-for example, nonlinear switching-circuit transient responses-the circuit analysis is the dominant user of computer time in any one iteration. In this case techniques that minimize the total number of iterations, and not the simplicity of each step, will prove to be far more economical. Furthermore, as the number of design variables increases, the number of combinations of variable values increases exponentially, and any randomnumber approach (disregarding the element of luck) will fail.
5. There is a slight error in the bibliography. The Temes and Calahan paper ("Computer-Aided Network Optimization: The State-of-the-Art") appeared in the November, 1967-not 1962-Proceedings of the IEEE. A more recent reference, which contains a wealth of articles in this area, is the IEEE Transactions on Circuit Theory, Special Issue on ComputerAided Circuit Design (January, 1971).

Let me conclude by saying that I am not in disagreement with Mr. Taylor's results. My strong objections are to the implication that the random-number method is, in general, superior to more formal and more powerful techniques. My experience has been otherwise in four years of research on CAD and teaching of the subject at the University of California, Berkeley. Just as no single minimization technique can be claimed to be the most effective in all situations, no generalization about any one method should be based on experience with one class of relatively simple circuits. My word to any(continued on p.16)

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| Function | Replaces Pin-for-Pin | $\begin{gathered} \text { Price } \\ (100-999) \end{gathered}$ |
| :---: | :---: | :---: |
| Dual 3-Input NOR plus | CD4000AD/AE | 3.10/ .78 |
|  |  |  |
| Quad 2-input NOR Gate | CD4001AD/AE | 3.401 .78 |
| Dual 4 -input NOR Gate | CD4002AD/AE | 3.401 .78 |
| 18 -bit Shift Register | CD4006AD/AE | 9,10/ 3.89 |
| Dual Pair and Inverter | CD4007AD/AE | $2.65 / .78$ |
| 4 -bit Full Adder | CD4008AD/AE | $10.15 / 4.02$ |
| Hex Inverter/Buffer | CD4009AD/AE | 5.25/1.69 |
| Hex Inverter | CD4010AD/AE | $5.25 / 1.69$ |
| Quad 2-input NAND Gate | CD4011AD/AE | 3.401 .78 |
| Dual 4 -input NAND Gate | CD4012AD/AE | 3.401 .78 |
| Dual Type D Flip-Flop | CD4013AD/AE | $4.75 / 1.62$ |
| Dual 4 -bit static SR, | CD4015AD/AE | 10.15/4.24 |
| serial in/parallel out |  |  |
| Quad Analog Switch/ | CD4016AD/AE | 5.15/1.62 |
| Quad Multiplexer |  |  |
| Decade Counter/Divider | CD4017AD/AE | $10.60 / 4.24$ |
| 8 -bit static SR, serial or | CD4021AD/AE | 9.75/ 4.24 |
| parallel in/serial out | CD4023AD/AE | 3.401 .78 |
| Triple 3 NOR Gate | CD4025AD/AE | $3.401 \quad .78$ |
| Dual J-K Flip-Flop | CD4027AD/AE | $5.25 / 2.50$ |
| BCD Decimal Decoder | CD4028AD/AE | 7.92/ 3.67 |
| Triple Full Adder (pos) | CD4032AD/AE | $6.91 / 4.24$ |
| Triple Full Adder (neg) | CD4038AD/AE | $6.91 / 4.24$ |
| 12 Stage Binary Counter | CD4040AD/AE | $10.20 / 5.40$ |
| Triple Gate | - | 4.30/1.69 |
| Expandable A.O.I. |  | $4.64 / 2.24$ |
| Quad exclusive OR Gate | CD4030AD/AE | $3.79 / 1.63$ |
| Dual 4-bit Latch | - | 24.70/13.75 |
| BCD Up/Down Counter | - | 11.47 / 6.35 |

## †Motorola Device \#

MC14512AL/CL
MC14514AL/CL
MC14515AL/CL
MC14516AL/CL
MC14518AL/CL MC14519AL/CL

MC14520AL/CL
MC14522AL/CL
MC14526AL/CL
MC14527AL/CL
MC14528AL/CL
MCM14505AL/CL

| Function |
| :--- |
| 8-channel Data Select |
| 4-bit Latch/4-to-16 |
| Line Decoder (Hi) |
| 4-bit Latch/4-to-16 |
| Line Decoder (Low) |
| Binary Up/Down Counter |
| Dual BCD Up Counter |
| 4-bit AND/OR Select, |
| Quad excl NOR Gate |
| Dual Binary Up Counter |
| Programmable BCD |
| Divide-by-N 4-bit Counter |
| Progammable Binary |
| Divide-by-N 4-bit Counter |
| BCD Rate Multiplier |


| Replaces <br> Pin-for-Pin | Price <br> $(100-999)$ |
| :---: | ---: |
| $=$ | $7.20 / 4.00$ |
| - | $24.70 / 13.75$ |
| - | $24.70 / 13.75$ |
| - | $11.47 / 6.35$ |
| $=$ | $12.90 / 7.20$ |
| - | $4.75 / 1.91$ |
| - | $11.47 / 6.35$ |
| - | $11.85 / 6.60$ |
| - | $11.85 / 6.60$ |
| - | $11.85 / 6.60$ |
| - | $6.50 / 3.10$ |
| - | $31.30 / 17.50$ |

$>$ November and December Introductions <
MC14034AL/CL
MC14502AL/CL
MC14511AL/CL
MC14517AL/CL MC14529AL/CL

MC14530AL/CL MC14531AL/CL MC14581AL/CL MC14582AL/CL

| 8-bit Static Bus Register | CD4034AD | $12.00 / 9.00$ |
| :--- | :---: | ---: |
| Strobed Hex Inverter | - | $7.15 / 3.30$ |
| BCD-to-7 Segment Latch/ | - | $15.00 / 7.15$ |
| Decoder/Driver | - | $24.00 / 14.35$ |
| Dual 64-bit Static S/R | - | $7.20 / 4.00$ |
| Dual 4-channel Analog | - | $4.75 / 1.91$ |
| Data Selector | - | $4.30 / 5.10$ |
| Dual Majority Logic Gate | - | 10.3015 |
| 12-bit Parity Tree | - | $24.70 / 14.35$ |
| ALU | $7.15 / 3.30$ |  |

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## U.S. move to break up IBM raises hopes of competitors

The Federal Government's move in court to split up the International Business Machines Corp. to encourage competition has sent shock waves through the computer industry and produced shivers in the stock market.

Reacting to the Government's plan, competitors of IBM-organized in a newly created Computer Industries Association-have moved to voice their views. Meetings by the group have been scheduled in Los Angeles, San Francisco and New York, and the comments of the computer industry have been invited. The association is composed largely of peripheral-equipment manufacturers, but the group's counsel says it is not looking at the issues solely from that viewpoint. Many members hope to bolster the Justice Dept.'s case.

The Government made its stern stand known in briefs filed with the Federal District Court in New York in response to IBM's petition for a separate, immediate trial on the issue of how much of the market the giant computer company actually controls. The U.S. brief, filed Oct. 16, appeared to agree with IBM's contention that a survey of the market was a necessary first step to a decision on the company's future. But the Justice Dept.'s attorney, Raymond M. Carlson, said the tentative goal was to break up IBM. The brief phrased it legally this way:
"Divestiture relief designed to dissipate the enormous market power of the current IBM computer manufacturing and marketing structure by the formation of the total domestic and international computer facilities of IBM (manufacturing, marketing, research and development, capital, patents and know-how) into several discrete, separate, independent and competitively balanced entities ca-
pable of competing successfully in domestic and international markets with one another and with other domestic and foreign competitors."

The brief called for injunctive provisions to prohibit IBM from acting to frustrate the court by changing ownership or control of subsidiaries or otherwise altering its procedures except in the ordinary course of business.

Once divestitute is effected, according to the present plan, the Government would also prohibt IBM from using "bundling" in marketing systems or software or from pricing its wares in such a way as to hinder competition. It also would stop the company from announcing new products or services before they were ready to be marketed, a tactic considered an attempt to hinder competition.

The plan, the Government brief stated, "reflects plaintiff's best economic judgment as of this date, based upon an incomplete and sometimes equivocal factual record."

Meanwhile IBM is seeking to counter charges that it has monopolized the computer market, citing statistics that its business dropped from $68.4 \%$ of the EDP market in 1952 to $38.1 \%$ at the end of 1970 .

## Liquid-crystal display avoids flicker trouble

A laser-driven, liquid-crystal dis-play-a new type of "wet" display that can retain an image almost indefinitely and does not have to be replenished continuously to avoid flicker problems, as conventional CRTs do-has been demonstrated as feasible.

Scientists at Bell Telephone Laboratories, Murray Hill, N.J., have shown a large-screen, black-andwhite display that uses a laser
beam to draw opaque lines in a liquid-crystal film contained in a glass slide. If the crystal slide is placed in a projection system, such as an ordinary $35-\mathrm{mm}$ projector, the pattern written on the slide is projected as black lines on a white background.

Through use of a special optical system, the liquid-crystal slide can be written upon while it is being illuminated by the projector's lamp system, thus providing a real-time display.

The liquid crystal is ordinarily transparent, but when the heat of a low-power laser strikes it, its molecules are disordered, and the result is a frosty or opaque region. To clear the liquid-crystal plate, an audio-frequency voltage is applied.

In Bell Laboratories' experimental system the laser beam is moved


Information generated from the keyboard directs laser light over the surface of the liquid crystal slide. As the laser writes on the cell, light from an ordinary slide projector displays the information on the viewing screen.
horizontally by a galvanometermirror system and vertically by an acousto-optic deflector-modulator. The beam is cut off or turned on by the acousto-optic deflectormodulator.

Deflection information is stored in a memory as pulses required to produce the vertical or horizontal movements to write the various alphanumeric characters or to create
line drawings.
A liquid-crystal light valve about the size of a $35-\mathrm{mm}$ slide was used in the demonstration system. The inner surfaces of the liquid-crystal glass cell were coated with a thin film of transparent metal that absorbed the heat from the laser beam.

An Nd:YAG laser with an IR wavelength of $1.06 \mu \mathrm{~m}$ was used, but Bell scientists say that $\mathrm{He}-\mathrm{Ne}$ or GaAs lasers could be applied as well. Where the heat is absorbed by the metal film, the liquid-crystal material becomes disorganized, scattering visible light.

Line widths of $15 \mu \mathrm{~m}$ or more can be obtained by moving a 5 mW laser beam at speeds up to 10 $\mathrm{cm} / \mathrm{s}$ across the liquid crystal cell. The laser scanning system employed is capable of writing at speeds up to several thousand characters a second.

Written images can be viewed for weeks with little loss in resolution or contrast, the scientists say. Erasure is accomplished by applying 35 V at 1.5 kHz between the transparent electrodes.

## New semi processes to be tested in space

Can manufacturing and processing techniques not possible on earth be performed successfully in the zero gravity of space?

Some of the answers will be forthcoming after NASA's Skylab space vehicle goes into orbit next April. Skylab will carry a newly developed electric furnace that will allow experiments in composite casting and 10 other tests, all of keen interest to semiconductor manufacturers.

The furnace can reach and hold temperatures up to 1000 C and can achieve a variety of "cool down" rates on demand. Besides composite casting, the experiments will do the following:

- Determine how much improvement can be obtained in the perfection and chemical homogeneity of crystals grown by chemical vapor transport under weightless conditions.
- Measure self-diffusion and im-purity-diffusion effects in liquid metal in space flight and characterize the distributing effects, if any,
caused by spacecraft acceleration.
- Determine the degree of microsegregation of doping impurities in germanium, caused by convectionless directional solidification.
- Grow doped germanium crystals of high chemical homogeneity and structural perfection and study their resulting physical properties in comparison with theoretical values for ideal crystals.
- Produce void-free samples of silver and aluminum reinforced with oriented silicon carbide whiskers.
- Produce doped semiconductor crystals of high chemical homogeneity and structural perfection and evaluate the influence of weightlessness in attaining these properties.
- Learn how weightlessness affects directional solidification of binary semiconductor alloys, and if single crystals are obtained, find out how their semiconducting properties depend on alloy composition.
- Produce highly continuous, controlled structures in samples of fiber-like sodium fluoride-sodium chloride and plate-like bismuthcadium and lead-tin eutectics, and measure their physical properties.
- Determine how pore sizes and pore shapes change in grids of fine silver wires when they are melted and resolidified in space.
- Determine the effects of weightlessness on the formation of lamellar structure in eutictic alloys when the alloys are directionally solidiffied.


## Marine Corps to test near 'hands off' radio

A manpack radio that has "so many automatic features it's almost a hands-off radio" has been
delivered to the Marine Corps by Hughes Aircraft for test and evaluation.

In addition the set's weight is less than half that of a competitive unit.

According to Donald Q. Hall, project manager at the Hughes plant in Fullerton, Calif.: "All you have to do is to go into the trans$\mathrm{mit} /$ receive mode, select your frequency and hit the press-to-talk switch. The antenna is tuned, the set is aligned and the transmitter comes up to full power, all automatically."

The 10 -pound radio, including battery pack has been named the AN/PRC-104. The closest competitor in its frequency and range category is the AN/PRC-74C, which weighs in at 24 pounds.

Providing 280,000 channels in the 2 -to- 30 MHz frequency band the Hughes radio puts out 20 W of peak envelope power. This gives it a range of a few thousand yards or a few thousand miles, depending upon the type of antenna and the combination of ground and atmospheric propagation used. An eightfoot whip antenna gives about 25 miles of range. A dipole antenna will give a few hundred miles. The radio uses single sideband modulation.

Including batteries, the radio is only a foot wide, 10 inches tall and $2-1 / 4$ inches thick. The silver-zinc battery provides at least 16 hours of service before recharging is necessary.

Hall says the transceiver consumes less than 5 W of average power. A special monitoring mode permits operation at reduced power during periods of light message traffic. This automatically cycles the receiver on and off for one second out of every 10 , with a special alarm circuit alerting the operator to incoming messages.

## News Briefs

A 4000-W cw carbon-dioxide laser will be installed at Ford Motor Co.'s automotive assembly div. in Allen Park, Mich., as a prototype production machine tool. Developed by Hamilton Standard of Windsor Locks, Conn., the system is designed to make deep-penetration welds on auto underbodies at a rate of 60 underbodies an hour. A solar furnace capable of focus-
ing 5000 F of thermal radiation energy from the sun onto a four-inch-diameter area of steel will be installed at the White Sands Missile Range in New Mexico. Fortyfive feet high and more than 100 feet long, the solar furnace will be used to simulate sudden thermal radiation outbursts, such as would emanate from the blast of nuclear weapons.

## Teradyne's L100 Automatic Circuit Board Test System speaks for itself.



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75 Paris $8^{\circ}$, France. Tel. 2657262.
JERADNE


# The new telemetry: Less data, real-time analysis and adaptive 

Real-time analysis, adaptive control, less but more meaningful data and low cost are the trends for the 1970 s, a panel discussion at the International Telemetering Conference concluded.

Representatives of several government agencies and industrial concerns gave their opinions at the three-day meeting in Los Angeles last month.
"Telemetering will no longer be treated as a separate system function in the space program," said William E. Miller, principal engineer for the Space Station and Sortie Laboratory at NASA headquarters in Washington, D.C. "Rather, it will be included as part of a unified data system, similar to the unified S-band system on the Apollo program."

He pointed out that K band and X band would be the main telemetry bands in the 70 s , with X band used particularly on deep-

## David N. Kaye <br> Senior Western Editor

space missions. To make the data more readily accessible from deep space, he said, a data-relay satellite would be developed.

Miller believes it will be necessary to have real-time processing of telemetered data on the ground rather than bulk collection and processing.
"We may," he said, "get as much as $10^{13}$ bits of data per day back from a space station. Without realtime processing, we'll be lost in the flood."

Finally, Miller said, "we need new technologies for storing the data on the vehicle prior to transmission."
"Tape recorders aren't good enough," he noted, "maybe bubble memories will do the job."

But cost will be a major factor in anything NASA funds, he reminded the panel.

## On to digital aircraft

Maj. William H. Hargrove of the Minuteman Program Office at Nor-


DC-10 flight test data is processed in real-time at McDonnell Douglas Corp., Long Beach, Calif., while the aircraft is in the air. Flexibility of data formatting is allowed by the large-screen CRT displays.
ton Air Force Base, San Bernadino, Calif., spoke of a new Air Force program aimed at developing an all-digital aircraft. Flight controls, electronic countermeasures systems, navigation and guidance systems-all will use a centralprocessor multiplexing system with a digital data bus architecture. The aircraft is to have digital integrated displays in the cockpit, with a capability for telemetering all data to the ground if desired. Work on digital simulation for this program is going on at Wright-Patterson Air Force Base in Ohio. The program is associated with remote pilotless aircraft programs that the Air Force is pursuing.

Maj. Hargrove stressed that cost was the major factor in future decision-making. "We have major reliability problems," he said. "Programs have been cut back to the point where we can't stand any loss of data due to malfunctions."

Cost, he went on, also has pushed the Air Force to concentrate on "operational realism" in all its testing. This means that a telemetry pod cannot ride external to a missile; no testing hardware will be allowed to affect the operational performance of the system under test.

## C band for hi-g conditions

Burton E. Norman, who said he was working on instrumentation plans and programs at the Army's White Sands Missile Range in New Mexico, called for C band telemetry systems for in-barrel testing of weapons under hi-g conditions. He also said that improved telemetry systems were needed to define the environment that missiles encounter. As a general trend, he noted, "time multiplex telemetry is likely to take over a larger portion of


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s'Hertogenbosch (Holland), Sydney, Stockholm, Tokyo, Toronto, Turin, Vienna.
the systems than it has now, compared to frequency multiplex telemetry."

The Coast Guard, which mailed in information to the session chairman but did not send a speaker, reported that its plans in telemetry centered on automated lighthouses and remote automated data buoys. Many of the data buoys will be used as environmental monitors, since the Coast Guard is planning to devote a large portion of its R\&D budget in the 70 s to pollution control and monitoring.

## Test ranges plan cutbacks

Robert Pickett, Federal Electric Corp.'s manager of telemetry systems at Vandenberg Air Force
a desired rate and then format the data on the ground.

## The European scene

Turning to Europe and its telemetry problems, Martin Deckett, a market planner for TRW Systems in Redondo Beach, Calif., pointed out that telemetry and other electronic systems projects tend to be spread over many nations. As a result, he said, Europeans must be adept at writing interface specifications.

Deckett noted that the most important telemetry project in Europe at present is the Helios spacecraft -an experimental collection station for space data. It is being built by the West Germans to have variable


Telemetry systems at the Atlantic Fleet Weapons Range in Puerto Rico receive signals from missiles being fired by the fleet in practice drills.

Base in California, said that the Western Test Range would be eliminating telemetry ships completely and would be cutting back on instrumentation redundancy on the ground. To maintain range reliability, the move will be towards computerized preflight checkout systems. The aim is to make sure that the available equipment is all working before the missile goes down the range.

Because of budget cutbacks, fewer missiles will fly, Pickett said, adding: "We need higher data rates to handle the more concentrated instrumentation that each bird will carry."

He pointed to the need for adaptive telemetry systems. These would call for programmed data from a sensor, would transmit the data at
data-rate transmission of from 8 to $4000 \mathrm{~b} / \mathrm{s}$. It is to have an operational range of 100 to 200 million miles, with signals from the spacecraft received by a 100 -meter steerable dish at Effelsberg, West Germany.

According to Deckett, different countries have specialized in different technologies; on large systems, they all come together. Although they still require subsystems from the United States, Deckett said, the next few years will see technology expanding in Europe to eliminate such dependence.

## Biotelemetry: A surge in growth

Biotelemetry, the sending of bio-logical-data signals, had "no clear goal" in the 1960s, said Roland

Radar of the Dept. of Physiology at the University of Southern California. Neither did it have a market, he noted. The nation's space effort provided the spur for change. The current annual biotelemetry market is about $\$ 10$-million in clinical telemetry, $\$ 3$-million in commercial research and $\$ 5$-million in government research.

Among the main markets that Radar sees for biotelemetry in the 70 s are these: hypertension studies; electrocardiogram and electroencephalogram transmission from ambulances, intensive-care units and progressive-care units; the study of cattle and swine in meat production; and drug testing with implant telemetry.

Loyle Baltz, an engineer with the safety testing group at the Ford Motor Co., Dearborn, Mich., spoke on the use of telemetry to test automotive safety. He pointed out that the majority of safety tests were run with hydraulic sleds and barrier crashes. "About 80 to $85 \%$ of our testing uses accelerometers and FM data recording," he told the panel. "We also measure speed, loading and pressure."

The present swing is toward the use of crash-proof, on-board telemetry equipment.

Olin King, president of SCI Systems, Inc., of Huntsville, Ala., outlined the myriad industrial applications of telemetry. He said realtime feedback for positive process control would be one of the largest markets for telemetry in the 70s. And he emphasized the growing move towards bringing the processor into the telemetry system and towards using real-time data bus technology.
"Since many telemetry systems operate in a dynamically changing environment," King said, "systems must be able to adapt."

King foresaw a large market for biotelemetry systems in the emergency treatment of accident victims. More and more communities are attempting to treat patients rapidly at the scene of an accident and on the way to the hospital to reduce fatalities. The telemetry of the patients' vital signs to a doctor at the hospital, King said, will help to save many of the 60,000 people who die each year in the U.S. because of inadequate care before reaching a hospital.

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AT THE TOKYO SHOW

# Japan's electronics makers keep pace despite recession at home 

An air of pessimism pervaded this year's Japan Electronics Show, despite the fact there were more exhibitors and visitors than at any previous show.

A total of 415 exhibits drew over 353,000 visitors, but there were relatively few new products on display. The emphasis was on the application of existing products.

The reasons given for the pessimism: Increasing restrictions by the U.S. and European countries on the import of Japanese electronics products and a slowdown in the growth rate of the $\$ 9.5$-billion Japanese electronics industry, caused by a recent recession. Sales of Japanese products are declining both at home and abroad.

Nevertheless the products on display, though not always new, were varied, and they indicated that the Japanese are keeping pace with trends in the consumer, instrument and component areas.

A CMOS LSI circuit for a 10 digit pocket calculator was display-

[^4]ed by the Tokyo Shibaura Electric Co. (Toshiba). The chip is powered by 6 V dc and consumes 2 mW .

A silicon-gate PMOS LSI circuit was introduced by Mitsubishi Electric. It contains a crystal unit with an oscillating frequency of


Industrial robot developed by Tokyo Shibaura Electric Co.
$16,384 \mathrm{~Hz}$. The company also exhibited a CMOS LSI circuit for a quartz-crystal electronic watch with a liquid-crystal display.

And Hitachi displayed a silicon gate CMOS LSI chip, also for a quartz-crystal electronic watch. Powered by a $1.3-\mathrm{V}$ mercury cell, the chip is being mass-produced by ion-implantation techniques.

As in the U.S., computer mainframe manufacturers in Japan have been debating the merits of core, semiconductor and wire memories in the last few years. However, following IBM's announcement that it will incorporate an IC memory into its 370 series, Japanese manufacturers are beginning to develop semiconductor stores.

Hitachi introduced a high-speed bipolar memory system consisting of six ICs. It has an access time of 30 ns , a cycle time of 70 ns and power consumption of $5 \mathrm{~mW} /$ bit.

Mitsubishi exhibited various MOS LSI memories ranging in capacity from quad 256 -bit types to single 1024-bit.

On the other hand Toko, Inc.,

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For full information on the "Scotchflex" systems approach to circuitry, write to Dept. EAH-1, 3M Center, St. Paul, Minn. 55101.

a long-time manufacturer of wire memories, displayed its HS-200S wire memory, featuring $65-\mathrm{k}$ to 1-Megabyte capacity, an access time of 180 ns and cycle time of 250 ns. The company says the memory is priced at 3 yen ( 1 cent) per bit.

Oki Electric Industry Co. introduced a new wire memory, the OPM-600/30, which will be put on the Japanese market by the end of this year. It has a capacity of 8192 words ( 19 bits per word), access time of 200 ns , cycle time of 400 ns and power consumption of 40 W .

## Liquid crystals displayed

A variety of liquid-crystal, plasma and LED displays were shown. Most notable were the liquid-crystal watches and clocks exhibited by Matsushita Electric, Hitachi, Tamura Electric and others.

A liquid-crystal display for desktop calculators was introduced by the Asahi Glass Co. It is reported to consume $0.3 \mathrm{~mW} / \mathrm{cm}^{2}$, with a contrast ratio of up to 150 to 1 and a life of more than 10,000 hours.
Tamura announced that it was beginning to market a liquid-crys-


Wire memory from Oki Electric Industry Co. has a capacity of 8192 words, a $200 \cdot \mathrm{~ns}$ access time and consumes 40 W .
tal digital clock. Mitsubishi showed a $128 \times 128-\mathrm{mm}$ plasma display panel, consisting of 40 letters by 28 lines, for use in computer terminals. The panel emits reddish yellow with a brightness of about 300 ft-L.

As is usually the case at Japanese electronics shows, consumer electronics dominated the exhibits. There weren't too many color-TV innovations this year, a star attraction at previous shows. But Matsushita demonstrated a calculator/ TV that enables the viewer to do his calculations on the TV screen; a snapshot TV that offers a viewer a glossy photo of a desired image,
and a memory TV that reproduces a scene for repeat viewing. Two similar memory sets were displayed by Hitachi and Sanyo Electric. Sanyo's set can record a TV image and reproduce it on a magneticsheet memory.

Other color-TV innovations included a Sanyo set with a built-in timer that automatically changes to the desired channel when the preset time has arrived. For the lazy viewer, Sharp Corp.'s "channel display" TV employs an MOS/ LSI circuit that displays on the screen for 1.2 seconds the channel number of the program viewed.

Mitsumi exhibited its new semiconductor inductance element, which has opened up the possibility of fully integrating most circuits that have inductors, except possibly high-voltage ones (see "IC Device Integrates Inductors; CoilLess Radio and TV Coming," ED 21, Oct. 12, 1972, p. 36). The company says the element-designated Semicon $L$ is being incorporated in trap circuits for the signal carrier in color TV sets and in the i-f amplifier/detector circuit of radios.

The show, the largest electronics one in Japan, ran in Tokyo from Sept. 21 through Sept. 27. ■■

## Pyroelectric array, a first, introduced

The first commercially available linear imaging array to have a response from the visible region to greater than $15 \mu \mathrm{~m}$ in the infrared has been introduced by the Laser Precision Corp., Yorkville, N.Y.

The 32 -element array is a pyroelectric type. To date, says Bruce C. McIntosh, design engineer at Laser Precision, the detector arrays used in infrared imaging systems have been composed of classical sensors, such as lead sulfide, indium antimonide, thermopiles, and cryogenic detectors. Each of these types, he says, presents a compromise in spectral response, response time and operating temperature.

But the pyroelectric detectors used in the new array, McIntosh reports, have the best combination of the following: broad spectral
response, fast response to radiation and room-temperature operation. Typical characteristics of the new arrays, he says, are:

- Response time of better than $1 \mu \mathrm{~s}$.
- Spectral response variation within $\pm 2 \%$ from the visible region to greater than $15 \mu \mathrm{~m}$.
- Sensitivity capable of detecting signals down to $4 \times 10^{-10} \mathrm{~W}$.
- Ability to withstand peak powers of greater than 10 kW .
- High thermal stability of less than $0.2 \%$ sensitivity variation per ${ }^{\circ} \mathrm{C}$.
- An operating temperature range from -30 to 100 C .

The pyroelectric effect, McIntosh explains, occurs in ferroelectric materials that possess a permanent electric polarization, the magnitude of which is highly dependent on
temperature. A small variation in temperature causes a finite change in the material's polarization, thus producing a change in the potential difference across the detector element.

In the array each detector is connected to an IC preamplifier. The preamps, McIntosh says, are scanned electronically by an auxiliary sweep generator that drives MOSFET switches connected to each preamplifier. For each sweep, the outputs of all the detectors are fed sequentially to an output cable and to an oscilloscope for display.

For a fairly small number of elements in an array, the scope waveform appears as a series of step functions, each representing the energy level of an individual element. The more elements in the array, the smoother the curve.

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# Pulse width and phase encoding compete to be cassette standard 

Although the digital cassette recorder industry is reported near agreement on a standard for recording of digital data on a cassette, a competing recording method is being pushed by three manufacturers. It is called pulse-width encoding. It has, its makers say, the advantage of being much less sensitive to speed variations of the tape than the phase-encoding technique incorporated in the standard developed by ANSI (American National Standards Institute).

In phase encoding, a bit cell is defined on tape. A negative-going flux reversal in the middle of the cell determines a binary ONE and a positive-going flux reversal in the middle of the cell determines a binary ZERO. This technique of recording can stand approximately $\pm 25 \%$ speed variation before it starts to suffer significant bit errors. The ANSI standard requires that the long-term bit spacing or speed variation be held to a maximum of $\pm 4 \%$. Thus ANSI-compatible recorders are required to be well within tolerable speedvariation limits and to operate at an effective constant speed.

In pulse-width encoding, the speed does not have to be constant over the short or long term. The technique allows for at least a 4-to1 speed variation with no bit errors. Since there is no need to maintain a tight speed tolerance, the motor-control circuitry is less expensive.

The bit cell is divided into three portions. The cell starts with a positive-going flux reversal at the beginning of the cell and ends with a positive-going flux reversal at the end of the cell. The state of the cell is determined by the position of the negative-going flux re-

[^5]

Pulse-width encoding requires that the bit cell be divided into three segments with a positive flux transition at both the beginning and the end. The position of the negative transition in between determines whether the contents of the cell are a ONE or a ZERO. Phase encoding requires a negative going flux transition in the middle of the bit cell for a ONE and a positive going flux transition in the middle of the bit cell for a ZERO.
versal. Depending upon the manufacturer, a binary ONE is either defined as the negative reversal at the one-third point or at the twothirds point in the cell. The binary ZERO is the two-thirds point if the binary ONE is the one-third, and vice versa if the positions are reversed.

## 3 firms pushing technique

Three companies are pushing the pulse-width encoding technique: Redactron of Hauppauge, N.Y.; Data General of Southboro, Mass., and Electronic Processors of Englewood, Colo. Redactron and Data General choose the negative reversal at the one-third point, and Electronic Processors at the twothirds point.

All three companies use basically the same method of reading data from the tape. According to Joseph Godbout, systems design engineer at Data General: "We use an up-down counter that is triggered by the positive flux reversal at the beginning of the cell. It starts counting up till it hits the
negative reversal; then it starts counting down. If it reaches ZERO before the next positive reversal, the negative reversal was in the first half of the cell, or at the onethird point. Therefore we read a ONE. If the counter doesn't get to ZERO before the positive reversal at the end of the cell, we read a ZERO."

The period of indecision between a ONE and a ZERO is a single count of the counter and is determined by the counter rate. The precision of the technique is determined, according to Tenny Lode, chief scientist at Electronic Processors, by the number of bit cell divisions that the counter produces.

Data General uses a TTL counter and counts at 800 kHz . This divides the cell into about 70 parts. Redactron uses an MOS counter and counts at 480 kHz . This divides the cell into about 42 parts. Electronic Processors uses a TTL counter and counts at 144 kHz .

Because of a slower tape speed in their recorder the cell is divided into 140 parts for extremely high precision.

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# NASA's biggest solar-cell array to power first space station in '73 

When the first manned orbital space station, Skylab, eases into orbit next spring, it will deploy two solar-cell arrays, each twice as large as any the National Aeronautics and Space Administration has ever flown. One array will provide power for the manned workshop and the other for the Apollo telescope.

The workshop array consists of two wings, each 28 by 30 feet. They extend straight out on either side of the workshop, making it resemble a fat aircraft with short, wide wings.

Although measuring a total of 1680 square feet, the array's solarcell area comes to 1350 square feet. Weighing 5060 pounds, the twowing array consists of six sections, three to a wing, each section divided into 10 active panels. Each panel contains 2464 king-size silicon cells, each 2 by 4 cm . The total number of cells comes to 147,840 .

Normally solar cells measure 2
by 2 cm . The larger, rectangular cells, however, provide several advantages over the smaller square ones. They reduce the number of cells required, which in turn reduces the number of nonproductive spaces between cells and the number of connections. This in turn, improves reliability.

## Consumes almost 11.5 kW

The power output requirement for the array when it first goes up will be 11.483 kW in an environment of 55 C (power increases as the temperature drops at a rate of $0.48 \%$ per 1 C). After eight months the array must provide at least 10.48 kW at 55 C . The array's energy will be channeled to eight power control groups.

The workshop array was built by the TRW Systems Group, Redondo Beach, Calif., under contract with Skylab's prime contractor, McDon-nell-Douglas Astronautics, Hunt-
ington Beach, Calif.
The other array, which will power $18 \mathrm{Ni}-\mathrm{Cd}$ batteries for the Apollo telescope, consists of four undulating wings covering 1567.5 square feet, of which 1060.2 square feet are solar cells. The array contains 164,160 cells, two-thirds of which measure 2 by 2 cm and the remaining third 2 by 6 cm . The array weighs 4221 pounds.

The power requirement at the beginning of the mission will be 11.293 kW at 55 C ; after eight months it will be 10.48 kW .

Power is collected from the solar panels through harnesses routed through channels in beams used to stabilize the array. The harnesses are continuous, without connectors or breaks from the panels to the junction box collector. This was done, TRW says, to increase reliability. The longest harness route is 80 feet long.

There are six harnesses in each wing, two per section. - =


Silicon solar-cell array for Skylab is twice as large as any solar-cell array ever flown by NASA. It consists of two wings with a total solar-energy conversion area of 1350 square feet. It contains 147,840 cells.


Skylab, NASA's first manned orbital space station, will be powered by two giant solar-cell arrays.

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## technology abroad

Replacement of an analog signal processing system in a television camera video circuit by its digital equivalent has reduced noise and distortion, improved stability and given almost error-free signals, according to engineers at EMI Electronics, Middlesex, England. An EMI 2004 monochrome camera was used in a demonstration. The camera was standard, except that an output from the head amplifier fed the digital equipment. The signal, after ana-log-to-digital conversion, underwent five separate digital processes : horizontal-aperture correction; black-level control and blanking insertion; gain control; gamma correction; and synchronizingpulse insertion.

CIRCLE NO. 441

Several large superconducting coils for the field windings of a $1000-\mathrm{kW}$ dc motor and generator -the heart of a new marine propulsion system-have been successfully tested by International Research and Development Co., Ltd., Newcastle upon Tyne, England. The coils have a radial depth of about 85 mm and a length of about 45 mm . They are wound with superconducting cable and impregnated with epoxy resin. To meet the stress due to rapid changes of magnetic field level, the superconductor is a twisted composite that includes a highresistivity component and, in addition, is impregnated with a high-resistivity solder.

CIRCLE NO. 442

A radar Doppler system that gives the distance of a vessel from its berth in addition to its closing speed as it approaches is being developed by GEC-Marconi Electronics of Britain. The system, intended for use at a refinery terminal on the river Thames, consists of transmitter/receiver units operating in the $14-\mathrm{GHz}$ band
together with 21 -inch dish antennas that produce a $3^{\circ}$ transmission beam. For range indication a Gunn diode is frequency modulated by a varactor diode. The frequency difference between transmitted and received signals is converted to distance.

CIRCLE NO. 443

The use of a relatively new group of intermetallic compounds for magnets of exceptionally high coercivity and energy is being investigated by the Brown-Boveri research center in Baden, Switzerland. These new materials are rare-earth/cobalt compounds. The rare earths and mixtures of them used in the program include yttrium, lanthanum, cerium, praseodymium, neodymium and samarium. Samarium cobalt ( $\mathrm{SmCo}_{5}$ ) magnets have been fabricated with an energy product that is twice that of conventional hard magnets and an almost linear demagnetization curve with a coercivity of 700 oersted. The energyproduct of the $\mathrm{SmCo}_{5}$ magnets is about $16 \times 10^{6}$ gauss-oersted. A principal use of this type magnet is in watches and clocks.

CIRCLE NO. 444

A specially constructed golddoped silicon diode that changes junction capacitance with exposure to light has been used in two new circuits. The first is an optically tuned oscillator that has a $60-\mathrm{Hz}$ frequency variation when subjected to light energies of 1 eV and 1.8 eV . It is hoped to increase this change by raising the oscillator frequency, reducing stray capacitances and back-biasing the diode. The second circuit is an optically tuned potentiometer in which resolution similar to that obtainable with precision potentiometers has been achieved. With circuit modifications, the resolution reportedly can be improved to the order of $2.10^{-5}$.

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# washington report 

## Remember the SST we killed and buried?

Like an abandoned cat that suddenly appears on the front porch, quietly washing its face, the SST program is back. NASA has awarded three contracts "to study the technology requirements for future supersonic commercial aircraft." Boeing received $\$ 316,415$; McDonnell Douglas, $\$ 259,000$ and Lockheed, $\$ 231,015$. For the previous SST program, which lasted 10 years, prime contractor Boeing received $\$ 1.1$-billion before the SST became an emotional, political issue and was killed on Mar. 25, 1971.

The cost of killing the SST was $\$ 100$-million more than it would have cost to go on and complete the first phase of the program-fly two prototypes, William M. Magruder, SST project manager then, and special assistant to the President on technology and science now, told an Air Force Association meeting in New York on Oct. 17.

The new studies, NASA says, are focused on commercial aircraft for the 1985-1990 period. This happens to be just about the time the Soviet SST and the British-French joint venture will begin to get old.

## More uhf tuner waivers granted

The Federal Communications Commission is holding fast to its decision that there be no more waivers after Dec. 31, of rules providing that tuning for uhf channels on TV receivers be comparable in accuracy to tuning for vhf channels. Two more manufacturers-the Admiral Corp. and Philco-Ford Co., using Sarkes-Tarzian 70-position detent uhf tunershave been granted extensions of previous waivers of accuracy requirements until the end of the year. Meanwhile FCC officials say tuners made by the other major U.S. manufacturer-General Instrument's F.W. Sickles Div.-are meeting the $\pm 3 \mathrm{MHz}$ requirement, but it is too late for TV receiver manufacturers to change their designs to accommodate different tuners.

## Money at last for defense projects

The President has signed a $\$ 74.4$-billion defense appropriations bill for fiscal 1973 -some $\$ 5.2$-billion less than the Pentagon requested. A final House-Senate conference compromised on differences in the two chambers' bills, resulting in these midway figures on key programs: $\$ 69$-million for two advanced airborne command-post aircraft and $\$ 53$-million for research and construction of a third; $\$ 20$-million to start development
of a new attack helicopter for the Army to replace the canceled Cheyenne; $\$ 6$-million for the Navy's submarine-launched cruise missile; and $\$ 578$ million for 35 Lockheed S-3A Viking antisubmarine warfare aircraft.

The Senate position prevailed on two programs, giving \$421-million for 30 Air Force F-15 aircraft and $\$ 42.5$-million for the Air Force's Minuteman command-data buffer electronics program.

The conference dropped a House proviso that the Trident missile and submarine contracts should be open to competition. Instead, the conference compromised: $51 \%$ of the dollar value of major components for the systems must be open to competitive bidding.

## High Court to rule on software patents

The Supreme Court is expected to issue a landmark decision soon on whether computer software programs are patentable. The U.S. Patent Office has opposed the idea through lower courts and is joined in its opposition by industry leaders, such as IBM, Honeywell and Burroughs. The question arose over a 1963 patent claim by Bell Laboratories. Bell stated in a recent brief that public disclosure of processes and programs through patenting would eliminate duplication and wasted time that it felt had resulted from the secrecy now maintained by industry. Opponents predict a chaotic situation and a stifling of technology if software patents are granted.

## Defense management found wanting

The Defense Dept. is reviewing its tactical communications designs after a scathing report by a House Armed Services Subcommittee just before Congress adjourned. The subcommittee charged that most tactical communications equipment was obsolete, slow-moving and badly managed. Military commanders have tended to pass up the procurement of available technology in favor of the development of sophisticated systems that take years to develop and then become obsolete rapidly, the panel said, adding: "There is little tactical communications equipment in the field today which employs technology developed later than 1962." Meanwhile the General Accounting Office has lobbed a few shells at the Pentagon's management of minor weapons systems. It reported that in a study of 15 Navy weapons that cost less than $\$ 50$-million to develop, experimental work had not been performed sufficiently on most of them before fullscale development was started.

Capital Capsules: The House of Representatives has installed a $\$ 1$-million Control Data Corp. computerized voting system in its chamber for use next year. It's designed to save more than 90 hours a year per member on roll-call votes. . . . The Aerospace Industries Association says employment seems to be stabilizing. Jobs in the year ending June, 1973, it reports, will decline mildly from 923,000 to 914,000 , compared with a sharp drop in the last three years of more than 500,000 . . . The Treasury Dept. will issue new antidumping laws shortly, aimed particularly at foreign entries into the U.S. electronics market. . . . The Dept. of Transportation has decided to go ahead with the design of a $\mathbf{1 0 0 \%}$ federally funded rapid transit system in Denver. Initial investment will be $\$ 11$-million.


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## editorial

## Get enriched lif not rich] with those high flyers

Most of us take a fling, now and then, with those high-flying stocks that often come up in the electronics industry. Hoping to find another IBM or Texas Instruments, we shell out a few bucks to buy stock in a company we feel might have a brilliant future. Some of us come through such investment experiences richer, some poorer. And most of us learn nothing from the experience.

It's an entirely different matter when we are part of the high-flying company. Despite the grand intentions of ambitious founders, most small companies never grow big; many
 die. And if you're involved as one of the founders, or as part of the early management of a fledging company, the chances are that you will be in for plenty of heartaches.

It's likely that you'll have to work 25 hours a day, give up any dreams of a nice vacation, forget about your salary at many times, and work in many areas where you are by no means comfortable. You might find yourself saying: "Here I'm supposed to be a director of engineering, and I spend half my time doing a song and dance for investors." You might find your wife asking: "If we're owners of this damn company, how come we ain't rich?" And you might find your emotional resources strained to the limit as every day seems to offer just another struggle to stay afloat.

Eventually you might become resigned to the fact that your company will never make you a millionaire. And that even if it survives, it will always mean lots of hard work. Or your company may finally merge with another. And you become just one more guy who used to be an executive in his own company. Or your company may die as creditors finally close in and say, "Sorry, Charlie, no more."

Even if that happens, you're ahead of the game. Even if you lose some savings, even if you miss a year or two of the good and "natural" lifeseeing your wife and kids on evenings and weekends and vacationing with the family-you're still ahead.

Very few people involved in startup companies wind up with 40 -foot yachts and $\$ 200,000$ homes with butlers and maids strewn about. Even if their companies go bankrupt, almost all come out richer. Not in money. But in the wealth of experience they develop in a short period of time.

Let's not avoid experience. It's enriching.


George Rostixy
Editor-in-Chief

## Remember the commitment we made to " 883 " in this '69 ad?



# Now it's JAN ICs. Series 54 TTL, developed by TI, has been chosen as the standard. And the same HI-REL Task Force that helped you stay on course through 883 and 38510 (with the MACH IV program) can do it again with JAN. 

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In 1969, TI made industry's biggest commitment to MIL-STD-883. We developed the first clear-cut procurement program-MACH IV-and established a special HI-REL Task Force to see it through. When MIL-M- 38510 came along, we expanded MACH IV and kept the same Task Force on the job.

Now comes JAN-M-38510 with its new challenges and uncertainties. And the same Task Force is still on the job. This same group of managers who worked for you in ' 69 are still working for you today, along with some new members. Together, they bring more than 120 man-years of HI-REL experience to the task -in the areas of reliability and process engineering, product planning, product engineering, marketing, quality control, manufacturing and HI-REL assembly. The Task Force is fully committed to making JAN IC availability a reality - and TI has committed the money, facilities and manpower to support the team.

## Full qualification program underway

Already you have a broad choice of JAN ICs from TI, and the list is growing rapidly under an aggressive qualification program. Our objective is to lead the industry.
It's an objective we are confident of reaching. After all, TI is the leader in developing and delivering Series $54 / 74$ TTL-the series chosen as the JAN IC standard for TTL logic. And JAN IC qualifications are simplified by TI's extensive experience in 38510 /

MACH IV processing. Incidentally, TI's $38510 / \mathrm{MACH}$ IV program supplements the JAN IC program, where JAN slash sheets have not been written or JAN ICs are not available.

## High-volume production facilities

We've been delivering MACH IV ICs, in volume, for nearly three years - and high reliability military ICs for more than a decade. So squarely behind our new JAN effort are TI's unmatched 54/74 HI-REL domestic production facilities in Houston and Dallas. From 150,000 -plus burn-in sockets to environmental shake, rattle and roll labs, to scanning electron microscopes, IR scanners, microprobes, Radiflo and computer data acquisition.

You'll find our JAN ICs already on distributors' shelves. You can count on TI's production strength to maintain excellent JAN IC availability.

## Send for procurement aids

We've prepared a booklet answering common questions about JAN ICs, as well as a handy pocket card which lists existing JAN IC specs, the corresponding
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are everywhere.
You'll find them
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ucts as pocket calculators
and computer I/O terminals;
telephones and communication terminals; and electronic cash registers and stock-quotation terminals. Their ubiquity almost guarantees that you'll use a keyboard product in the near future. And when you do, our advice is to take a closer look at the keyboard itself-some day you might have to specify one.

If you do, you're in for a tough time. Each keyboard in the wide range of products with keyboards is unique to its application-in short, customized. Not only must the designer choose from among an overabundance of suppliers-at least 60-but also from among a head-swimming selection of keyswitches, encoders, buttons, frames and options.

## Which keyswitch is best?

The keyswitch is the heart of a keyboard, and every major manufacturer seems to feel that his keyswitch must be different from all others. The result: over a dozen different types to choose from. And, naturally, each manufacturer insists that his type is best. The truth is each type has advantages and disadvantages; as with most engineering problems, tradeoffs must be made.

Keyswitches can generally be sorted into two major categories: contacting and noncontacting. The former includes the reed switch, the most popular keyswitch until 1968, and a widely diverse assortment of physical-contact (alternate make and break) switches-such as the crossbar, the elastomeric (elastic diaphragm), the mer-cury-filled tube, the magnetic-repulsion and the coil-spring crosspoint switch, to name a few.

[^6]

Clare-Pendar's 16 -station numeric keyboard uses gold crossbar keyswitches and TTL/LSI for ASCII encoding.

The noncontact switch-sometimes mislabeled the solid-state switch-includes the Hall-effect device, the capacitive-coupled switch, the saturating magnetic core and the photoelectric switch. Of these, only the Hall-effect and the photoelectric keyswitch are truly solid state in that they use semiconductor devices to deliver their output signals.

The hard-contact, or mechanical switch, though the oldest of keyboard switches, remains popular
today for one simple reason: It's the least expensive of all the keyswitches. And while it was, for a time, eclipsed by the reed and the noncontact switch, it's making a strong comeback. Many manufacturers are expanding their mechanical switch line-and dropping their prices. Improved manufacturing techniques-plus the use of MOS circuity, which can easily handle high-impedance closures-have led to improved mechanical switches. And they have an added attraction: multiple contacts, which allow easier coding.

The major objections to the mechanical switch have been high contact resistance, contact bounce and relatively short life compared with solidstate switches. They're also difficult, and expensive, to seal (for this reason, most enclosures function as dust covers, not as environmental seals).

However, manufacturers of mechanical switches dismiss the life/reliability and contactbounce / high-impedance arguments as overemphasized. For example, Clare-Pendar, a manufacturer of cross-bar and reed keyswitches, states : "Since MOS encoders tolerate switch-contact enclosures of up to 200 $\Omega$, the integrity of the contact closure has much less effect on mean cycles before failure than in the old days, when the integrity of the contact had direct impact on keyboard output. With the gold crossbar keyswitch and MOS encoding, keyboard reliability equals that of solid state-at approximately half the cost."

Bounce, it's claimed, can be easily removed with delay or filter circuitry. As for life, most mechanical switches are specified at 10 -million operations, a figure that the manufacturers contend is sufficient for most applications.

But some of the newest mechanical switches are promoted as having low bounce and long lifetimes. Stacoswitch, a recent entry in the keyboard industry, says that its keyswitch has contact bounce of less than $100 \mu \mathrm{~s}$. Maxi-Switch is offering a bifurcated-contact switch with a bounce of less than $500 \mu \mathrm{~s}$. And Cherry Electrical Products has a new crosspoint switch with reported bounce of less than $300 \mu \mathrm{~s}$ and life of at least 100 -million operations. Oak Industries, an
old-line manufacturer, also offers a 100 -million operation switch, one that Oak claims has never failed in a customer's test program.

## Reeds are still popular

Until 1968, when the Micro Switch Div. of Honeywell introduced its Hall-effect keyswitch, the reed switch dominated the keyboard industry. The hermetically-sealed contacts-actuated by a moving permanent magnet-eliminated the corrosion and contamination problems of mechanical contacts.

The popularity of the reed stems not only from the sealed contacts, but also from the type's known characteristics: its long life-about an order of magnitude greater than that of nonsealed switches-its ease of replacement and its ability to handle fairly high loads.


Micro Switch's line is represented by these MOS and TTL-encoded data-preparation and communications keyboards. Each uses the Hall-effect keyswitch.

The major disadvantage of reeds is contact bounce. However, mercury-wetted types are available to reduce the bounce and improve dryload (low voltage, low current) switching. One problem that's rarely mentioned, however, is that reeds can resonate when excited by vibrations of the correct frequency.

While reed switches have a longer life, they cost approximately twice as much as mechanical types, so tradeoffs must be made. Also, reed keyswitches-and reliability-vary considerably from manufacturer to manufacturer, depending on their mechanical design, such as magnet orientation and path of travel. Depending on design, reeds also can have either Form A or Form

## Keyboard switching techniques



Typical contacting type is this Cherry keyswitch which uses crossed knife-edge contacts made of a gold alloy.

In the popular reed keyswitch, the contacts close when the plunger-attached magnet comes near the reed (Key Tronic).



Micro Switch's Hall-effect IC chip gives a pulse output when plunger magnet passes near chip.


Control Devices' keyswitch couples an ac signal to switching circuitry via the coupling plates on the key.


When the keyswitch is operated, the ferrite core unsaturates and transformer-couples the sense and drive wires (Licon).

B contacts and both tactile and audible feedback.
Reed keyswitch vendors include Key Tronic, Cherry, Controls Research, Maxi-Switch, ClarePendar, Alco and Dialight.

## Unusual designs offered

In their quest for originality, keyswitch manufacturers have come up with some unusual designs. Stackpole Magsat, for example, markets a unique magnetic-repulsion keyswitch that pro-
vides a 1 -ms pulse output and has tactile feedback and an audible click. Controls Research manufactures the BI-PAC, a switch that uses as contacts two coaxial compression springs, which are integral with the pin terminals. And Mechanical Enterprises offers its Mercutronic switch, in which a mercury-filled, flexible tube gives bounceless closures. When the key is in the up position, the tube is pinched, breaking the continuity. Depressing the key releases the tube, allowing the mercury to flow together. The de-


Stackpole/Magsat's flying magnet switch uses magnetic repulsion to create a pulse. The output is independent of user-caused variations in key operation.

Depressing the key joins two pools of mercury in this Mechanical Enterprises' keyswitch. The mercury-filled tube is pinched off when key is released.


Photoelectric keyswitch uses a light source, photocells and coded masks (Western-Digital Systems).

Wild Rover's thin keyboard provides a multiple.contact closure when a flexible grating contacts fixed, bottom lands on a PC board.

vice can operate in any position.
Still another type of contacting keyswitch is the elastomeric, or elastic-diaphragm, switch. Versions are offered by at least four manufac-turers-Datanetics, Flex Key, Donnelly Mirrors, and Chomerics-but the basic principle of each is similar. Depressing a key forces a conductive or plated elastomeric membrane against a contact grid or band on a PC board. The advantages claimed include sealed construction, little or no bounce, high reliability and life, and-because of batch fabrication-low price.

The major objections to this switch are relatively high contact resistance and a spongy touch, unfamiliar to those used to typewriter keys, which have tactile and audible feedback. However, contact resistance has been reduced by the use of a silver-filled elastomer (Chomerics). And the explosion of the market for mini-calculators has led to increased acceptance of the diaphragm switch: Its wafer-thin profile is ideal for the application.

The Control Products Div. of Texas Instruments markets its Klixon, a snap-action disc that acts as a combination contact, return spring and tactile-audio indicator. The technique allows TI to make 0.15 -inch-thick keyboards.

Another company, Wild Rover, manufactures a low-profile keyboard that uses an entirely different principle. In this switch, a flexible, multielement electrical grating is pushed against a rigid bottom contact. The multipoint contact is said to distribute the transient energy evenly over the multiple elements, thereby decreasing contact resistance and dissipation.

## The Hall-effect: a new trend

Of the noncontacting keyswitches, Micro Switch's Hall-effect type is by far the most popular. Introduced in 1968, the Hall-effect switcha true solid-state type-revolutionized the keyboard industry. The switching element, consisting of a Hall generator, trigger circuit and amplifier, is integrated on a 0.04 -square-inch chip. When the key is depressed, a magnet moves past the Hall element, generating an analog voltage. The voltage is converted to a digital output and then amplified to produce a $1-\mathrm{ms}$ pulse output.

The advantages of this switch include no moving contacts, no bounce, high reliability and long life, no EMI, no variations in contact resistance and, finally, no misses. The tradeoffs? Higher initial cost and the need for some standby power. However, four price reductions in four years have kept Micro Switch competitive with other manufacturers. And the company states that the quality and reliability of its keyswitch actually
reduces costs by eliminating the need for incoming inspection and by reducing service calls.
The magnetic core switch, manufactured by Licon, works by keeping a ferrite core saturated initially with a magnetic field. When the key is depressed, the field is removed and the core unsaturates and transformer-couples an ac signal to the output. The ac voltage is then rectified and used to drive IC logic. The chief advantage is the reliability associated with the ferrite core.

Both Control Devices and Raytheon offer capacitively coupled switches. In the Control Devices unit, depressing the key capacitively-couples an ac signal into signal-shaping circuitry, which converts the signal to a logic level. The Raytheon unit combines capacitive switching with a scanning technique to encode the keys.

At least three companies-Western Digital Systems, TEC and Symbolic Displays-make


Oak Industries' low-profile contacting keyswitch is intended for calculator applications.
photoelectric keyswitches in which photodetectors sense the various light patterns produced when the keys are struck. The technique yields a unique code for each key.

## Watch those switching specs

System restrictions, such as physical size, available power and coding requirements should help the designer narrow the number of keyswitches to be investigated. According to manufacturers, engineers make two major mistakes in specifying a keyboard: First, they bring in the manufacturer late in the design process; second, they don't familiarize themselves with keyboards adequately. As a result, they tend to either underspecify, or overspecify.

Important specs include force, travel, bounce, load-handling capacity, electrical noise, contact resistance, reliability, human factors and environmental factors-such as operating-temperature range, vibration and humidity. Any of these


Donnelly Mirrors evaluates the lifetimes of its flexiblediaphragm keyswitch on this test rig.
may be important, depending on the application.
For commercial keyboards, force and travel have become standardized at about 3 ounces and $3 / 16$ of an inch, respectively-at least for typewriter and high-speed entry applications. However, for miniature calculators with low-profile keyboards, travel may be negligible.

As for bounce, the makers of contactless switches argue that little or none is desirable for two reasons: First, the keyboard electronics will be considerably simplified; second, if you start out with marginal bounce specs, you may run into reliability trouble later-despite compensating electronics.

## Is there a reliable reliability spec?

Life and reliability specs, together with cost, form a major battle line between keyswitch mak-ers-and especially between the makers of contacting and noncontacting switches. Every manufacturer claims long life and high reliability for
his product. But for the engineer-who is caught in the middle-there are no standards for failure or for keyswitch life testing. As a result, manufacturers tend to define and test for life in a way that makes their product look good.

Thus an engineer is presented with pages of statistical data that ostensibly demonstrate hundreds of millions-or even billions-of operations, cycles, etc. But these specifications are meaningless unless the manufacturer defines the failure criteria and the actual test conditionsfor example, loading, environment, power levels and other factors.

When failure criteria are given, an engineer must still decide on their validity as applied to his own equipment. Thus failure can be defined as either missing or erroneous outputs, as catastrophic failure, as an arbitrary level of contact resistance or contact bounce, or by other factors.

Except for catastrophic failure, none of these constitutes absolute failure; they are relative terms that may be important to a particular application.

Micro Switch, the largest supplier of keyboards, says: "Reliability concerns the 'miss' characteristics of the keyswitch, where a 'miss' is the failure of a switch to open and close once during one complete cycle of the actuator. Mechanical contacts, regardless of the packaging or operating mechanisms, can miss from time to time."

Controls Research, a mechanical-switch maker, counters with this argument: "An occasional intermittent output may be critical in a key-tostorage device, but not to a teletypewriter communications terminal. A keyboard can fail in many ways, and even the best life-test data can't predict when or how it might fail in a customer's situation."

## Running around in cycles?

The figure of 10 -million operations has been generally used as an acceptable keyswitch lifetime. Of course, as Datanetics points out, it completely ignores the rest of the keyboard, which can involve considerable hardware.

However, increasing competition has led to spiraling claims for keyswitch lifetimes that leave the 10 -million figure far behind.
"Many switches," says Clare-Pendar, "are rated at a hundred-million cycles; yet the probability of ever seeing a hundred-million cycles is practically nil. Reliability in meantime-beforefailure or mean-cycles-before-failure-at a specified confidence level-is the most meaningful criteria."

Maxi-Switch has published statistical data for
keystroke usage ratios. Using the text of the Gettysburg Address as an example, the data show that the space bar, with $17 \%$ of the total strokes, is the key used most often. The space bar, together with the five vowels, make up $49.5 \%$ of the total strokes, Maxi-Switch says. Thus if a keyboard were operated continuously at 40 words a minute, 24 hours a day for one year, the space bar would accumulate a total of $17,870,400$ operations (based on five-letter words). This, says Maxi-Switch, demonstrates that a keyboard is unlikely to have a requirement for 50 -million to 100 -million operations.

## Don't forget the keyboard itself

With mountains of claims and counterclaims of superior switching techniques and reliability to climb over, it's easy to forget the rest of the keyboard. As Micro Switch points out, many systems are completely designed before the keyboard is considered.

Taking that approach will no doubt lead to higher costs, a marginal design, or both. This is because keyboards are not as simple as they appear but require a host of decisions. Questions that should be asked include:

Do I want a direct-wired matrix (array), a self-coded board or a fully-decoded keyboard? What physical size do I need? Does my application call for a thin keyboard or can I use a medium or high profile? Should I specify PCboard or steel-frame construction? Is weight a problem? Does the keyboard environment require oil or splash-proofing? Is mono-mode or multimode the way to go? What about buttons: styles, colors, arrangements? What about the human elements? Do I need tactile or audible feedback? Is N-key rollover called for, or can I use two-key rollover? Which special features, if any, are important to my application: Strobe? Data-key idle? Enable input? These, as well as other questions, can't be ignored.

Probably the first decision to be made concerns the keyboard/electronics interface. Three options are generally available: You can buy a collection of uncoded switches and build your own coding electronics; you can buy a collection of self-coded switches-such as the ferrite core, capacitor-coupled and photo-optical types-and build your own interface; or you can opt for a full keyboard system containing the keyswitch array, all coding electronics, plus circuitry for buffering, keyboard modes and other elected features. Which way to go depends on your technical expertise and the economics of building your own keyboard (as a percent of total system cost) versus buying one commercially.


The Bi-Pac, Controls Research's coaxial-spring keyswitch, is used in the company's calculator keyboard.

Actually, most requirements don't allow selection of an off-the-shelf keyboard. Variations in mounting, output codes, key configurations and other factors usually necessitate some customizing. This is why, as Clare-Pendar points out, major keyboard manufacturers have a "boilerplate" specification, which can be used as a point of departure for a particular application. Specs can be as tight or as loose as a designer wantsbut tight specs tend to increase prices.

## Which code is best?

A few keyboard types have become fairly standard, at least as far as output codes and character assignments are concerned. These include the so-called data-preparation keyboard, which uses the eight-bit (plus parity) Extended Binary Coded Decimal Interchange Code (EBCDIC), and the so-called communications or dataentry keyboard, which uses the seven-bit (plus parity) American National Standard Code for Information Interchange (known as ANSI, ASCII, or USASCII).

These two types are sometimes confused: The data preparation board is used mostly for key-to-storage (tape, disc, cassette) and other keypunch applications, while the communications keyboard is popular for graphic and computer I/O terminals. As such, the key assignments for the two vary considerably-the basic communications (ANSI) keyboard separates the alpha sec-
tion from the numerals and symbols, while the data-preparation (EBCDIC) keyboard assigns numerals and symbols to the same keys as the alpha characters.

The ANSI code, now a Federal standard, is used in most of the keyboards sold today. This is because the code, when used with proper char-acter-to-key assignments, permits key shiftslower case to upper case, for example-by inversion of a single bit. Thus this logical-bit-pairing technique saves money, since only a simple inversion is needed to distinguish between shifted and unshifted modes.

The second most popular code, the EBCDIC, was developed to avoid nonlogical bit pairing in keypunch applications, where key assignments differ from those of the ANSI keyboard.

Other available codes include BCD, Baudot, hexadecimal and even custom types. But these are usually more expensive; so stick with the ASCII and EBCDIC, if you can.

Keyboards that assign one code for each key are said to operate in mono-mode. With more than one symbol, the keyboard operates in multi-mode-with two symbols for each key designated as dual-mode, three symbols as tri-mode and so on. For dual-mode, such as lower-case/ upper-case operation, one shift key is required. For tri-mode both a shift and a control key must be operated. The shift key usually has alternate action. It may remain below the level of the unoperated keys to designate shift mode, or it may be lighted to designate a shift. Lighted keys are said to be easier to use since no key-position judgement is required.

The best operating mode (mono, dual, etc.) depends on available keyboard space, the number of characters needed and, as always, on economic considerations. For example, it may be less expensive to use a separate block of numeric keys and to operate in mono-mode than to assign the numerals to other keys and operate in dual-mode. Assigning multifunctions will save panel space, but throughput will be lowered, because an operator must press an additional control key.

As with most other keyboard characteristics, the method of producing the output code-and the output logic level as well-varies from manufacturer to manufacturer. Some manufacturers arrange the switches into an array in which an actuated switch shorts across a row and column. High-speed scanning circuitry then spots the closed switch and delivers a coded output.

Others use interim codes to address bipolar or MOS ROMs. And still other manufacturers use self-coded keys with diode matrices to connect the switches to the various bit lines. Except for logic levels, the results remain theoretically the
same for all manufacturers: A unique output code for each keyswitch. But cost tradeoffs can be made.

Clare-Pendar offers this analysis: For three or four-level keyboards, or where non-bit-compatible codes are required, the minimum cost encoder is MOS. For simple mono or dual-mode keyboards with bit-compatible shifts, TTL decoding frequently offers a cost advantage.

## N-key rollover-do you really need it?

The use of MOS has thrown new fuel on a long-simmering argument over the need for N key rollover (NKRO). NKRO, an expression coined by Micro Switch, is a technique to prevent erroneous signal transmission during burst speed typing. When a key is struck, correct information in the proper sequence will be transmitted, even though any number of previously struck keys are still depressed. The technique is an extension of 2-key rollover (2KRO) in which an electronic interlock prevents errors when two keys are simultaneously depressed.

Some manufacturers say that 2 KRO is sufficient for all applications. Others contend that the 10-to- $15 \%$ price differential for NKRO discourages those who want it. And still others claim that their NKRO costs nothing.

Controls Research, for example, states: "Since the cost impact, on large keyboards at least, is really zero with the right encoder chip, NKRO should be specified."

Raytheon says: "NKRO is an excellent feature in high-speed typing applications, since it substantially reduces the number of errors. However, for all other slow-speed and one-finger applications, 2 KRO is quite adequate."

Studies by the Honeywell System and Research Div. for Micro Switch seem to substantiate the Raytheon statement. The studies show that about $30 \%$ fewer errors were made by highspeed typists with NKRO than with 2KRO.

Key Tronic proposes still another approachselective N-key rollover, a combination of 2 KRO and NKRO. The keyboard becomes locked after a fixed number of keys have been depressed-and they are held depressed. Key Tronic claims that this approach is less expensive than NKRO but just as advantageous.

## We're all human

The NKRO debate is usually coupled with another highly controversial area-human factors. Perhaps the least understood of keyboard characteristics, such factors as tactile and audible feedback, switch and button feel and key


MOS-encoded keyboard (above) from Maxi-Switch uses a single DIP to provide any eight-bit code. Contrast this
spacing and arrangements are the most hotly debated of all keyboard aspects. Micro Switch says that this aspect of keyboards has been of major concern for a decade.
Perhaps the most controversial parts of the Honeywell studies were these conclusions about displacement and keyboard operating characteristics :

- There is no advantage to having a snap or audible click in the keyboard operation.
- There is no difference in performance between stepped and sloped keyboards.
- Efficiency in the use of electric keyboards is greatest if force and displacement are held within the following limits : 0.9 to 5.3 ounces and 0.05 to 0.25 inches at the full travel of the keys.
- Most typists who are already familiar with electric keyboards should be able to bring their throughput to normal standards on an electronic keyboard within 10 days, usually less. (Throughput is defined as speed minus errors.)

Raytheon agrees: "If, by human factors, you mean keyswitch touch and feel, we think that causes too much concern. People will naturally feel more comfortable with a key feel that they have been accustomed to, but they also adjust rather rapidly to a different key feel, provided it doesn't have clearly objectionable characteristics."

However, Dialight states: "Human factors, next to reliability, are the most important considerations in the efficient, error-free and nonfatiguing operation of the keyboard."
Key Tronic sums up the human factor issues this way: "It seems that different segments of the market want different amounts of noise. Calculators are generally noiseless, whereas alphanumeric keyboards are produced with a small-tomoderate amount of noise. Also, key travel is continually being decreased. A number of years ago a travel of 0.187 inches was more or less an industry standard. As newer switches are being

with the company's TTL scan encoded keyboard (above), which uses 14 DIPs to provide codes to 16 bits.
designed, the travel is being shortened. Calculators, for example, have essentially no travel compared to alphanumeric keyboards."

The dispute over the necessity of tactile feedback has resulted in four general schools of thought. The first tries to duplicate the feel of the electric typewriter, in which the key force increases with key displacement until the moment of switch closure. At that point, the force or pressure drops suddenly, accompanied by an audible signal. The second school opts for a constant pressure, usually light for most applications (an exception is keyboards for aircraft, where key pressures are kept relatively high to avoid accidental keying). The third school takes a middle road by increasing the key force with displacement but eliminating the sharp change at closure. The fourth school just ignores the entire situation.

## The old problem: Build or buy?

One question that can't be ignored and must be answered right at the beginning of any keyboard specification is: Should I build my own? As might be expected, suppliers are split on this issue. The battle line is drawn, with those who make only keyswitches lined up on one side and those who make complete keyboards on the other. Sitting on the line-with a foot in each campare those suppliers who make both keyswitch modules and entire keyboards.

In general, consider building your own keyboard :

- If the quantity justifies the effort economically. Oak, for example, feels that if you need only 500 to 1000 keyboards a year, you can't build your own economically. However, if you're using five to ten thousand a year, you probably can afford to hire the expertise, if you don't already have it. In contrast, Clare-Pendar feels that you should consider building your own if
you need over 50,000 units a year-or less than 100.
- If you've got both the technical people and the facilities. The latter includes PC-board and semiconductor-device fabrication capabilities.
- If "standard" designs don't suit your purpose. For example, you may have an integrated design, with all major components mounted on a single PC board to eliminate interconnect costs. Discrete keyswitches can be mounted to this master board as well, thereby avoiding connectors, mounting hardware, etc.

The don't-build-your-own arguments run this way:

- It will invariably cost more to make your own keyboard than to buy one commercially. This is because designers tend to look at material and labor costs alone and to forget the hidden costs of engineering support, scrap, transportation, tooling and the like. Also forgotten are testing costs, which can be substantial-especially for large numbers of units that require automatic or semi-automatic test equipment.
- For encoded keyboards, a keyboard vendor will negotiate a large-quantity, annual contract with an LSI-chip supplier and pass the savings on to customers who buy modest quantities of keyboards.
- Even if a keyboard requirement is unique, it is still easier and cheaper to modify an existing keyboard. For example, in the case of the integrated design, a keyboard vendor can usually supply a larger PC board, or he can build on a customer-supplied board.


## If you decide to buy, then what?

Cost will probably influence your build-or-buy decision more than any other factor. And, if you decide to buy, cost will be a key factor in selection. The plethora of competing suppliers has led to mergers, shakeouts, new products-and tumbling prices. All of which means it's a buyer's market. Let's take a quick look at what's being offered.

Micro Switch, everyone agrees, is the leading supplier of keyboards, with an estimated onethird of the total market. (The No. 2 position is not so well agreed on, being contested by a half dozen or so, of the 60 remaining manufacturers.)

Micro Switch offers its Hall-effect keyboards in practically any desired alphanumeric array and electronic configuration. The choice includes communications, data preparation and numeric keyboards, two-key or N-key rollover, multimode operation and MOS, TTL or DTL encoding.

Latest developments at Micro Switch include an across-the-board price reduction (the fourth
in four years) of up to $13 \%$; computerized qual-ity-assurance testing, and both lighted and bilevel switches.
Controls Research offers modules or complete keyboards with either reed switches or its BIPAC mechanism. Encoding can be either TTL/ MSI scanning electronics or MOS with built-in NKRO. The former is used in the company's Model 7100, a 53 -position keyboard with ANSI array and four-mode ASCII encoding. The 7100 sells for $\$ 49$ in quantities of 5000 . The latter is offered in a single, 40-pin MOS chip for coding up to 64 keys.

For those who wish to build their own board, Oak offers its 400 series, a gold-alloy crossbar keyswitch that sells for less than 30 cents in large quantities $(500,000)$. Oak will supply any spring pressure or key stem (sloped or stepped) height, as well as a choice of switch configurations: SPST and DPST, N/O or N/C, and al-ternate-action.

No doubt spurred on by increasing mini-calculator sales, Oak has recently introduced its 415 series, a low-profile keyswitch. The 415 stands 0.415 -inches high, including the key cap, and sells for 20 cents each (with key cap and legend) in quantities of one-million.

Cherry manufactures both switch modules and complete keyboards. Both reeds and gold crosspoint switches are offered in a variety of contact forms. The low-profile gold crosspoint, Cherry's latest switch, comes with straight or angled stem, momentary or alternate action, and it's lighted, if desired. The Form A, momentary-contact version sells for 29 cents in quantities of 250,000 and is said to have extremely long life.

Cherry's standard keyboards include 66-key, tri-mode ASCII, 55 -key quad-mode ASCII and 12 and 16 -key numeric boards. Scanning electronics are used to encode the switches. As with most manufacturers, Cherry offers custom designs and many special features.

Raytheon's line includes plug-in reed modules and its CAPSCAN keyboard (designed to replace the reed line). The CAPSCAN combines capacitive switching with TTL-compatible scanning logic and offers 2 KRO or NKRO, splashproofing, any operating force and, as an option, lighted key caps.

Key Tronic offers a full line of reed keyswitch modules and keyboards, as well as complete customizing service. The latter includes PCboard, injection molding, electronic assembly and tool and die capabilities.

Other features offered by Key Tronic include MOS encoding with selected repeat, stepped or sloped boards by rotating the key caps 180 degrees, selective NKRO, lighted alternate action,
and pROMs to change codes in the field.
Stackpole/Magsat offers a variety of keyswitches and keyboards designed around its digital switch module. The $1-\mathrm{ms}$ pulse output (similar to Micro Switch's) allows Stackpole to use static encoders. Standard features include tactile/audible feedback; NKRO; shift, control and multimode keys; TTL-compatible output and strobe.

The latest Stackpole developments include a mode in which every key can be a repeat key without the need to depress a control key first and the LO-PRO, a new line of low-profile, mechanical keyswitches, designed for calculators and other low-throughput devices.

Clare-Pendar offers reed and gold crossbar keyswitches in various configurations. Individual switches are mounted on steel frames, as opposed to mounting on PC boards, so that the keyboard becomes a rigid structural member (Licon, Oak and others also use this construction). Also offered are alternate action; illuminated keytops;

MOS encoders, including NKRO ; three-color keytops; one, two or three poles, and IBM-Selectricshape keytops.

Maxi-Switch has three different switch styles to choose from: the 1800 series keystrip, which uses a series of steel frames and glass reed contacts; the 2700 series individual glass reeds; and the 3100 series, a bifurcated gold-contact mechanical switch that sells for 21 cents in large quantities. The latter is Maxi-Switch's entry in the calculator market. Also offered are TTL or MOS encoders, multimode, two or N-key rollover and delayed strobe.

The increasing demand for low-cost keyboards for desktop and pocket calculators has proved a boon for those manufacturers offering waferthin and low-profile keyboards. These include Donnelly Mirrors; Datanetics, with its elastic diaphragm switch; Stacoswitch, with its new IP Series, a modular, mechanical-contact, multistation keyboard; and Wild Rover, with its TC Series of multiple-contact switches and boards. .-

## Need more information?

The companies and products cited in this report have, of necessity, received only cursory coverage. They've been selected for their illustrative, or in some cases, unique qualities. Companies not mentioned may offer similar products. Readers may wish to consult these manufacturers for further details:
Alco Electronic Products, Inc., 1551 Osgood St., N. Andover, Alco Electronic Products, $685-4371$. (John Smerigan, Product
Mass. 01845. (617) $685-437$. Pircle 400
Manager)
Automatic Electric Co., 400 N. Wolf Rd., Northlake, III. 60164. (312) 562-7100. Circle 401
The Capitol Machine and Switch Co., 87 Newtown Rd. Danbury, Conn. 06810. (203) 744-3300. (Arthur E. Wilson, Cherry Electrical Products Corp., 3600 Sunset Ave., WauElectrical Products Corp., 3600 Sunset Ave., Wau-
kegan, III. 60085. (312)
689-7600. (Peter Bartheli)
Circle 403
Chomerics, Inc., 77 Dragon Ct., Woburn, Mass. 01801. (617) 935-4850. (A.' Klepper)
01801. (617)
Circle 404

Clare-Pendar Co., P.O. Box 785, Post Falls, Idaho 83854. (208) 773-4541. (Stephen F. Meyer, Marketing Manager)

Colorado Instruments, 1 Park St., Broomfield, Colo. 80020. (303) 466-1881. (Walter Pounds, Keyboard Marketing

Control Devices, Inc., 204 New Boston St., Woburn, Mass. 01801. (617) 935-1i05. (Leo Gilson, Sales Manager) Mass.

Controls Research Corp., 2100 S. Fairview, Santa Ana, Calif. 92704. (714) 557-7161. (Don Schulze, Product Sales Manager) Circle 408
Datanetics Corp., 18065 Euclid St., Fountain Valley, Calif.
92708. (714) $549-1191$. (Joseph DeClue, Engineering Vice 92708. (714) 549-1191. (Joseph DeClue, Engineering Vice
President)

Dialight Corp., 60 Stewart Ave., Brooklyn, N.Y. 11237. (212) 497-7600.

Circle 410
Donnelly Mirrors, Inc., 49 W. Third St., Holland, Mich. 49423. (616) 396-1441. (Dick Tierney, Marketing Manager)

Elec-trol, Inc., 26477 N. Golden Valley Rd., Saugus, Calif. 91350. (805) 252-8330. (Ray Freed) Clrcle 412

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Licon Div., Illinois Tool Works Inc., 6615 Irving Park Rd., Litton ABS, OEM Products Div., 600 Washington Ave., Carlstadt, N.J. 07072 . (201) 935-2200. (Frank Misiewicz) The Maxi-Switch Co., P.O. Box 4404, Minneapolis, Minn. 55421. (612) 529-7601. (Craig G. Stout, Sales Manager)

Mechanical Enterprises, 5249 Duke St., Alexandria, Va. 22304. (703) 751-3030. Micro Switch, Div. of Honeywell, 11 W. Spring St., Freeport, III. 61032. (815) 232-1122. (Edward C. Leibig, Keyboard Milli-Switch Corp., 1400 Mill Creek, Gladwyne, Pa. 19035. Milli-Switch Corp., 1400 Mill Creek, Gladwyne, Pa. 19035.
(215) $642-9222$. National Cash Register Co., Main and K Sts., Dayton, Ohio National Cash Register
45401. (513) 449-2000. Circle 427
Oak Industries Inc., Crystal Lake, III. 60014. (815) 459-5000. (P. M. Hassett, Vice President, Marketing) Circle 428 Raven Electronics Corp., Freeport Blvd., Reno, Nev. 89501. (702) 359-3700.

Raytheon Co., Industrial Components Operation, 465 Centre St., Quincy, Mass. 02169. (617) 479-5300. (Frank D. KenGeorge Risk Industries, Inc., 802 S. Elm, Kimball, Neb. 69145. (308) 235-4645. (Tom Kelsey, Sales Manager) 431

Stackpole Components Co., P.O. Box 14466, Raleigh, N.C. 27610. (919) 828-6201. (Robert H. Tillack, Product Manager, Keyboards and Keyswitches) Circle 432
Stacoswitch, Inc., 1139 Baker St., Costa Mesa, Calif. 92626. (714) 549-3041. (John Wardlaw, Marketing Services Manager Symbolic Displays Inc., P.O. Box 4322, Irvine, Calif. 92664.
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Unimax, Ives Rd., Wallingford., Conn. 06492. (203) 269. 8701. $\quad$ Circle 437 Western Digital Systems, 7100 Mesa Dr., Austin, Tex. 78731.
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## Need to keep digital data secure? Scramble the data with pseudorandom sequences that are easily generated with ICs and that provide numerous, easily changed codes.

At one time confidential data was stored in safes and transferred by bonded couriers to keep it secure; today data is stored in unsecured computers and transferred over readily tapped telephone lines. Yet the need for security is as great as ever. One solution is to encipher the data.

Enciphering isn't a new idea, but older techniques, such as the use of manual code books, are too slow for the volume and speed with which computers can handle data. Further, frequent code changing to maintain security is cumbersome. Today's electronic enciphering systems overcome these disadvantages. Most automatic systems use pseudorandom code sequences to scramble the data. These sequences are easily generated with shift-registors and EXCLUSIVEOR gates.

Pseudorandom enciphering/deciphering systems offer these advantages:

- They make a message appear like a random sequence.
- They can change codes easily.
- They provide a very large repertoire of different codes.
- They encipher and decipher automatically.


## Bit generator provides 'random' sequences

A completely random sequence would never show any ordered pattern-all possible combinations of ONEs and ZEROs are equally probable. A pseudorandom bit-stream, however, appears random for only a limited length of time. It repeats its sequence after a finite number of bits. The maximum length of the binary sequence prior to a repeat can be calculated by the expression $2^{n}-1$, where $n$ is the number of shift-register stages used in the sequence generator (Fig. 1). As n becomes large, the generated bit-stream approaches an ideally random sequence. Because n is an exponent, very long sequences are generated with modest numbers of stages- $\mathrm{n}=20$ provides a maximum stream of $1,048,575$ bits.

[^7]

1. A three-stage, pseudorandom code generator provides a sequence that repeats every seven clock pulses.

Table 1. Three-stage word generator

| CLOCK | FLIP-FLOP STATES |  |  |
| :---: | :---: | :---: | :---: |
|  | FF3 | FF2 | FFI |
| 1 | 1 | 0 | 1 |
| 2 | 1 | 1 | 1 |
| 3 | 0 | 1 | 0 |
| 4 | 1 | 1 | 1 |
| 5 | 0 | 1 | 0 |
| 7 | 1 | 0 | 1 |
| 8 | 1 | 1 | 1 |
| 15 | 1 | 1 | 1 |


2. Data are scrambled by the modulo-2 addition of the message with a pseudorandom bit-stream that is gen-
erated in a feedback shift-register circuit. The clear message modifies and is modified by the bit-stream.

Fig. 1 is a simple implementation of many possible pseudorandom digital-sequence generators. For simplicity, the circuit shows only three D flip-flops ( $\mathrm{n}=3$ ) and one EXCLUSIVE-OR. Such a small $n$ is not a practical value for encrypting. The EXCLUSIVE-OR functions as a modulo-2 adder. The circuit provides a ONE output only when a single logic ONE is present at the inputs- $1+0=1,0+1=1,1+1=0$ and $0+0=0$. The rules are the same as for binary addition without a carry. As shown, the EXCLUSIVE-OR is connected between $\mathrm{FF}_{1}$ and $\mathrm{FF}_{2}$. The remaining stages are connected as a recirculating shift register, with the output of stage $\mathrm{FF}_{1}$ fed back to $\mathrm{FF}_{3}$ and ORed with the output of $\mathrm{FF}_{2}$ to form the input of $\mathrm{FF}_{1}$.

Table 1 shows the sequence of flip-flop states as the initial register contents are recirculated through the EXCLUSIVE-OR gate connections. In this example the flip-flops are arbitrarily preset with a 101-bit sequence. The pattern repeats after every group of seven clock pulses, in accordance with the sequence-length formula

$$
2^{n}-1=2^{3}-1=7
$$

A different initial pattern merely starts the sequence at the corresponding point in the truth table (Table 1). The only disallowed combination of states for the registers $\mathrm{FF}_{1}, \mathrm{FF}_{2}$ and $\mathrm{FF}_{3}$ is the all-ZERO pattern. It is easy to see that if the generator should, for some reason, fall into this
pattern, the successive states remain all ZERO. This disallowed all-ZERO pattern explains the $(-1)$ in the sequence-length formula.

## Next, the data are scrambled

The data scrambler of Fig. 2 uses six D-type flip-flops, with the output of the $\mathrm{FF}_{1}$ stage fed back directly into the input of the $\mathrm{FF}_{6}$ stage. The $\mathrm{FF}_{1}$ output is EXCLUSIVE-ORed, by $\mathrm{OR}_{1}$, with the output of the $\mathrm{FF}_{4}$ stage to form the input to $\mathrm{FF}_{3}$. The placing of EXCLUSIVE-OR, between $\mathrm{FF}_{4}$ and $\mathrm{FF}_{3}$ is arbitrary in this example. In the absense of data input, the circuit will function as a pseudorandom word generator. Since the system has six stages, it can make a maximum of $2^{6}-1$, or 63 , state changes before the patterns repeat.

The uncoded message is fed to $\mathrm{OR}_{2}$ and combined with the output of $\mathrm{FF}_{2} . \mathrm{FF}_{1}$ then receives and provides the scrambled output. The original message modifies the pseudorandom sequence, at the same time that the message is itself modified by this sequence.

Table 2 shows the sequence of flip-flop states for Fig. 2 with a specific 16 -bit message fed into the circuit. The circuit starts with all flip-flops reset to ZERO. The input word and the scrambled output are as follows:

Message input - 1011001100110010

3. To decode the data, the encoded message is introduced into a feed forward shift-register circuit. Scrambled
and clear messages s and c , and signal b are identical to corresponding signals in Fig. 2 but delayed one bit.

Scrambled output - 1010010100000010
By comparing the two, we see that 11-bit positions are alike and five are different. Hence, for the simple circuit of Fig. 2, the scrambling process yields a correlation of

$$
\frac{\text { Alike }-\overline{\text { Alike }}}{\text { total Bits }}=\frac{11-5}{16}=0.375
$$

A longer input sequence and more flip-flop stages would cause the correlation of input and
output to approach zero. A completely randomized message would have zero correlation. Such an ideally encoded message would have the statistics of noise and be very difficult to crack. The main weakness of any code is the presence of a measurable degree of order or correlation. The more random a message appears, the more difficult it is to decipher.
The position of the EXCLUSIVE-OR used for

| CLOCK | DATA | FLIP-FLOP STATES |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | INPUT | FF6 | FF5 | FF4 | FF3 | FF2 | FFI |  |
| 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 |  |
| 2 | 0 | 1 | 0 | 0 | 1 | 0 | 0 |  |
| 3 | 1 | 0 | 1 | 0 | 0 | 1 | 1 |  |
| 4 | 1 | 1 | 0 | 1 | 1 | 0 | 0 |  |
| 5 | 0 | 0 | 1 | 0 | 1 | 1 | 0 |  |
| 6 | 0 | 0 | 0 | 1 | 0 | 1 | 1 |  |
| 7 | 1 | 1 | 0 | 0 | 0 | 0 | 0 |  |
| 8 | 1 | 0 | 1 | 0 | 0 | 0 | 1 |  |
| 9 | 0 | 1 | 0 | 1 | 1 | 0 | 0 |  |
| 10 | 0 | 0 | 1 | 0 | 1 | 1 | 0 |  |
| 11 | 1 | 0 | 0 | 1 | 0 | 1 | 0 |  |
| 12 | 1 | 1 | 0 | 0 | 0 | 0 | 0 |  |
| 13 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |  |
| 14 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |  |
| 15 | 1 | 0 | 0 | 0 | 1 | 0 | 1 |  |
| 16 | 0 | 1 | 0 | 0 | 1 | 1 | 0 |  |


|  |  | FLIP-FLOP STATES |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CLOCK |  |  |  |  |  |  |  |  |  |
|  |  | FF6 | FF5 | FF4 | FF3 | FF2 | FFI | FF7 |  |
| 1 |  | 1 | 0 | 0 | 1 | 0 | 0 | 1 |  |
| 2 |  | 0 | 1 | 0 | 0 | 1 | 0 | 0 |  |
| 3 |  | 1 | 0 | 1 | 1 | 0 | 1 | 1 |  |
| 4 |  | 0 | 1 | 0 | 1 | 1 | 0 | 1 |  |
| 5 |  | 0 | 0 | 1 | 0 | 1 | 1 | 0 |  |
| 6 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 |  |
| 7 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 |  |
| 8 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 1 |  |
| 9 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 0 |  |
| 10 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 |  |
| 11 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 |  |
| 12 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |  |
| 13 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |  |
| 14 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |  |
| 15 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 |  |
| 16 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 |  |


4. A flip of the switch changes the shift-register circuit into either an encoder or decoder. Setting switches $S_{1}$
feedback in the encoder of Fig. 2, so far arbitrarily chosen, dictates the manner in which the decoder must be configured to restore the message to its original format.

Fig. 3 is a circuit that will decode data scrambled by the circuit of Fig. 2. In the encoder circuit data was fed back to EXCLUSIVE-OR ${ }_{1}$, and also directly to the input of the $\mathrm{FF}_{6}$ stage. The decoding circuit of Fig. 3 uses a similar configuration with an EXCLUSIVE-OR, also between stages $\mathrm{FF}_{3}$ and $\mathrm{FF}_{4}$. However, no feedback is used. The shift-register stage, $\mathrm{FF}_{7}$, serves only to synchronize the output data with the clock.

Table 3 shows the flip-flop states for Fig. 3 during the unscrambling process. The data input is the scrambled output from the encoding circuit (Fig. 2). The feedback and feedforward bus lines of Figs. 2 and 3 have the same signals on them. Also, both Figs. 2 and 3 have the same number of shift stages, $\mathrm{FF}_{2}$ through $\mathrm{FF}_{6}$, and these flipflops have the same input points from the feedback/feedforward buses. Therefore the signals $b$ (inputs to both $\mathrm{OR}_{2} \mathrm{~s}$ ) in both circuits are identical. In Fig. 2, the output of $\mathrm{FF}_{2}$ mixes with and scrambles the clear message, c. In Fig. 3, b and s (scrambled message) are both delayed one clock pulse versus Fig. 2. By modulo-2 addition, if
$\mathrm{X} \oplus \mathrm{Y}=\mathrm{Z}$, then $\mathrm{X} \oplus \mathrm{Z}=\mathrm{Y}$
In Figs. 2 and 3 therefore, if
$\mathrm{b} \oplus \mathrm{c}=\mathrm{s}$, then $\mathrm{b} \oplus \mathrm{s}=\mathrm{c}$.

## A combination scrambler and decoder

Conversely the circuit of Fig. 3 (the decoder) can also act as an encoder and be decoded by the circuit of Fig. 2. The circuit pair has a reciprocal transfer characteristic. The scrambled message, however, is different when the roles of the two circuits are reversed.
through $\mathrm{S}_{\mathrm{n}}$ changes the scrambling code. A 16 -stage system provides up to 65,535 different codes.

In the combination circuit of Fig. 4 a flick of a switch ( $\mathrm{S}_{\mathrm{o}}$ ) converts the circuit into either an encoder or decoder of digital data. The encoding/ decoding transfer characteristics of the circuits are determined by the settings of switches $\mathrm{S}_{1}$ to $\mathrm{S}_{\mathrm{n}}$ in the feedback/feedforward path.

A 16-stage register can generate a maximum of ( $2^{16}-1$ ), or 65,535 , bits before repetition occurs. Also, since the switches $S_{1}$ to $S_{n}$ can be set in any pattern (except all to ground), there are 65,535 different coding sequences. However, not all are of the maximum length. And though not implemented in Fig. 4, another coding variation can be provided by starting the generator in any one of the 65,536 possible initial states.

Since the nature of scrambling requires hiding the message and showing no patterns-that is, a complete lack of redundancy-errors in transmission go undetected. In fact, an error picked up in transmission is multiplied by the decoding process. Each input bit affects several output bits. Therefore error detecting and correcting performed after decoding is made more difficult. In applications where large blocks of data are moved or stored, it may be wise to use a test word (not coded) for each data block to insure that the transmission process has not gone out of synchronism. Loss of synchronism would result in complete loss of data. However, here lie the seeds for code cracking. Any unencoded information, or error detection and correction schemes added after the message is encoded, provide clues for deciphering the message.

Fig. 4 is probably the simplest configuration that can be used for pseudorandom encoding and decoding, but the security it can provide is limited. The art of data encoding is highly developed, and for obvious reasons, the most advanced concepts are kept secret.

## .

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# Pseudorandom codes can be cracked with just a short sequence of the text in both clear and scrambled form. Knowing the limitations is a first step to better designs. 

On the surface, a pseudorandom code appears to be a good way to keep digital data secure. Thousands of long, nonrepetative sequences are available from a simple circuit (Fig. 1) to scramble data transmissions. ${ }^{1,2}$ A 20 -stage circuit can supply a sequence of $1,048,575$ bits. But the method does have a weakness. Although the encoded data may have the statistical appearance of randomness, a linear relationship exists between the message and the enciphered text. Only a limited amount of information-on the order of 2 n ( $\mathrm{n}=$ number of shift-register stages) bits of enciphered text and the corresponding message-is enough to break the code.

Let's examine the linear relationships. The basic shift-register circuits in Figs. 2 and 3, can be represented analytically by the delay operator $D^{j}$. If $t_{0}$ represents the delay of one shift register, then the transfer function of $n$ shift register stages can be represented by $\mathrm{D}^{\mathrm{nt}}$ o. The input and output sequences are represented by $\mathrm{I}\left(\mathrm{t}-\mathrm{nt}_{\mathrm{o}}\right)$ and $0\left(\mathrm{t}-\mathrm{nt}_{\mathrm{o}}\right)$.

All "additions" are done with EXCLUSIVEORs and therefore obey modulo-2 rules, and all operations are linear.

For Fig. 2, $0_{a}(t)=1_{a}(t)\left[\left(D^{2 t}+1\right) D^{3 t}+1\right]$ and

$$
0_{a}(t)=I_{a}(t)\left[D^{5}+D^{3}+1\right], \text { af- }
$$ ter dropping the $\mathrm{t}_{\mathrm{o}}$ notations and combining terms. Similarly analysis of the circuit in Fig. 3 shows that

$$
\begin{aligned}
0_{\mathrm{b}}(\mathrm{t}) & =\left[0_{\mathrm{b}}(\mathrm{t})\left[\left(\mathrm{D}^{2}+1\right) \mathrm{D}^{2}\right]+\mathrm{I}_{\mathrm{b}}(\mathrm{t})\right] \mathrm{D} \\
& =0_{\mathrm{b}}(\mathrm{t})\left(\mathrm{D}^{5}+\mathrm{D}^{3}\right)+\mathrm{I}_{\mathrm{b}}(\mathrm{t}) \mathrm{D} .
\end{aligned}
$$

Since in modulo-2 arithmetic if $A+B=C$ then $A+C=B$, it follows that

$$
I_{b}(t) D=0_{b}(t)\left[D^{5}+D^{3}+1\right]
$$

An encipher/decipher operation is obtained by cascading the two circuits-with the output of Fig. 2 into the input of Fig. 3.

Making $0_{\mathrm{a}}(\mathrm{t})=\mathrm{I}_{\mathrm{b}}(\mathrm{t})$

$$
I_{a}(t) D=0_{b}(t)
$$

Thus the final output of the pair of cascaded circuits equals the input delayed by one bit. The

[^8]original input text $I_{a}(t)$ is correctly recovered as $0_{b}(\mathrm{t})$ delayed by one bit.

A generalized combined encipher/decipher circuit is shown in Fig. 1. A similar analysis shows that the labels "encipher" and "decipher" on switch $\mathrm{S}_{\mathrm{o}}$ can be interchanged. By feeding the output of Fig. 3 into Fig. 2, we make the original encoder a decoder and vice versa. Thus

$$
0\left(\mathrm{t}_{\mathrm{b}}\right)=\mathrm{I}\left(\mathrm{t}_{\mathrm{a}}\right)
$$

and $0_{a}(t)=I_{b}(t) D$. Therefore the input to Fig. 3, $I\left(t_{b}\right)$, is recovered as $0_{a}(t)$, also delayed by one bit.

## Breaking the code

In many data blocks a small fragment of plain text and the corresponding encrypted text can be deduced or may become known by prearrangement with an accomplice. Very often a highly formatted text is transmitted. Formatting provides strong clues for code-cracking by intelligent guessing or covert prearrangement. For example, the heading of a column of names could contain the sequence, "space NAME space." This could be at a certain place in the text quite innocently or placed there by design. If an eight-bit code is assumed, this provides an unauthorized person with 48 bits ( 6 chars $\times 8 \mathrm{~b} / \mathrm{char}$ ) of clear and corresponding enciphered text. This is more than enough information to break an ordinary pseudorandom linear code produced by a circuit of 20 stages.

In general, the code of any n-stage code generator, with arbitrary feedback-with switches $\mathrm{S}_{1}$ to $\mathrm{S}_{\mathrm{n}}$ arbitrarily set (Fig. 1)-can be broken with any 2 n bits of clear and corresponding enciphered text. Breaking the code consists of determining the switch settings and the initial states of the flip-flop stages. Once these conditions are known, the complete text can be deciphered.

Consider the five-stage circuit in Fig. 2. In the most general formulation of the problem, assume that the plain-text data consisting of bits $\mathrm{X}_{1}, \mathrm{X}_{2}$, $\ldots, \mathrm{X}_{2 \mathrm{n}}$ and the corresponding enciphered data $\mathrm{Y}_{1}, \mathrm{Y}_{2}, \ldots, \mathrm{Y}_{2 \mathrm{n}}$ are known. The most important


1. A generalized encipher/decipher system can generate pseudorandom, nonrepetitive sequences as long as
( $2^{n}-1$ ) bits. With $n=20$-stages of registers, many thousands of sequences are as long as $1,048,575$ bits.

2. The transfer function of this five-stage encoder provides a linear relationship between input and output.

3. The decoder's transfer function is the reciprocal of the encoder and delayed by one clock pulse.
information, the code key, is represented by the states (open or closed) of the various switches $S_{1}, S_{2}, \ldots . S_{i} \ldots S_{n}$.

An open switch at point i is specified by $\mathrm{S}_{\mathrm{i}}=0$ and a closed switch by $\mathrm{S}_{\mathrm{i}}=1$. The initial state of the shift register stages are designated by $\mathrm{Z}_{1}, \mathrm{Z}_{2}, \ldots \mathrm{Z}_{\mathrm{n}}$, where $\mathrm{Z}_{\mathrm{i}}$ can assume the values of either ONE or ZERO.

A careful analysis of a five-stage encoder will indicate that the shift register encoding scheme can be represented by the matrix in Fig. 4.

Using the well-known rules of matrix multi-
plication we can solve the matrix for the circuit of a five-stage encoder, where $\mathrm{n}=5$ in the generalized circuit of Fig. 1. With the encipher/ decipher switch in the encipher position, we get

$$
\begin{aligned}
& \mathbf{Y}_{1}+X_{1}=Z_{1} \\
& \mathbf{Y}_{2}+X_{2}=S_{1} X_{1}+Z_{2} \\
& \\
& \vdots \\
& \mathbf{Y}_{6}+X_{6}=S_{1} X_{5}+S_{2} X_{4}+\mathrm{S}_{3} \mathrm{X}_{3}+\ldots \mathrm{S}_{5} \mathrm{X}_{1} \\
& \\
& \vdots \\
& \mathbf{Y}_{10}+X_{10}=\mathrm{S}_{1} \mathrm{X}_{9}+\mathrm{S}_{2} \mathrm{X}_{8}+\mathrm{S}_{3} \mathrm{X}_{7}+\ldots \mathrm{S}_{5} \mathrm{X}_{5} .
\end{aligned}
$$

All additions and multiplications are by mod-ulo-2 rules.

Let the switch settings of $\mathrm{S}_{1} \ldots \mathrm{~S}_{\mathrm{n}}$ correspond to the configuration of Fig. 2. Therefore, the transfer characteristic of the circuit as previously derived is

$$
\mathrm{S}_{5}+\mathrm{S}_{3}+1=\mathrm{D}^{5}+\mathrm{D}^{3}+1 .
$$

If the input signal,

$$
I_{a}(t)=10111000011
$$

it can be represented by delay operators as

$$
\left(1+\mathrm{D}^{2}+\mathrm{D}^{3}+\mathrm{D}^{4}+\mathrm{D}^{9}+\mathrm{D}^{10}\right)
$$

Multiplying
$\left(\mathrm{D}^{5}+\mathrm{D}^{3}+1\right) \cdot\left(1+\mathrm{D}^{2}+\mathrm{D}^{3}+\mathrm{D}^{4}+\mathrm{D}^{9}+\mathrm{D}^{10}\right)$
and noting that in modulo-2 addition $\mathrm{D}^{\mathrm{n}}+\mathrm{D}^{\mathrm{n}}=$ 0 and ignoring values where $\mathrm{n}>10$ we get

$$
\begin{aligned}
0_{a}(\mathrm{t}) & =1+\mathrm{D}^{2}+\mathrm{D}^{4}+\mathrm{D}^{6}+\mathrm{D}^{8}+\mathrm{D}^{10} \\
& =10101010101,
\end{aligned}
$$

and

$$
\begin{aligned}
X+Y & =I_{a}(t)+0_{a}(t) \\
& =00010010110 .
\end{aligned}
$$

Although the 10 unknowns $\mathrm{S}_{1}$ to $\mathrm{S}_{5}$ and $\mathrm{Z}_{1}$ to $\mathrm{Z}_{5}$ can be solved with the 10 equations that result from the matrix, for simplification, assume that all register stages are initially reset-all Zs equal 0 . In that case, only the lower left quarter of the matrix equation has to be solved. In the encoding process of $I_{a}(t)$ to $0_{a}(t)$ it was tacitly assumed that all the $\mathrm{Zs}=0$.

Evaluating the matrix

$$
\left[\begin{array}{c}
\mathbf{Y}_{6}+\mathbf{X}_{6} \\
\mathbf{Y}_{7}+\mathbf{Y}_{7} \\
\mathbf{Y}_{8}+\mathbf{X}_{8} \\
\mathbf{Y}_{9}+\mathbf{X}_{9} \\
\mathbf{Y}_{10}+\mathbf{X}_{10}
\end{array}\right]\left[\begin{array}{l}
0 \\
1 \\
0 \\
1 \\
1
\end{array}\right]=\left[\begin{array}{llllll}
1 & 1 & 1 & 0 & 1 \\
0 & 1 & 1 & 1 & 0 \\
0 & 0 & 1 & 1 & 1 \\
0 & 0 & 0 & 1 & 1 \\
0 & 0 & 0 & 0 & 1
\end{array}\right] \cdot\left[\begin{array}{l}
\mathrm{S}_{1} \\
\mathrm{~S}_{2} \\
\mathrm{~S}_{3} \\
\mathbf{S}_{4} \\
\mathrm{~S}_{5}
\end{array}\right]
$$

We get

$$
\begin{aligned}
\mathrm{S}_{1}+\mathrm{S}_{2}+\mathrm{S}_{3}+\mathrm{S}_{5} & =0 \\
\mathrm{~S}_{2}+\mathrm{S}_{3}+\mathrm{S}_{4} & =1 \\
\mathrm{~S}_{3}+\mathrm{S}_{4}+\mathrm{S}_{5} & =0 \\
\mathrm{~S}_{4}+\mathrm{S}_{5} & =1 \\
\mathrm{~S}_{5} & =1 .
\end{aligned}
$$

And solving these five simultaneous equations provides the answers $\mathrm{S}_{5}=1, \mathrm{~S}_{4}=0, \mathrm{~S}_{3}=1$, $\mathrm{S}_{2}=0$, and $\mathrm{S}_{1}=0$.

Instead of this procedure a more elegant approach is to calculate the inverse of the $5 \times 5$ matrix:

$$
\left[\begin{array}{l}
\mathrm{S}_{1} \\
\mathrm{~S}_{2} \\
\mathrm{~S}_{3} \\
\mathrm{~S}_{4} \\
\mathrm{~S}_{5}
\end{array}\right]=\left[\begin{array}{lllll}
1 & 1 & 0 & 1 & 0 \\
0 & 1 & 1 & 0 & 1 \\
0 & 0 & 1 & 1 & 0 \\
0 & 0 & 0 & 1 & 1 \\
0 & 0 & 0 & 0 & 1
\end{array}\right] \cdot\left[\begin{array}{l}
0 \\
1 \\
0 \\
1 \\
1
\end{array}\right]=\left[\begin{array}{l}
0 \\
0 \\
1 \\
0 \\
1
\end{array}\right]
$$

## Rules for protecting security

Knowing then the weakness of a linear pseudorandom scrambler, what can we do to help keep information secure? Several operational rules are apparent:

- Never send an encoded bit stream, where the clear text consists of all ONEs or ZEROs. This exposes the pseudorandom sequence to possible recognition. A noise-triggered pulse generator should be used to fill in for no-message periods. The line should always be active, if at all possible whether or not messages are on the line.
- Don't provide clues to the starting and ending of messages, especially formatted messages. The bit stream should appear random at all times whether or not messages are on the line.
$\left[\begin{array}{l}r_{1}+x_{1} \\ r_{2}+x_{2} \\ r_{3}+x_{3} \\ r_{4}+x_{4} \\ r_{5}+x_{5} \\ r_{6}+x_{6} \\ r_{7}+x_{7} \\ r_{8}+x_{8} \\ r_{9}+x_{9} \\ r_{10}+x_{10}\end{array}\right]=\left[\begin{array}{lllll:lllll}0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ x_{1} & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ x_{2} & x_{1} & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ x_{3} & x_{2} & x_{1} & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ x_{4} & x_{3} & x_{2} & x_{1} & 0 & 0 & 0 & 0 & 0 & 1 \\ \hdashline x_{5} & x_{4} & x_{3} & x_{2} & x_{1} & 0 & 0 & 0 & 0 & 0 \\ x_{6} & x_{5} & x_{4} & x_{3} & x_{2} & 0 & 0 & 0 & 0 & 0 \\ x_{7} & x_{6} & x_{5} & x_{4} & x_{3} & 0 & 0 & 0 & 0 & 0 \\ x_{8} & x_{7} & x_{6} & x_{5} & x_{4} & 0 & 0 & 0 & 0 & 0 \\ x_{9} & x_{8} & x_{7} & x_{6} & x_{5} & 0 & 0 & 0 & 0 & 0\end{array}\right] \cdot\left[\begin{array}{l}s_{1} \\ s_{2} \\ s_{3} \\ s_{4} \\ s_{5} \\ z_{1} \\ z_{2} \\ z_{3} \\ z_{4} \\ z_{5}\end{array}\right]$

4. By arranging the enciphering circuit's variables and constants in a matrix, you can use the established rules of matrix manipulation to crack a code with only $2 n$ bits of clear and correspondingly encoded text.

- Change the switch settings ( $\mathrm{S}_{1}$ to $\mathrm{S}_{\mathrm{n}}$ ) and the initial condition ( $\mathrm{Z}_{1}$ to $\mathrm{Z}_{\mathrm{n}}$ ) frequently to change the encoding. Use prearranged secret schedules that should also be changed often by only one trusted operator. And keep the setting mechanisms secured and hidden.
- Employ trustworthy personnel. This is extremely important. An accomplice can readily and in a very short time (a few minutes) compromise all messages sent with a particular code. setting and do it in a difficult to uncover manner.

Of course, more complex systems using nonlinear relationships and mathematical operation other than the simple modulo- 2 make code-cracking a far more difficult task. ${ }^{3}$ Still, the system can never be more reliable than the people who operate it. And the problems faced by a potential intruder in compromising even the most advanced system are mostly a matter of time and expense to solve.

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As shown in the figure, the circuit consists of three four-input NOR gates, with the output of each gate feeding the input of the two other gates. Each of the three inputs also feeds two of the three gates. Note that negative logic is used -that is, ONE $=$ LOW and ZERO $=$ HIGH.

When a LOW is applied at input 1, and a HIGH at the remaining inputs, the outputs of gates 2 and 3 are forced to be HIGH. This results in all inputs to gate 1 being HIGH, causing its output to be LOW. This LOW, in turn, feeds gates 2 and 3 . Thus, when input 1 is removed, the LOW output of gate 1 will maintain a LOW input at

Demetrios Kostopoulos, Reliability Engineer, Systems Consultants, Inc., Ridgecrest, Calif. 93555.


1. The Tri-Flop consists of three four-input NOR gates interconnected as shown.
gates 2 and 3, insuring permanency of the state that was initiated by input 1 . Similarly, when a LOW is applied-at different times-to inputs 2 or 3, outputs 2 and 3, respectively, are forced to be LOW.

The table shows the output/input relationship of the Tri-Flop. Note that LOW inputs can be momentary (pulsed) and that outputs remain in their last forced state until retriggered. The Boolean equations of the Tri-Flop for quiescent operation, in which all three inputs are HIGH, are:

$$
\begin{aligned}
& \text { Output } 1=\overline{(\text { Output 2) }} \cdot \overline{(\text { Output 3) }} \\
& \text { Output } 2=\overline{(\text { Output 1) })} \cdot \frac{(\text { Output 3) }}{(\text { Output 1) }} \cdot \overline{(\text { Output 2) }} \\
& \text { Output } 3=\overline{\text { ( }}
\end{aligned}
$$

For transient operation, where one of the three inputs is LOW, the equations are:

Output $1=$ Input 1
Ouput $2=$ Input 2
Output $3=$ Input 3
If one of the outputs is removed, the equations reduce to those of the familiar R-S flip-flop.

## Many applications exist

As an example of a Tri-Flop application, let's compare the magnitudes of two numbers, A and

2. Magnitude comparator illustrates use of the Tri-Flop to compare two serial binary numbers.
B. where the three mutually exclusive states are $\mathrm{A}=\mathrm{B}, \mathrm{A}<\mathrm{B}$ or $\mathrm{A}>\mathrm{B}$. Figure 2 shows the Tri-Flop magnitude comparator, in which A and $B$ are sequential binary numbers. A and $B$ enter the comparator serially, with their most significant bits first. The comparator is initially set by a PRESET pulse, so that, at the beginning of the comparison, output $\mathrm{A}=\mathrm{B}$ is LOW, outputs $\mathrm{A}<\mathrm{B}$ and $\mathrm{A}>\mathrm{B}$ are HIGH, and the clock input is LOW.
With the most significant bits of $A$ and $B$ entered for comparison, the clock goes HIGH, enabling gates $L$ and $G$. If the examined bit of A is less than that of $\mathrm{B}(\mathrm{A}=\mathrm{ZERO}$ and $\mathrm{B}=$ ONE), gate L's output goes LOW, setting the Tri-Flop in the state in which output 3 is LOW and outputs 1 and 2 are HIGH. Output 3 indicates that $\mathrm{A}<\mathrm{B}$ and disables gate G , thus inhibiting the comparator from being altered by the following less significant bits.

If the examined bit of number A is greater than that of $\mathrm{B}(\mathrm{A}=\mathrm{ONE}$ and $\mathrm{B}=\mathrm{ZERO})$, gate G's output goes LOW, setting the Tri-Flop in the state where output 2 is LOW and outputs 1 and 3 are HIGH. Output 2 indicates that $\mathrm{A}>\mathrm{B}$, and at the same time it disables gate L, inhibiting the comparator from being altered by the following less significant bits.

The first nonequal A and B bits decide the magnitude relationship and lock the comparator in the corresponding state. For example, if $\mathrm{A}=$ 01101101 and $\mathrm{B}=01110011$, the comparator will indicate that $\mathrm{A}=\mathrm{B}$ until the fourth bit (see timing diagram, Fig. 4). The A $<$ B output will
then go low, concluding the comparison.
For comparison, a conventional comparator design is shown in Fig. 3. Of the two designs, we see that considerable hardware savings exist with the Tri-Flop approach; the savings are greater when the network is used repeatedly.

Other Tri-Flop applications include polarity determination (positive, zero, negative), ternary arithmetic ( $0,1,2$ ) and coordinate indentification ( $\mathrm{X}, \mathrm{Y}, \mathrm{Z}$ ).

## Table: Tri-Flop truth table

| INPUTS* |  |  | OUTPUTS** |  |  | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 2 | 3 |  | 2 | 3 |  |
| H | H | H | NO CHANGE |  |  | STATES ALLOWED FOR TRI-FLOP OPERATION |
| L | H | H |  | H | H |  |
| H | L | H |  | L | H |  |
| H | H | L |  | H | L |  |
| L | ᄂ | H |  | H | H | STATES NOT ALLOWED FOR TRI-FLOP OPERATION |
| H | L | L | H | H | H |  |
| L | H | L |  | H | H |  |
| L | L | L |  | H | H |  |
|  |  |  |  |  |  |  |


4. Timing diagram for Tri-Flop comparator with $\mathrm{A}=$ 01101101 and $B=01110011$.

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## Optimize photodiode detector design <br> by using a graphical approach to maximize avalanche diode gain and minimize preamplifier noise.

In very narrow optical filters, such as those used to detect laser radiation, internal amplifier noise often limits system performance. Since the typical front end consists of a photodiode coupled to a preamplifier, performance is improved if the diode can be made to furnish gain.

The use of avalanche gain within the photodiode to enhance the output signal reduces the effect of noise in the following amplifier. An accurate prediction of noise performance, of course, must include the effects of the diode and the following amplifier.

Although optimum avalanche gain in the system can be determined mathematically, the computation is tedious, and it obscures the gain and signal-to-noise-ratio trade-offs. A graphical approach is simple and fast, and it gives the designer greater insight into the optimization of the photodiode preamplifier combination.

## Break down the problem

Three currents must be considered in analyzing avalanche photodiode action (Fig. 1) :

1. The mean square signal current from a photodiode operating in the avalanche mode. It is expressed as

$$
\begin{equation*}
\mathrm{i}_{\mathrm{s}}{ }^{2}=\left(\mathrm{q} \mathrm{~m} \mathrm{n}_{\mathrm{a}} \phi \mathrm{M}\right)^{2} / 2, \tag{1}
\end{equation*}
$$

where $q$ is the charge of an electron, $m$ is the modulation index, $\mathrm{n}_{\mathrm{a}}$ is the ac quantum efficiency, $\phi$ is the average photon flux and M is the avalanche gain.
2. Internal photodiode noise current, which increases at a greater rate than the signal because of the current gain associated with the avalanche process. It is given by

$$
\begin{equation*}
\mathrm{i}_{\mathrm{n}}{ }^{2}=2 \mathrm{q} \Delta \mathrm{f}\left(\mathrm{qn}_{\mathrm{o}} \phi+\mathrm{I}_{\mathrm{co}}\right) \mathrm{M}^{\mathrm{d}}, \tag{2}
\end{equation*}
$$

where $\Delta f$ is the noise bandwidth, $\mathrm{n}_{\mathrm{o}}$ is the dc quantum efficiency, $I_{\text {co }}$ is avalanche region leakage and $d$ is a constant dependent mainly on the electron ionization ratio, which lies between 2.3 and 4.
3. The total second stage noise current, includ-

Ralph Kordes, President, General Analog, 1552 Clearview Lane, Santa Ana, Calif. 92705


1. Avalanche gain in the photodiode stage (a) of a twostage front end reduces the effects of noise in the second-stage preamp. The equivalent circuit of both stages (b) contains the signal current, $\mathrm{i}_{\mathrm{s}}$, and noise currents, $i_{n}$ and $i_{x}$.
ing the thermal noise of the input circuit, the guard ring leakage noise of the detector and the equivalent input noise of the following transistor amplifier. It is expressed as

$$
\begin{equation*}
\mathrm{i}_{x}=\left(2 q \mathrm{I}_{x} \Delta f\right)^{1 / 2} \tag{3}
\end{equation*}
$$

where $I_{x}$ is the equivalent dc noise current.

## Plot diode characteristics

By referring the diode signal and noise currents to the amplifier input terminals, we can compare the diode currents to the amplifier noise current. The determination of the optimum avalanche gain and the improvement of the signal-to-noise ratio is done graphically. This analysis assumes that, at moderate avalanche gains, the photodiode equivalent parallel resistance remains large compared with the normal circuit impedances.

A step-by-step procedure produces a graph (Fig. 2) of $\log$ current vs $\log$ M. This permits quick determination of optimum signal-to-noise ratio:

1. Using log-log paper, draw the second stage amplifier noise level $i_{x}$, which is independent of

## Avalanche in photodiodes

When photons of light interact with the atoms in a reverse-biased diode junction, free electrons are created. This is the basis of operation of a conventional photodiode. In an avalanche photodiode the doping level is such that, with the proper bias voltage, the free electrons can interact with other atoms and generate additional free electrons. This is the avalanche condition. The avalanche photocurrent can be made proportional to light intensity.

2. Optimum gain and minimum diode signal are found graphically with a root sum square of the noise currents, $i_{n}$ and $i_{x}$. The optimum gain occurs at an avalanche gain of 30 in the design example. This value results in a signal-to-noise improvement of 14.3.
the gain parameter M , as a line parallel to the abscissa.
2. Calculate $i_{n}$ for $M$ equal to 1 (Eq. 2). This calculation gives a point on the ordinate where $\mathrm{M}=1$.
3. Draw a line from the $\mathrm{M}=1$ ordinate with a slope equal to $d / 2$. This line represents the avalanche diode noise $i_{n}$ at the second-stage input terminals as a function of avalanche gain (M). It is derived by taking the log of both sides of Eq. 2 and solving for the $\log$ of $i_{n}$ over the $\log$ of $2 q \Delta f\left(2 n_{o} \phi+I_{\text {co }}\right) M$.
4. Combine the noise sources on a root sum square basis to give a curve of total noise current at the second-stage input as a function of avalanche gain M (total noise $=\sqrt{\mathrm{i}_{n}{ }^{2}+\mathrm{i}_{x}{ }^{2}}$ ). This curve is asymptotic to the avalance and secondstage noise curves. The noise currents are combined by multiplying the intersection point by $\sqrt{2 .}$

This gives a point on the total noise curve at the crossover point (the point at which the two currents are equal). The total noise curve can then be drawn by fairing it asymptotically to the second-stage noise curve and the avalanche diode
noise curve. If greater accuracy is needed, two additional points can be calculated on either side of the crossover point.
5. Draw a line with a slope of one so that it is just tangent to the total noise curve. This is the signal current curve and comes from taking the $\log$ of Eq. 1, which is the expression for the signal current at the second-stage input as a function of avalanche gain M. The point of tangency is therefore the point where $\mathrm{S} / \mathrm{N}=1$.
6. Read off the optimum avalanche gain at the point of tangency between the total noise curve and the signal curve.
7. Read off the intersection of the signal current curve and the ordinate that is the minimum input signal level.

The signal current curve is any line with a slope of 1 (any signal level). If this line is always above the total noise curve, we have $\mathrm{S} / \mathrm{N}$ values greater than 1 ; if it is always below, we have $\mathrm{S} / \mathrm{N}$ values of less than 1 . If it is tangent to the noise curve, we have a unique situation in which $\mathrm{S} / \mathrm{N}=1$. If the signal current curve intersects the noise curve, we have a band of M values where $\mathrm{S} / \mathrm{N}$ is greater than 1 (between the points of intersection).

## Designing a front end

Consider an amplifier with the following specifications:
A. $\mathrm{i}_{\mathrm{x}}=5 \times 10^{-13} \mathrm{~A} / \sqrt{\mathrm{Hz}}$ at 1 MHz . This is an average value across the bandwidth of interest.
B. The bandwidth is relatively narrow (of less than one decade).
C. $\mathrm{I}_{\mathrm{co}}=2 \times 10^{-10} \mathrm{~A}$, and this is large compared with the dc component of incoming radiation. This is especially true in the case of the detection of laser radiation, where pulse widths on the order of 10 to 100 ns at repetition rates of a few thousand Hertz are being considered.
D. The value of the electron ionization constant, d, is 3.

Using these data, we follow the seven-step procedure just outlined:

1. Draw a horizontal line at $5 \times 10^{-13} \mathrm{~A} / \sqrt{\mathrm{Hz}}$
2. If we assume that $\mathrm{qn}_{\mathrm{o}} \phi$ in Eq. 2 is small compared with $\mathrm{I}_{\mathrm{co}}, \mathrm{i}_{\mathrm{n}}$ is $5.65 \times 10^{-15} \mathrm{~A} / \sqrt{\mathrm{Hz}}$. This value of $i_{n}$ is a point on the ordinate.
3. Since $\mathrm{d}=3$ for this example, draw a line with a slope of $3 / 2$ from this point.
4. The total noise at the second stage is the root sum square of $i_{x}$ and $i_{n}$ and is asymptotic to the avalanche diode noise curve and the secondstage noise curve.
5. The signal current curve is shown as a line with a slope of 1 , and is drawn just tangent to the total noise curve.
6. The point of tangency occurs at $M \simeq 30$.
7. Since the intercept of $i_{s}$ with the ordinate

occurs at $\mathrm{i}=3.5 \times 10^{-14} \mathrm{~A} / \mathrm{V} \overline{\mathrm{Hz}}$ this is the minimum detectable signal based on $\mathrm{S} / \mathrm{N}=1$.

The plot of Fig. 2 is based on these calculations.

## Don't forget practical problems

As shown in Fig. 2, the point of tangency is not sharply defined. Consequently $M$ is not a critical parameter from a system performance standpoint. However, the bias voltage on the diode is critical, since the avalanche gain can change by an order of magnitude with a small change in bias voltage. This is especially true if the optimum M or the point of operation of the diode is approaching the critical avalanche gain, which is the gain where the diode is no longer linear (the diode has gone into complete avalanche).

In a system with a large photodiode load resistance and a wide variation in the dc level of the incoming signal, it's possible for $\mathbf{M}$ to vary by as much as a decade. The corresponding variation in the $\mathrm{S} / \mathrm{N}$ ratio can be calculated from the plot of Fig. 2. When the ratio reaches excessively high values, it is then necessary to hold the bias potential on the avalanche diode at precise levels.

The ratio of the second-stage noise to the shot noise of the diode is the primary factor in the amount of signal-to-noise improvement that can be achieved through the use of an avalanche diode. It is apparent from Fig. 2 that the improvement in the signal-to-noise ratio is greatest for high noise ratios. This condition requires high optimum $M$ values. Low M values are required for low noise ratios. Becasue the partial derivative of $\mathbf{M}$ with respect to the diode bias voltage ( $\mathrm{E}_{\mathrm{b}}$ ) approaches infinity as $\mathrm{E}_{\mathrm{b}}$ approaches $\mathrm{E}_{\text {crit }}$ (the bias potential where the diode is no longer linear), it follows that high $\mathbf{M}$ values require an increasingly more stable and precisely held bias potential.

Another factor in regard to high optimum M values is that the critical potential for the diode is a function of temperature and time and possibly other factors, which make the operation at high optimum M values undesirable.

As a result of these considerations a regulated, temperature-independent bias source for the photodiode is a prime requisite for proper operation. -

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# The ABC's of engineering management 

 in review, as told to us this year by eight men who made thesuccessful transition from design bench to manager's desk.

Richard L. Turmail, Management Editor

How do you get a promotion?
How do you manage people?
How do you make management decisions?
How do you handle a project?
Electronic Design asked engineering managers questions like these in a management series, "Challenges to the Engineer Who Manages." Since, according to reader response, the opinions of many of these managers were perceptive and helpful, we have compiled a review of their observations. Following are the opinions of eight of the managers on promotions, decisionmaking and the handling of people and projects.

## Experience wins promotions

Unless you're heir to the family business or you marry the company president's daughter, you're not going to become a manager unless you have experience, knowledge and an ability to
manage people. Two kinds of experience are essential to promotions: the work experience and the training experience.

Rick Spofford, engineering manager of converter products at Analog Devices, Inc., Norwood, Mass., has worked as a designer, an applications engineer, a marketing engineer and a project engineer. He believes that this broad work experience has helped him get his present management spot, because it opened his mind to problems he might never have seen as an engineer.
"My years in marketing as an applications engineer," Spofford says, "have helped to broaden my background. If my career course had not included a couple of passes at marketing, my development as an engineering manager might have been slower."

On the other hand, Dick Moore, R\&D manager at Hewlett-Packard, Loveland, Colo., says that one of the most important steps in his experience was the year's training he received in the com-


Dick Moore, R\&D Manager, HewlettPackard, Loveland, Colo.


Herb Sobel, Lab Manager, Ray. theon, Bedford, Mass.


Robert C. Wilson, President, Collins Radio Co., Dallas, Tex.


Rick Spofford, Manager of Converter Product Engineering, Analog Devices, Inc., Norwood, Mass.
pany's production department. He learned how all the gears of the departments meshed. He says that this training helped him make the transition from one management spot to another.
"Going from a design engineer to a group leader of from three to 10 people," Moore says, "isn't really too hard a transition. The next step when you're working through group leaders is tough; I think that a guy needs some kind of discontinuity in his work to grasp it."

Roger Cady, manager of PDP-11 computer engineering at Digital Equipment Corp., Maynard, Mass., explains: "A broad experience will give the engineer a breadth of knowledge of a product's function that no engineering spec could ever equal."

But Cady also believes that a good technical background helps an engineer advance. He says that some people want to become managers when they think they don't have it technically. "That's ironic," he says, "because a good engineering manager must be a good technical man."

Technical knowledge and an ability to get things done are cited by Ray Gerr, group leader at Ballantine Laboratories, as the main factors that led to his promotion. "I was a clean-up man," he says. "If a product didn't work, I cleaned it up by refining the design or by repositioning a resistor-or, as a last resort, by revising the specs."

## Handling people the biggest challenge

As for managing people, four of the managers touched on the ways they do the job: by organizing, by inspiring, by inducing cooperation and by example.
"People-awareness are the two most important buzz words for an engineering manager to remember," Cady says, "because he works with people instead of things. My ability to work with people was the most important factor contributing to my promotion."

One of the ways Cady handles people is by


Robert W. Gress, Manager, Resistor Product Development, Corning Glass Works, Bradford, Pa.


Ray Gerr, Group Leader, Ballantine Laboratories, Boonton, N.J.


Roger Cady, Manager of PDP-11 Computer Engineering, Digital Equipment Corp., Maynard, Mass.


Howard Bogert, Engineering Vice President, Unicom, Cupertino, Calif.
asking them for $110 \%$ of their capacity and $120 \%$ of their knowledge on a project. He says that they realize then that they're going to have to learn something new before they can complete the job.

Gerr says that the best way he has found to manage engineers is to try to get them to work together as a team, to cooperate without being in competition with one another. "It's important to the success of my projects," he says, "for me to establish an atmosphere of trust and confidence. I want my people to know that they can ask questions without being called down for not knowing the answer.

Herb Sobel, laboratory manager at Raytheon, Bedford, Mass., says he tries to manage his people by example, by getting personally involved in a project. "I figure that an engineer is going to feel that if it's important enough for me to work on it, it's important enough for him to work on it," he says, adding that management methods should have enough flexibility to foster creativity. He believes that the electronics industry loses tremendous creativity by having too many controls on its people.
Spofford believes that a manager shouldn't feel that to motivate his people he must be more technically competent than any member of his staff. He says: "My strength as a manager is that I'm a catalyst; I can bring the experiences of engineers I've known together with the experiences of my staff and point out the differences and the similarities of the problems." Spofford also believes that an engineer's work should be varied, that he shouldn't be kept on any one product line for extended periods of time, even though he may be the most qualified for the product.

## Decisions, decisions, always decisions

As the ad says, "No one can take the ultimate weight of decision off your shoulders," especially when you're a manager. But insight into decisionmaking can ease the burden. A successful de-cision-maker should be flexible, imaginative, informed and organized.

On flexibility, Spofford says: "One adjustment a new manager finds he must make is to take immediate action on whatever comes up, instead of deferring it for a couple of days as he could have done as an engineer."

On being imaginative, Gerr observes: "A manager is often forced to do what he was told never to do as an engineer-make a decision without having all the information to support it."

How important is being informed? Robert C. Wilson, president of the Collins Radio Co., Dallas, Tex., says: "Most engineers and managers are being called on every day to make decisions that require knowledge outside the realm
of engineering. The sooner they can apply a knowledge of business to their engineering decisions, the sooner they're going to get the unqualified support of corporate management."

And Moore says this about the need for a manager to be organized: "If I define properly what I'm going to engineer in the first place, then not only can I use design and production shortcuts that save me time, money and problems, but, I'll design a product that sells."

## Pointers on projects

If you're a good decision-maker, your chances of running a successful project are pretty good. However, every project has its own peculiar set of problems. More than one project manager has lost his temper, and sometimes his confidence, worrying the product from design to production.

Here are four crucial phases of a project and what the managers have to say about them.

1. Design and prototyping phase.

Moore says: "Engineers tend to wring out their circuits and think the system will always come together easily. A model of the product is surer and will quickly reveal important system parameters. Keep the initial design group small in number. A large group is like a turning flywheel, which is hard to move into a new direction."

Howard Bogert, engineering vice president of Unicom, Cupertino, Calif., says: "If your entire manufacturing operation is out of house, you must anticipate the builder's problems and questions before you send him the design. It means that your engineers have to learn to be interdisciplinarians; they're responsible for a number of functions that are usually handled by a variety of departments in an in-house operation."
2. Sudden problems.

Sobel says: "If I didn't have some kind of staff group that I could motivate quickly to handle sudden problems, I'd probably lose a year or more in getting some projects started."
3. The moment of truth.

Gerr says: "To be or not to be-that is the project question." He shares the answer with the marketing department. "When a project runs too long," Gerr observes, "my decision to keep it running depends on whether or not I think the product can still be produced inexpensively enough to make a profit for the company."
4. Product acceptance.

Robert W. Gress, manager of resistor product development at the Corning Glass Works, Bradford, Pa., says: "Before manufacturing can accept our new products for processing, we have to provide both the design and a manufacturing method. We must also sell our new programs to our marketing force, which in turn guides us as to what will sell in the marketplace." "


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## ideas for design

## Logarithmic-feedback zero-crossing detector features wide input range and TTL output

The high frequency response and good dc characteristics of monolithic transistor arrays act to increase the range of a fast logarithmic-feedback zero-crossing detector. The circuit, with the component values shown in the diagram, works over a $50-\mathrm{dB}$ range of input variation.

The CA3018 array, used as a "mini op amp," has a TTL-compatible output drive. Transistors $Q_{1}$ and $Q_{2}$ are a differential input pair, with $Q_{2}$ loaded by resistor $R_{6}$. Transistor $Q_{3}$ acts as an output emitter follower. Resistor $R_{6}$ is bootstrapped to zener-connected $Q_{4}$, enabling the single stage of amplification to provide more than 50 dB of voltage gain.

Negative feedback through diodes $\mathrm{CR}_{1}-\mathrm{CR}_{4}$
constrains the peak output limits to $\pm$ twice the forward voltage drop per diode about the TTL transition region of 1.4 V . The negative feedback and the logarithmic variation of the output voltage with increasing signal input voltage allow the wide range of input variation. Positive feedback through resistor $R_{5}$ causes the circuit to have a defined lower threshold and a snap-action transition, allowing noise-free zero-axis detection. Phase control of the axis crossing is provided by resistor $R_{2}$, which offsets the voltage center of the input transition by varying the bias on $\mathrm{Q}_{2}$.

Walter G. Jung, 1946 Pleasantville Rd., Forest Hill, Md. 21050.

Circle No. 311



## RCA put 1,238 devices on a 150 mil COS/MOS chip. What are your LSI requirements?

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For example, the $149 \times 150 \mathrm{mil}$ timing circuit above was integrated from a breadboard containing 1,238 discrete devices. Just one of many custom chips designed with RCA's unique silicon interconnect process to provide high packaging density.

RCA maintains a staff of systems engineers who are experienced in the
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These facilities consist of computers for logic simulation, artwork digi-tizer-plotter systems that can cut turnaround time by $33 \%$ in typical circuits, Mann Pattern Generator facilities to speed mask preparation, and Teradyne Model J-283 digital IC systems which functionally evaluate complex arrays.

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work to help reduce package count, cut assembly costs, and achieve excellent cost effectiveness in your systems.

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products that make products pay off

## FSK oscillator uses twin-T network for high phase sensitivity

The twin-T network offers good stability for an inductorless audio oscillator because of its steep phase change around resonance. But it also exhibits a peak (and not readily controllable) insertion loss at resonance that must be offset by amplifier gain to meet the criteria for oscillation. By varying some of the component values in the filter (see diagram), you can achieve a tradeoff between phase sensitivity and insertion loss.

A solution that reduces the insertion loss to 20 to 30 dB while retaining nearly all of the steep phase transition can be found by examination of the internal phase-canceling mechanism. In particular, the ratio $\rho$ of impedances between the series and parallel elements can be derived from the characteristic frequency for the network shown:

$$
f_{o}=\frac{1}{2 \pi R C}
$$

where

$$
\begin{aligned}
& \mathrm{R}=\mathrm{R}_{1}=\mathrm{R}_{2} \\
& \mathrm{R}_{3}=\frac{\mathrm{R}}{\rho} \\
& \mathrm{C}=\mathrm{C}_{1}=\mathrm{C}_{2} \\
& \mathrm{C}_{3}=\rho \mathrm{C} .
\end{aligned}
$$

For an ideal twin-T network, the impedance ratio $\rho=2$.

Changes in impedance ratio $\rho$ strongly affect the insertion loss while retaining the basic relationship between characteristic frequency $f_{o}$ and the component values. If $\rho$ is increased from

2 to 2.5, the insertion loss decreases from near 60 dB to below 30 dB , while the phase plot remains comparatively steep. Another increase to $\rho=3$ lowers the insertion loss by only an extra 3 dB , and the phase slope gets rather shallow. Based on these findings, the FSK oscillator shown was designed for a $\rho$ of 2.5 .

Emitter-followers $Q_{3}$ and $Q_{5}$ allow the twin-T network to have a high impedance with lowvalued capacitors. A logic signal applied at input A of the oscillator changes the frequency from 2025 Hz to 2225 . Diodes $\mathrm{CR}_{1}-\mathrm{CR}_{4}$ in a second feedback path, around transistor $Q_{4}$, crudely stabilize the amplitude to yield a sinusoidal output with less than $10 \%$ harmonics.

Transistor $Q_{1}$ switches resistor $R_{4}$ into the twin-T network, thereby lowering the resistance of $R_{3}$ and shifting $f_{0}$ upward. Transistor $Q_{3}$ buffers the network against the damping effect of the input impedance of $\mathrm{Q}_{4}$. The diode network between the collector and base of inverter $Q_{4}$ reduces the gain as the signal amplitude reaches the turn-on voltage of the diodes. Capacitor $\mathrm{C}_{3}$ blocks dc current from the diodes. Transistor $\mathrm{Q}_{2}$ shunts capacitor $\mathrm{C}_{4}$ across $\mathrm{Q}_{4}$ 's collector load, thus grounding the output of the oscillator when it is keyed off. Emitter-follower $Q_{5}$ closes the feedback loop to the network and couples the output into a $600-\Omega$ load, such as a telephone line.

Claus H. Claassen, IBM, Systems Development Div., Monterey and Cottle Rds., San Jose, Calif. 95114.

Circle No. 312


Twin-T network can be slightly detuned to minimize insertion loss in this FSK oscillator without sacri-
ficing phase sensitivity. The resulting stability exceeds that for the usual twin-T oscillator.


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# Fast ECL eliminates laser rangefinder resolution problems 

Obtaining good and reliable resolution from short-distance laser rangefinders presents no problem when a MECL III voltage comparator is used as the threshold element. The most commonly used threshold element-a tunnel diodehas the right speed and sensitivity, but because of its low switching levels, rf noise can lead to detection errors.

A laser rangefinder with an MC 1650 comparator achieves a resolution of 1 meter over a nominal range of 1 km .

The rangefinder uses a 1 MW peak power Nd : YAG pulsed laser, an avalanche photodiode with a temperature-regulated biasing power supply, and a wideband amplifier between the photodiode and the comparator. Since the laser beam travels a meter in 6.673160 ns at standard temperature and pressure, a pulse repetition frequency of 149.89625 MHz should be used to get resolution of 1 meter.

The "main bang detector" samples a part of the outgoing laser beam and starts the range counter. The reflected beam, focused by the collection optics onto the silicon avalanche photodiode, provides a stop pulse. The counter uses MECL III logic for the first decade and MECL II and DTL logic for the later stages. The count'er output is in BCD, ready for the LED readout, which displays the range in meters.

One of the most critical parts is the front end of the receiver-the circuit that stops the range counter. For $1-\mathrm{m}$ resolution, the rise time of the threshold element should be around 1 or 2 ns . The MC 1650 comparator exhibits a $1.5-\mathrm{ns}$ rise time. The comparator has a differential-amplifier input that makes it less sensitive to com-mon-mode disturbances than tunnel diodes and other threshold detectors.
When the laser is pulsed, energy is transferred from the storage capacitor to the laser flash lamp. The firing of the flash lamp involves very high peak currents, resulting in electromagnetic noise strong enough to switch any lowvoltage logic circuits close to the lamp.

To overcome this problem, the range counter should be reset after the flash lamp fires-but before transmission of the laser beam. Then whatever happens to the range counter when the flash lamp fires has no influence on ranging performance. The delay between the firings of the flash lamp and the Q -switch in the laser is about $150 \mu \mathrm{~s}$. The range counter should thus be reset about $100 \mu \mathrm{~s}$ after the flash lamp fires.

This work was performed while the author was with Holobeam Inc., Paramus, N.J.

Mordehai Shmaia, State of Israel, Ministry of Defense, Armament Development Authority, P.O. Box 7063, Tel Aviv, Israel. Circle No. 313


ECL comparator threshold, higher than that of a tunnel diode, prevents electromagnetic noise from
triggering the range counter in this laser rangefinder. A 1-meter resolution results.

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## Charge pump circuit eliminates bias supply for NMOS devices

A charge-pump technique replaces the required -2-V back-bias supply for NMOS devices, reducing circuit cost and enhancing those applications that are hindered by additional power supplies.

The circuit of Fig. 1 oscillates at 330 kHz (with capacitor $\mathrm{C}_{1}=0.001 \mu \mathrm{~F}$ ) supplying bias voltages up to 3 mA at -2 V . This technique encourages the use of NMOS devices, emphasizing their ease of interfacing as a result of TTL/ DTL compatibility.

To determine a suitable output current and load resistance, add up the maximum back-bias leakage currents of the NMOS devices. Then use a de current (Fig. 2) of 10 times the calculated value. This current flows through the resistive divider $R_{1}-R_{2}$ to produce the required bias volt-
age.
For example, a system using N-channel shift registers and a character generator has leakage currents totaling $100 \mu \mathrm{~A}$ with a bias supply specified as $-2 \mathrm{~V} \pm 20 \%$. The resistive divider is chosen to draw 1 mA while dividing the supply from 2.5 V down to 2.0 V . Thus resistor $\mathrm{R}_{1}=$ $500 \Omega$ and $R_{2}=2 \mathrm{k} \Omega$.

The bias supply of Fig. 1 has about $50-\mathrm{mV}$ ripple. Although this does not affect the operation of NMOS devices, filter capacitor $\mathrm{C}_{4}$ can be increased if less ripple is desired.

Bud Broeker, Motorola Semiconductor Products Inc., 5005 E. McDowell Rd., Phoenix, Ariz. 85008.

Circle No. 314


1. Substrate back-bias supply for NMOS devices generates 3 mA at -2 V , using capacitors $\mathrm{C}_{2}$ and $\mathrm{C}_{3}$ in charge-pump technique.

2. Output current characteristic is determined by frequency as set by capacitor $\mathrm{C}_{1}$.

## IFD Winner for July 6, 1972

Don Gazzano, Ampex Corp., 401 Broadway, Redwood City, Calif. 94063. His idea "Modified reed switch acts as overload current sensor" has been voted the Most Valuable of Issue award.

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## new products

## Programmable transmitter is asynchronous or synchronous



Western Digital Corp., 19242 Red Hill Ave., Newport Beach, Calif. 92663. (714) 557-3550. See text; 4 wks.
With the sudden popularity of universal asynchronous receiver/ transmitter integrated circuits for data communications, the mixing of asynchronous and synchronous transmission in the same modem circuit has become a new frontier. Motorola was the first to address this problem with its MC2257 asynchronous/synchronous transmitter and MC2259 asynchronous/ synchronous receiver.

Western Digital is now introducing a pair of circuits that surpass the Motorola circuits in capability, but substantially exceed them in price. Among the added or improved capabilities are internal programmability, full TTL compatibility, speed, word length and tri-state outputs.

Called the PT1482A transmitter (see diagram) and PR1482A receiver, the circuits are produced with silicon-gate MOS technology. An internal control register in each makes programming possible. An eight-bit word presented to the control register selects the following:

- Mode: Synchronous at up to 640 kHz down to dc. Asynchronous at up to 640 kbauds down to dc. Isochronous, with the same range of data rates as asynchronous operation but allowing transmission of asynchronous data over synchro-
nous equipment and lines.
- Clock: Choice of $1,16,32$ or 64 times the $640-\mathrm{kHz}$ transmitter clock.
- Word length: $5,6,7$, or 8 bits plus odd, even or no parity.
- Stop bit length: 1 or 2 bits in the asynchronous mode.

Three registers in addition to the control register are included in the transmitter. They include a data register for holding the data word to be transmitted, a fill holding register for holding a sync word that is inserted into the data stream when no input data are in the data register, and a transmitter register for outputting the data as a serial bit stream.

The receiver also has three additional registers. They include a receiver register for accepting the serial input data, a holding register for outputting the data on up to nine parallel lines and a match register for identifying sync words transmitted by the transmitter.

Both circuits require powersupply voltages of $+5 \mathrm{~V},-12 \mathrm{~V}$ and ground.

The ICs can stand 0 to 70 C temperatures, and they come in a hermetic ceramic dual-in-line package.

Pricing on the PT1482A in 100 quantities is $\$ 21.70$. The PR1482A in the same quantities costs $\$ 26.05$. By comparison, the Motorola transmitter sells for $\$ 9$ and the receiver for $\$ 10.75$ in 100 quantities.
For Western Digital CIRCLE No. 251 For Motorola

CIRCLE NO. 252

## Dual channel audio amp supplies 2 W

National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, Calif. 95051. (408) 723-5000. $\$ 3.95$ (100 up); stock.

A dual audio amp IC, called the LM377, provides up to 2 W of continuous rms power into an $8-\Omega$ load on each of its channels. Complete short circuit and thermal limiting is provided on the chip. Other features include an input impedance of $>10 \mathrm{M} \Omega$, gain of 100 dB , distortion only $0.5 \%$ at 1 kHz and power bandwidth of 65 kHz . The supply voltage is 18 V dc.

CIRCLE NO. 253

## Calculator chip handles over 20 digits

General Instrument Corp., 600 W . John St., Hicksville, N.Y. 11802. (516) 733-3000.

A one-chip MOS calculator, called the C-500, handles numbers in excess of 20 digits while maintaining eight-digit accuracy. The unit features four function operation, constant in all four modes, chain operations and power calculations. A minus sign is used to indicate negative numbers. The new calculator stores exponents in the range $10^{-20}$ to $10^{+79}$ and will compute accurately any answer within that range without overflow.

CIRCLE NO. 254

## 741-type op amp is four times faster

Intersil, Inc., 10900 N. Tantau Ave., Cupertino, Calif. 95014. (408) 257-5450.

A high-speed version of the 741 op amp, named the 741HS, has a slew rate that's four times faster than the standard 741 , plus improved input offset ( $\mathrm{I}_{\mathrm{OS}}$ ) and input bias ( $I_{b}$ ) currents. The military version of the 741 HS has a slew rate of $0.7 \mathrm{~V} / \mu \mathrm{s}, \mathrm{I}_{\mathrm{os}}$ of 20 nA and $\mathrm{I}_{\mathrm{b}}$ of $0.1 \mu \mathrm{~A}$; the standard 741 has corresponding values of $0.2 \mathrm{~V} / \mu \mathrm{s}, 300 \mathrm{nA}$ and $1.5 \mu \mathrm{~A}$, respectively.

CIRCLE NO. 255


Wivio

ICs \& SEMICONDUCTORS
Sense amp/line receiver good for 1103 RAM


Texas Instruments Inc., P.O. Box 5012, M/S 308, Dallas, Tex. 75222. (214) 238-3741. Plastic, \$4.26 (100 up) ; stock.

A dual MOS sense amp and a line receiver-designated SN75207 and SN75208, respectively-replace the SN75107 and SN75108 and are ideal for 1103 MOS RAM applications. These devices' tight input sensitivity ( $\pm 10 \mathrm{mV}$ ) permit faster memory cycles and also make the ICs well-suited for line receiver applications by allowing use of longer transmission line lengths. The SN75207 features a TTL-compatible pull-up output while the SN75208 has an open collector output that permits wire-OR logic connections with similar output configurations.

CIRCLE NO. 256

## Radio receiver ICs cover AM/FM bands

N. V. Philips, P.O. Box 523, Eindhoven, The Netherlands.

Three ICs for radio receivers, designated the TBA690, TBA700 and TBA570, cover both the AM and FM bands to 30 MHz . The TBA690, with an output power rating of 0.6 W , and the TBA700, with 1 W , have a supply voltage range from 2.7 V to 12 V ; these are intended for operation with batteries. In addition to the classB output stage, each IC incorporates a driver, preamp, two-stage i-f amp, agc and a stabilized bias circuit. The TBA570 is a generalpurpose IC for both battery and main line operation. All active functions are integrated except the FM detector and the output stage.

CIRCLE NO. 257

## 4-channel decoder available on a chip

Electro-Voice, Inc., 600 Cecil St., Buchanan, Mich. 49107. (616) 6956831.

A four-channel decoder IC chip accurately decodes all existing matrix systems, including E-V STERO-4 and CBS SQ, without manual switching. The IC chip contains all the circuitry required to decode except the power supply and a simple phase-shift network. It can be used at different points in the audio circuit depending on the extent of control vs economy desired.

CIRCLE NO. 258

Driver/receiver ICs replace standard types


Motorola Semiconductor Products, P.O. Box 20912, Phoenix, Ariz. 85036. (602) 273-3466. MC55107L and MC55108L: \$4.80; MC55109L and MC55110L: \$5.15; MC75109P and MC75110P: \$2.80; MC75450P: $\$ 1.40$; MC75451P: \$1.05 (100 up).

Six driver/receiver ICs are pin-for-pin replacements for the industry standard 55107-110 and 7545051 types. Two are dual line receiv-ers-the MC55107, which features an active pull up and the MC55108, which uses open collector outputs. Two others are dual line driversthe MC55109-110. The MC55109 switches a $6-\mathrm{mA}$ current sink to either of two output terminals in response to the input logic conditions. The MC55110 sinks 12 mA . Both ICs provide a pair of inhibit inputs to disable the output currents and allow multiple drivers to share a pair of lines in party-line systems. The MC75450 and MC75451 are dual line peripheral drivers that may be used to interface TTL and DTL logic inputs.

CIRCLE NO. 259


Pause for a moment. Then focus your imagination on the split end. The second effort man. He'll get downfield and snag the pass for those important fourth quarter yards.
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ICs \& SEMICONDUCTORS
1024-bit n-channel RAM accesses in 200 ns

## 

Electronic Arrays, 501 Ellis St., Mountain View, Calif. 94040. (415) 964-4321. \$27.50 (100 up).

The EA 1502-a $1024 \times 1$ random access memory with input level shifters and an output sense amp on the chip-eliminates the need for off-chip interface circuits while maintaining a $200-\mathrm{ns}$ maximum access time from the TTL drive circuit to the TTL receiving circuit. The memory is an n-channel silicon-gate RAM. It operates on $\pm 12 \mathrm{~V}$ power supplies with power dissipation typically 115 mW . It also has EA's proprietary bit cell design which permits an "automatic refresh mode" (one write pulse for each system cycle) and eliminates the need for periodic memory busy signals.

CIRCLE NO. 260

## Low frequency PLL sinks up to 100 mA



Exar Integrated Systems, Inc., 750 Palomar, Sunnyvale, Calif. 94086. (408) 732-7970. XR567CN: $\$ 4.95$ ( 100 up).

The XR-567, a monolithic phaselocked loop, operates over a 0.01 to 500 kHz frequency range and has a logic-compatible output that sinks up to 100 mA of load current. Center frequency stability, at 25 C case temperature, is $35 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$. Bandwidth is 0 to $14 \%$, adjustable. The frequency range can also be adjusted over a $20: 1$ range by a single external resistor.

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# Triple outputs in a modular DIP offered in new power-supply line 



Zeltex, Inc., 1000 Chalomar Rd., Concord, Calif. 94520. (415) 6866660. See text.

The already overcrowded modular power supply market has another entrant. But unlike the timid swimmer who gingerly sticks one toe into the water before entering, Zeltex, Inc., has plunged right in with a splash: It is offering triple, dual and single outputs in its new 48 -model line, with choices of ac or dc inputs, two package sizes and unregulated or regulated out-
puts (with three degrees of regulation). To top it off, prices appear to be lower than those of competitive units.

Sure to create a big stir is the Z series triple-output supply. It's the first to be packaged in a 1.44 -cubic-inch modular DIP. The supply provides both $\pm 15 \mathrm{~V}$ and +5 V at 40 and 200 mA , respectively. And input voltage can be either 12 or 28 V dc. Load and line regulation for the triple output version is, typically, $0.5 \%$, and combined
ripple and noise is 5.0 mV . The triple-output Z Series costs $\$ 99$.

The dual-output models of the Z Series yield $\pm 15 \mathrm{~V}$, with output currents ranging from 30 to 250 mA . Inputs can be either 115 V ac or 5 V dc. Three choices of regulation are available: precision ( $\pm 0.02 \%$ ), limited $( \pm 0.5 \%)$ and unregulated $( \pm 10 \%)$. Single output models are also available, with 5,15 or $200-\mathrm{V}$ outputs at 250,100 and 20 mA , respectively. Input voltage can be either 115 V ac or 5 V dc.

The T-series is cased in a standard $2.5 \times 3.5 \times 0.88$-inch package and is interchangeable with many competitive supplies. This series also offers triple, dual and single outputs in three input-voltage classes: 115 V ac, 230 V ac and 100 to 125 V ac. Output currents range from 25 to 1000 mA , and regulation from 0.02 to $1.0 \%$.

Other important specs, applicable to both series, are short-circuit protection, an output impedance of $0.2 \Omega$ at 10 kHz , input isolation of $50 \mathrm{M} \Omega$, input-output isolation of 500 V dc and no current derating from -25 to +71 C .

Prices for the $Z$ Series range from $\$ 19$ to $\$ 99$ and for the $T$ Series from $\$ 16$ to $\$ 69$.

CIRCLE NO. 250


## ANALOGY

THE A-740 19 THE FASTEST.MOST ACCURATE PIN CUSHION CORRECTION MODULE AVAILABLE. CORRECTION ACCURACIES ARE BETTER THAN O.2\% BANDWIDTH IS 10 MHz ; SLEW RATE: 40 V/MS MIN. ITALSO MATHEMATICALLY CORRECTS DYNAMIC FOCUS DISTORTION. THIS COMPL ETE MINI ANA LOG- COMPUTER IS PACKAGED IN A SNGLE 3.5" $\times$ $2.5^{\prime \prime} \times 0.87^{\prime \prime}$ EPOXY MODULE, READY TO USE. DONT WAIT ON PINS AND NEEDLESWRITE FOR SPECS NOW.


1220 COLEMAN, SANTA CLARA, CALIF. 95050

## How much did it cost you last month

Four thousand dollars? That would be less than a $1 \%$ slipthrough of defective IC's for a relatively small application working with smaller PC Boards. Ten or twenty thousand dollars? The numbers increase drastically with greater volumes and higher complexity Boards. And we use a conservative $\$ 25.00$ estimate for replacement of a defective IC in a PC Board.

Packed into most IC shipments, from the most meticulous manufacturer, you'll find a nominal percentage of defective devices, due to transit damage, handling, or human error.
And did your supplier's tests fully cover your own tight functional parameters? Mar-ginal-operating circuits can mean extensive trouble-shooting, rework and expensive replacement parts, just as often as catastrophic failures. And it costs real money either way.

OK, so inspection pays off, as a general rule. But how do you get down to specifics-what can testing do for you? We can help. Fairchild Systems, the leader in semiconductor test systems, offers a mathematical tool for determining the economic feasibility-and the financial justification-of pre-use testing your semiconductor parts. The name of the game is SAVE. Short for System Analysis of Value Economics: a computer-based procedure that knows how to simulate your particular operations over a broad spectrum of test inspections -incoming, QA, PCB, wafer and final production testing.
SAVE enables you to detail, step-by-step, all your applications, configurations, number of stations and daily work shifts. Types and quantities of devices used are accounted for, with your average rate of untested defectives balanced in.
After completed forms are run through Fairchild Systems' SAVEprogrammed computer, you get the readout. Hard-nosed data on total
comparative costs for testing; including a breakdown of operating expenses, system costs, test equipment replacement, payback period, plus actual net savings realized.
You'll also use SAVE's figures for evaluating alternative system configurations to provide cost-effective solutions. And many plant designers take advantage of the in-depth operational simulation for "fine tuning" systems for optimum performance and maximum software/hardware utilization.

And SAVE's computerized analysis helps you determine the most productive, most economical level of test equipment from our advanced Sentry series-the largescale Fairchild System with unbeatable speed, sophistication and flexibility. From MOS to bipolar, from IC's to discretes, or LSI-with software compatible to all, and the lowest throughput costs in the field-the Sentry family offers you total semiconductor testing capability with enormous expansion and adaptability options. And SAVE shows you how to make the most of it ...and make your world just a little more perfect.

# Fairchild Systems 

## Power supplies offer 4 V to 1 kV , to 23 A



Acopian Corp., 131 Loomis St., Easton, Pa. 18042. (215) 258-5441. $\$ 35$ to $\$ 190$; 3 days ARO.

The $U$ Series of unregulated power supplies has been expanded to include models with outputs ranging from 4 to 1000 V at currents up to 23 A . (Previously, 10 A was the highest available output current rating.) They provide an economical means for powering loads such as relays, lamps, display devices and small motors. Outputs are capacitively filtered and may be used in either polarity. No derating is required through an ambient operating temperature range of -10 to +65 C . All models are housed in extruded aluminum cases.

CIRCLE NO. 262

## 12-bit d/a converter meets MIL-STD-883



Micro Networks Corp., 5 Barbara Lane, Worcester, Mass. 01604. (617) 756-4635. \$265; 2 wks.

The MN413H d/a converter may be used either as a straight 12 -bit $\mathrm{d} / \mathrm{a}$ or as an 11-bit plus sign multiplying $\mathrm{d} / \mathrm{a}$. The unit is designed to meet or exceed the Class B requirements of MIL-STD-883. The linearity of $\pm 1 / 2$ LSB and absolute accuracy of $\pm 1$ LSB are maintained over the full -55 to +125 C temperature range. The input logic is both TTL and CMOS compatible, with unit power dissipation of 500 mW typical. The unit is an assemblage of hybrid circuitry packaged in hermeticallysealed flat packs housed in a $1.7 \times$ $0.825 \times 0.260-\mathrm{in}$. shell.

CIRCLE NO. 263

Display tube supply gives 200 V at 25 mA


Instrument Displays, Inc., 225 Crescent St., Waltham, Mass. 02154. (617) 894-1577. \$18.00; stock.

Instrument Displays has introduced an encapsulated power supply producing 200 V at 25 mA from $115 \mathrm{~V}, 50$ to 400 Hz or 220 V $50 / 60 \mathrm{~Hz}$. It provides power for any readout requiring 170 to 200 V , and it has enough power for up to eight NIXIE display tubes. It can be soldered directly to the PC board or used with mating socket. Ripple and noise is 2 V pk to pk and operating temperature is -25 to +70 C . Size is $3.5 \times 2.5 \times 1.25$ inches.

CIRCLE NO. 264

## Tomorrow's keyboard. <br> (We left most of the key parts out.)

All the moving parts, for example.
And most of the expensive ones.
Instead we used unique, highly conductive elastomer materials, and thereby ushered in a new era.
We call this the Chomerics EF Keyboard. It's fail-proof, less than $3 / 16^{\prime \prime}$ thick, and it comes in any configuration you want to dream up.

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The EF is the most revolutionary design/ function keyboard in the world. Pretty soon everybody will be trying to make keyboards like it.


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Chomerics knows keyboards like a book. You can have copies of our literature for the asking.

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Our new series of 5 Amp Silicon NPN/PNP Plastic Power Transistors are manufactured by the triple diffused planar process, giving you these advantages: low leakage and fast switching times, with $\mathrm{f}_{\mathrm{T}}$ typically 8 MHz . Other characteristics include typical gain of $100 @ 1.0 \mathrm{Amp}$ and typical VCE (sat) @ 2.0 Amps of less than 0.5 Volts. They are constructed with tin plated leads to provide optimum solderability and permanent contact with a one piece collector. Intended for complementary use, these low cost devices are recommended for motor speed controls, inverters, series regulators and audio amplifiers. Available in a TO-220 package, the transistors are $80 \notin$ per pair in 100 quantities.

TYPICAL GAIN SPECIFICATIONS

| NPN Type Numbers | H $_{\text {FE }}$ @ 1.0A, 4V | PNP Type Numbers |
| :---: | :---: | :---: |
| SDT 5101 | $35-70$ | SDT 5111 |
| SDT 5102 | $60-120$ | SDT 5112 |
| SDT 5103 | $100-300$ | SDT 5113 |

Write today for our spec sheets and application information.

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# Dialight sees a need: 

(Need: A large $5 / 8^{\prime \prime}$ high LED readout at a low $\$ 5.80^{*}$ price.)

## See

Each digit in this bezel assembly contains a Dialight lightemitting diode, decoder/driver, and resistor network that produces a bright, highly visible readout that can be easily installed in a panel. The readout display is supplied with discrete gallium phosphide or gallium arsenide phosphide diodes arranged in a seven-segment format. These generate a bright, highly legible red character ( 0.625 inch

high-the largest size character in the industry) with the lowest power consumption for a character of this size. Ideal for mounting on a control panel, or in a digital clock, meter, credit-card verifier, TV channel indicator, or hospital room status-board indicator. The contrast ratio between the illuminated and non-illuminated segments is further enhanced by a one-piece red nonglare window


Dialight is a company that looks for needs...and develops solutions. That's how we developed the industry's broadest line of LED light sources, indicator lights and readouts. No other company offers you one-stop shopping in LED visual displays. And no one has more experience in the visual display field. Dialight can help you do more with LEDs than anyone else because we have done more with them. Talk to the specialists at Dialight first. You won't have to talk to anyone else.

Here are a few products in this family: 1. Multidigit readout assembly in $0.205^{\prime \prime}$ character height 2. Status display module with 6 LEDS with adjustable light cells 3. LED readout in character height $0.625^{\prime \prime} 4$. Alphanumeric display complete with code generator / driver character height $0.300^{\prime \prime} 5.5 \times 7$ dot matrix alphanumeric display in character height $0.300^{\prime \prime} 6$. Hexadecimal display with logic character height $0.270^{\prime \prime}$. Single digit LED readout module in $0.125^{\prime \prime}$ character height. 8. Numeric display with integral TTL MSI circuit chip with counter character height $0.270^{\prime \prime}$ 9. Single digit LED readout module in 0.270" character height (MAN 1 equiv.). * 1000 lot quantity for 730.0003


Please send data on your LED readouts.

## NAME

## TITLE

COMPANY
ADDRESS
$\overline{\text { CITY STATE }}$

## DIALIGHT

Dialight Corporation, A North American Philips Company 60 stewart Avenue, Brooklyn, N.Y. 11237 (212) 497-7600

# MODULES \& SUBASSEMBLIES 

## Unit converts X-Y to polar coordinates



Computer Conversion Corp., 6 Dunton Ct., E. Northport, N.Y. 11731. (516) 261-3300. $\$ 995$ (large quantities); 6 weeks ARO.

These new solid-state devices convert inputs in rectangular form ( $\mathrm{X}, \mathrm{Y}$ ) to equivalent polar coordinates ( $R$, Angle), with an angular accuracy of $\pm 15$ minutes of arc at $\mathrm{R}=10$. The converters accept $X$ and $Y$ inputs from 0 to $\pm 10 \mathrm{~V}$ dc and provide an output of $R$ accurate to $\pm 0.1 \%$ of full scale over a range of 0.05 to $\pm 10 \mathrm{~V}$. Conversion time is 2 s max. and output impedance is less than $1 \Omega$. Input power required for Part No. RP600 is $\pm 15 \mathrm{~V}$ at 100 mA and +5 V at 75 mA . The units are available in a single module package $3.2 \times 3.4 \times 1.8-\mathrm{in}$. high, with pin type header suitable for chassis or PC mounting.

CIRCLE NO. 265

## Active bandpass filters are externally tuned

Frequency Devices, Inc., 25 Locust St., Haverhill, Mass. 01830. (617) 372-6930. From \$58 (100); stock to 2 wks .

Series 760 active filters are two-pole-pair Butterworth bandpass devices externally tunable over a 1000:1 range with four equal resistors. Three models, 760, 762 and 764 , cover the range 0.05 Hz to 20 kHz with maximum center frequencies ( $f_{o}$ ) at $50 \mathrm{~Hz}, 500 \mathrm{~Hz}$ and 20 kHz , respectively. Specifications include: factory set unity gain, adjustable to $\pm 20 \mathrm{~dB} ; \mathrm{f}_{\mathrm{o}}$ is $\pm 3 \%$ with matched $1 \%, 100-\mathrm{ppm} /{ }^{\circ} \mathrm{C}$ resistors; $f_{o}$ stability is $\pm 0.03 \% /{ }^{\circ} \mathrm{C}$; passband gain at $f_{0}$ is $\pm 0.03 \mathrm{~dB}$; $\mathrm{Z}_{\mathrm{o}}$ is $1 \Omega$; Qs of 5 or 10 are standard; case size is $2 \times 3 \times 1 \mathrm{in}$. for 760 and $2 \times 3 \times 0.6 \mathrm{in}$. for 762 and 764.

## Digital translator converts binary to BCD

Interface Engineering Inc., 386 Lindelof Ave., Stoughton, Mass. 02072. (617) 344-7383.

Digital translators bridge the interface between binary angle data and BCD-encoded displays, printers and shaft positioning devices. Two models are available, packaged in $3 \times 4 \times 0.4-\mathrm{in}$. cubes. The DD107-5 is a parallel ripplethrough translator that accepts up to 15 bits of binary angle input and provides an 18 -line BCD output with resolution of $0.01^{\circ}$, an over-all accuracy of $0.015^{\circ}$ and a full scale of $359.99^{\circ}$. The DD107-4 accepts up to 12 bits of binary angle input data and outputs 14line BCD with a resolution of $0.1^{\circ}$, an over-all accuracy of $0.055^{\circ}$ and a full scale of $359.9^{\circ}$. Conversion time of both models is 500 ns .

CIRCLE NO. 267
High-power supplies use regulating transformers


Technipower, Inc., Ridgefield, Conn. 06877. (203) 438-0333. $\$ 150$ and up (OEM).

Using a regulating transformer concept, the field-repairable RT series offers an economical approach to high-power ac-dc requirements without the usual disadvantages associated with ferro-resonant and SCR-type regulators. The units are available in 250,500 and 1000 W power ranges with output voltages of 5 to 48 V dc and currents up to 120 A. Specifications of $\pm 0.5 \%$ regulation for combined line, load and frequency, and ripple at $1 \%$ or 150 mV rms are featured. Overvoltage protection is optional for all models and a low-ripple option of $200 \mathrm{mV} \mathrm{pk}-\mathrm{pk}$ is available on the $5-\mathrm{V}$ model. Short-circuit protection, output-voltage adjustment and remote sensing are standard.

## TV character generator stores 4 pages, shows 2



GCB Closed Circuit TV Corp., 74 Fifth Ave., New York, N.Y. 10011. (212) 989-4433.

Model CG-240 character generator has a four-page, two-channel capability that makes it possible to display one page of a channel while composing, editing or previewing any one of the remaining three pages of the preview channel. An audio-record/playback interface allows inexpensive off-line storage of additional display information. The character generator meets EIA RS170 video standards and RS-232 data standards.

CIRCLE NO. 269

Electronic calculator prints output


Unicom Systems, Inc., 10670 N. Tantau Ave., Cupertino, Calif. 95014. (408) 255-3650. See text.

The 1000 P -priced at $\$ 295$-is said to be the first electronic printing calculator under $\$ 300$. This is less expensive than mechanical printing calculators, which are usually in the $\$ 500$ to $\$ 800$ range. Major features include add, subtract, multiply, divide, grandtotal memory register, automatic decimals, constants, chain calculations, credit balance, two-color printing, repeat add/subtract, automatic retention of last item and a buffered keyboard.

Translator converts codes to time display


Moxon Inc., SCR Div., 222 Michelson Dr., Irvine, Calif. 92664. (714) 833-2000. From $\$ 1000 ; 1$ to 4 months.

A new time-code translator, the Model 520, featuring plug-in-module construction,-- provides day/ hour/minute/second panel displays. Low-power, seven-segment, wideangle, gas-discharge displays are used. Some of the common codes handled by the translator include IRIG A, IRIG B, IRIG C, IRIG E, IRIG H, IRIG G, NASA 28, NASA 36, AMR-B2, AMR-C2, AMR-D5 and XR-3. Various parallel BCD formats, Slow-Code Type-B outputs and others are optionally available.



## 1.

The multi-speed chart drive is the equal of any competitor on the market today. Further, the Omm/Scribe ${ }_{r m}$ is field adjustable. precisely for English/ Metric scaling. Quick, fast, convenient.

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## Infrared scanner shows image on a CRT

Dynarad Inc., 1420 Province Hwy., Norwood, Mass. 02062. (617) 7629450.

Dynarad's Model 210 infrared scanning systems use an optical system to scan and focus the infrared radiant energy from an object onto a sensitive detector. This detector is cooled in liquid nitrogen to increase its sensitivity. Its output is amplified, processed, and the thermally detected image is displayed on a cathode ray tube. By means of a calibrated dial, the temperature of any part of an object can be determined. The scanning system is passive. It simply receives the natural thermal radiation emitted by an object. Applications include breast cancer detection, pollution monitoring, nondestructive testing, electronic circuit evaluation, remote sensing, intrusion detection and power line monitoring.

CIRCLE NO. 272

## Unit reads embossed

 data on cards

AMP Inc., 449 Eisenhower Blvd., Harrisburg, Pa. 17105. (717) 5640101.

The new AMP embossed-card reader eliminates the need for cards with an extra coding medium such as punched holes or a magnetic stripe. It reads standard credit cards embossed with Farrington 7B font in accordance with the ANSI $\times 4.13-1971$ standard. The card actuates a transport mechanism, and a photoelectric device reads at 24 char $/ \mathrm{sec}$. The output is DTL (negative logic) compatible. Cards are inserted into and retrieved from the same slot.

CIRCLE NO. 273

Mini-calculator breaks a price barrier


Casio, Inc., Consumer Products Div., One World Trade Center, Suite 4011, New York, N.Y. 10048. (212) 432-9230. See text; stock.

The Casio mini-calculator, at the suggested retail price of $\$ 59.95$, is said to represent a major price breakthrough in electronic calculators. When the batteries begin to run down, the display will gradually darken. Batteries can therefore be replaced long before a miscalculation occurs. The life of the four penlight batteries, in continuous duty, is approximately 11 hours, and in intermittent duty about 14 hours. This is equivalent to about six months of average use.

CIRCLE NO. 274


This new template by RapiDesign combines into a single unit, $30^{\circ}, 45^{\circ}, 60^{\circ}, 90^{\circ}$ triangles and a protractor. Eliminates wasted motion and drawing board clutter by combining into a single template these often used instruments. The Triangle-Lizer, like all RapiDesign products, is a sturdy drafting instrument. One side is beveled for inking. The Triangle-Lizer comes in 3 sizes: $4^{\prime \prime}, 6^{\prime \prime}$ and $8^{\prime \prime}$. Ask for Triangle-Lizer, one of nearly 200 RapiDesign templates at your drafting supply store, or contact us direct for a free 1972 catalog.

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EEROL


Case dimensions of our new 4900 Series switches are less than $.250^{\prime \prime}$ by $.300^{\prime \prime}$ by $.100^{\prime \prime}$. Ratings are $2 \mathrm{amp} .125-250 \mathrm{~V}$ AC; 2 amp . (Res.) and 1 amp . (Ind.), 30V DC. Meet MIL-S-8805 specs. Available with 4 types of terminals, pin plunger and various lever type actuators.
new Our new 4800 Series sub-miniature sub-miniature switches are rated $5 \mathrm{amp} .125-250 \mathrm{~V}$ AC
switches
 with 5 amp . resistive and $21 / 2 \mathrm{amp}$. inductive ratings at 30 V DC. Case dimensions are approximately $.400^{\prime \prime}$ by $.800^{\prime \prime}$ by $.250^{\prime \prime}$. Available with 6 terminal types, a variety of lever actuators and optional bifurcated or dual gold contacts. Meet MIL-S-8805 specs. McGill Manufacturing Co., Inc., Electrical División, Valparaiso, Indiana 46383

Available from
Authorized McGill Electrical and Electronic Distributors

## Programmed controller replaces fixed logic



Gulf-Western Industries, Inc., Eagle Signal Div., 736 Federal, Davenport, Iowa 52803. (319) 3268111. \$2070 (unit qty.).

Controlpac 600 is a programmable logic controller that provides 16 inputs, 16 outputs, a 256 -word pROM and a 256-bit RAM. Word size is 12 bits. This system can replace dedicated hard-wired, relay or solid-state logic systems. Although its internal wiring is also fixed, the program can be changed to accommodate many types of machines or processes. Inputs and outputs are expandable to 512 ports each, the pROM to 4096 words and the RAM to 512 bits.

CIRCLE NO. 389

## Differential amplifier has remote gain control

Ectron Corp., 8133 Engineer Rd., San Diego, Calif. 92111. (714) 2780600. From $\$ 695$.

The 760 series of programmable dc amplifiers, for data-acquisition and process-control systems, accepts TTL, binary or 10 -line logic commands to control up to 10 gain steps. Available options include digital-data storage, local gain readout, manual gain override and a wide choice of performance characteristics such as $0.01 \%$ gain accuracy, $1 \mu \mathrm{~V} / \mathrm{C}$ stability, a $300-\mathrm{V}$ common-mode rating, $126-\mathrm{dB}$ com-mon-mode rejection, active input filters and dual outputs.

While spending long suniess days in alaska, Professor Flake came up With this ingeniou invention which he calis "The Combination ChickenFooler and ThreeMinute EgGAlarm. Candle (A) burns downtothe polnt where string breaks. This allows hammer (B) To fall knocking slefping bird (C) off SWing. Removal of birds weight Allows PhonoGRAPH ARM TO FALL ON RECORD (D) AS RECORD PLAYER (E) BLASTS OUT"JADA", SQUIRREL THAT HAS BEEN TRAINED TO RESPOND, TURNS TREADMILL ATTACHED TO GENERATOR ( $\boldsymbol{F}$ ). GENERATOR LIGHTS BULB(G) AND CHICKEN, THINKING IT IS SUNRISE, LAYS EGG WHICH ROLLS INTO TEAKETTLE ON ELECTRIC HOT PLATE. (H) STEAM FROM TEAKETLE TRAVELS THROUGH HOSE ATTACHED TO BED (I) AS STEAM INFLLTES RUBBER BALLOON, IT PRESSES AGAINST POINTOFKNIFE (J) AND IS PUNCTURID.THE RESULITING 'NOISE AWAKENS SLEEPING MAN JUST IN TIME TO RETRIEVE THREE-MINUTE EGG FROM TEAKETTLE.


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# innovation yesterday 



## innovation today



## ECONOMY

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## Printer/plotter uses electrolytic process



Litton Industries, Datalog Div., 1770 Walt Whitman Rd., Melville, N.Y. 11746. (516) 694-8300.

Called the Alphagraphic 1100, the unit functions both as a line printer and digital plotter. As a line printer it generates copy at the rate of 180 lines $/ \mathrm{min}$ with a 128 -column line. As a plotter, it provides a resolution of 75 elements per in. The manufacturer believes it to be the only printer/ plotter on the market employing the electrolytic printing process. The machine requires neither ink nor toners and it readily interfaces with most minicomputers.

CIRCLE NO. 275

## Digitizer traces lengths and measures areas

Numonics Corp., Hancock St. \& Rt. 202, N. Wales, Pa. 19454. (215) 643-7410.

You can measure an area, locate coordinates, or trace a path length with Numonics' graphics calculator. It also connects to a variety of external computing devices to allow you to measure peak heights, compute averages, do computeraided design and numerical controlling. Solutions appear on the display either continuously or upon command, and in the English or metric system. Reading area can be expanded from the standard $24 \times 24 \mathrm{in}$. The resolution is from 1.0 to 0.25 mm . Slides can be projected onto a screen for measurement.

CIRCLE NO. 276

## The Elegant Molded Parts



For elegant applications. Custom-made or standard, EPC parts come with thin walls down to 5 mils, tolerances to $\pm .05 \%$ - even threaded bushings. In six different materials: fluorocarbon, nyIon, glass-reinforced nylon, DAP, polyester and epoxy. For temperature ranges that go up to $200^{\circ} \mathrm{C}$.

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Modem interface tester checks terminal hook-up


Nu Data Corp., 32 Fairview Ave., Little Silver, N.J. 07739. (201) 842-5757. \$95 (unit qty).

Series 921 modem-interface testset checks interconnections between modems and terminals conforming to Standard EIA 232. The pocket-size test unit ( $4.5 \times 2.5 \times$ 1.6 in .) provides access to 25 interconnecting leads. It can disconnect any of these leads and cross-connect any terminal by cord patching. Seven leads are permanently monitored by LED displays. Interface signal voltages greater than +3 V will turn on the LED displays. Also two independent LEDs can be patched to any interface terminal on either the modem side or the terminal side. One independent monitor turns on for a positive voltage $(+3 \mathrm{~V}$ to $+24 \mathrm{~V})$ and the other for a negative voltage $(-3 \mathrm{~V}$ to $-24 \mathrm{~V})$. The input impedance of each display is $33,000 \Omega$. All the terminals can be probed with an external meter or oscilloscope. The unit is battery powered by two AA cells and weighs $3 / 4 \mathrm{lb}$ with batteries.

CIRCLE NO. 277

## PC data modems are Bell-202C compatible

Intertel, Inc., 6 Vine Brook Park, Burlington, Mass. 01803. (617) 273-0950. Mod. 2020: \$330; Mod. 2021: $\$ 440$ (unit qty); stock.

A pair of modems provide Bell202C compatible operation at speeds to 1200 baud over dial-up telephone lines. The Model 2020 is Bell-202C5 compatible, while the Model 2021, which includes a fivebaud reverse channel, is Bell202C6 compatible. Standard features in both models include an-swer-back tone generation, automatic answering, receiver squelch, and soft-carrier turn off. They are constructed on a $4.5 \times 9.0 \mathrm{in}$. (Model 2020) and $6.25 \times 9.0 \mathrm{in}$. (Model 2021) PC card.

CIRCLE NO. 278

## innovation yesterday

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The electric iron was a wonderful invention. It made one of life's little chores easier. So did the Digiswitch and the Miniswitch when Digitran developed them in 1959. They're easier and more efficient to use. Nice large digits. And your equipment will look better too.
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## TEE LED LITE

## TRANSISTOR CONTROLLED LED INDICATOR SAVES PC BOARD SPACE

If you're driving LED's directly from IC outputs you're wasting valuable PC board space! TEC's new L-1017 Series LED Indicator requires $1 / 10$ th the current (only 1.6 ma ) of ordinary LED's (using 15 ma ) - lets you drive one L-1017, plus nine other loads from one IC logic circuit. Transistor driver and LED are housed in a .284 dia. x $1^{\prime \prime}$ long body that mounts in a 194" panel hole on $1 / 2^{\prime \prime}$ centers. Wire-wrap terminals standard. L-1017 Series turns on with "high" input, logic " 1 " (IC driven LED's indicate logic " 0 "). L-1017 signals: $0 \mathrm{~N},+2.5$ to +5 VDC ; OFF, 0 to +0.8 VDC . Supply: +5 VDC @ 15 ma , maximum. Special lens design increases LED brilliance and side viewing - available in red or clear, spherical or $2 \times$ SIZE flat top lens style.
Immediate Del. \$2.30 ea., 100-499 qty.


ACTUAL SIZE

## DIRECT DRIVE LED INDICATOR

 L-1015 Series LED Indicator is identical to L -1017 Series above in all mechanical details. Internal resistors allow operation directly from 5.0, 6.3, 10 and 24 VDC supplies - permit LED indicator substitution for incandescents.Immediate Del. Price, \$1.30 ea., 100-499.

## TEC LED LITE INDICATOR PACKAGES <br> 

Select a rugged, long life LED indicator or switch/indicator from more than 20 TEC designs. More brilliance, easier mounting, attractive appearance are added to the various LED's packaged by TEC.

See TEC-LITE for the complete line of readouts, indicators, switches, display panels, keyboards, CRT terminals.
TEC, Incorporated; 9800 North Oracle Road, Tucson, Arizona 85704; or phone (602) 297-1111



## DATA PROCESSING

Calculator family uses single MOS/LSI chip

Texas Instruments Inc., P.O. Box 5012, M/S 84, Dallas, Tex. 75222. (214) 238-3741. See text.

TI's line of electronic calculators uses a MOS/LSI calculator-on-a-chip integrated circuit to provide complete four-function capability: add, subtract, multiply and divide. The TI-3000 desk calculator (under \$85) uses an eight-digit gas-discharge readout; the TI-3500 (under \$100) desk model uses a 10 -digit gas-discharge readout and provides chain or constant operation as well as two, four or floating decimal point. The TI-2500 portable calculator (under \$120) has an eight-digit LED display.

CIRCLE NO. 300

## Precision electronic clock is TTL compatible



Thiem Industries, Inc., 1918 W. Artesia Blvd., Torrance, Calif. 90504. (213) 321-1911. From \$299; 45 days.

Using a precision time source, the Digitime Series M clock provides time-of-day, elapsed time, or count-down/up time, with a display and TTL-compatible logic outputs. The clock has a 24 -hour time range with outputs in hours, minutes, tenths of minutes, seconds and tenths of seconds. A digital calendar companion unit can extend this range to days and months or elapsed days of the year.

CIRCLE NO. 301

Line printer adjusts speed to line width


Data Printer Corp., 201 Vassar St., Cambridge, Mass. 02139. (617) 492-7484.

Computer output printers, Models 236 and 306, accept almost any size paper and print at 200 to 600 lines $/ \mathrm{min}$. The printing rate is varied automatically according to the length of the printed line. Model 236 prints a 132 -column line at 200 lines/min, a 96 -column line at 300 lines $/ \mathrm{min}$ and a 48 -column line at 600 lines $/ \mathrm{min}$. The 306 prints 132 -column line at 130 lines $/ \mathrm{min}$ and 72 column line at 600 lines/ min. Both models handle paper widths from $3-1 / 2$ to $19-1 / 2$ in. and a six-part form.

CIRCLE NO. 302

## Minicomputer claims many firsts



Prime Computer, Inc., 17 Strathmore Rd., Natick, Mass. 01760. (617) 655-6999. $\$ 7100$ (basic); Nov., 1972.

The PRIME 200 claims lots of firsts. It is the first small computer to employ only a MOS memory, eliminate all wires within the mainframe, employ $100 \%$ microprogrammed logic and offer both microdiagnostics and parity on every byte of information as standard features. The unit is a parallel 16 -bit computer with a $750-\mathrm{ns}$ cycle-time memory that is expandable in 8 k increments to 32 k words. It has a complement of 124 instructions.

CIRCLE NO. 303


You can upgrade your data com systems to handle virtually any
data transmission scheme at any rate up to 640 kilobits per second. With the new Western Digital Programmable Synchron-ous-Asynchronous Transmitter (P/SAT) and Receiver (P/SAR).

Because both chips are LSI subsystems, they're ideal for any equipment from computers and concentrators to modems and peripherals. They'll save you time and money on hardware, space, power, system design and manufacturing.
And because the P/SAT and P/SAR are both programmable, they'll give you all the flexibility your systems need. A single command defines all the transmission parameters, so you're not locked into synchronous, asynchronous or even isochronous operation. (In the isochronous mode, your equipment can transmit asynchronous data through synchronous equipment at up to 640 kilbaud rates.) A new command is all it takes to change the transmission scheme.
For easy system interface, our P/SAT (part PT1482A) and P/SAR (part PR1472A) are fully TTL-compatible; provide three-state inputs, outputs and status flags for bus compatibility; and are double-buffered.

They're the most powerful, most versatile data com chips available. Contact us now for all the details. Western Digital has more data communications subsystem chips, standard and custom, working in more applications than anyone. We'd like to show you why. Western Digital Corp., 19242 Red Hill Avenue, Newport Beach, Calif. 92663 . Tel: 714/557-3550.


## Staggered fingers let case-mounted semi's work harder in less space



Now you can safely operate such devices as TO-3, TO-6, TO-66, plastics, at many times their bare case power rating using our patented Staggered Finger dissipators. We've got over 70 different models with dissipation capabilities ranging from 3 to 35 watts in natural convection, up to 125
watts in forced air. Why are they better? Staggered Finger design increases dissipating surface, cuts re-radiation, and produces turbulence in forced air. Send for catalog. IERC, 135 W . Magnolia Blvd., Burbank, Calif. 91502 , a subsidiary of Dynamics Corporation of America.

## IERC



## Heat Sinks

## INFORMATION RETRIEVAL NUMBER 71

# Optima Instrument Gases: versatile and colorful. 

Designed for optimum usability, these hardy cases are available in 72 sizes for both 19 and 24 inch, panel-mounted instruments. Select from hundreds of twocolor combinations of a tough, good looking, vinyl finish. Get information on the complete Optima line of cases, racks
and consoles. Write Optima Enclosures, Division of Scientific-Atlanta, Inc,, 2166 Mountain Industrial Boulevard, Tucker, Georgia 30084. Or call (404) 939-6340.



CMOS shrinks DPM power input to 100 mW


Datascan, Inc., 1111 Paulison Ave., Clifton, N.J. 07013. (201) 4782800. See text; 30 days ARO.

Datascan has developed the first DPM using CMOS technology. The 100 mW power input of the Model 820 ( 4.8 to 8 V at 20 mA , nom.) is said to be at least eight times lower than any other DPM now on the market. Typically, four D-cells can power the unit for at least 600 hours, making it ideal for portable applications. As for specs, the meter features $0.3-\mathrm{in}$. LED readouts, $100 \%$ overrange, auto-polar operation, an accuracy of $\pm 0.1 \%$ $\pm 1$ digit, a drift of $0.008 \% /{ }^{\circ} \mathrm{C}$ max. and a reading rate of five per second. A specially designed input stage eliminates zero drift and keeps the bias current to less than 1 nA . The basic unit has a 100 mV FS range, with $5000 \mathrm{M} \Omega$ input impedance. Full scale ranges up to 100 V are also available, as are current shunts. In addition to low-power consumption-less than $10 \mu \mathrm{~W}$ for all the digital circuitrythe CMOS circuits result in a wide range of input supply voltagefrom 3 to 15 V -high noise immunity of one-half the input voltage and a temperature range of -15 to +70 C . The unit is housed in a $3.5 \times 2.5 \times 1.3-\mathrm{in}$. aluminum case. The front bezel, which contains a red tinted filter, measures $1.5 \times 3.2 \mathrm{in}$. Mounting and insertion into the panel is from the front. Prices should start at about $\$ 200$ for one unit and reduce to approximately $\$ 150$ each for 100 meter quantities. It is anticipated that future prices will be reduced by $50 \%$.

CIRCLE NO. 304

# The second edition of our microprogramming handbook is more revealing than the first 

The original edition made many microprogramming concepts public for the first time. Now we have an expanded 450-page version with information on the Micro 1600, the mini that makes microprogramming irresistible. Our new paperback covers microprogramming from the most elementary stages all the way up to descriptions of complex computer architecture and special purpose applications.

Everything you need to know is there. Tutorial primer. Glossary of computer technology. Application information. Computer users' reference material. Complete details on hardware, software, and firmware for the Micro 800 and Micro 1600. Even if you already have the first edition, you'll value the second. Send for a copy today. It's free. Write to Microdata Corporation, 17481 Red Hill Ave., Irvine, Calif. 92705, (714) 540-6730.

## Microdata




Counter-timer plugs into 7000-series scope


Tektronix, Inc., P.O. Box 500, Beaverton, Ore. 97005. (503) 6440161. \$1475.

The 7D15 universal counter/timer is a scope plug-in. Signals from the scope can be used to arm and control the unit. The measured signal can be seen on the CRT, along with the measurement interval and the counter Schmitt trigger signal. Through various scope plug-ins, signals can be preconditioned. There are eight modes for this dcto $-225-\mathrm{MHz}$ unit: time interval, time interval averaging, period, multiperiod, frequency, frequency ratio, totalize and manual stop watch. Resolution is 10 ns in singleshot time measurements, and is 100 ps in time-interval averaging. The CRT READOUT displays a full eight digits.

CIRCLE NO. 305

## Monitor plug-ins allow in-shop measurements

Motorola Communications and Electronics, Inc., 1301 E. Algonquid Rd., Schaumburg, Ill. 60172. (312) 358-7900.

A full line of newly developed "off-the-air" plug-ins are now available for the Motorola S1327A Series service monitor. With better than $20 \mu \mathrm{~V}$ sensitivity, these preselectors allow the technician to make frequency and deviation measurements of distant FM communication transmitters right from the shop or service van. Lowband 25 to $50-\mathrm{MHz}$ vhf and highband 145 to $175-\mathrm{MHz}$ vhf units are presently available with 406 to $420-\mathrm{MHz}$ uhf and 450 to $512-\mathrm{MHz}$ uhf units to be available in the near future.

## MESSAGE FOR DADDIES

Get yourself a good, thorough examination once $\alpha$ year. Once a year, let your doctor really look you over. It'll take $\alpha$ little time, and a little patience. And maybe he'll poke around a little more than you'd really like. And so he should.
The whole idea is to keep you healthy. If nothing's wrong (and more than likely, there isn't) hooray! Come back next year. But if anything's suspicious, then you've gained the most important thing of all: time.

We can save l out of 2 persons when cancer is caught in time, caught early. That's a good thing to know. All Daddies should know how to take care of themselves so that they can have the fun of taking care of their kids. Don't be afraid. It's what you don't know that can hurt you.

AMERICAN CANCER SOCIETY

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Materi PC pads are the fixerimontacts of the relay

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Send for Test Sample and PC Board Preparation Aids to simplify design and production of your module.
For action write or call 212-EX 2-4800.

## Portable DMM offers 2-1/2 digits for $\$ 170$

Dynascan Corp., 1801 W. Belle Plaine Ave., Chicago, Ill. 60613. (312) 327-7270. \$169.95.

The Model 281 DMM features include a 2-1/2-digit display with automatic decimal point, $100 \%$ overrange, full overload protection, positive overrange and wrong polarity indication, high sensitivity, $1 \%$ accuracy and $10 \mathrm{M} \Omega$ input impedance. The 281 's 32 ranges include dc and ac volts ( $0-1 \mathrm{kV}$ ), dc and ac current $(0-10.0 \mathrm{~A})$ and resistance $(0-10.0 \mathrm{M} \Omega)$. All ohms ranges are constant-current. Dc accuracy is $\pm 1 \%$ of reading, $\pm 1$ digit. On the lowest ac $V$ and dc $V$ ranges, the 281 reads 100 mV FS. And test voltage on the lowest ohms ranges is only 100 mV .

CIRCLE NO. 307

## Rf power amplifier delivers 10 W into $50 \Omega$



RF Communications, Inc., (part of PRD Electronics), 1680 University Ave., Rochester, N.Y. 14610. (716) 244-5830.

A new rf power amplifier is introduced by PRD Electronics, Inc. The Model RF-825 is a broadband power amplifier designed for highpower applications in the $10-\mathrm{Hz}$ to $10-\mathrm{MHz}$ frequency range. It requires no tuning and no adjustments. The amplifier provides a conservatively rated 10 W output into $50 \Omega$, but can deliver over 15 W when higher levels of drive are applied. The output impedance is less than $3 \Omega$. The RF-825 can also be operated to 20 MHz , and beyond, with reduced output. Significant features are the selectable $40 / 60$ dB gain and selectable input impedances of $50 \Omega$ or $1 \mathrm{M} \Omega$. Harmonic levels at rated output are typically 50 dB down, and 30 dB down at 15 W .

## How the tomous  good



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RFL Industries, Inc.

## INSTRUMENTATION

## Portable DMM offers 3-1/2 digits for \$345



United Systems Corp., 918 Woodley Rd., Dayton, Ohio 45403. (513) 254-6251. \$345; stock.

A portable DMM, the Model 262 C , has a $3-1 / 2$-digit LED display and a basic accuracy of $0.05 \%$. The unit operates from the 60 cycle line or with an integral eighthour battery. A self-check battery status indicator is also integral. The complete unit is housed in a rugged all-metal case.

CIRCLE NO. 320

## Two-channel recorder gives choice of plug-ins

Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, Calif. 94304. (415) 493-1501. Starts at \$1740; stock.
A choice of input plug-ins, a new ink system, and carbide-tipped, stainless-steel pens are the hallmarks of a new two-channel oscillographic recorder. Three preamplifier plug-ins are available with sensitivities of: $1 \mu \mathrm{~V} / \mathrm{div}$. with a differential, floated and guarded input; $1 \mathrm{mV} /$ div. with a differential, balanced to ground input; and $20 \mathrm{mV} / \mathrm{div}$. with a single-ended input. Chart width is $50 \mathrm{~mm}, \mathbf{2 5 \%}$ wider than available on other comparably priced recorders. Overshoot is less than $2 \%$. Chart speeds are from 1 to $125 \mathrm{~mm} / \mathrm{sec}$. Frequency response over 50 divisions ( 55 mm ) is $\pm 2 \%$ of FS from dc to 40 Hz . Rise time measured at the pen, from $10 \%$ to $90 \%$ of 50 mm , is 7.5 ms for the Model 17400 A plug-in, and 7.0 ms for the Models 17401A and 17402A.

CIRCLE NO. 321

## 512-MHz freq counter accepts 10 mV input



Ballantine Laboratories, P.O. Box 97, Boonton, N.J. 07005. (201) 3350900. $\$ 895$.

This frequency counter, a 512MHz , nine-digit instrument, is said to measure in one-fourth to onetenth the time of similar instruments. The Model 5700A counts directly to 200 MHz and pre-scales by two only for its $512-\mathrm{MHz}$ operation. The counter measures from 5 Hz to 512 MHz and offers a sensitivity at 500 MHz of 10 mV rms. The instrument also offers an optional frequency offset capability which enables the user to offset the counter's displayed frequency by a fixed number of cycles from the frequency being measured.

CIRCLE NO. 322
 SWITCHING
 PROBLEM!

[^9]
# Our 256-track star is ready to run. 

Librascope's new L107B Disc Memory racks up a new record in the low-cost compact field. Just $9^{\prime \prime}$ by $14^{\prime \prime}$, this head-per-track memory comes with either 128 or 256 data tracks. Packs up to 18 million bits of random-access storage. And weighs just 25 pounds.

Like its smaller running mate, the L107A (up to 7 million bits), the L107B has championship stamina. Long life, no routine maintenance, high reliability. And rugged. It can take 10 G shock, without isolators, while operating in any position.

What's more, the big-capacity L107B is completely compatible with the smaller L107A's electronics. If you need more memory, simply upgrade from the A to the new B model by making a small space adjustment.

For full data on this front-runner, call (213) 245-8711. Or write to Librascope Division of The Singer Company, 833 Sonora Avenue, Glendale, California 91201.

## SINGER <br> AEROSPACE \& MARINE SYSTEMS

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## Liquid-crystal display operates on low power



Radionics Laboratory Inc., P.O. Box 211, Kingston, N.J. 08528. $\$ 50$ (unit qty).

Liquid crystal readouts are effective in applications that need to conserve power. A three-digit unit draws only $10 \mu \mathrm{~A}$ at $30-40 \mathrm{~V}$ and 20 Hz to 10 kHz . Suitable IC CMOS drivers are available from several manufacturers. Standard display units include $3,3-1 / 2$, and 8 -digit models. Each digit is separated by a decimal point. Since tooling costs are moderate, specials are easily fabricated.

CIRCLE NO. 323

Tiny potentiometer has large size performance


New England Instrument Co., 14 Kendall Lane, Natick, Mass. 01760. (617) 873-9711.

High accuracy ( $0.35 \%$ absolute linearity) found in larger units is featured in NEI's new Filmpot. Some specs include: special linearities to $0.15 \%$, a resistance of $250 \Omega$ to $130 \mathrm{k} \Omega$, low inertia, low torque (less than 0.1 oz -in.), a power rating of $1 / 2 \mathrm{~W}$, and a temperature range of -65 to +125 C . Key features of this subminiature pot are its small case size, less than $1 / 2 \times 1 / 2 \mathrm{in}$., and a life of over 10 -million revolutions, per MIL-R39023.

CIRCLE NO. 324

Slide switch provides 60 dB rf isolation


Stackpole Components Co., P.O. Box 14466, Raleigh, N.C. 27610. (919) 828-6201.

Stackpole's SS-156 DPDT highseparation slide switch provides signal isolation in excess of 60 dB (vs 40 dB for conventional switches) between the switch sections. The isolation is measured at approximately 70 MHz . In one application, a video source or antenna is selected as input to a receiver. Switch isolation is sufficient to insure that acceptable FCC levels of radiation from the antenna are not exceeded, when the video signal is selected.

CIRCLE NO. 325


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24400 Highland Rd., Richmond Hts., 0. 44143


Latching relay has separate release coil


American Zettler, Inc., 697 Randolph Ave., Costa Mesa, Calif. 92626. (714) 540-4190. From $\$ 2.41$ (2500); stock.

The Thinpak series latching relay requires no continuous power to remain energized. Designated the AZ 630 , the relays use two separate coils to eliminate polarity reversing problems when unlatching. The units are available with coil voltages from 6 to $60-\mathrm{V}$ de and their operate and release times are less than 3 ms . They have PCstyle terminals with $0.1-\mathrm{in}$. grid spacing. Height is less than 0.5 in .

CIRCLE NO. 326

## Posistor replaces start device in ac motors



Murata Corp. of America, 2 Westchester Plaza, Elmsford, N.Y. 10523. (914) 592-9180.

Posistor semiconductors eliminate the need for mechanical centrifugal switches and start relays in single-phase ac motors. The Posistor unit is connected in series with the starter winding. Initially it has very low resistance allowing maximum current to flow. After the motor comes up to speed, the resistance increases to isolate the starting winding from the power source. Available units cover the $120 / 220 \mathrm{~V}$ and 7 to 12 A ranges.

CIRCLE NO. 327

Mercury switch can take tough environments


Compac Engineering, Inc., 398 Commercial St., San Jose, Calif. 95112. (408) 286-4844.

Mercury tilt switches in Compac's new line are actuated by a change in position or velocity. Electrodes are sealed in a glass envelope which has been evacuated, purged and back-filled with an inert gas to prevent oxidation and to provide a stable contact resistance. The mercury is purified. These sealed switches are particularly suited to industrial, dirt-prone environments.

## For 29ф - How Good Can This New Reed Relay Be?



Our new 1A Series offers Mil Q 9858A specs at the lowest commercial prices: Hermetically sealed coils - Magnetically shielded for high density packing Switches 1 amp or 250 V @ 20 watts - Coil voltages of $1,3,5,6,10,12, \& 24$ volts - Contact resistance less than 100 milliohms - Micro size $.275^{\prime \prime}$ OD x . $950^{\prime \prime}$ long.
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## COMPONENTS

## Read-after-write head uses Hall effect



Pioneer Electronics Corp., Components \& Data Products Div., 525 W. Remington Dr., Sunnyvale, Calif. 94087. (408) 245-3511. \$66.60 (100299); stock.

The Model RAW-7201 magnetic reading head is a dual-gap, Halleffect type that generates an output signal proportional only to the recorded magnetic flux. Output is totally independent of tape speed. This feature eliminates the need for additional circuitry to compensate for output-level variations at different tape speeds. The unit has read-after-write capability and is designed for 0.15 in . cassette applications.

CIRCLE NO. 328

## Variable-speed motors provide 3\% regulation

Sequential Information Systems, Inc., 249 N. Saw Mill River Rd., Elmsford, N.Y. 10523. (914) 5925930. From $\$ 150$; 30 days.

A variable-speed motor line features permanent-magnet dc motors with built-in, solid-state controllers. Known as Pak-Drives, the systems' feedback circuits maintain set speed to better than $3 \%$ from no-load to full rated torque, $10 \%$ line voltage variations, and a 30:1 speed range. Even at low speeds the motors deliver smooth and full torque. Systems with better regulation and wider speed ranges are also available. The units are transformer isolated for short circuit and shock prevention. Four sizes with ratings of $1 / 24,1 / 12$, $1 / 8$ and $1 / 4 \mathrm{hp}$ provide a torque range of 14 to $110 \mathrm{oz}-\mathrm{in}$.

## Thermoplastic terminal block resists breaking

Curtis Development and Manufacturing Co., Inc., 3266 N. 33rd St., Milwaukee, Wis. 53216. (414) 4451817. See text.

Thermoplastic terminal blocks offer freedom from breaking, unlike the more common phenolic models. The thermoplastic material is more pliable than thermosetting plastics. And thermoplastic materials can be molded faster, thereby providing lower costs- $7 \phi$ per pole in volume purchases. The new block is available with one to 25 poles on $7 / 16$ in. centers. A 6-32 screw draws on an internal nut assembly to clamp wires, eliminating the need to put lugs on wires. A variety of back terminations are available, including PC, wire-wrap and quick-disconnect types. The block is rated at 300 V and 10 A .

CIRCLE NO. 330

## Solid-state rectifiers replace high-volt tubes

Electronic Devices, Inc., 21 Gray Oaks Ave., Yonkers, N.Y. 10510. (914) 965-4400.

The R-2AV2 Solid-Tube is a solid-state rectifier for direct, plugin replacement of vacuum tubes 2AV2, 2BA2 and 1V2 in color television receivers. Four additional solid-state units replace a total of 25 vacuum tubes. The units eliminate a potential source of X-ray radiation, run cool and are longer lived than the vacuum tubes. Peakinverse voltage is 9 kV , peak-repetitive forward current is 100 mA , average forward current is 5 mA . Voltage drop at 5 mA is 20 V .

CIRCLE NO. 331

## Thermal cut-off switch protects transformers

Micro Devices Corp., 1881 Southtown Blvd., Dayton, Ohio 45419. (513) 294-0581.

A Microtemp installed on a transformer winding can directly sense excessive temperature to interrupt the power and prevent damage. A second version provides a quicker-acting way to sense overload. It uses a small heating element fitted to the sensor. The heating element is, connected in series with the secondary-coil winding.

Active CMagnetic Pickup provides Digital Output


Airpax scores with the "all-in-one" Digital Output Transducer that gives you the drive you need for digital circuitry.

When used in proximity to gears or other ferrous discontinuities, the D.O.T. features:

- TTL output independent of powersupply voltage ( 5 to 15 V dc ) and load impedance.
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- Operation up to $50,000 \mathrm{~Hz}$.
- Oil and moisture resistant.
- Reverse polarity protection on power supply.
- Wide temperature range.
- No mechanical linkage required.


## Applications:

- Speed sensing and control
- Synchronizing
- Counting
- Positioning
- Flowmetering
- Timing

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## All American Line

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INFORMATION RETRIEVAL NUMBER 86

## MIIDA MODEL 6354 31/2 DMM with 0.1\% DC ACCURACY. <br> - $0.1 \%$ (DC) and $0.5 \%$ (AC) voltage accuracy ■ Measures

 to 200 megohms resistance 45 Hz to 20 kHz frequency response - Range anddisplay hold mode - Automatic polarity display

Panel-mount socket accepts LEDs, lamps


Data Display Products, 8036 Westlawn Ave., Los Angeles, Calif. 90045. (213) 641-1232. \$0.65; stock.

A panel-mount socket provides front-panel replaceability for FanIn Series panel lights, which have a built-in lamp driver with an optional built-in lamp test. The sockets may also be used with the company's standard line of LEDs, incandescent, and neon $1 / 4 \mathrm{in}$. dia. panel indicators. The sockets are designed for no-mistake contact between light cartridge and socket; they are keyed so that the light will only start into the socket in the correct position, and no rotation in the socket is possible.

CIRCLE NO. 333

## Capacitor packages reduce board space

Engineered Components Co., 2134 W. Rosecrans Ave., Gardena, Calif. 90249. (213) 321-6565.

Multi-Cap Packages contain various combinations of values of either metallized-polyester or metallized-polycarbonate capacitors in sizes ranging from $385-\mathrm{mils}$ wide by $225-$ mils high in lengths of from 300 mils (with two capacitance values) to 1.40 in . (in $100-\mathrm{mil}$ increments) which will accommodate up to 13 capacitance values. They are also available in a double width case 800 -mils wide $\times 225-$ mils high with a length of 1.400 in . which could accommodate a maximum of 26 capacitance values. This packaging concept greatly reduces the board space required for a given set of components while reducing the number of parts that must be handled.

Edge-card connector has 60 -contact positions


Berg Electronics, Inc., York Expwy., New Cumberland, Pa. 17070. (717) 938-6711.

A crimp-to-wire edge-card connector provides mechanical termination of wire in the No. 30 to No. 34 AWG range. The connector has 60 contact positions in a doublerow housing with $50-\mathrm{mil}$ centerline spacing. The connector has a $1-\mathrm{A}$ rating; it features polarization and card guides.

CIRCLE NO. 335

## Metal core PC board also dissipates heat

International Electronic Research Corp., 135 W. Magnolia Blvd., Burbank, Calif. 91502. (213) 849-2481.

A single-sided metal core circuit board serves as a heat-dissipator and structural-member, often acting as the complete power supply chassis. The metal circuit board is coated with Insultek, which displays the seemingly contradictory characteristics of high dielectric strength and high electrical surface resistance while exhibiting quite high thermal conductivity.

CIRCLE NO. 336

## Laminated ribbon cable features 3-mil accuracy

Berk-Tek, Inc., P.O. Box 60, Reading, Pa. 19607. (215) 376-8071.

A laminated ribbon cable has the conductor spacing controlled to $\pm 3$ mils to assure consistent and reproducible electrical characteristics. The cable is claimed to be available to any length and number of conductors in a wide range of insulations, sizes and color coding.

CIRCLE NO. 337


# Having P/C repro problems? Maybe your artwork is too fat. 

What you see here is a clay model representing art made with die-cut symbols. To your eye your artwork doesn't look like that. But the lens of the repro camera picks up the buildup of those die-cut symbols and tape.

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You can apply Mecanorma symbols with pinpoint accuracy, too. Unlike die-cut symbols, Mecanorma comes in handy transparent strips which give you rapid and precise positioning. No sticky situations, either. The symbols and the carrier sheet can actually come in contact with your work surface without the symbol sticking before you want it to.

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Newport Laboratories Inc., 630 East Young Street, Santa Ana, California 92705 (714) 540-4914.


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The Standard in monolithic crystal filters.

## evaluation samples

## Scope tracer blanks

Oscilloscope signals can be recorded in color, faster and less expensive than with photographic techniques. Trace records can be in black, red and green using a normal single color oscilloscope. Direct positive record on a clear plastic film base with high contrast results in excellent electrostatic copies. Because of the clear base, the oscilloscope signals can be projected using overhead projectors or large slide projectors. The recording blanks are reusable or can be permanent. The cost per record is up to $80 \%$ less compared with photographic records. JTEM Scope Tracer.

CIRCLE NO. 340

## Heat transfer washers

The A26-2020 aluminum oxide ceramic washer exhibits a very low thermal impedance and a breakdown voltage of 1000 V minimum. A low thermal resistance ( 0.15 C/W) insures that the full power capability of the semiconductor can be used. The thermal performance is approximately five times better than mica and is comparable with an equivalent hard anodized aluminum washer. Jermyn.

CIRCLE NO. 341

## Rotary shaft seal

A spring loaded Teflon cartridge seal is designed for miniature applications where very low friction, compact size, long trouble free life, chemical inertness, and indefinite shelf life are necessary. The seal consists of a Teflon sealing ring, a stainless steel holding ring (or mild steel) and an elliptical garter spring with canted coils that can deflect over $50 \%$ of its original size upon loading. Pressures can be withstood from vacuum to 250 psi, temperatures from -65 to 400 F , and speeds to 5000 rpm depending on pressure, temperature and size. Bal Seal Engineering Co.

CIRCLE NO. 342

## Dc power supplies

Regulated voltage, constant current dc power supplies come in voltages from 2 to $\pm 15 \mathrm{~V}$ and currents from 60 mA to 1 A . Volt sensor voltage comparators are also available. Requests will be followed by a questionnaire to determine qualifications before the sample is sent. California Electronic Manufacturing Co., Inc.

CIRCLE NO. 343

## Epoxy resin

A solventless low-viscosity epoxy resin casting system is particularly effective for large mass castings because of its low shrinkage and low exortherm on cure. Tra-Cast 3010 can be used to impregnate finely wound coils and motor stators, or to coat printed circuits, or to embed transformers, modules, connectors and other types of circuitry where a low residual strain is needed in order to maintain the integrity of the embedded unit. Tra-Con, Inc., Resin Systems Div.

CIRCLE NO. 344

## Terminal-insulating pod

Ultrapod, an ultrasonically applied insulating pod, offers a new method of insulating preterminated receptacles that is reported to be faster or less expensive than any conventional method in use today. The preformed pod is fed from continuous reels, folded over the terminal, ultrasonically sealed, and cut off in a single cycle of the automatic tooling which can apply insulating pods at rates up to 2000 per hour. AMP Inc.

CIRCLE NO. 345

## Molding material

Designed for use in automotive, appliance and computer/communication applications, a thermosetting molding material costs $45 \phi / \mathrm{lb}$. A two-stage glass-and-mineral filled phenolic, the material is formulated to provide high heat, strength and electrical properties, plus good dimensional stability. Hooker Chemical Corp., a subsidiary of Occidental Petroleum Corp.

## EXAR'S IC WAVEFORM GENERATOR SPEAKS FOR ITSELF



Yes, the XR-205 monolithic waveform generator speaks for itself in a cost-saving way that systems designers can understand.

Just one Exar XR-205 device provides sine, square, triangle, ramp and sawtooth waveforms without additional active components. By adding a second XR-205, you can create amplitude, frequency or phase modulated varieties of these basic waveforms. So two XR-205 for $\$ 16.00$ each, in 1-24 quantities, are able to replace comparable large discrete waveform generators selling from $\$ 200$ to $\$ 1300$. At the same time they greatly reduce system weight, complexity and power consumption.

Waveshaping is accomplished by a unique monolithic diode-resistor combination with only about 2 percent total harmonic distortion. The harmonic content of the sinusoidal output is relatively independent of the modulation input and frequency of operation.

Here's how the XR-205 works: each chip contains three separate logic sections. A Voltage-Controlled OscilIator section with a frequency range of 0.1 Hz to more than 10 MHz generates the basic waveforms by means of an emitter-coupled multivibrator. A Balanced Modulator provides the wave-shaping and amplitude modulation. A highly linear four quadrant analog multiplier is used for suppressed carrier modulation as well as for conventional double sideband AM generation. A Buffer Amplifier provides a low impedance output with high current drive capability.

To get you started at a modest cost, ask us for the XR-205K Waveform Generator Kit. The kit sells for $\$ 28.00$ in small quantities and is comprised of two XR-205 devices, a drilled and etched circuit card, and detailed assembly and hookup instructions.

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## application notes

## Logic probes

A series of application notes describes the application of two inexpensive digital test instruments to the diagnosis of specific logic functions. One measures logic levels and the presence or absence of pulses and pulse trains. The other determines period, pulse width, and delay of pulses and pulse trains in digital logic circuitry. Advanced Digital Research Corp., Mountain View, Calif.

CIRCLE NO. 347

## Thermoforming glossary

A twelve-page glossary of the most commonly used terms and techniques in plastic sheet thermoforming contains over 85 thermoforming definitions as well as illustrations of the eleven most common thermoforming techniques. In addition there is a section on the advantages and limitations of thermoforming. Harva Co., Inc., Schoharie, N.Y.

CIRCLE NO. 348

## Dual channel encoder

Application report TRN-104 "The Dual Channel Encoder" describes the required logic to be used with a dual channel incremental encoder to perform the functions of direction sensing, pulse multiplying, and mechanical noise cancellation. Various logic schemes are presented with associated diagrams to perform the required actions. Trump-Ross Industrial Controls Inc., Billerica, Mass.

CIRCLE NO. 349

## Data communications

"Sherry's guide to data communication buzz words" is as delightful as it is enlightening. Sprinkled among the more serious definitions of common and not-socommon terms used in data communications, are salted away some puns, comments and photographs of Sherry. They add zest. International Communications Corp., Miami, Fla.

CIRCLE NO. 350

## Self-extinguishing resin

Cycoloy ABS alloy, Grade KHP, a recently developed self-extinguishing thermoplastic resin with good molding and extrusion characteristics, is the subject of a 16page technical report. The bulletin ( $\mathrm{P}-177$ ) points out that Cycoloy KHP not only meets the UL SE-0 rating for self-extinguishing characteristics of $60-\mathrm{mil}$ thickness, but also provides the balance of engineering properties-impact strength; high temperature, chemical, and abuse resistance; and ease of molding-required in automotive, appliance, toy, electrical, electronics, communications, business machines and other applications demanding high-performance plastic resins. Borg-Warner Corp., Marbon Div., Washington, W. Va.

CIRCLE NO. 351

## Disc drive formatting

A 32-page application note shows how to design a sectored format for the company's disc drives. The application note provides an analysis of the parameters within a disc drive which contribute to sector format considerations. Each parameter is discussed and algebraic expressions are provided. Included in the application note are formulas for calculating a sector to provide information needed to determine sector timing requirements for specific applications. A table is supplied which gives calculated results for various sector, disc speed and data rate values. Pertec Corp., Chatsworth, Calif.

CIRCLE NO. 352

## Valve sizing guide

A 12-page bulletin on valve sizing simplifies the usually difficult task of sizing a valve. The information is contained in easy to use tables. Given the specific gravity of the product and the pressure drop through the valve, the appropriate valve capacity can simply be read from the table. In addition, a brief introduction describes the pressure drop (and how to calculate it) in clear and elementary terms. Foxboro Co., Foxboro, Mass.

CIRCLE NO. 353


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| RF 12 D 500 | $\pm 12 \mathrm{Vdc} @ 500 \mathrm{~mA}$ | 65.00 |
| RF 12 D 1000 | $\pm 12 \mathrm{Vdc}$ @ 1 A | 88.50 |
| RF 15 D 500 | $\pm 15 \mathrm{Vdc} @ 500 \mathrm{~mA}$ | 65.00 |
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new literature


Minicomputer peripherals
A 12-page catalog covers the general product areas of each of the company's four divisions. A brief description of the company, specifications on typical magnetic tape input/output systems, a modular data acquisition system, digital cassette drives and mass memory devices, all with software and interfaces to a wide range of minicomputers are included. Datum Inc., Anaheim, Calif.

CIRCLE NO. 354

## Tape transports

The 2900/2800/2500 disc-drive systems and the 8420 tape transport, 8803 controller and 2007 drive are described in three pieces of literature. Speed and cost savings for each of the units, all of which are designed as IBM-compatible peripherals, are featured. Mohawk Data Sciences Corp., Herkimer, N.Y.

CIRCLE NO. 355

## I/O interface

A six-page brochure explains how interfacing a minicomputer to your process is no longer a custom engineering problem. Included is a compilation of condensed technical data and prices for the various products comprising the $\mathrm{a} / \mathrm{d}$ line of products. Computer Products, Fort Lauderdale, Fla.

## Switches and keyboards

"Design Engineer's Switch and Keyboard Selector Guide" describes more than 200 standard types of snap-action switches, matrix selector switches, thumbwheel and leverwheel switches and electronic data entry keyboards. The pocketsize, two-color brochure has 14 pages of illustrations, brief descriptions, sizes and electrical ratings for quick, easy reference. Included with this short-form catalog is a listing of offices in Europe, Australia, Japan, as well as Canada and the U.S. Cherry Electrical Products Corp., Waukegan, Ill.

CIRCLE NO. 357

## Switching, control devices

An expanded line of switching and control devices is fully illustrated in a 50 -page catalog. Descriptive photos, diagrams, drawings, comparative specs, dimensions, applications and prices of over 300 items in 12 product categories are included. Some of the products featured include liquid level switches, miniature relays, mercury plunger relays, mercury tilt switches, pressure switches, reed relays, security switches, magnets, keyboard switches and legends. All items listed are in stock for immediate delivery. Compac Engineering, Inc., San Jose, Calif.

CIRCLE NO. 358

## Minicomputer biography

The biography of a minicomputer developed for use as an OEM system component is documented in a 16 -page brochure. The fourcolor document describes the development, features and applications of the company's 16 -bit NAKED MINI 16 and ALPHA 16 minicomputers and the philosophy that led to their creation. In addition to minicomputer performance and specifications, the brochure details the exhaustive quality assurance procedures that contribute to the products' reliability. Computer Automation, Inc., Irvine, Calif.

CIRCLE NO. 359

## THE HANDS-OFF 500 MHz METER.



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Our Model 6421B Frequency Meter direct-counts up to 500 MHz . That's right ... 500 MHz direct count.

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INFORMATION RETRIEVAL NUMBER 105

NEW LITERATURE


## TI multiple semis

A multiple semiconductor cata$\log$ highlights all the product capabilities of the company's smallsignal transistor department. The 96-page catalog covers devices with more than one chip in a package. The booklet also details the packaging of diodes and transistors in IC compatible packages. The products discussed include duals, quad transistors, Darlington pairs, dou-ble-emitter choppers, diode arrays and diode matrices. Interchangeability guides are provided. Other silicon small-signal low-power transistor numerical listings are shown along with a 1 N listing of silicon diodes. Texas Instruments Inc., Dallas, Tex.

CIRCLE NO. 360

## Panel mount switches

Five basic double-pole snap action switches and one single-pole switch with "push-push" alternate action, are illustrated and described in a four-page, two-color brochure. Included is complete ordering information (part numbers and prices). Cherry Electrical Products Corp., Waukegan, Ill.

CIRCLE NO. 361

## Magnetic tape recorders

A short form catalog describes representative examples of the company's line of magnetic tape recorders/reproducers, transducers, recording oscillographs, computer peripherals and associated equipment. The 12 -page color document contains photographs and brief, informative descriptions of the various products. Bell \& Howell's Electronics \& Instruments Group, Pasadena, Calif.

CIRCLE NO. 362

## Pushbutton switches

A lighted pushbutton switch line-offering low power, 1 A or less, multifunction switching-is described in a four-color bulletin. The eight-page publication contains complete specifications, performance data, line drawings, photographs and a lamp selection guide. Oak Industries, Inc., Switch Div., Crystal Lake, Ill.

CIRCLE NO. 363

## Test instrumentation

Detailed descriptions and specifications for the company's complete line of design and lab instrumentation are provided in a 36 -page catalog. Included are standard electronic test and measurement equipment-frequency counters, scopes, generators, digital voltmeters, power supplies, strip chart recorder systems and modules and digital instrumentation. Also included is the Model EU-801E minicomputer interface, complete digital systems, individual modules and plug-in logic circuit cards. Also in the catalog is the Heath/Malm-stadt-Enke Laboratory Stationa complete teaching/learning environment for all phases of electronics. Heath/Schlumberger Scientific Instruments, Benton Harbor, Mich.

CIRCLE NO. 364

## Thermal digital printer

TP-10 thermal digital printer and TP-10M plug-in print module are described in a two-page, twocolor bulletin. The bulletin includes feaures, capabilities, paper, options, specifications and prices of both units. PPM, Inc., Subsidiary of Torq Engineered Products, Inc., Bedford, Ohio.

CIRCLE NO. 365

## Switch applications

"Uses Unlimited" describes the growing use of precision snap-action switches. Three unusual switching ideas submitted by readers are also featured in the eightpage publication. Two pages describing trends in limit and enclosed switches and reviewing the packaging varieties available in rotary switches complete the issue. Micro Switch, a div. of Honeywell, Inc., Freeport, Ill.

CIRCLE NO. 366


Type RS assemblies:
Widest choice available from any source
Almost any relay or timer can be mounted in these compact assemblies that feature printed circuit boards of glass epoxy G-10. Barrier-type screw, or clamp-type terminal blocks are highly break-resistant. Terminals are permanently numbered for quick identification. Highest quality sockets provide great conductivity, relay retention, and protection. Up to 24 sockets snap in/out of $48^{\prime \prime}$ pre-punched vinyl trackusing only 2 or 3 mounting screws.
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INFORMATION RETRIEVAL NUMBER 107

## NEW Heathkit 2½ Digit DMM



## only 7995*

A compact, solid-state multimeter with digital readout - at a fantastic kit-form price. The new Heathkit IM-1202 has four overlapping ranges to measure voltages from 10 mV to 1000 V on DC (either polarity), 10 mV to 700 V rms on $\mathrm{AC}, 10 \mathrm{uA}$ to 2.5 A on AC or DC current. Five resistance ranges measure from 1 ohm to 2.5 megohms. Front panel polarity switch reverses inputs without changing leads. 6 lbs.
NEW Heathkit 30 MHz Counter


## only $169^{95^{*}}$

The Heathkit IB- 1100 gives 1 Hz to over 30 MHz counting on a full 5 -digit readout with 8 -digit capability. The lighted overrange indicator makes misreading virtually impossible. Stable time-base circuitry assures accuracy better than $\pm 3 \mathrm{ppm}$ from $22^{\circ}$ to $37^{\circ}$ C. Diode protected J-FET gives improved triggering over 100 mV to 150 V input range. Solid-state circuitry mounts on one large board for easy assembly. 6 lbs .

## Send for FREE '73 Catalog



INFORMATION RETRIEVAL NUMBER 109

# MORE! new V-PAC* POWER SOURCES for Op amps - Line receivers - Comparators 



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Operate from 7 to as many as 35 of these types from a single V-PAC source: 709, 741, 710, 711, 720.

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## NEW LITERATURE

## Thumbwheel switches

Circuit diagrams, specifications and other important features of the company's 545 series of thumbwheel switches are described in bulletin SW 6120. Typical applications include computers, business machines, automatic machine tools, test equipment, process controls, telemetering and utility controls. Dialight Corp., Brooklyn, N.Y.

CIRCLE NO. 367

## Counters and controls

Precision counters and counter systems are covered in a 36 -page catalog which includes a comprehensive section of technical information and application tips. Hecon Corp., Eatontown, N.J.

CIRCLE NO. 368

## Modems

Models 1038 and 1039, a pair of Bell 103F compatible integral modems capable of asynchronous full duplex operation up to 300 baud on two-wire private lines, are described in a four-page brochure. Included are descriptions, detailed theory of operation and specifications. An outline drawing of the PC card is included as well as a block diagram and a data mode timing diagram. Intel, Santa Clara, Calif.

CIRCLE NO. 369

## Digital clocks

A bulletin covers the Series 30,000 IC digital clocks, which feature a positive logic in addition to the negative logic. Also included are a description of the standby power feature and a price list. Chrono-Log Corp., Broomall, Pa .

CIRCLE NO. 370

## Magnetic test system

The Delta 400 stored program magnetic test system is described in a 36 -page brochure. The brochure includes sections on cost advantages, operating data, data acquisition and software details-as well as comprehensive application notes. Computest Corp., Cherry Hill, N.J.

CIRCLE NO. 371

## Power supplies

A "Design As You Order" cata$\log$ allows the user to specify and order custom single or multiple output power supplies at standard prices. A simple-to-use order form is provided with each catalog. The complete power supply ordered is provided in an encapsulated miniature package that is pre-tested and ready for use. Arnold Magnetics Corp., Culver City, Calif.

CIRCLE NO. 372

## Modular data comm system

A modular approach to adding low-cost IBM-compatible data communications capabilities to either an existing or future system is described in an eight-page brochure. The brochure contains a functional description and suggested design configurations for the data communications modules. They include a data communications formatter, a peripheral controller and associated control panel, a MOS buffer board and related peripheral formatters and equipment. Pertec Corp., Chatsworth, Calif.

CIRCLE NO. 373

## Disc memories

A complete series of head per track disc memories is presented in a six-page brochure. These memories range in capacity from 0.5 megabits to 65 megabits. The literature describes track configuration, access time, data rate, electronic circuits and features a logic diagram. Alpha Data, Inc., Canoga Park, Calif.

CIRCLE NO. 374

## High-level logic family

To keep pace with the progressive enlargement of the company's high-level logic family, the H100 series, an applications handbook has been published that sets out numerous suggestions for the use of these noise-free elements. An introduction to the logic, a presentation of the range and operation of the elements and some 50 applications are included. Copies of this handbook are available at $\$ 3$ each. SGS-ATES Semiconductor Corp., 435 Newtonville Ave., Newtonville, Mass. 02160.

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## bulletin board

Interdata has a limited two-forone offer to universities and colleges using minicomputers for classroom training purposes. Under the terms of the offer, which expires Dec. 31, 1972, purchase of an Interdata Model 70 would carry with it at no extra cost an additional control store board that converts the general-purpose Model 70 into an Interdata Model 50 data communications processor. When sold separately, both the Model 70 and the Model 50 carry a single unit list price of $\$ 6800$ with 8 -k bytes of core.

CIRCLE NO. 375
National Beryllia Corp. has issued an "odd lot" parts and price list of obsolete Berlox and alumina plates, discs, rods and tubes. All parts are first-quality material which can often be adapted to experimental and pilot plant requirements. Available in limited quantities only, the parts are priced below the normal cost of precision ceramics.

CIRCLE NO. 376

## Price reductions

Optical Electronics, Inc., has reduced prices on its entire line of analog function modules and operational amplifiers. The price reduction averages $20 \%$ across the line and in some specific cases is as much as $68 \%$. Some examples are: the Model 3329 has been reduced from $\$ 79$ to $\$ 29$, the Model 3337 has been reduced from $\$ 65$ to $\$ 32$ and the Model 5734B has been reduced from $\$ 215$ to $\$ 73$.

CIRCLE NO. 377

## Raytheon Co.'s Special Microwave

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## vendors report

Annual and interim reports can provide much more than financial-position information. They often include the first public disclosure of new products, new techniques and new directions of our vendors and customers. Further, they often contain superb analyses of segments of industry that a company serves.

Selected companies with recent reports are listed here with their main electronic products or services. For a copy, circle the indicated number.
Statham Instruments, Inc. Medical electronics.

CIRCLE NO. 379
Sola Basic. Air traffic control systems, transformers, meters, switches, communication systems and electronic controls systems.

CIRCLE NO. 380
Sparton Corp. Military electronics, transducers, switches and wave analyzers.

CIRCLE NO. 381
Harris-Intertype Corp. Electronic communications equipment, semiconductors, radio and broadcast equipment, control systems, aerospace, computers and memory systems.

CIRCLE NO. 382
Sanders Associates, Inc. Military electronics, electro-optics, radar, computer terminals and distributed processing systems, microwaves and switches.

CIRCLE NO. 383
Bell Industries. Ceramic capacitors, precision components, electronic systems and graphic arts.

CIRCLE NO. 384
New Hampshire Ball Bearings, Inc. Ball bearings and subassemblies.

CIRCLE NO. 385
National Semiconductor Corp. Discrete semiconductors, ICs, LEDs.

CIRCLE NO. 386
Scientific-Atlanta, Inc. Telecommunications, CATV, antennas and microwave test instrumentation.

CIRCLE NO. 387


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INFORMATION RETRIEVAL NUMBER 116


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