

DEV-->0  
ID\*4

STATREC OFFSET	BUS ID	X'FF'	DATA TYPE
STATREC OFFSET	H REG ID	L REG ID	DATA TYPE
STATREC OFFSET	LINE ID	NOT USED	DATA TYPE

BUS  
ENTRY

DATA TYPE COD

0 FLAG (1 BIT)

4 REG (8)

8 ADDR (16)

12 REG PAIR (16)

16 LINE (1)

0 MIDEVTAB (1 ENTRY PER DEVICE)

SYMBOLIC DEVICE NAME	DEV ID
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0 MISYMTAB (1 ENTRY PER SYMBOLIC NAME)

REG CALL DYNAMO
REG DATA DYNAMO

REG-->0  
ID\*4 +2

LINE-->0  
ID\*4 +2

MIREGTAB (1 ENTRY PER REG)      MILINTAB (1 ENTRY PER LINE)

LINE IMAGE DIR OFF
BRITE FLG   SUBBLK CNT

BUS-->0  
ID\*8

BUS SUBLK DUMMY PTR
SOURCE REG ID   DEST REG ID
TOTAL BUS DATA PATH LENGTH (1+2)
BUS DATA PATH LENGTH (2)

MIBUSTAB (1 ENTRY PER BUS)

FIGURE 1: STATIC TABLES IN MIDASCB

```

0
+2 NEXT LINK (0 IF NULL)
+4 INTERP INCREMENT BUS SEC1
+6 INTERP INCREMENT BUS SEC2
+8 DEST REG DYNAMO DATA LENGTH
+10 BUS SEC1 SUBIRK DATA OFFSET
+12 ->BUS SEC2 SUBIRK DUMMY
SOURCE REGISTER CALL DYNAMO
DYNAMIC ANIMATION LIST -- BUS ELEMENT

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(LEN1*INTVAL)/TOTL
INTVAL-INCR1
(2-REG, 4-ADDR)

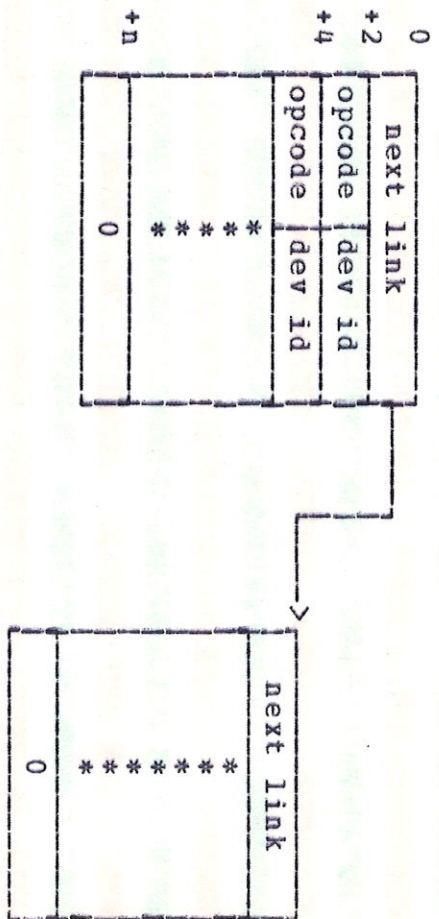
```

```

>0
+2 NEXT LINK (0 IF NULL)
+4 0
+6 high order reg call dynamo
low order reg call dynamo
dynamic animation list -- reg element

```

figure 2: dynamic animation list



display order blocks

opcode	normal action	realtime action
01 - brite	brighten line	same
02 - dim	dim line	same
03 - flash	change reg contents and flash	same but no flash
04 - change	change reg contents	same
05 - move	flash source reg, and change contents, move data to dest reg, chg dest reg.	chg source, dest regs
06 - noop	no-operation	same
00 - eod	end of block	same

figure 3: midas display orders