UNIVAC 90/60, 90/70, and 90/80

UNIVAC's large-scale 90/80 computer system is now available in three models, the 90/80-2, 90/80-3, and 90/80-4. The 90/80-3 provides about the same performance level as the earlier single-model 90/80, while the 90/80-2 provides about 25 percent less performance. The 90/80-4 is a high-performance version that includes a high-speed buffer (cache) memory and provides about 35 percent greater performance than the 90/80-3.

The 90/60, 90/70, and 90/80 mainframe computer systems make up UNIVAC's large-scale system offerings. The Series 90 family is a byte-oriented, IBM-compatible group of computers that was originally introduced to compete with IBM's System/370 Models 135 through 158. The principal operating system for these high-end models is the VS/90 virtual memory system. UNIVAC has kept pace with IBM's 303X processors by introducing the 90/80-2, 90/80-3, and 90/80-4 systems.

MANAGEMENT SUMMARY

The roots of UNIVAC's Series 90 family extend back to the company's 9000 Series systems and to the Spectra 70 product line which UNIVAC purchased from RCA in 1972. The UNIVAC 90/60 and 90/70 computers were introduced in October 1973, followed by the 90/80 in June 1976. In between, in June 1974, UNIVAC added another member to the Series 90 family, the small-scale 90/30 (see Report 70C-877-04).

The 90/60 and 90/70 systems were designed to provide a compatible upgrade path for users of UNIVAC's maturing 9000 Series and Series 70 (ex-RCA) systems, and were also aimed at the large number of IBM System/360 Model 30 and Model 40 users.

With the 90/80 system, the largest member of the family introduced to date, UNIVAC provided a clear path for its own users to upgrade from 90/70, 90/70, and large-scale ➢

CHARACTERISTICS

MANUFACTURER: Sperry Univac Division, Sperry Rand Corporation, P.O. Box 500, Blue Bell, Pa. 19424. Telephone (215) 542-4011.

MODELS: UNIVAC 90/60, 90/70, and 90/80. The 90/80 is further divided into Models 90/80-2, 90/80-3, and 90/80-4.

DATA FORMATS

BASIC UNIT: 8-bit byte. Each byte can represent 1 alphanumeric character, 2 decimal digits, or 8 binary bits. Two consecutive bytes form a 16-bit "halfword," four consecutive bytes form a 32-bit "word," and eight consecutive bytes form a 64-bit "doubleword."

FIXED-POINT OPERANDS: Can range from 1 to 16 bytes (1 to 31 digits plus sign) in decimal mode; 1 halfword (16 bits) or 1 word (32 bits) in binary mode. Certain operations use a doubleword (63-bit integer field plus sign) in binary mode.

FLOATING-POINT OPERANDS: Standard floating-point hardware provides for addition, subtraction, multiplication, division, loading, storing, and sign control of short or long format operands. The short format provides 24-bit precision and is represented by one word, which uses bit 0 for the sign, bits 1 through 7 for the exponent, and bits 8 through 31 for the fraction. Long format is represented with a doubleword which provides 56-bit precision; the long format is similar to the short format except that the fraction is contained in bit positions 8 through 68. A guard digit is carried by the hardware for intermediate "place holding" during addition/subtraction, multiplication, comparison, and halving. Extended-precision floating-point is available only on the 90/80.

INSTRUCTIONS: 2, 4, or 6 bytes in length, specifying 0, 1, or 2 main storage addresses, respectively.

INTERNAL CODE: EBCDIC or ASCII, depending upon setting of a mode bit in the program status word by certain processor instructions. The processor is sensitive to zone fields and edit control characters.

MAIN STORAGE

STORAGE TYPE: MOS (metal oxide semiconductor).

CAPACITY: 90/60—from 512K to 2048K bytes in 7 sizes: 512K, 768K, 1024K, 1280K, 1536K, 1792K, or 2048K bytes. ➢

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Series 70 systems. At the time of the announcement, UNIVAC also declared that another primary marketing target was those IBM System/370 Model 135 and 145 users who were looking to upgrade to Model 158's. However, less than a month after the UNIVAC 90/80 announcement, IBM announced the System/370 Models 138 and 148, which were bigger and more powerful systems than the 135 and 145, yet much lower in price.

UNIVAC answered the IBM challenge within two months, in late August 1976. The UNIVAC answer was direct and to the point—and very encouraging to the industry. The 90 Series systems were enhanced significantly, memory configuration changes were made, and sizeable price reductions went into effect.

The 90/60 system was affected the most by the changes. The minimum memory capacity was increased from 131K to 524K bytes and the maximum from 524K to 2 million bytes, while the processor speed was increased by 25 percent. Moreover, memory prices were slashed to the point that the new basic 90/60 Processor, with 524K bytes of main memory, could be purchased for 15 percent *less* than the old 131K-byte basic processor. A speed-up kit was also offered to existing 90/60 users—a move that paralleled the IBM 135-3 and 145-3 announcement and provided these users with the 25 percent increase in processor speed.

The maximum memory capacity of the 90/70 system was doubled from 1 megabyte to 2 megabytes, and mainframe and memory prices were also significantly reduced.

Prices for the 90/80 were also reduced dramatically to offset the effects of the IBM announcement. The initially announced rental prices for the 90/80's 524K-byte increments of main storage were reduced by 42 percent. Purchase prices for the same increments were reduced by 57 percent.

In October 1977, the original 90/80 processor was replaced with two new models, the 90/80-2 and the 90/80-3. These new models are intended to enhance the competition with IBM's 370/148 and to extend the 90/80's performance capabilities into the range of the IBM Model 3031 processor. The 90/80-3 is about equal in performance level to the earlier 90/80, and the 90/80-2 has about 25 percent less performance than the 90/80-3. The two new computer systems are based on similar processors, one with a machine cycle time of 130 nanoseconds (90/80-2), and the other with a machine cycle time of 98 nanoseconds. The most significant enhancement over the earlier 90/80 system was the use of 16K-chips in main memory, which permitted the consolidation of the central processor and peripheral processor into a single cabinet. The number of channels included with each basic system was also increased from two (one block multiplexer and one byte multiplexer) to five (four block multiplexer and one byte multiplexer).

At the same time, UNIVAC also introduced the 8450 high-performance disc subsystem. The 8450 dual disc

90/70—from 128K to 2048K bytes in 11 sizes: 128K, 192K, 256K, 384K, 512K, 768K, 1024K, 1280K, 1536K, 1792K, and 2048K bytes. 90/80-2—1024K or 2048K bytes. 90/80-3—2048K, 3072K, or 4096K bytes. 90/80-4—2048K, 4096K, 6144K, or 8192K bytes.

CYCLE TIME: 600 nanoseconds per 4-byte access in the 90/60 and 90/70; 450 nanoseconds per 8-byte access in the 90/80. The 90/80-4 also features odd/even interleaving; eight bytes are fetched from each of two banks on each memory cycle.

HIGH-SPEED BUFFER: The 90/80-4 contains a 32K-btye buffer made up of bipolar memory with an access time of 32 nanoseconds. The buffer is organized in two 16K-byte sections, one for instructions and one for operand data. On memory store operations, data is automatically written through to memory.

CHECKING: In all models, a single-bit error-correcting, double-bit error-detecting code is appended to each 32-bit word. Upon reading each word from memory, single-bit errors are automatically corrected and transferred, while multiple-bit errors are detected and flagged for appropriate program action.

STORAGE PROTECTION: The standard storage protection feature uses 16 keys to provide read and/or write protection for 2048-byte blocks of storage. An interrupt is generated whenever a read or write instruction is attempted in an unauthorized storage location. Storage protection is also provided through the virtual address structure, which does not allow users to map into each other's address space.

RESERVED STORAGE: The first 604 bytes of main storage in the 90/60 and 90/70, and the first 1024 bytes in the 90/80, are reserved to hold specific operating information.

CENTRAL PROCESSORS

REGISTERS: The programmer has access to sixteen 32-bit general registers that are used for indexing, base addressing, and as accumulators. (A second full set of 16 registers is used by the operating system.) Four double-word floating-point registers are standard.

DYNAMIC ADDRESS TRANSLATION: This feature, standard in all 90/60, 90/70, and 90/80 central processors, translates virtual storage addresses into real main memory addresses as each instruction is executed. The DAT feature is identical in both the 90/60 and 90/70 processors and incorporates a Content Addressable Memory (CAM) consisting of eight 32-bit registers. Addresses are 24 bits in length, and include a 4-bit block designator, a 4-bit segment designator, a 4-bit page designator, and a 12-bit byte designator.

The total addressable virtual memory space is 8,388,608 bytes, organized into a hierarchy of blocks, segments, pages, and bytes. A page consists of 4,096 bytes, one segment includes 16 pages, one block contains 16 segments, and the entire addressable virtual memory space is comprised of 8 blocks. Block, segment, and page tables are maintained in main storage for each executing program, the contents of which are used to construct physical main memory addresses for each instruction.

The CAM maintains the real page addresses for the eight most recently referenced pages, all of which can be examined concurrently within 30 nanoseconds. If a "CAM hit" occurs (in which the block, segment, and page designators of the instruction match a real page address in the CAM, the CAM page address in concatenated with the instruction's 12-bit displacement address to form the required real main memory address.

The DAT uses a three-table look-up procedure to develop real page addresses for instructions that are not found in the CAM. The block, segment, and page designators in the instruction point to locations in the block, segment, and

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CHARACTERISTICS OF THE UNIVAC 90/60, 90/70, AND 90/80 SYSTEMS

	90/60	90/70	90/80-2	90/80-3	90/80-4
SYSTEM CHARACTERISTICS					
Date of introduction	October 1973	October 1973	October 1977	October 1977*	May 1978
Date of first delivery					
Principal operating system	VS/9	VS/9	VS/9	VS/9	VS/9
Relative performance level	1.00	1.45	2.60	3.50	4.80
Monthly rental, basic system**	$7,894	$11,064	$20,665	$26,400	$32,535
Monthly maintenance, basic system	$1,094	$1,361	$2,100	$2,700	$3,300
MAIN STORAGE					
Type and Size	4K MOS	4K MOS	16K MOS	16K MOS	16K MOS
Cycle time, nanoseconds	600	600	450	450	450
Bytes fetched per cycle	4	4	8	8	8
Interleaving	No	No	No	No	2 to 1
Minimum capacity, bytes	524,288	524,288	1,048,576	2,097,152	2,097,152
Maximum capacity, bytes	2,097,152	2,097,152	2,097,152	4,194,304	4,194,304
Increment size, bytes	262,144 or 524,288	262,144	1,048,576	1,048,576	1,048,576
Error correcting	Yes	Yes	Yes	Yes	Yes
HIGH-SPEED BUFFER	None	None	None	None	
Type	—	—	—	—	Bipolar
Cycle time, nanoseconds	—	—	—	—	150
Capacity, bytes	—	—	—	—	32,768
PROCESSING UNIT					
Cycle time, nanoseconds	200	200	130	98	98
Floating-point arithmetic	Yes	Yes	Yes	Yes	Yes
Extended floating-point arithmetic	No	No	Yes	Yes	Yes
Decimal arithmetic	31 chars. max.	31 chars. max.	31 chars. max.	31 chars. max.	31 chars. max.
Programmable registers:					
User-programmable	16	16	16	16	16
Executive use only	16	16	16	16	16
Floating-point	4	4	4	4	4
Instruction Repertoire:					
Non-privileged 9400/9480	Yes	Yes	Yes	Yes	Yes
Non-privileged Series 70	Yes	Yes	Yes	Yes	Yes
Non-privileged IBM System/360	Yes	Yes	Yes	Yes	Yes
Non-privileged IBM System/370	No	No	Yes	Yes	Yes
Total instructions	144	144	154	154	154
Dynamic Address Translation (DAT):					
Type***	CAM	CAM	TLB	TLB	TLB
Translation time, nanoseconds (assumes DAT hit)	32	32	32	32	32
No. of page entries	8	8	32	32	32
Range of memory covered by DAT	32,768	32,768	131,072	131,072	131,072
I/O CONTROL					
No. of channels per system, maximum	5	6	8	8	8
Aggregate data rate, bytes per second	4,600,000	5,700,000	8,000,000	8,000,000	8,000,000
High-speed channels:					
Type of channel	Selector	Selector	Block mux.	Block mux.	Block mux.
No. of high-speed channels	1-4	1-5	4-7	4-7	4-7
No. of subsystems per channel	8	8	8	8	8
Data rate, bytes per second	1,100,000	1,100,000	1,500,000	1,500,000	1,500,000
Multiplexer channel:					
No. of subsystems	8-16	8-24	8-16	8-16	8-16
No. of subchannels	15	15	256	256	256
Data rate, bytes per second	175,000	175,000	175,000	183,000	183,000

*Original 90/80 computer was introduced in June 1976 and was equivalent in performance to the 90/80-3.
**One-year lease.
***CAM=Content Addressable Memory.
TLB=Translation Lookaside Buffer.

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drive employs two fixed, sealed disc modules and has a total capacity of 307 megabytes.

In May 1978, UNIVAC rounded out its high-end computer line with the introduction of the 90/80-4. The 90/80-4, due for initial delivery in early 1979, is claimed to provide 35 percent more performance than the 90/80-3. The company also claims that the new high-performance model will outperform the IBM Model 3031 by 16 to 20 percent.

The 90/80-4 employs the same basic 98-nanosecond CPU that is used in the 90/80-3 and includes the same five-channel group (one byte multiplexer and four block multiplexer channels) that is included with the basic 90/80-3 system. Main memory is made up of the same 490-nanosecond MOS memory.

The major enhancements of the 90/80-4 over the 90/80-3 processor include doubled memory capacity (eight megabytes compared to four megabytes) and the use of a 32K-byte high-speed buffer storage (cache memory). The high-speed buffer is divided into two equal partitions, one for instructions and one for operands.

Two-way memory interleaving of independent 1-megabyte modules has also been added. The 90/80-4 memory is composed of pairs of these independent modules, each of which can be addressed separately. By placing all odd-numbered addresses in one module and even-numbered addresses in the other, two-way interleaving is achieved. Under this new scheme, 16 bytes of data are fetched during each memory cycle.

The 90/60 and 90/70 incorporate the architectural features of the earlier UNIVAC 9700 system, but both are equipped with metal oxide semiconductor (MOS) main memory in place of the plated wire memory originally supplied with the 9700. MOS main memory first appeared in the UNIVAC product line in the 9480 computer, announced in March 1973, and has since replaced plated wire technology in the main memories of the newer models of UNIVAC's popular large-scale 1100 Series computers.

With the 90/80, UNIVAC continued its use of MOS main memory and introduced some new architectural features. The 90/80 is the first UNIVAC computer to use multi-layered printed circuit boards and emitter coupled logic (ECL) circuitry, and its main storage has a self-correcting capability using an error correction code (ECC) technique.

The announcement of the Series 90 family also brought indications from UNIVAC that a consolidation product would bring together the company's byte-oriented small-to-medium-scale systems and the incompatible large-scale, word-oriented 1100 Series computers. Early in 1978, UNIVAC announced the discontinuance of this development effort, stating that while such a system was possible, it could not be produced at a competitive price with present technology.

page tables maintained in main memory by the operating system for each program. UNIVAC estimates that the three memory accesses can be executed within 1.8 microseconds.

In the 90/80 system, the dynamic address translation mechanism is the translation lookaside buffer (TLB), which consists of thirty-two 32-bit registers. The real page addresses are entered in the TLB in groups of four, as opposed to the single-page entry method used in the 90/60 and 90/70. In all other respects, the DAT feature is the same in all three systems.

CONTROL STORAGE: In addition to main storage, a fast writeable control storage is available for the microprograms used to support integrated emulation, floating-point hardware, microdiagnostics, and the native-mode instruction set. The floating-point hardware is included in the basic prices. The cycle time of this separate MOS memory is 80 nanoseconds per 72-bit word access. Data is loaded into the writeable control storage via a cassette prepared by UNIVAC support personnel. An additional control storage module is available to support SMOOTH.

INSTRUCTION REPERTOIRE: In the 90/60 and 90/70, all 132 nonprivileged instructions of the IBM System/360 instruction set are provided. Also included are an add immediate instruction, an emulation aid instruction, and floating-point instructions. The standard instructions handle fixed-point binary arithmetic, decimal arithmetic using variable-length operands in packed formats, packing and unpacking, radix conversion, editing, loading, storing, comparing, shifting, branching, and logical operations, as well as instructions for handling ASCII or EBCDIC characters.

The 90/80 system native instruction set is upward-compatible with the 90/60 and 90/70 instruction sets. In addition, the 90/80 native instruction set includes all nonprivileged instructions of the IBM System/370 universal instruction set, plus extended floating-point capabilities and instructions unique to the 90/80 system.

INSTRUCTION TIMES: All times are for register-to-indexed-storage (RX) instructions, except where indicated, and are estimated in microseconds.

	90/60	90/70	90/80-2	90/80-3	90/80-4
Binary add/subtract (32 bits):	2.10	1.50	1.27	0.96	0.58
Floating-point add/subtract (short):	6.18	5.58	4.32	3.25	2.95
Floating-point multiply (short):	12.20	11.60	6.15	4.62	4.41
Floating-point divide (short):	29.10	28.50	15.42	11.58	11.19
Floating-point add/subtract (long):	6.83	6.23	5.23	3.94	3.55
Floating-point multiply (long):	35.60	35.00	22.58	16.87	16.75
Floating-point divide (long):	72.15	71.55	30.18	22.65	22.35
Add decimal (10-digit packaged data)*	16.20	15.60	8.47	7.76	6.52
Compare decimal (10-digit packed data)*	15.90	15.30	5.07	4.06	3.63
Pack decimal (10 digits)*	22.50	21.90	6.41	5.22	4.23
Branch on condition	1.20	1.20	0.67-1.15	0.40-0.84	0.26-0.53
Load (32-bit binary)	2.10	1.50	1.22	0.92	0.55
Store (32-bit binary)	2.40	1.80	1.37	1.07	0.63
Load multiple (six 32-bit registers)	7.50	6.90	2.54	2.13	1.77
Move (16 bytes)*	11.70	11.10	3.58	3.01	2.17
Compare (16 bytes)*	15.30	14.70	3.62	2.97	2.50

*Storage-to-storage instructions.

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The principal operating system for the 90/60, 90/70, and 90/80 systems is VS/9, an enhancement of the VMOS (Virtual Memory Operating System) that was developed for the Series 70 (ex-RCA) systems. The 90/60 and 90/70 can also employ the earlier OS/4 operating system which evolved from the UNIVAC 9400 Disk Operating System. OS/4 is still available from UNIVAC, but will not be enhanced in the future.

THE UNIVAC 90/70

The UNIVAC 90/70 was originally announced as the UNIVAC 9700 in November 1971 and was first delivered one year later, in Austria. Its reception in the U.S. was delayed as prospective users tended to hold off until the 90/70 could be delivered with the OS/7 operating system, a new operating system announced for the 90/60 and 90/70 computers. Early in 1975, UNIVAC surprised most industry observers by announcing the end of OS/7 development efforts in favor of adopting the most recent release of the ex-RCA Virtual Memory Operating System, renamed VS/9, as the principal operating system for the 90/60 and 90/70.

The 90/70 can be equipped with up to 2 million bytes of semiconductor main memory and has an instruction set comparable to that of the IBM System/360 Model 50, including floating-point arithmetic.

As it was originally announced, the 90/70 incorporated advanced features such as writeable control storage, relocation hardware, and indirect addressing. The relocation and indirect addressing features became redundant and were replaced by Dynamic Address Translation hardware when UNIVAC replaced OS/7 with the virtual memory operating system, VS/9. The Series 90 "DAT box" is based on the design of the original Content Addressable Memory (CAM) in the ex-RCA 70/7 and uses a 24-bit address consisting of a 4-bit block designator, a 4-bit segment designator, a 4-bit page designator, and a 12-bit byte designator to address any byte in main memory. Since the high-speed CAM maintains translation information on the most recently referenced pages, a "CAM hit" can be processed in 30 nanoseconds. According to UNIVAC studies, some 99 percent of all address translations are made in the CAM without main storage access. The writeable control storage is used for the microprograms that implement the system's emulation capabilities as well as its expanded instruction set and differs from that of the System/370 in that it does not use up any main storage capacity.

THE UNIVAC 90/60

When first introduced, the UNIVAC 90/60 was a scaled-down version of the 90/70, intended to serve as an upgrade system for users of smaller UNIVAC 9000 Series and Series 70 systems, such as the 9400 and the Series 70/35, 70/45, and 70/2, for which the 90/70 would represent too large a jump in performance and cost. In August 1976, however, the 90/60 was upgraded extensively to compete against the IBM System/370 Model 138.

The above instruction timings have been calculated assuming no channel interference. Instruction timings for the 90/80-4 also assume a 90-percent buffer storage hit rate.

EMULATION: Emulation features are available for IBM System/360 and 370 DOS and for UNIVAC Series 70 TDOS and DOS through the Spectra Mode of Operation (SMOOTH) hardware.

CONSOLE: The 90/60 and 90/70 system console consists of a keyboard with operator controls and a Uniscope 100 CRT display unit. The standard mode of operation provides for display of messages on the CRT screen; hard copy is provided by the console printer as an optional feature under VS/9, but is required for operation under OS/4. Under VS/9, those error messages which are printed on the console printer with OS/4 are written on direct-access storage for subsequent high-speed printing at the system manager's convenience. The hard-copy console printer operates at up to 30 cps and connects to the processor via the multiplexer channel; it uses one physical controller connection on the multiplexer. The system console can be switched by the multiple channel switch to operate on a selector channel for diagnostic purposes if required.

The 90/80 system console consists of a keyboard, a Uniscope 200 CRT 1920-character display unit, switches, and indicators housed in a cabinet that is separate from the peripheral processor. The system console communicates with the processor through the byte multiplexer channel, and includes all controls and indicators necessary to operate and monitor the operation of the system. The operator controls consist of an alphanumeric typewriter keyboard, cursor control keys, editing keys, control keys, and indicators.

As an optional feature, an incremental printer can be connected to the 90/80 system console to provide additional hard-copy output. It can be used to duplicate messages displayed on the visual display screen and to log informative messages that need not be displayed or responded to. The incremental printer, mounted in a separate cabinet, has a 96-character set (including upper case and lower case) and a 132-position print line, prints up to 200 characters per second, and has a paper feed rate of 30 lines per second.

The 9000 Series Channel Adapter, which provides either a multiplexer or selector interface to a UNIVAC 9200/9300/9400 or 90/60, 90/70, or 90/80 subsystem, is housed in the system console.

A multiple channel switch (MCS) is available to provide a capability for switching a subsystem or string of subsystems from a multiplexer or selector channel on one processor to the same type of channel on another processor or the same processor. Included with the basic MCS is cabinetry, a power supply, an operator's panel, and space for 5 additional MCS Expansion switches.

INPUT/OUTPUT CONTROL

I/O CHANNELS: The basic *90/60* Processor has one standard multiplexer channel. It can physically connect up to 7 low-speed systems and a multi-channel communications controller (MCC) for a total of 15 subchannel addresses. Two subchannel expansion features provide an expansion capability of addressing up to 63 subchannels. The maximum aggregate multiplexer channel transfer rate is 175,000 bytes per second.

One selector channel is standard on the UNIVAC 90/60, and three additional selector channels can be added. The 90/60 selector channels perform in the same manner as the 90/70 selector channels (below). The second selector channel is housed in the processor cabinet, and the third and fourth require the channel expansion cabinet.

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At the present time, there is little performance difference between the 90/60 and the 90/70.

Like the 90/70, the 90/60 offers an instruction repertoire that includes the complete IBM 360/50 set of instructions. The 90/60 also incorporates the same architectural features as the 90/70, including the DAT feature, writeable control storage, and MOS main memory. Originally, the memory range was different: a minimum of 512K bytes on the 90/60 versus 128K bytes on the 90/70. Later, the minimum memory for the 90/70 was also raised to 512K bytes. Maximum memory capacity for both systems is 2 million bytes.

THE UNIVAC 90/80

The UNIVAC 90/80 was announced at the National Computer Conference in June 1976, with first deliveries scheduled for the fourth quarter of 1976. (A Japanese version of the 90/80 system, known as the OUK 90-800, had been introduced two months earlier in Tokyo.) With the introduction of the 90/80, UNIVAC expanded its family of virtual memory systems from medium-scale into the large-scale range.

The minumum main memory size of a 90/80 system is 1024K bytes, and this can be expanded up to 4096K bytes in modules of 512K bytes. The error-correcting MOS memory has a cycle time of 450 nanoseconds per 8-byte access. The system is designed around two processors, an Instruction Processor and a Peripheral Processor, each with separate processing capabilities.

The Instruction Processor is the processing and control portion of the 90/80 system. It contains the sequencing and controls for interrupt action, timing facilities, initial program loading, and instruction execution.

The microprogrammed Peripheral Processor provides the input/output processing facilities for the 90/80. This design frees the Instruction Processor from handling input/output processing, thereby gaining the efficiencies of specialized design as well as the added benefits of distributing the central processing workload.

One major difference between the 90/80 processor and the 90/60 and 90/70 is the dynamic address translation mechanism employed. The 90/80 uses a translation lookaside buffer similar to that used in IBM System/370 processors.

The entry-level 90/80-2 system includes a 130-nanosecond CPU with 1024K bytes of main memory, a peripheral processor with one 183-KBS byte multiplexer and four 1500-KBS block multiplexer channels, system console, power distribution panel, and motor/alternator. Main memory is expandable to 2048K bytes, and the I/O subsystem is expandable to 16 channels through the addition of an F2011-00 interface extender. The peripheral processor provides 240 block multiplexer subchannels in the basic system and up to 496 subchannels in the expanded configuration. The 90/80-2 can be upgraded to 90/80-3

One multiplexer channel is standard is the *90/70*. It can physically connect up to 7 low-speed subsystem controllers and a multi-channel communications controller (MCC) for an aggregate of 15 subchannel addresses. The subchannel expansion feature provides an additional 16 subchannels. A second subchannel expansion features provides 32 more subchannels for a total of 63 subchannel addresses. The expanded interface feature can be added to provide up to 8 additional physical controller connections for a total of 16 controllers if the subchannel expansion feature has been added; otherwise, up to 7 additional controllers can be attached, not to exceed 15 physical subsystems. The maximum aggregate multiplexer channel transfer rate is 175,000 bytes per second.

One selector channel is standard on the UNIVAC 90/70, and four more can be added. Eight high-speed device controllers can be attached to each selector channel for a maximum throughput of 1.11 million bytes per second per channel on a data path 4 bytes wide. Each control unit can attach up to 16 I/O devices. Only one device can transfer data to or from main memory along a given selector channel at a time. Thus, simultaneous access of two or more high-speed devices requires that each be connected to a different selector channel. The second selector channel is housed in the processor cabinet, and selector channels 3, 4, and 5 require the channel expansion cabinet.

A Direct Control feature is available as an option supported by the user's own code. It is used to provide a special interface between two UNIVAC 90/60, 90/70, or 90/80 processors and includes two instructions for transfer of control information between the processors.

The Peripheral Processor provides the I/O processing facilities for the *90/80* system. It permits a maximum of eight I/O channels. The minimum 90/80 system includes one byte multiplexer channel and four block multiplexer channels. The maximum number of byte multiplexer channels is two per peripheral processor. The maximum number of block multiplexer channels is six, unless the second byte multiplexer channel is not selected; in this case, up to seven block multiplexer channels may be configured. Each block multiplexer channel has eight physical connections to which control units can be attached.

Data transfers between a 90/80 block multiplexer channel and main storage are 8-byte parallel. A block multiplexer channel, in conjunction with a control unit designed for block multiplexer operation, can disconnect and reselect devices between transfers of blocks of data within command chains. This operation permits concurrent execution of channel programs for several devices on one channel by multiplexing blocks of data. This capability applies only to nonshared subchannels of the block multiplexer channels; i.e., only one device is assigned per subchannel.

Subchannel storage in the 90/80 peripheral processor is pooled for block multiplexer channels and is expandable from the basic 240 subchannels up to 496 subchannels through the subchannel storage expansion feature. The subchannel storage pool provides 16 shared subchannels and 224 nonshared subchannels, expandable up to 480. Shared subchannels are assigned to devices at installation time. Nonshared subchannels are dynamically assigned as I/O operations are being initiated. Operations for which no subchannel storage is available are initiated and executed as if to a selector channel.

When operating with a shared subchannel, the channel does not disconnect for command chaining as is the case with the selector channel. However, when ending status is presented, the channel disconnects and becomes available to other channel devices. When the pool of nonshared subchannels has been exhausted or the block multiplexing

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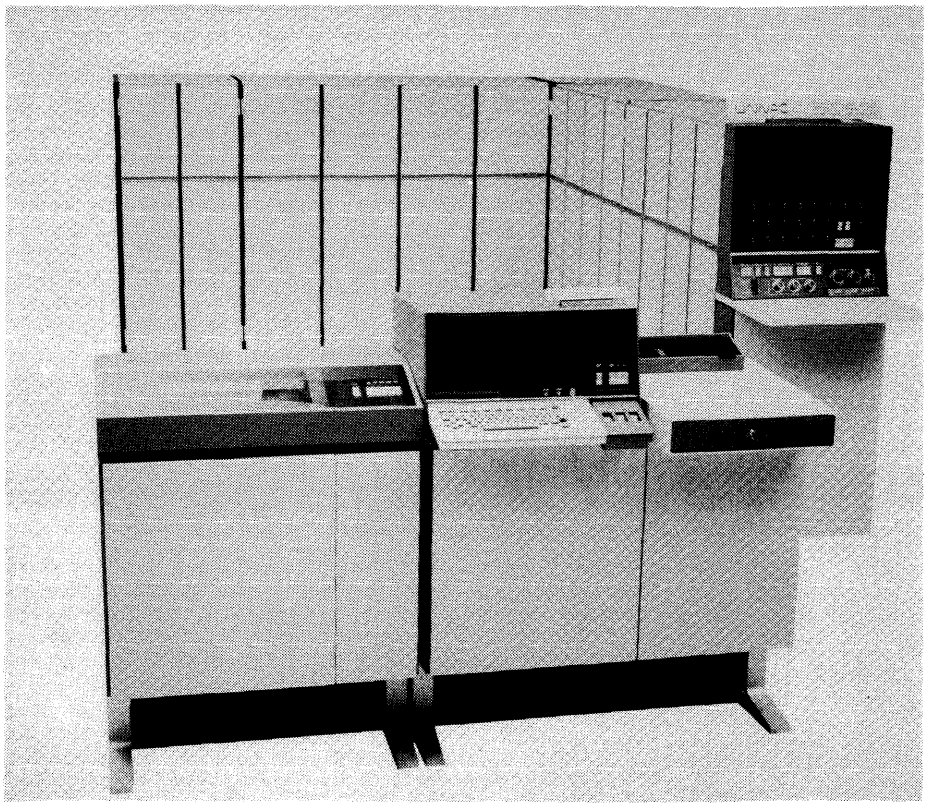

Univac's 90/60 represents the low end of the company's line of large-scale, byte-oriented computer systems. The 90/60 and the lookalike 90/70 compete with IBM's System/370 Model 148 and the Model 3031 processor.

status using the F2756-01 upgrade kit. Prices for the 90/80-2 start at $798,250 on purchase or $20,665 per month on a 1-year lease.

The 90/80-3 is nearly identical to the 90/80-2, differing only in processor cycle time and memory capacity. This higher-performance version features a machine cycle time of 98 nanoseconds, providing about 30 percent more internal performance than the 90/80-2. Its memory capacity is 4 megabytes, twice that of the 90/80-2. The basic 90/80-3 configuration includes the CPU with 2048K bytes of main memory, expandable to 4096K bytes, a peripheral processor with one 183-KBS byte multiplexer and four 1500-KBS block multiplexer channels, system console, power distribution panel, and motor/alternator. Like its smaller counterpart, the I/O subsystem for the 90/80-3 is expandable to 16 channels through the F2011-00 interface extension.

The performance of the 90/80-3 is estimated to be about 85 percent that of IBM's Model 3031 processor, while the basic system price is about 86 percent that of the 3031. Pricing for the 90/80-3 starts at $1,019,700 on purchase and at $26,400 per month on a 1-year lease.

The basic 90/80-4 system is priced at $1,256,000 purchase or $32,535 per month on a 1-year lease. The basic system includes the 98-nanosecond CPU, two megabytes of main memory, a five-channel group consisting of one byte multiplexer channel and four block multiplexer channels, the 32K-byte high-speed buffer, and a system console. Memory is expandable in 2-megabyte increments that are priced at $319,000 purchase or $7,000 per month. The

control bit in control register 0 is zero, and a START-I/O instruction is executed on the block multiplexer channel, the channel operates as a selector channel. The channel remains busy until the pending interrupt conditions are accepted by the instruction processor.

The 90/80 peripheral processor has one byte multiplexer channel provided with the basic system configuration. A second byte multiplexer channel can be added. Each byte multiplexer channel has eight physical connections to which standard control units (for such devices as a card reader, card punch, or line printer) and a multichannel communications controller can be attached. The number of physical connections to each byte multiplexer channel can be expanded to 16. Each byte multiplexer channel provides 256 nonshared subchannels, 128 for communications devices. The byte multiplexer has two modes of operation: multiplexer and control-unit-force-burst. In the multiplexer mode, the channel facilities are shared by a number of concurrently operating I/O devices, with the I/O interface being assigned to a control unit only long enough to transfer one byte of data. Upon completion of this data exchange, the I/O interface is available to another control unit requesting service, with this operation continuing until all units requesting service have been serviced. The operation repeats itself until all units are completely serviced. In the control-unit-forced-burst mode, the control unit stays connected to the I/O interface until normal termination is signaled by the control unit for the device (i.e., until the byte count goes to zero or the end of the record is detected). Data transfers between a byte multiplexer channel and main storage are 4-byte parallel.

CONFIGURATION RULES: On the 90/60 and 90/70, high-speed peripheral devices (tape and disk drives) must be connected to a selector channel. Up to eight control units can be connected to each selector channel, and up to 8 or 16 drives can be connected to each control unit. Low-speed devices, including the multi-channel communications controller, card readers, line printers, and the system

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I/O subsystem can be expanded by adding up to three additional block multiplexer channels. First customer deliveries of the 90/80-4 are scheduled for the first quarter of 1979. UNIVAC expects the 90/80-4 to outperform IBM's Model 3031 by 16 to 20 percent.

PERIPHERAL EQUIPMENT

Since the original announcement of the Series 90 systems, UNIVAC has added an attractive selection of fixed-head and removable disk pack drives to the product line-up. The new random-access storage devices are manufactured by UNIVAC's ISS subsidiary and are also available for the large-scale 1100 Series equipment.

The Model 8405 Fixed-Head Disc was announced in January 1975 as a replacement for the earlier 90/70 OSSF and has a capacity of 3.1 million bytes per fixed disc or 24.8 million bytes per eight-drive subsystem. In contrast to the OSSF, the 8405 subsystem is available for all Series 90 systems, primarily for use as a high-performance paging storage for VS/9. The IBM 3330-equivalent 8430 Removable Disc System was also announced in January 1975, while the double-density (200-million-byte) 8433 Removable Disc System was announced in July 1975. Along with the large-capacity 8433 discs, UNIVAC also announced a double-capacity version of the fixed-head disc drive, the 8405-00 Fixed-Head Disc Subsystem, which can store 6.3 million bytes of data per unit. All of the new direct-access devices are served by a common microprogrammed control unit that allows various combinations of fixed-head and removable disc storage to be configured to suit each installation's direct access storage requirements and also supports state-of-the-art features such as command retry and automatic error detection and correction.

The 8450 Dual Disk Drive, announced in October 1977, is a dual-drive, fixed-media unit, each drive having a capacity of 153 megabytes. In addition, 1 million bytes of fixed-head storage can be added to each spindle. Average head positioning time is 23 milliseconds, and average rotational delay is 8.3 milliseconds (3600 rpm). The data transfer rate for the ISS-manufactured unit is 1.26 million bytes per second. The 8450 subsystem requires a 5040-95 controller that controls up to sixteen 8430, 8433, or 8450 disk drives (eight dual-drive units).

In addition to the 8405 Fixed-Head Disc, which can be used as an extremely fast dedicated backing store for VS/9, the 8425, 8430, 8433, and 8450 Disc Drives can also be used as VS/9 backing stores. When moving-arm direct-access storage devices are used, paging storage and data storage can be intermixed on the same disc drive and paging storage can be spread over several devices to minimize contention.

The medium-speed Uniservo 14 Magnetic Tape Unit was announced for the UNIVAC 90/30 computer system in March 1975 and joined the larger Series 90 peripheral line-up in July 1975. It has a data transfer rate of 96,000 bytes per second when recording in the 1600-bit-

console, are normally connected to the multiplexer channel, which can accommodate up to 16 control units and 63 subchannel addresses.

On the 90/80, high-speed peripheral devices must be connected to a block multiplexer channel, and low-speed devices to a byte multiplexer channel. Each block multiplexer channel connects up to eight high-speed subsystems. Up to seven block multiplexer channels are available (one standard and six optional). The byte multiplexer channel connects up to eight subsystems, including the operations console, and is expandable through an option to connect up to 16 subsystems. A second byte multiplexer is optionally available. The seventh block multiplexer channel and the second block multiplexer channel are mutually exclusive.

SIMULTANEOUS I/O OPERATIONS: Concurrently with computing, the 90/60 and 90/70 processors can control multiple I/O operations with a combined data rate of up to 175,000 bytes/sec on the multiplexer channel, plus one I/O operation with a data rate of up to 1.1 million bytes/sec on each selector channel.

In the 90/80 system, the microprocessor of the 90/80 Peripheral Processor controls channel operations. Once the Instruction Processor initiates an I/O operation, the channel is able to execute this operation independently. Data transfers between peripheral devices and main storage can be performed by all channels concurrently. Control logic is provided to monitor the data transfers among the channels and to assist in servicing the many conditions that can occur during the transfer of data. The maximum aggregate I/O data transfer rate of a peripheral processor is 8 million bytes per second. The maximum byte multiplexer channel data transfer rate is an aggregate of 183,000 bytes per second. The maximum block multiplexer channel transfer rate is 1.5 million bytes per second.

MASS STORAGE

8405 FIXED-HEAD DISC SUBSYSTEM: Provides very fast access to up to 50.3 million bytes per subsystem stored on non-removable head-per-track discs. The average rotational delay is 8.34 milliseconds, and the maximum is 16.67 milliseconds. Each 8405-00 disc unit can store 6,291,456 bytes of information on 12 recording surfaces. There are 72 tracks per recording surface (including 8 spare tracks) and 864 tracks per spindle (including 96 spares). Each track has a capacity of 8,192 bytes. The data transfer rate is 622,000 bytes per second.

The 8405 Disc Subsystem uses the microprogrammed 5039 control unit, which can control a mixture of 8405 fixed-head discs and 8430 and 8433 disc pack drives. An F2076 8405 fixed-head disc feature is required for attachment of up to eight 8405 units in single-unit increments. The 5039 control unit performs command retry and automatic error detection and correction.

8425 DISC DRIVE: A double-density version of the earlier 8414 disc drive, the 8425 stores 58 million bytes per disc pack. Each IBM 2316-compatible pack has 406 tracks on each of the 20 surfaces used for data recording. Each track can contain up to 7,294 eight-bit bytes. Average arm positioning time is 30 milliseconds, average rotational delay is 12.5 milliseconds, and the data transfer rate is 312,000 bytes per second. A 5024-99 controller is used to control up to eight drives, for an on-line capacity of 466 million bytes. Options for the 8425 include dual access (which is used on each drive when two controllers on separate selector channels are employed) to provide read/write simultaneity on any two drives), and dual channel (two channel connections for the same controller, with access controlled by an operator's switch).

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per-inch phase-encoded mode and a rental price of $1,393 (including maintenance) for a 9-track dual-drive subsystem, approximately 10 percent less than a two-drive Uniservo 12 configuration.

In May 1976, the Uniservo 30 Series was introduced. These magnetic tape drives are high-performance units that feature Group Coded Recording (GCR) at a density of 6250 bits per inch, as introduced by IBM back in March 1973. There are five models in the series with data transfer rates ranging from 160,000 to 1,250,000 bytes per second.

UNIVAC has also made substantial modifications to the communications capabilities of the 90 Series systems as a result of the release of VS/9. At the time of the initial announcement of VS/9, UNIVAC released versions of the Series 70 CCM (Communications Controller Multi-Channel) to support VS/9 communications software on the Series 90 systems. The CCM, however, had a limited capacity of 48 half-duplex lines and a maximum transmission speed of 300 characters per second. Then, in July 1975, UNIVAC announced a new Multi-Channel Communications Controller (MCC) as a replacement for the CCM. The MCC is actually a version of the UNIVAC 3760 Controller that emulates the functions of the CCM and supports up to 128 half-duplex or 64 full-duplex communications lines. The MCC also has a peak throughput capacity of 25,000 characters per second and can concurrently support up to eight different line speeds ranging from 45 to 56,000 bits per second.

SOFTWARE SUPPORT

When the 90/60 and 90/70 computers were introduced, the primary operating system was to be OS/7, which was announced in November 1971 and scheduled for delivery in March 1973. But the development efforts encountered numerous difficulties, causing the operating system's delivery to be slipped by nearly a year. A version of OS/7 was demonstrated on the 90/60 system when it was announced in October 1973, but the full-fledged operating system with all its features implemented was still somewhere off in the future. For the most part, new 90/60 and 90/70 accounts were started off with the OS/4 Operating System, which is essentially an expansion of the UNIVAC 9400/9480 Disc Operating System.

In the meantime, work was continuing at UNIVAC on the VMOS Operating System, and the ultimate announcement of a virtual memory operating system for Series 90 computers was considered only a matter of time. That time came in January 1975, when UNIVAC disclosed that further development of OS/7 was to be abandoned and that Release 11 of VMOS, renamed VS/9, would be supplied to all new 90/60 and 90/70 installations, although customers running OS/7 would continue to receive support at its then-current level. VS/9, thus, now serves as the ultimate upgrade operating system, not only for UNIVAC 9000 Series computers, but also for Series 70 DOS systems and Series 70 Model 45 and Model 6 installations operating under versions of the TDOS operating system.

8430 DISC SUBSYSTEM: Provides large-capacity random-access storage on removable disc packs with storage capacities comparable to the standard-density (100-million-byte) IBM 3330 disc storage subsystem. Each disc pack stores up to 100,018,280 bytes of data. Data is recorded on 404 tracks per surface (plus 7 spares). Each track can contain up to 13,030 bytes. There are 19 read/write heads (one for each recording surface) in each comb-type access mechanism. Average head movement time is 27 milliseconds, average rotational delay is 8.3 milliseconds, and the data transfer rate is 806,000 bytes per second.

From two to eight 8430 disk pack drives can be attached to a 5039 control unit in combination with up to eight 8405 fixed-head disc drives. The 8430 disc pack drives can also be intermixed with 8433 disc storage drives on the 5039 control unit. A sixteen-drive expansion feature expands the capability of the 5039 control unit to up to sixteen 8430 and/or 8433 disc storage drives. A dual-access feature and a second 5039 control unit permit simultaneous read and write operations on any two 8430 disc drives. The 8430 features a command retry facility and error correction coding circuitry.

8433 DISC SUBSYSTEM: Provides random access to very large quantities of data stored on removable "double-density" 3330-type disc packs. Each industry-standard disc pack contains 200,036,560 bytes in free-format recording mode or 190,279,680 bytes in VS/9 format. There are 808 tracks (plus 7 spares) on each of the 19 recording surfaces. The average head positioning time is 30 milliseconds, and the average rotational delay is 8.3 milliseconds (3600 rpm). Data transfer rate is 806,000 bytes per second.

From two to eight 8433 disc pack drives can be connected to a 5039 control unit for a total of 1.6 billion bytes per subsystem. A sixteen-drive expansion feature expands the capability of the 5039 control unit to up to 16 drives, or 3.2 billion bytes. The 8433 and 8430 disc pack drives can be intermixed on one 5039 control unit up to the maximum of 8 or 16 drives. In addition, 8433 and 8430 disc pack drives can be intermixed with 8405 fixed-head disc drives. A second 5039 control unit and the dual access feature permit simultaneous read/wrote operations to be performed on any two drives. The 8433 includes a command retry facility and error correction coding circuitry.

8450 DISC SUBSYSTEM: Employs sealed, fixed media and provides up to 336 megabytes in free format or up to 307 megabytes in VS/9 format. The non-removable discs provide 15 recording surfaces, each having two zones of 555 tracks (plus 5 spares) and services by two read/write heads. Up to 1 megabyte of fixed-head storage can be added to each unit. The average head-positioning time is 23 milliseconds, and the average rotational delay is 8.3 milliseconds (3600 rpm). Data transfer rate is 1,260,000 bytes per second.

The 8450 disc drives connect to the processor through the 5040 storage control unit (SCU), which permits the drives to be intermixed with 8430 and 8433 disc drives. The 5040 SCU can control up to 8 disc drives in its basic configuration and up to 16 drives if an expansion feature is added. Additional features of the 5040/8450 subsystems include rotational position sensing, error correction facilities, and enhanced command retry.

INPUT/OUTPUT UNITS

UNISERVO 12 MAGNETIC TAPE UNIT: A medium-speed tape drive that reads and records data on standard ½-inch tape in IBM-compatible phase-encoded or NRZI format. Available in both 9-track and 7-track versions. Tape speed is 42.7 inches per second, forward or backward. The standard 9-track version has a recording density of 1600

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UNIVAC claims that VS/9 provides nearly all the functions of IBM's MVS at a substantially lower cost in hardware overhead. VS/9 *can* execute in a minimum of 262K bytes of main memory, although UNIVAC states that most VS/9 systems are much larger. Resident supervisor sizes are estimated at 18 4096-byte pages (72K bytes) for batch operation and 22 to 24 4096-byte pages (88 to 92K bytes) for batch and interactive execution.

UNIVAC acquired the VMOS operating system, VS/9's predecessor, with the takeover of the RCA customer base in January 1972. VMOS is an outgrowth of the original Time-Sharing Operating System (TSOS) released for the RCA Spectra 70/46 in 1967 and for the Spectra 70/61 two years later. With the announcement of the RCA Series computers in 1970, the name was changed from TSOS to Virtual Memory Operating System to add a new fillip to RCA's marketing campaign—since RCA's product line included virtual memory capabilities that were not yet available for the IBM System/370 computers. Since the demise of the RCA computer operation, UNIVAC states that along with maintaining the operating system for current VMOS users, it has enhanced the system's reliability, added new recovery techniques, tuned the scheduling algorithm, and improved its memory management facilities.

In contrast to the delays that accompanied the early development efforts on OS/7, VS/9 was ready for delivery for Series 90 systems at the time of its announcement and, in fact, was already installed and running in a 90/60 customer site.

VS/9 includes three levels of communications software support: the Communications Access Method (CAM), the Communications Oriented Software (COS), and the Virtual Integrated Communications Access Method (VICAM) introduced with the UNIVAC 90/80. CAM is essentially a set of re-entrant software routines to facilitate the implementation of simple inquiry/response programs. COS is a user-tailored communications system that can supervise up to six user programs and provide support for communications applications ranging from inquiry/response to full store-and-forward message switching. VICAM is a set of generalized software components that provide a wide range of functions for user applications. A set of prescribed procedures in the form of macro instructions affords the user an interface to remote devices and message files.

Information management software, which is making a significant contribution to the marketing success of the UNIVAC 1100 Series systems, is also receiving strong emphasis in the Series 90 software product line. DMS/90, a generalized data base management system based on CODASYL specifications, was originally introduced for the 90/60-90/70 computers in 1973 and now executes under VS/9. IMS/90, an on-line storage and retrieval system, was released under VS/9 early in 1976.

bpi (in phase-encoded mode) and a data rate of 68,320 bytes per second; the optional dual density feature permits operation at 800 bpi (in NRZI mode) at a data rate of 34,160 bytes per second. The 7-track version can operate at 200, 556, or 800 cpi, with corresponding data rates of 8,540, 23,740, or 34,160 characters per second. The data conversion feature, for 7-track drives, converts each group of four 6-bit characters from tape into three 8-bit bytes in main storage, and vice versa.

From 1 to 16 Uniservo 12 tape units can be connected to a Uniservo 12 tape control, and up to 8 controls can in turn be connected to each UNIVAC Series 90 selector channel. Optional features enable the tape control to be connected to two selector channels, permitting simultaneous read/read, read/write, or write/write tape operations, with bimodal (7- or 9-track) compatibility.

With addition of the Uniservo 16 capability option, any combination of up to sixteen Uniservo 12 and Uniservo 16 drives may be connected to the Uniservo 12 control. A Uniservo 12/16 control is also available which includes the Uniservo 16 capability as a standard feature.

UNISERVO 14 MAGNETIC TAPE UNIT: Reads and records data on standard ½-inch tape in IBM-compatible phase-encoded or NRZI formats. Available in both 9-track or 7-track versions. Tape speed is 60 inches per second, forward or backward. The standard 9-track version has a recording density of 1600 bpi (in phase-encoded mode) and a data rate of 96,000 bytes per second. The optional dual density feature permits operation at 800 bpi (in NRZI mode) at a data rate of 48,000 bytes per second, while the 7-track NRZI version operates at 200, 556, or 800 cpi, with data rates of 12,000 33,400 or 48,000 characters per second.

The Uniservo 14 magnetic tape units use the 5045 control unit, which includes the controller and housing for two magnetic tape units. A maximum of eight tape units can be attached to each 5045 control unit. Features available with the Uniservo 14 include automatic tape loading, dustproof wraparound tape cartridges, single-capstan drive, and a dual-channel option that permits non-simultaneous operation on two channels on a single processor or shared operation between two central processors.

UNISERVO 16 MAGNETIC TAPE UNIT: A high-speed tape drive that reads and records data on standard 1/2-inch tape in IBM-compatible phase-encoded or NRZI formats. Available in both 9-track and 7-track versions. Tape speed is 120 inches per second, forward or backward. The standard 9-track version has a recording density of 1600 bpi (in phase-encoded mode) and a data rate of 192,000 bytes per second; the optional dual density feature permits operation at 800 bpi (in NRZI mode) at a data rate of 96,000 bytes per second. The 7-track version operation at 200, 556, or 800 bpi, with corresponding data rates of 24,000, 66,720, or 96,000 characters per second.

From 1 to 16 Uniservo 12 and Uniservo 16 tape units can be connected to a Uniservo 12/16 control, or any combination of 1 to 16 Uniservo 12, 16, or 20 tape units can be connected to a Uniservo 20 control, and up to 8 tape controls can in turn be connected to each selector channel. Optional features enable the tape control to be connected to two selector channels, permitting simultaneous read/read, read/write, or write/write tape operations.

UNISERVO 20 MAGNETIC TAPE UNIT: A high-speed tape drive that reads and records data on standard 1/2-inch tape in IBM-compatible formats. Available in a 9-track version only. Tape speed is 200 inches per second, forward or backward. The Uniservo 20 has a recording density of 1600 bpi (in phase-encoded mode) and a data rate of 320,000 byes per second. Standard features include a power window, automatic tape threading, and a wraparound cartridge.

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COMPATIBILITY

With an instruction set comparable to that of the IBM System/360 Model 50, both the 90/60 and 90/70 offer a high degree of compatibility with the IBM 360/30 and 360/40 computers. Compatibility with System/360 is also achieved through compatible source languages that are essentially the same as their System/360 counterparts. Differences in "privileged" instructions between the System/360 and Series 90 operating systems can be handled by the System/360 emulator, which is supported on the 90/60 and 90/70 as a stand-alone function and provides an interim solution for System/360 users with requirements for additional processing power.

UNIVAC states that the architectural similarities between the Series 70/7 and the 90/60 and 90/70 permit programs to be interchanged among these systems under the VS/9 Operating System. According to the vendor, conversions from smaller Series 70 systems can be accomplished with relative ease by recompiling programs written in the COBOL, RPG, and FORTRAN languages, which UNIVAC estimates are nearly 99 percent compatible with their VS/9 counterparts. In addition, the Series 70 Mode of Operation Through Hardware (SMOOTH) emulates Series 70 TOS, TDOS, and DOS environments.

An Assembly translator is provided to convert Series 70 TDOS and DOS source programs and user-written macros to equivalent VS/9 Assembly source language. Series 70 magnetic tape files are compatible with VS/9 and are acceptable as input to VS/9 and to user programs. UNIVAC provides utilities to transcribe Series 70 disc files to magnetic tape and to reload the data on discs in a format acceptable to VS/9 data management routines and VS/9 programs. Transportability of Series 70 communications programs to VS/9 is facilitated through the availability of the functional capabilities of the TDOS and DOS Communications Oriented Software (COS) and CCM-equivalent hardware.

OS/4 FORTRAN and RPG II programs can be recompiled for execution under VS/9. A Virtual Memory Editor (EDT) can also be used for modifying source-language programs stored in VS/9 files in sequential or indexed sequential format. EDT supports a comprehensive repertoire of commands for creating, deleting, inserting, copying, moving, modifying, and prefixing and suffixing of lines and text within lines. The Editor can also scan programs for specified character strings and modify the strings according to user directives. OS/4 magnetic tape files are directly acceptable as input to VS/9 software, while utilities are available to dump OS/4 disc files and reload them in VS/9-acceptable format. Finally, a Library Transcriber is provided for translating, OS/4 Source, Proc, and COBOL COPY libraries into VS/9 program files for use by VS/9 program preparation components.

To date, UNIVAC has remained one of the most fully bundled ocmputer manufacturers, supplying all systems software, compilers and assemblers, file and data base management systems, and most applications programs free of charge to its computer customers.

From 1 to 16 nine-track, 800 or 1600 bpi Uniservo 12, 16, and/or 20 tape units can be connected in any combination to the Uniservo 20 control unit, and up to 8 tape controls can in turn be connected to each selector channel. With the 7-track capability and 9-track addition feature, Uniservo 12 and 16 tape units in the Uniservo 20 subsystem may be 7- or 9-track. Two or more control units may be used in the Uniservo 20 subsystem to provide simultaneous dual access for read/write, read/read, and write/write operations on any appropriately equipped Uniservo 16 or 20 tape units connected to the control units. Each control unit in a simultaneous dual access system has its own power supply and independent access path to provide increased reliability. Individual tapes cannot be switched off-line without removing all the tape connected to that controller from service.

UNISERVO 30 SERIES TAPE UNITS: High-performance units that record data on ½-inch tape in IBM-compatible formats. There are five models in the series, three of which use Group Coded Recording (GCR) at a density of 6250 bits per inch. All five models use the Uniservo 5042 control unit, and Uniservo 30 series tape units can be intermixed in any combination on the same subsystem, provided the proper control unit is included to accommodate the various tape unit types. The basic control unit can handle one to eight Uniservo 30 series tape units. Optional features in the control unit and the addition of a second control unit, also with appropriate features, permit communication with up to 16 tape units in a dual-access mode.

All of the models in the Uniservo 30 series, with one exception, can be used with 90/60, 90/70, and 90/80 systems; the Uniservo 36 model is available for use only with the 90/80 system. The five models in the Uniservo 30 series and their characteristics are as follows:

Uniservo 30 (7-track)—a conventional NRZI unit with a transfer rate of 160,000 bytes/second at 800 bpi, 111,200 bytes/second at 556 bpi, or 40,000 bytes/second at 200 bpi. Tape speed is 200 inches/second.

Uniservo 30 (9-track)—a unit designed for NRZI and PE (phase encoded) recording. The transfer rate is 320,000 bytes/second at 1600 bpi or 160,000 bytes/second at 800 bpi. Tape speed is 200 inches/second.

Uniservo 32—a 9-track unit designed for GCR and PE recording. The transfer rate is 470,000 bytes/second at 6250 bpi or 120,000 bytes/second at 1600 bpi. Tape speed is 75 inches/second.

Uniservo 34—a 9-track unit designed for GCR and PE recording. The transfer rate is 780,000 bytes per second at 6250 bpi or 200,000 bytes per second at 1600 bpi. Tape speed is 125 inches/second.

Uniservo 36—a 9-track unit designed for GCR and PE recording. The transfer rate is 1,250,000 bytes/second at 6250 bpi or 320,000 bytes/second at 1600 bpi. Tape speed is 200 inches/second. This model is available only for the 90/80 system.

600-CPM CARD READER: Reads 80-column cards serially by column at 600 cpm. Can be equipped to read 51- or 66-column short cards or UNIVAC 90-column cards. Reads in either EBCDIC or card-image mode. Has a 2400-card feed hopper and two 2000-card stackers; ASCII translate is optional. Multi-read error checking is a standard feature.

1000-CPM CARD READER: Identical with the 600-cpm unit except for its greater speed.

250-CPM CARD PUNCH, 0604-99: Punches 80-column cards in row-by-row fashions at 250 cpm, in either EBCDIC or card-image mode. Has a 1000-card feed hopper and two

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USER REACTION

Datapro contacted 15 users of the Univac 90/60, 90/70, and 90/80 for their reactions and experiences with their respective systems. Eight of the 15 responses were gathered from questionnaires mailed in September 1977, and an additional 7 responses were obtained in mid-1978 through telephone interviews with users contacted from leads furnished to us by the original eight. The sample covers companies involved in several different fields, including financial, city government, insurance, education, and entertainment. A total of 19 systems are represented—nine 90/60's, seven 90/70's, and three 90/80's.

Most of those systems, 16 of the 19, were being used for business data processing. Communications, scientific/engineering, program development, and data base management were listed as additional applications, and each was mentioned by from 3 to 7 of the users. One 90/60 system was being used for educational purposes.

At the time the companies were surveyed, their systems had been installed for an average of 20 months. Eight respondents noted that they were renting their systems, five had purchased them, and two were using third-party lease-purchase plans.

Memory sizes ranged from 393K bytes to 2 megabytes. The most common size was 1 megabyte, represented by 6 of the 19 systems. The average memory size was 841K bytes for the 90/60's and 1143K bytes for the 90/70's. Two of the three 90/80's had 2 megabytes of memory.

The average disk storage capacity was 556 megabytes for the nine 90/60's, with a range of 393 to 1200 megabytes. For the 90/70's, the disk capacities averaged 1650 megabytes and ranged from 500 to 3300 megabytes. The three 90/80's had 800, 1200, and 2000 megabytes of disk storage. Most of the 90/60 systems included 3 or 4 magnetic tape units, the 90/70's had from 3 to 16 units, and the 90/80's had between 4 and 8. The number of remote terminals connected to the systems ranged from a low of one, on a 90/80 system, to 350 on a dual 90/70 system.

The results of the user survey are presented below, along with the weighted average ratings obtained in our previous survey of 90/60, 90/70, and 90/80 users in 1976. The category of applications programs has been omitted because most UNIVAC users develop their own applications software.

	Excellent	Good	Fair	Poor	1978 WA*	1976 WA*
Ease of operation	6	8	1	0	3.3	2.9
Reliability of mainframe	6	7	2	0	3.3	2.9
Reliability of peripherals	5	6	3	0	3.1	2.8
Responsiveness of maintenance service	7	6	2	0	3.3	3.5
Effectiveness of maintenance service	6	7	2	0	3.3	3.1
Technical support	5	3	6	0	2.9	2.8
Operating systems	7	6	2	0	3.3	2.6
Compilers and assemblers	7	7	1	0	3.4	2.8
Ease of programming	7	5	1	0	3.5	3.0
Ease of conversion	6	4	1	1	3.3	3.0
Overall satisfaction	5	9	1	0	3.3	2.7

*Weighted Average based on 4.0 for Excellent.

1000-card output stackers, with program control of stacker selection. Can be equipped with a pre-punch read station, giving the unit read/punch capabilities.

PAPER TAPE SUBSYSTEM: Consists of a 300-char/sec F1033-02 reader, 110-char/sec F1032-02 punch, and 0920-02 control unit in a single cabinet. Reads and punches 5-, 6-, 7-, or 8-level tape. Spoolers are optional for both the reader and punch take-up. Supported under OS/4 only.

0770 PRINTERS: Printing speeds for 48-character sets are 800 lines per minute for Model 0770-00, 1400 lines per minute for Model 0770-02, and 2000 lines per minute for Model 0770-04. The respective skipping speeds for these three models are 50, 75, and 100 inches per second. All can have character sets from 24 to 384 characters in size, and all have 132 print positions as standard. An optional feature for all models can increase the number of print positions to 160 without affecting the printing speed. All have a single-space print time of 8.75 milliseconds, line spacings that are operator-selectable at 6 or 8 lines per inch, and forms dimensions from 3 to 22 inches wide and up to 24 inches long. The printers use a new horizontal print band technique. Their control units have a standard Series 90 interface.

The three 0770 Printers have the following features in common: all use interchangeable print band cartridges; all can identify the cartridge type under program interrogation to ensure that the operator has placed the proper band in the printer for the run; all use a program-loaded vertical format buffer in place of a paper tape format loop; and all have swing-out print carriages, easy ribbon replacement without rewinding, simplified line finding, lighted print areas, automatic print gap (forms thickness) adjustment, powered, program-controlled top covers, automatic power forms stackers, and enhanced acoustical covers to reduce operating noise.

0776 PRINTERS: Two versions of the 0776 band printer are offered, the 760-lpm Model 0776-00 and the 940-lpm Model 0776-02. The printers feature 136 print positions, a full-line buffer, and interchangeable print cartridges with 48, 96, 128, 192, or 384-character sets. An F2245-00 print expansion option is required for print arrays greater than 64 characters.

The print rate for both models decreases with the use of larger print arrays. The print rates in lines per minute for the different-sized arrays are as follows:

No. of print characters	Model 0776-00	Model 0776-02
48	760	940
64	600	750
96	420	540
128	325	420
192	225	290
384	115	150

The forms advance rate is 22 inches per second for both models, and the total forms advance time produced by any one command is limited to a maximum of 1.2 seconds to prevent paper runaway. Line spacing can be either 6 or 8 lines per inch and is software-selectable.

The 0776 printers will accept multipart forms with a pack start thickness up to 0.018 inch. Thicker form packs can be used, but with reduced print quality. Form widths can range from 4 to 18.75 inches, and form length can be up to 24 inches. Vertical formatting is under control of a software-loaded buffer that contains space for up to 192 skip/stop positions, equivalent to a 24-inch form at 8 lines per inch.

2703 OPTICAL DOCUMENT READER: Reads printed numeric data from individual documents ranging from 2.75

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In 11 of the 12 categories, the weighted average user ratings for the 90/60, 90/70, and 90/80 systems improved substantially over those obtained during the previous Datapro user survey conducted nearly two years earlier. Of particular note is the 0.7-point increase, from 2.6 to 3.3, in the survey population's rating of the UNIVAC operating system. At the time the previous survey was taken, the older and now-superseded OS/4 was the principal operating system. The subsequent change to VS/9, which was the operating system in use in 11 of the 19 systems, evidently did much to improve this previously low-rated category. Another notable increase was in the ratings given the category of Compilers and Assemblers, up six-tenths of a point from the previous survey.

The only category that showed a drop in the average user raing was Maintenance Service Responsiveness, down two-tenths of a point. The decrease, however, is not considered especially significant in view of the substantial increases in the other 11 categories and the fact that maintenance ratings were still solidly in the Good-to-Excellent range.

The "bottom-line" category of Overall Satisfaction is probably the most accurate measure of the user's overall feelings about the systems, and a healthy 0.6-point improvement in this category moved the systems out of the Fair-to-Good range and into the Good-to-Excellent range. It's clear that Univac has resolved the early problems with the 90/60, 90/70, and 90/80 systems and turned them into impressive contenders for an increasing share of the large-scale computer market.□

to 4.25 inches in height and 2.00 to 8.75 inches in length. Basic speed of 300 six-inch documents per minute can be increased to 600 dpm by an optional feature. Other options permit reading of vertical pencil marks and of standard 80-column punched cards. The Modulus-10 Check Digit option compares a computed modulus-10 check digit with a check digit printed on the document. Character set consists of the digits 0-9 and four special symbols, in either UNIVAC H-14, OCR-A, or OCR-B (ECMA) font. Has a 2000-document feed hopper and three 1000-document stackers. Supported under OS/4 only.

90/60 CHANNEL ADAPTERS: Permit any of the following small-to-medium-scale UNIVAC data processing systems to be connected to the 90/60 via their respective multiplexer or selector channels: 1004/1005, 9200, 9200 II, 9300, 9300 II, 9400, 9480, 90/60, 90/70, or 90/80. Supported under OS/4 only.

90/70 CHANNEL ADAPTERS: Permit any of the following small-to-medium-scale UNIVAC data processing systems to be connected to the 90/70 for communication via their respective multiplexer or selector channels: 9200, 9200 II, 9300, 9300 II, 9480, 90/60, or 90/70. Supported under OS/4 only.

Each attachable processor can function as an I/O subsystem providing peripheral capabilities. For details of the Series 9000 computer systems, please refer to Report 70C-877-01.

COMMUNICATION CONTROLS

MULTI-CHANNEL COMMUNICATIONS CONTROLLER: Announced in July 1975, the Multi-Channel Communications Controller (MCC) operates under the VS/9 Operating System and emulates the earlier Communications Controller Multichannel (CCM), the Series 70 communications controller that was transferred to 90/60 and 90/70 systems with the announcement of VS/9. In addition, the MCC supports a larger number of communications lines, can handle higher line speeds, and can accommodate a variety of line speeds and communications protocols, including UNIVAC and other industry-standard terminals, plus computer-to-computer communications.

The MCC software has been enhanced to relieve the host processor of line and terminal polling. The software enhancements also provide a software multiplexing function that allows the MCC to address beyond the 63-line address limitation of the 90/60 and 90/70 multiplexer channels.

The MCC is available in three versions. Model 1 can handle a maximum of 16 half-duplex or full-duplex lines, Model 1A accommodates up to 32 half- or full-duplex lines, and Model 2 can be configured with up to 64 full-duplex or 128 half-duplex lines. The MCC supports line speeds ranging from 45.45 to 56,000 bits per second, with a maximum total throughput capacity of 25,000 characters per second. It operates under control of the host central processor and performs character sequence detection and insertion, code translation, and cyclic, longitudinal, and vertical redundancy character generation and checking.

A Test Assistance Program allows individual lines, line adapters, modems, and terminals to be tested off-line without disrupting production processing. Software support for the communications network is created through a system generation procedure designed to facilitate the addition of new lines and line types.

DATA COMMUNICATIONS SUBSYSTEMS: Remote communications devices can also be connected to a UNIVAC Series 90 system by means of from one to four Data Communications Subsystems. The DCS-1, DCS-1C, DCS-4, and DCS-16 subsystems can accommodate 1, 1, 4, and 14 half-duplex or full-duplex lines, respectively. Each DCS is connected directly to a multiplexer subchannel. Any combination of up to 4 DCS's can be connected to a UNIVAC 90/60 or 90/70, subject to a limit of 30 lines maximum. The DCS hardware is supported only under OS/4.

Each DCS consists of a single Line Terminal Controller, plus a Line Terminal and Communications Interface for each connected line. Numerous models of line terminals and interfaces permit asynchronous and/or synchronous transmission over a wide range of communications services at speeds of 75 to 250,000 bits per second. The DCS-1C is a Binary Synchronous Data Communications Subsystem that enables a Series 90 computer to communicate with an IBM System/360 computer, using either EBCDIC or ASCII code and either Transparent or Nontransparent mode. The free-standing 8577-02 DCS Cabinet used with the DCS-1 or DCS-1C houses up to 4 of these units in any combination. The DCS-4 or DCS-16 includes its own free-standing cabinet and power supply.

Program scheduling is performed automatically by priority level on either a first-in, first-out or first-in, first-fit basis. After the expiration of a specified number of minutes, a first-in, first-fit program automatically reverts to first-in, first-out status. VS/9 makes extensive use of re-entrant input and output spooling routines, although user programs can also request dedicated card readers and printers.

The VS/9 Data Management System automatically allocates files to mass storage devices and maintains a System File Catalog of file use and current and previous generations of files. Files can be assigned to public or private volumes and can be classified by owners as shared or non-shared. Both

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read-only or read-write access to files can be specified, with optional password protection. Data is allocated to disc in blocks of 2,048 bytes. File access methods supported include SAM, ISAM, PAM (Primary Access Method for random access), EAM (Evanescent Access Method for temporary files), and BTAM (Basic Tape Access Method).

VS/9 reliability and recovery capabilities include a Hardware Error Recovery System (HERS) that analyzes mainframe errors and attempts to recover from transient errors. The Statistical Historical I/O Error Rates (SHIOR) utility monitors the activity of designated peripherals and accumulates data on peripheral errors. The Basic Processor Exerciser (IHBPXR) exercises internal CPU logic in an on-line environment to detect malfunctions primarily associated with arithmetic logic.

VS/9 accounting functions include the collection of data on the utilization of system resources identified by user and/or account number, a billing routine to generate a report based on that data, and a SNAP (System Net Activity Program) that monitors CPU, I/O, and paging activity and maintains statistics on response times, system load, task scheduling, and task page-size characteristics and the availability of file paging space.

Interactive processing capabilties provided by VS/9 include Extended BASIC, FAST FORTRAN for fast compilation and immediate execution of FORTRAN programs, a Virtual Memory Editor for file creation and deletion and modification, a COBOL Program Development System (CODE), a Desk Calculator mode of operation, Sort/Merge, and the Interactive Debugging Aids.

COMMUNICATIONS ACCESS METHOD (CAM): CAM consists of a group of re-entrant subroutines for establishing communications between a VS/9 problem program and one or more remote terminals. When multistation lines are employed, an extension to CAM provides a polling facility that uses user-supplied directives to establish the sequence and frequency of the polling cycle. For single-station lines, CAM interfaces with the executive routines of VS/9 for physical terminal interfacing. Multiple programs, each interfacing with one or more terminals, can be supported. CAM is used primarily for implementation of simple inquiry/response applications.

VIRTUAL INTEGRATED COMMUNICATIONS ACCESS METHOD (VICAM): VICAM is a set of generalized software components that provide a wide range of functions for user applications. A set of prescribed procedures in the form of macro instructions affords the user an interface to remote devices and message files. The communications network (lines, terminals, buffers, and queues) is defined by an assembly process. This network definition is loaded dynamically in response to a user program request and is placed in an area within the executive called the communications control area (CCA). Functional components of VICAM are the Message Control Program (MCP) and the message processing program.

TERMINALS: The following UNIVAC devices, described elsewhere in DATAPRO 70, are supported for use as remote terminals with the Series 90 systems: UTS 400 (Report 70D-877-06), UTS 700 (Report 70D-877-07), and the UNIVAC 1900 Computer Aided Data Entry System (Report 70D-877-31).

SOFTWARE

OPERATING SYSTEMS: Originally, two operating systems were available for the UNIVAC 90/60, 90/70, and 90/80 systems: OS/4, an enhanced version of the UNIVAC 9400 Disc Operating System; and VS/9, an enhancement of the original VMOS (Virtual Memory Operating System) that was developed for Series 70 (ex-RCA) systems. VS/9 is now the principal operating system. OS/4 is no longer being enhanced but is still maintained at its current level for the convenience of those customers using it.

VS/9: Announced in February 1975, VS/9 offers functional capabilities for concurrent processing, data communications, and interactive processing. The virtual memory features of VS/9 allow programs to be located in memory in non-contiguous pages of 4,096 bytes each that are swapped in and out of main memory space of 8 million bytes, and its multiprogramming facilities can manage a theoretical limit of over 120 concurrent tasks.

The allocation of processor resources among tasks is accomplished by a supervisory scheduling algorithm, a hardware interval timer, and a system table of task queues. The tasks in the active queues compete for central processor time, with interactive tasks and those with higher priorities receiving larger time slices than batch tasks and lower-priority tasks. Input/output-bound tasks are given attention before compute-bound tasks. Priority levels can be dynamically adjusted during execution to bias the system toward batch or interactive processing.

VS/9 supports Class I and Class II problem programs. Class I programs remain resident in contiguous main memory locations and are not paged. Class II programs operate in the virtual memory mode, are allocated in 4K-byte pages, and require only the working set of each program to be resident in main memory for execution. Pages are paged out when they have been modified and the system requires pages of a higher priority. Pages that have been least used are paged out first if they have been modified, while those that have not been modified are simply overlaid.

Program scheduling is performed automatically by priority level on either a first-in, first-out or first-in, first-fit basis. After the expiration of a specified number of minutes, a first-in, first-fit program automatically reverts to first-in, first-out status. VS/9 makes extensive use of re-entrant input and output spooling routines, although user programs can also request dedicated card readers and printers.

The VS/9 Data Management System automatically allocates files to mass storage devices and maintains a System File Catalog of file use and current and previous generations of files. Files can be assigned to public or private volumes and can be classified by owners as shared or non-shared. Both read-only or read-write access to files can be specified, with optional password protection. Data is allocated to disc in blocks of 2,048 bytes. File access methods supported include SAM, ISAM, PAM (Primary Access Method for random access), EAM (Evanescent Access Method for temporary files), BTAM (Basic Tape Access Method), and RAM (Relative Access Method).

VS/9 reliability and recovery capabilities include a Hardware Error Recovery System (HERS) that analyzes mainframe errors and attempts to recover from transient errors. The Statistical Historical I/O Error Rates (SHIOR) utility monitors the activity of designated peripherals and accumulates data on peripheral errors. The Basic Processor Exerciser (IHBPXR) exercises internal CPU logic in an on-line environment to detect malfunctions primarily associated with arithmetic logic.

VS/9 accounting functions include the collection of data on the utilization of system resources identified by user and/or account number, a billing routine to generate a report based on that data, and a SNAP (System Net Activity Program) that monitors CPU, I/O, and paging activity and maintains statistics on response times, system load, task scheduling, and task page-size characteristics and the availability of file paging space.

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Interactive processing capabilities provided by VS/9 include Extended BASIC, FAST FORTRAN for fast compilation and immediate execution of FORTRAN programs, APL/90, a Virtual Memory Editor for file creation and deletion and modification, a COBOL Program Development System (CODE), a Desk Calculator mode of operation, Sort/Merge, and the Interactive Debugging Aids.

COMMUNICATIONS ACCESS METHOD (CAM): CAM consists of a group of re-entrant subroutines for establishing communications between a VS/9 problem program and one or more remote terminals. When multistation lines are employed, an extension to CAM provides a polling facility that uses user-supplied directives to establish the sequence and frequency of the polling cycle. For single-station lines, CAM interfaces with the executive routines of VS/9 for physical terminal interfacing. Multiple programs, each interfacing with one or more terminals, can be supported. CAM is used primarily for implementation of simple inquiry/response applications.

VIRTUAL INTEGRATED COMMUNICATIONS ACCESS METHOD (VICAM): VICAM is a set of sharable generalized software components that provide a wide range of functions for user applications. A set of prescribed procedures in the form of macro instructions affords the user an interface to remote devices and message files. The communications network (lines, terminals, buffers, and queues) is defined by an assembly process. This network definition is loaded dynamically in response to a user program request and is placed in an area within the executive called the communications control area (CCA). Functional components of VICAM are the Message Control Program (MCP) and the Communications User Program (CUP).

VICAM memory space is protected as part of the Executive. It operates at Executive priority and not as a user program.

The MCP is a modular software package that is capable of supporting either simple or complex communications environments. A single MCP provides concurrent support for multiple user message processing programs that use a variety of terminals and line types. MCP prevents conflicting facility assignments and releases facilities when jobs terminate. User programs are provided with macro programs that control table generation, handle data transfers to and from user-specified buffer areas, initialize and control communication facilities, and perform dynamic terminal and poll table entry alterations in the communications control areas.

The components that make up the MCP are:

Channel Control Routine (CCR)—provides the physical I/O interface for the remote device handlers to the communications controller and the specific types of communications subsystems.

Remote Device Handlers (RDH)—provide the software logic and control required to interface the unique characteristics of specific remote devices to the other VICAM components. VICAM has the capability of emulating the UNIVAC 1004 Card Processor Subsystem, the UNIVAC DCT 2000 terminal, and the IBM 2780 terminals. This feature allows communications with any remote host CPU that can send to and receive from any of these terminals.

Communications Network Controller (CNC)-coordinates message storage queues.

Communications Control Area (CCA)—contains all of the tables required to define and control a specific communications network configuration.

Message User Service Transcriber (MUST)—provides a message staging service that isolates a user program from the device dependence that is usual in data communications programs.

Deferred User Service Transients (DUST)—perform those functions that are not time-dependent or that are used infrequently, such as MCP initialization, CCA initialization, line connect and auto dialing, and program termination.

Message queuing—stacks complete messages in main storage while they are waiting to be served by a communications line handler or a message processing program.

Activity scheduling and priority control—performs activity scheduling with an optional priority suspension and scheduling capability.

Timer service—provides a centralized timing service for control of active data buffers and scheduling of activities for use by all MCP software elements.

The part of VICAM that Univac calls the Communications User Program (CUP) is the user-generated coding that processes incoming messages and generates any applicable response messages. This program interfaces with the Message Control Program through macro instructions provided for this purpose. These macros control the sending and receiving of messages, message routing and switching, time and date stamping, sequencing and sequence checking, source ID validation, message queue maintenance, destination validation, length checking, and priority control. Multiple message processing programs can operate concurrently under VS/9, subject to the availability of system resources. Program-oriented networks operating under control of a message processing program are able to create files of information that can be processed concurrently on another network by another message processing program.

COMMUNICATIONS-ORIENTED SOFTWARE (COS): COS is maintained for compatibility with the UNIVAC Series 70 systems. It has been superseded by VICAM, and no future enhancements are planned.

COS is a modular communications system that handles message communications traffic, code translation, queuing on intermediate storage, message logging, and transferring of messages to and from Communications User Programs. COS consists of three major components, the Communications Interrupt Analysis (CIA), the Communications Control Program (CCP), and one or more Communications User Programs. The Communications Interrupt Analysis component is an extension of the operating system that analyzes each communications interrupt and initiates the appropriate Communications Control Program function to process it. The Communications Control Program services all communications interrupts; performs communications line handling, message queuing, internal buffering, error handling, and code translation; and handles batched output and message switching functions. The CCP also serves as an interface to from one to six installation-written Communications User Programs which execute user-specified message-processing functions. Messages are transferred to and from the CUP's by means of GET and PUT macro-instructions.

OS/4: Provides essentially the same facilities as the UNIVAC 9400 DOS and is disk-oriented; no tape-oriented version is available. Enhancements which have been made to 9400 DOS consist of modifications to support the Series 90 console and the extra channels and larger memory of the Series 90, OS/4 requires at least two disk drives, a processor with 131K bytes of main memory, a card reader, and a printer (or a smaller UNIVAC computer connected as an

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I/O subsystem). Minimum resident memory requirement for OS/4 is about 24K bytes.

The system control facilities of OS/4 are divided into four main categories: Supervisor, Job Control, Data Management, and Message Control.

The Supervisor resides in main storage and schedules and coordinates all activities within the system. Its functions include interrupt handling, I/O scheduling and initiation, job time allocation, operator communication, job accounting, and control of multiprogrammed operations. Up to five independent programs can be executed concurrently if sufficient memory and peripherals are available. The Supervisor provides five different priority levels, three of which are available for users' programs.

The OS/4 Job Control routine controls transitions between job steps, suspension or cancellation of jobs, restarting of jobs, and termination of jobs. It receives its instructions from control cards which constitute a "job stream." Job streams can be stored in disk files for subsequent selection and execution.

Data Management provides comprehensive input/output control facilities, including record blocking and unblocking, I/O buffering, data validation, and label processing. These facilities are provided by subprograms which are generated as part of the operating system and referenced by macro-instructions in users' programs. Nonsequential files in disk storage can be accessed by either the Direct (random) Access Method, in which the user must specify the relative or absolute address of the desired disc record, or the Indexed Sequential Access Method, in which the user need only specify the key of the desired record. In a multiprogramming environment, the Data Management routines can be shared by all programs, thereby reducing main storage requirements.

The OS/4 Message Control Program provides macro-instructions that enable the user to generate custom-tailored message control and message processing routines to handle communications input/output. Messages of fixed or variable length can be queued in main and/or disc storage, and the generated routines can perform functions such as code translation, message sequencing, time stamping, and error checking. Main memory requirements for the Message Control Program routines range from 20K bytes for an entry-level system to 36K bytes for use of the complete facilities, not including buffering.

COBOL: UNIVAC offers three COBOL compilers for use under VS/9: American National Standard (ANS) 74 COBOL, ANS 68 COBOL, and BGCOB. In addition, the ANS 68 COBOL compiler runs under OS/4.

All of the VS/9 compilers are pageable programs. Source input can be retrieved from a cataloged file on disk, from a card deck, from a remote terminal, or from a disk-resident COBOL source library file. The compilers generate Class II programs, and the generated object modules are written on disk.

ANS 74 COBOL functional processing levels include: Nucleus—Level 2, Table Handling—Level 2, Sequential I/O—Level 2, Relative I/O—Level 2, Sort-Merge—Level 1, Segmentation—Level 1, Library—Level 1, Debug—Level 2, and Interprogram Communication—Level 2.

ANS 68 COBOL functional processing levels include: Nucleus—Level 2, Table Handling—Level 3, Sequential Access—Level 2, Direct Access—Level 2, Sort—Level 2, Segmentation—Level 2, and Library Functions—Level 2. This compiler also features a fully functional report writer facility that is not compatible with the ANS standard.

The BGCOB compiler is designed to be compatible with IBM F-level COBOL and with UNIVAC Series 70 COBOL.

FORTRAN: VS/9 FORTRAN (BGFOR) is an extension of ANS FORTRAN IV that is compatible with IBM H-level FORTRAN and produces optimized object code. It also provides comprehensive program error diagnostic and debugging facilities, and optionally produces a diagnostic file that can be interrogated by a post-compilation diagnostic utility.

VS/9 FAST FORTRAN is designed primarily to provide fast compilation of source programs, followed by immediate execution. It also provides a comprehensive set of error diagnostics to catch many common programming errors. The language acceptable to FAST FORTRAN is highly compatible with VS/9 FORTRAN.

An OS/4 FORTRAN compiler is available for operation on the minimum 90/60 or 90/70 system. It includes all the language facilities of full American National Standard FORTRAN, and is 360 FORTRAN F compatible. In addition, there are more than 20 useful language extensions, such as direct-access I/O statements and the ability to handle arrays of up to 7 dimensions.

REPORT PROGRAM GENERATOR: The VS/9 RPG II compiler is functionally equivalent to IBM RPG II and is designed to accept UNIVAC 9200/9300/9400 RPG source programs for generation and execution on a 90/60, 90/70, or 90/80. It is available for the minimum system configurations. The generated RPG object programs can be recorded on tape, disc, or the punched cards to eliminate the need for re-generation of the program before subsequent report runs. The object programs are relocatable modules that can be linked to other programs and stored in disc or tape libraries.

BASIC: An Extended BASIC is offered under the VS/9 Operating System. The UNIVAC BASIC language is similar to the original language developed at Dartmouth College but contains extensions to the arithmetic and control statements, file processing, and matrix commands. BASIC source programs can be catalogued for subsequent compile-and-execute operation.

ASSEMBLER: The OS/4 Assembler is directly compatible with 9400 BAL and is very similar to, although not totally compatible with, the Assembler languages for the UNIVAC 9200/9300 systems and the IBM System/360. The VS/9 Assembler supports predefined sets of macro variable symbols, allows macros to be defined anywhere in source programs, and permits multiple levels of sublists in macro-instruction operands.

INFORMATION MANAGEMENT SYSTEM (IMS/90): IMS/90 is an interactive, transaction-oriented file management system designed for use by nontechnical personnel. It requires no restructuring or reformatting of existing data files and uses DMS/90 (below) to access data base files. The IMS/90 data definition processor assists users in defining the format and valid data values for each field and also in defining which files can be modified.

Each file defined to IMS/90 can be secured against unauthorized access by assigning it a password. The password can be altered readily from day to day. Data is also protected from destruction by an automatic recovery/rollback feature and the automatic generation of a journal file for use by recovery routines.

One component of IMS/90, defined record management, constructs each record dynamically, just before delivering it to the action program that requested it. There is one data definition, and therefore each data definition record,

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can describe several subfiles along with the defined files. The defined file/subfile concept eases the problem of system enhancements. New applications can be added which require expansion to existing disc records, yet new data definitions can include definitions of the old subfiles, so that the action programs and the terminal operator procedures invoked in the old applications can be used unchanged.

The Uniform Inquiry Update Element (UNIQUE) query language provides a file inquiry and update capability. For each input message submitted, the terminal operator receives an answer so that he is quickly and constantly informed of the results of his commands. There commands are OPEN, CLOSE, DISPLAY, DELETE, OK, CANCEL, ADD, NEXT, CHANGE, LIST, MORE, DETAIL, ASSIGN, and SHOW.

IMS/90 also provides two sets of terminal commands, one for remote terminals and one for the master terminal. Remote terminal commands can be used to resolve various administrative or operational problems, or for educational purposes. The master terminal commands enable control of the communications network and assist in monitoring the system.

Users can code the action programs in standard COBOL or Assember language without extensive training in learning how to write programs for a communications environment. Messages to terminals are automatically sent without programmer instruction. Other features which reduce implementation costs are testing aids such as a batch processor and an option to use live data for test purposes. Further, action programs can be chained together, thus simplifying the programming effort.

DATA MANAGEMENT SYSTEM (DMS/90): DMS/90 is UNIVAC's data base management system for its 90/30, 90/60, 90/70, and 90/80 computers. It operates on the 90/60, 90/70, and 90/80 under the VS/9 Operating System. DMS/90 is designed in conformance with the CODASYL Data Base recommendations and represents a subset of these specifications. Its functional capabilities, therefore, are similar to those provided by DMS 1100, the data base management system for the UNIVAC 1100 Series computers, although there is no compatibility between the two systems at the machine level. DMS/90 is described in detail in Report 70E-877-01.

VS/9 UTILITY ROUTINES: The VS/9 utility routines are single-purpose programs which perform utilitarian tasks required in the day-to-day operation of a computer facility. These routines can be grouped into five categories: precompilation routines, postcompilation routines, linkage editors and loaders, media conversion routines, and system support utilities.

Precompilation routines are programs used to maintain libraries of source language programs or elements of programs. The existence of source language library facilities is a valuable aid to the programmer in program preparation and lends efficiency to the development of large programming projects. The specific routines and their functions are:

COBOL Library Update (COBLUR)—used for the maintenance of a COBOL source library on direct-access devices.

Macro Library Update (MLU)—used to create, update, or delete entries on a direct-access resident library of assembler macros.

Source Library Update (SLU)—used in conjunction with the assembler, this routine permits creating, updating, and deleting entries from a direct-access resident library of assembly-language source statements.

Postcompilation routines are programs designed to aid in achieving error-free compilations and to preserve the resultant object modules. The specific routines and their functions are:

Assembler Diagnostic Routine (ADIAG)—provides the user with the facility for interrogating, from a terminal, the error file created by the assembler during the assembly process. This file contains the same listing and diagnostic messages that are written to the system printer. Commands are provided for reformatting the listings, printing the listings, and accessing the diagnostic messages. The routine is also designed to aid in the definition of causes of the error flags received.

Background Compiler Diagnostic Routine (BDIAG)—provides the facility for interrogating the error file created for the FORTRAN and COBOL compilers. The compilers write the program and diagnostic listings to VS/9's SYSLST file. The user can request that a copy of the diagnostic messages be retained in the system. Using this routine, the diagnostics can be retrieved and printed at a terminal, allowing immediate determination of the accuracy of the program modules created.

Library Maintenance Routine (LMR)—provides the facilities necessary to create, update, delete, copy, and modify object modules in disc-resident libraries. The routine maintains a directory of the library in the front of the file to speed searching by the linkage editor and dynamic linking loader.

The *linkage editor and loader routines* are used to bind object modules into programs, load the programs, and, optionally, to provide the structure needed internally to support interactive debugging and (IDA) operations.

The linkage editor's primary function is to construct a loadable program from a set of object modules designated by the user. All addresses, external references, entry points, page alignment, read-only attributes, references to common areas, IDA symbolic dictionaries, and overlay and region structures are resolved in one operation. The routine generates statistics, reference resolutions, and load address assignments on a map listing and a cross-reference listing.

The loader is a control program that loads the programs that are created as output of the linkage editor and dynamically links Class II object modules into a loadable program. The loader obtains virtual storage for programs and resident storage for Series 90 Class I programs and establishes the environment for execution. A symbolic dictionary is constructed for interaction with the IDA if requested by the user. The dynamic linking facility of the loader does not provide all the functions of the linkage editor, nor does it provide load map listings.

The loader presently in use provides the same functionality as the static and dynamic linking loaders.

System support utilities are system service programs designed primarily for use by the system administrator in performing his function of administering, controlling, and maintaining viable computer system operations. They provide such facilities as file backup, system updating, volume initialization, and tape maintenance. The functions of these routines are as follows:

FILSAV is a program that provides a tape-oriented file maintenance facility to the system administrator and other users. It can be used in a batch or interactive mode. The system administrator can use this routine to maintain system files on a magnetic tape, usually for backup. All users can utilize the routine for saving and restoring catalogued files on tape. The routine copies files from

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disc storage or magnetic tape, duplicates tapes, updates saved-tape files, and restores tape files to disc.

FILSAV can select files using any of the following selection criteria:

By file names for a particular user

By user name (all files)

All files created or modified "today."

Any files not accessed (read or write) for the past "n" days.

The VS/9 Self-Loading System (SLS) is a multipurpose utility that runs in a self-contained environment. The heart of the SLS is the self-loading I/O handler (SLIOH) and a number of VS/9 error recovery modules that, when linked together, make up a mini-operating system that can run one job at a time. Linked to this mini-operating system is the direct-access volume support (RAVS) package and the emergency dump (EDUMP) system. The SLS provides the user with the facilities to build system residence, initialize volumes, copy VS/9 volumes from disc to tape or tape to disc, dump virtual memory and system files, and print the contents of a resident emergency dump tape. With the exception of dumping virtual memory and system files, these functions can also be performed during a standard VS/9 version.

Media conversion routines give the user additional facilities for displaying all or parts of files or volumes on a terminal (in an interactive task) or on a printer (for large volumes). The user can dump direct-access data to tape, reload direct-access data from tape, or copy data from one direct-access volume to another. In addition, he can initialize direct-access volumes for use by the file management system. A comprehensive set of utilities enables the user to perform device-to-device conversion for his files.

OS/4 UTILITY ROUTINES: An OS/4 Sort/Merge program capable of using disc and/or tape drives is available. It can sort fixed or variable-length records in either ascending or descending sequence, and includes provisions for the user's own coding. Disc-only sorts require enough disc capacity to hold all the records to be sorted plus sort control information. Tape-only sorts require 3 to 14 tape units, with no more than 6 tapes used for string collating. Tape/disc sorts use the disc drives to increase the length of the strings before collation is done on tape. The program's operation can be controlled by parameters entered either when the sort is generated or at run time. Up to 255 noncontiguous keyfields can be specified, using shared input devices, if desired, and reserved output devices. The COBOL SORT verb generates a linkage to the Sort/Merge utility program.

A Linkage Editor combines object modules produced by the COBOL, FORTRAN, RPG, or Assembly language translators into "load modules" which are suitable for loading and execution under OS/4 control.

Library Service routines facilitate the creation and maintenance of various types of libraries on tape and disc for OS/4.

Other available utility programs for the OS/4 operating system include data transcription routines, comprehensive data utilities to copy data from any input device to any output device, file maintenance routines, a dynamic (snapshot) dump, a terminal (postmortem) dump, and tape and disc listing programs.

VS/9 INTERACTIVE SERVICE PROGRAMS: The VS/9 interactive service programs provide functions and facilities designed to simplify the user's task of interfacing with his data files and programs. These programs include the Virtual Storage Editor (EDT), and Interactive Debugging Aid (IDA), the COBOL Program Development System (CODE) and Test File Generator (TFG), the Desk Calculator, and Sort/Merge.

EDT can be executed in either the batch or interactive mode, and will read or write SAM or ISAM files wholly or in part. With this editor it is possible to create, copy, delete, compare, and concatenante files, and to add, delete, and modify text within files. It also provides comprehensive facilities for defining procedures and for searching and restructuring files. EDT operates on text in virtual storage, and therefore makes very few references (or accesses) to physical storage devices. While processing the data in virtual storage, the originating data file, if any, is closed, serving as a backup file fully protected from any system failure that might cause an opened file to become inaccessible for the session. EDT also supports an edit-on-disc mode for very large files.

IDA provides the user with the facility for testing and modifying programs written in assembler, COBOL, or FORTRAN without having to include the debugging statements at compilation or assembly time. The user can use the same symbols for debugging that were used in writing the program. IDA can be used in batch or interactive mode. Commands are provided to start, stop, and resume execution of the object program; to examine the program and its results; and to modify the contents of virtual storage; IDA supports an audit mode which records the recent branches a program has taken.

IDA also provides the capability to modify the contents of the 16 general-purpose registers in the processor, and provides facilities for users to trap any error that would normally terminate program execution. Users can insert temporary logic changes through a symbolic IF statement that is similar, functionally, to a FORTRAN or COBOL source statement.

CODE provides facilities through which the COBOL programmer, with a minimum command set, is able to utilize all the power of the system without having to learn every software component involved in the effort. All files required for the program are automatically maintained by the subsystems. The programmer can terminate at any time during the development of a program and return later. CODE automatically handles all interfaces with other VS/9 software components, allowing the programmer to concentrate on the immediate task of designing a program. Using a set of 28 commands, the programmer can create, modify, edit, and delete source program statements; maintain and update source program files; define his own abbreviations or use standard abbreviations for language elements; verify syntax and compile the program; retrieve and analyze compiler-generated diagnostic messages and correct the source; load and execute the program; save the compiled program in an object module library; debug the program using symbolic IDA facilities while applying corrections directly to the source program; and use the TFG to create test data for debugging the program.

The Test File Generator (TFG) is an enhancement to CODE. It uses its own set of CODE-oriented commands to allow the programmer to create public disc files of any record description containing virtually any combination of test data. Files created by the generator can also be used as data files by the assembler, FORTRAN, or RPG programs, with only a skeletal COBOL compilation as the prerequisite for producing them.

The VS/9 Sort/Merge (DSORT) runs as a Class II pageable program. It may run as either a batch or interactive task.

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The user defines control fields and specifies various options for a sorting or merging application in sort/merge control statements. The program may be invoked by other user programs that require a sort/merge capability. The major options available to the user include: full record sort, record selection sort, tag sort, and file merge. User-written subroutines can be incorporated to tailor the program for individual requirements such as nonstandard input files. All input and output operations are performed using the VS/9 file management system; thus any device support by FMS can be used for input and output. Discs are used for work areas, and the user has the interactive ability to aid in the planning of a sort/merge application and to control the subsequent execution of the program.

APPLICATIONS PROGRAMS: Applications programs available under OS/4 include a Pert Management Control System, Linear Programming, UNIS (bill of material, inventory control, planning, and scheduling), APT (automatically programmed tools), PROFITS (on-line savings and loan processing), LINCO III (typesetting and line justification), NEWSCOMP (on-line newspaper text editing and typesetting), WIMS (Wholesale Inventory Management System), UPACS (UNIVAC Patient Accounting System), and Biomedical Programs (a collection of general-purpose statistical and mathematical programs).

Applications programs currently available under VS/9 include STAT-9 (statistical programs written in BASIC); Biomedical Statistical Series; Engineering Series (including Coordinate Geometry System, Continuous Systems Simulation, Electronic Circuit Analysis, etc.); Financial Series (including Bond Pricing and Bond Yield, Cash Flow Analysis, Investment Analysis, Depreciation Analysis, Loan or Purchase Analysis, Loan Interest Rate Analysis, Loan Repayment, Mortgage Fact Finder, Proforma Statement Analysis, and Return on Investment); Industrial Series (including Jobfit, Pallet, and Princeton Interactive Automatically Programmed Tools); Integrated Civil Engineering System (ICES); Management Sciences (including Exponential Smoothing, Forecasting, Linear Programming, and Critical Path Scheduling); Mathematical Series (including differential equations, integration, functional evaluation, function approximation, matrix operations, roots of functions, etc.); Scientific Subroutine Series (algorithms programmed in FORTRAN for inclusion in user programs through FORTRAN CALL statements); Test of Hypothesis Series; AUTOFORM automatic text formatting system; UNIS/90 (Univac Industrial System); and WIMS (Wholesale Inventory Management System).

PRICING

EQUIPMENT: All necessary control units and adapters are included in the indicated prices for the following configurations, and the quoted one-year rental prices and five-year lease prices include equipment maintenance.

UNIVAC 90/60 BASIC SYSTEM: Consists of 524K-byte 90/60 Processor (with virtual storage, one multiplexer channel, two selector channels, floating-point hardware, storage protection, and VS/9 software), console, 1000-cpm card reader with control, 250-cpm card punch, 1400-lpm printer, 600 megabytes of disc storage, and four 96KB, 1600-bpi, phase-encoded tape units. Purchase price is $677,200, monthly rental on a one-year contract is $19,588, and the monthly rate on a five-year lease is $17,134.

UNIVAC 90/60 MEDIUM SYSTEM: Consists of 1048K-byte 90/60 Processor (with virtual storage, one multiplexer channel, two selector channels, floating-point hardware, storage protection, and VS/9 software), console, 1000-cpm card reader, 250-cpm card punch, 1400-lpm printer, 1000 megabytes of disc storage, and four 192KB, 1600-bpi, phase-encoded tape units. Purchase price is $878,462, monthly rental on a one-year contract is $25,397, and the monthly rate on a five-year lease is $22,217.

UNIVAC 90/70 LARGE SYSTEM: Consists of 1572K-byte 90/70 Processor (with virtual storage, one multiplexer channel, four selector channels, floating-point hardware, storage protection, and VS/9 software), console, 1000-cpm card reader, 250-cpm card punch, two 1400-lpm printers, six 320KB, 1600-bpi, phase-encoded tape units, and 1200 megabytes of disc storage. Purchase price is $1,678,582, monthly rental on a one-year contract is $44,115, and the monthly rate on a five-year lease is $38,535.

UNIVAC 90/80-3 MEDIUM SYSTEM: Consists of 1048K-byte 90/80 Processor (with virtual storage, four block multiplexer channels, one byte multiplexer channel, floating-point hardware, storage protection, and VS/9 software), console, 1000-cpm card reader, 250-cpm card punch, two 1400-lpm printers, six 320KB, 1600-bpi, phase-encoded tape units, and 1200 megabytes of disc storage. Purchase price is $2,156,662, monthly rental on a one-year contract is $52,716, and the monthly rate on a five-year lease is $45,973.

UNIVAC 90/80-4 TYPICAL SYSTEM: Consists of a 90/80-4 processor with 2 megabytes of main memory, 32K bytes of high-speed buffer memory, peripheral processor, byte multiplexer channel, four block multiplexer channels, operator console, and motor/alternator; a 2028-megabyte disk storage subsystem consisting of four 307-megabyte 8450 disk drives, four 200-megabyte 8433 disk drives, and a dual-access disk control; a dual-access magnetic tape subsystem consisting of four 780-KBS Uniservo 34 magnetic tape units, four 470-KBS Uniservo 32 magnetic tape units, and a dual-channel controller; two 2000-lpm 0770-05 line printers; a 1000-cpm 0716-02 card reader; a 250-cpm 0604 card punch; and a communications control with 40 lines. Purchase price is $2,577,456, monthly rental is $56,939 on a five-year lease, and maintenance charges are $9,435 per month.

UNIVAC 90/80-4 LARGE SYSTEM: Consists of 2096K-byte 90/80 Processor (with virtual storage, five block multiplexer channels, one byte multiplexer channel, floating-point hardware, storage protection, and VS/9 software), console, 1000-cpm card reader, 250-cpm card punch, two 1400-lpm printers, 1600 megabytes of removable disc storage, and 6 megabytes of fixed-head disc storage. Purchase price is $3,131,122, monthly rental on a one-year contract is $66,101, and the monthly rate on a five-year lease is $57,093.

SOFTWARE AND SUPPORT: UNIVAC is still largely "bundled," and the equipment prices listed above include most of the UNIVAC software described in this report and all normal educational courses and professional assistance. However, in July 1975, UNIVAC released separate monthly rental prices for the UNIS (UNIVAC Industrial System) application program for 90/60, 90/70, and 90/80 systems operating under OS/4. Monthly charges for the UNIS modules are as follows: UNIS Master Data Processor—$75; UNIS Production Planning and Scheduling—$100; UNIS Inventory Mangement—$75, and UNIS Work Order Management—$25. The APL/90 program product is priced at $200 per month.

CONTRACT TERMS: The standard UNIVAC use and service agreements allow unlimited use of the equipment (exclusive of the time required for remedial and preventive maintenance). There are no extra-use charges. The basic maintenance charge covers maintenance of the equipment for nine consecutive hours a day between the hours of 7 a.m. and 6 p.m., Monday through Friday. Extended periods of maintenance are available at premium rates. The premiums for additional coverage are a percentage of the base maintenance rate and are as follows:

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Hours of Coverage

	4	8	9	10	12	16	18	20	24
Monday through Friday	—	—	0	10	20	25	35	40	45
Saturday	5	8	9	—	11	12	—	14	15
Sunday and Holidays	7	10	12	—	14	16	—	18	20

Maintenance service performed outside the contracted maintenance period is subject to the following rates:

	Monday through Saturday	Sunday and Holidays
Min. charge per call	$108	$128
Each add'l. hour	54	64
Max. charge per call	270	320

For users who elect not to contract for maintenance with Univac, the following per-call rates apply:

	Monday through Friday	Overtime and Saturday	Sunday and Holidays
Min. charge	$96	$108	$128
Each add'l. hour	48	54	64

On-call maintenance is also subject to travel time and expense charges.

UNIVAC offers reduced maintenance rates for multiple-processor installations. The percent premiums listed below apply to installations containing two or more processors or systems of the same type and located at the same address.

Two-Processor Installation
Hours of Coverage

	9	16	24
Monday through Friday	0	15	27.5
Saturday	6	8	10
Sunday and Holidays	7.5	10	12.5

Three or More Processors
Hours of Coverage

	9	16	24
Monday through Friday	0	12	22
Saturday	5	6.5	8
Sunday and Holidays	6	8	10

LONG-TERM LEASES: In addition to the basic 1-year agreement, UNIVAC offers an extended-term 5-year lease for 90/60, 90/70, and 90/80 systems at significantly lower monthly rates. Under the 5-year "level-payment" agreement, the monthly equipment charge is 85% of the 1-year rental rate shown in the accompanying price list. Under a 5-year "reducing-payment" agreement, the monthly charge is 95% of the 1-year rental rate during the first year, 90% the second year, 85% the third year, 80% the fourth year, and 75% the fifth year. Maintenance is not discounted under these plans.

UNIVAC also offers a 7-year lease to state and local governments and to educational institutions. Educational institutions are eligible for an additional 10 percent discount. The discount does not apply to maintenance service charge.■

EQUIPMENT PRICES

		Purchase Price	Monthly Maint.	Rental (1-year lease)*	Rental (5-year lease)*
90/60 PROCESSOR AND MAIN STORAGE					
3024-91	90/60 Processor with 524,288 bytes of main storage, one multiplexer channel, one selector channel, general register stack, floating-point controls, channel programming, storage protection, and two interval timers; expandable to 2,097,152 bytes Note: Minimum system for use with OS/4 or VS/9 requires processor; system console; minimum 8414, 8424, 8425, 8430, or 8433 disc storage subsystem; minimum magnetic tape subsystem; card reader; and printer	284,184	1,094	7,894	6,710
7025-81	Storage Expansion for 3024-91 processor; 262,144 bytes	46,800	200	1,570	1,100
7025-79	Storage Expansion; 524,288 bytes; expands main storage from 1,048,576 to 1,572,864 bytes	65,520	400	3,140	1,540
3024-89	90/60 Processor; same features as 3024-91 processor, but with 1,048,576-byte memory; offers 30 percent savings over 3024-91 processor with comparable memory; expandable to 2,097,152 bytes	349,700	1,494	11,034	8,250
F 2629-00	Performance Enhancement Option (increases instruction execution speed of 90/60 processor by 25 percent	9,000	25	250	210
90/70 PROCESSOR AND MAIN STORAGE					
3024-95	90/70 Processor with 131,072 bytes of main storage, one multiplexer channel, one selector channel, general register stack, floating-point controls, storage protection, and two interval timers; expandable to 2,097,152 bytes Note: Minimum system for use with VS/9 requires processor with 262,144 bytes main memory; system console; minimum 8414, 8424, 8425, or 8430 disc storage subsystem; minimum magnetic tape subsystem; card reader; and printer Minimum system for use with OS/4 requires processor with 131,072 bytes main memory; system console; minimum 8411, 8414, 8424, or 8425 disc storage subsystem; card reader; and printer	398,304	1,361	11,064	9,400

* Rental prices do not include equipment maintenance.

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EQUIPMENT PRICES

	90/70 PROCESSOR AND MAIN STORAGE (Continued)	Purchase Price	Monthly Maint.	Rental (1-year lease)*	Rental (5-year lease)*
7025-99	Storage, 65,536 bytes; expands main storage from 131,072 to 196,608 bytes	25,704	143	870	610
F1775-98	Storage; 65,536 bytes; expands main storage from 196,608 to 262,144 bytes	25,704	143	870	610
7025-98	Storage; 131,072 bytes; expands main storage from 262,144 to 393,216 bytes	30,600	229	1,035	725
7025-97	Storage; 131,072 bytes; expands main storage from 393,216 to 524,288 bytes	27,900	229	940	660
7025-81	Storage; 262,144 bytes; expands main storage by 262,144 bytes; requires that 524,288 bytes of main storage be present in the system	46,800	200	1,570	1,100
	90/60 AND 90/70 PROCESSOR FEATURES				
F1519-00	Expanded interface; expands multiplexer to 15 subsystems (16 if F1518-00 is present); available on 90/70 processor only	5,364	19	149	125
F1518-00	Subchannel Expansion; expands multiplexer up to 31 channels	1,800	6	50	43
F1337-99	Selector Channel; 833 KBS; includes channel programming and storage protection; selector channels 3, 4, and 5 require 1916-00 channel expansion cabinet	8,748	32	243	205
1916-00	Channel Expansion Cabinet for third and fourth F1337-00 selector channels	8,568	32	238	203
F1335-00	Direct Control; interface for another 9000 Series processor plus 2 instructions for transfer of control information	3,564	11	99	85
F1591-00	Programmable Emulator; provides programmable control for SMOOTH operation using special hardware instructions	9,900	65	275	235
4014-99	System Console; includes Uniscope 100 CRT; may be expanded by addition of one 0772-00 console printer and multichannel switch	20,268	92	563	480
0772-00	Console Printer; 30 cps	9,900	59	275	235
	90/80 PROCESSORS AND MAIN STORAGE				
3036-98	90/80 Processor with 524,288 bytes of main storage, peripheral processor with one byte multiplexer channel and four block multiplexer channels, key-in storage protection, power distribution panel, and motor alternator Note: Minimum system for use with VS/9 requires system console, minimum disc storage subsystem, minimum magnetic tape subsystem, card reader, and printer Minimum system for use with OS/4 requires F1915-00 90/60-90/70 mode, system console, printer, minimum disc storage subsystem, minimum magnetic tape subsystem, card reader, and printer	1,080,000	3,120	22,500	19,100
F2352-00	Storage; 524,288 bytes; expands main storage from 524,288 to 1,048,576 bytes	218,400	455	5,530	3,870
F2353-99	Storage; 524,288 bytes; expands main storage from 1,048,576 to 1,572,864 bytes	96,300	413	3,250	2,275
F2352-98	Storage; 524,288 bytes; expands main storage from 1,572,864 to 2,097,152 bytes	93,600	400	3,160	2,210
F2352-97	Storage; 524,288 bytes; expands main storage from 2,097,152 to 2,621,440 bytes	96,300	413	3,250	2,275
F2352-96	Storage; 524,288 bytes; expands main storage from 2,621,440 to 3,145,728 bytes	93,600	400	3,160	2,210
F2352-95	Storage; 524,288 bytes; expands main storage from 3,145,728 to 3,670,016 bytes	96,300	413	3,250	2,275
F2352-94	Storage; 524,288 bytes; expands main storage from 3,670,016 to 4,194,304 bytes	93,600	400	3,160	2,210
3044-93	90/80-2 Processor with 1,048,576 bytes of main storage, peripheral processor with one byte multiplexer channel and four block multiplexer channels, key-in storage protection, system console, power distribution panel, and motor alternator; expandable to 2,097,152 bytes in one 1-MB increment	798,250	2,100	20,665	15,500
F2756-01	90/80-2 to 90/80-3 Upgrade option for 90/80-2 processor with 2,097,152 bytes of main storage; requires F2672-99 storage expansion; subsequent expansion of the processor and peripherals must be as a 90/80-3 system	87,550	100	2,265	1,700
F2672-99	Storage; expands main storage of 90/80-2 processor from 1,048,576 to 2,097,152 bytes	180,250	500	4,670	3,500
3044-96	90/80-3 Processor with 2,097,152 bytes of main storage, peripheral processor with one byte multiplexer channel and four block multiplexer channels, key-in storage protection, system console, power distribution panel, and motor alternator; expandable to 4,194,304 bytes in 1-MB increments	1,019,700	2,700	26,400	19,800
F2672-98	Storage; expands main storage of 90/80-3 processor or equivalent from 2,097,152 to 3,145,728 bytes	159,650	400	4,135	3,500
F2672-97	Storage; expands main storage of 90/80-3 processor or equivalent from 3,145,728 to 4,194,304 bytes; requires F2672-98 storage expansion	159,650	400	4,135	3,100

* Rental prices do not include equipment maintenance.

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EQUIPMENT PRICES

	90/80 PROESSORS AND MAIN STORAGE (Continued)	Purchase Price	Monthly Maint.	Rental (1-year lease)*	Rental (5-year lease)*
F2743-99	90/80-3 to 90/80-4 Upgrade	236,900	600	6,135	4,370
3044-86	90/80-4 Processor with 2,097,153 bytes of main storage; 32K-byte buffer storage, one byte multiplexer channel, four block multiplexer channels; two-way memory interleave and system console; expandable to 8,388,608 bytes in 2-MB increments	1,256,600	3,300	32,535	23,180
F2672-95	Storage; 1,048,576 bytes; expands main storage from 2,097,152 to 3,145,728 bytes	180,250	500	4,670	3,325
F2672-94	Storage; 1,048,576 bytes; expands main storage from 3,145,728 to 4,194,304 bytes	159,650	400	4,135	2,945
F2672-93	Storage; 1,048,576 bytes; expands main storage from 4,194,304 to 5,242,880 bytes	159,650	400	4,135	2,945
F2672-96	Storage; 2,097,152 bytes; expands main storage by 2 MB	319,300	800	8,270	5,890
	90/80 PROCESSOR FEATURES				
0782-99	Console Printer	9,900	59	313	235
F1920-00	Block Multiplexer Channel	15,840	33	330	280
F1922-01	Byte Multiplexer Channel	16,800	35	350	300
F2011-00	Extended interface; expands byte multiplexer channel interface to provide capability for up to 16 subsystems	1,440	5	30	25
F1921-00	Subchannel expansion; provides 256 additional block multiplexer subchannels				
F1914-00	Direct control; provides an interface between a 90/80 and another 90/60, 90/70, or 90/80; requires comparable feature in other processor	4,800	10	100	85
F1915-00	90/60, 90/70 Mode; provides capability for 90/80 to operate as a 90/60 or 90/70 processor; required for operation with OS/4	4,800	10	100	85
	MASS STORAGE				
8405-00	Fixed-Head Disc (single 8405 disc with a storage capacity of 6,193,152 bytes; F2076-00 is prerequisite)	76,800	436	1,845	1,200
8405-04	Fixed-Head Disc (single 8405 disc with a storage capacity of 3,096,576 bytes; F2076-00 is prerequisite)	46,080	262	1,110	720
F-1664-00	Dual Access (provides dual access and simultaneous read/write operation on any two 8405 disc drives, required on each 8405 disc in the subsystem; also requires two 5039 control units)	2,160	5	52	34
5039-97	Control Unit; controls up to eight 8433 and/or 8430 Disc Storage Drives, Minimum of two 8433 or two 8430 drives required per subsystem. May be expanded to control up to sixteen 8433 and/or 8430 Disc Drives via F2076-00, or to control up to eight 8405-00/04 Fixed Head Disc Drives via F2076-00	57,600	294	1,385	900
F2047-00	16-Drive Expansion; provides the capability to attach up to sixteen 8433 and/or 8430 Disc Storage Drives to a 5039 Control	7,680	40	185	120
F2076-00	8405 Capability; adds capability to control up to eight 8405-00/04 Fixed Head Disc Drives	2,160	5	52	34
8430-00	Disc Drive provides a single disc drive, 100 MB	24,960	128	600	390
F2020-00	8430 Dual Access; provides dual access and simultaneous read and write operations on any two disc drives; required on both disc drives in the subsystem; also requires two Model 5039 Control Units and two selector channels	2,160	5	52	34
F2046-00	Dual Channel; provides non-simultaneous access to one Model 5039 Control Unit from two selector channels	4,080	16	85	72
F1230-00	Disc Pack; provides up to 100 million bytes of removable storage	750	—	46	30
8433-00	Disc Drive; provides a single drive 200 MB	36,480	186	875	570
F1223-00	Disc Pack; 200 MB	1,150	—	58	38
F2021-00	8433 Dual Access (provides dual access and simultaneous read/write operation on any two 8433 Disc Drives. Required on each 8433 disc unit in the subsystem. Also requires two 5039 control)	2,160	5	52	34
5024-00	8424/8435 Disc Control	57,072	438	1,189	1,010
8425-00	Disc Drive; 58 million bytes	21,216	121	442	375
F1043-00	Dual Channel	4,416	22	92	78
F2001-00	Dual Access for 8425-00 unit	2,304	5	48	40
F1214-01	Disc Pack for 8425 drives	433	—	21	18
8450-99	Disc Storage; provides two drives, 307 MB	66,600	226	2,140	1,390
8450-97	Disc Storage; provides two drives, 307 MB	74,600	250	2,390	1,590
F2717-99	8450 Fixed Head Conversion; converts 8450-99/98 to an 8450-97/96 by adding fixed-head storage to each spindle	13,600	24	250	200
F2718-99	8450 Dual Access; provides dual access and simultaneous read/write operations on any two 8450 disc drives; requires two 5040 controls				

*Rental prices do not include equipment maintenance.

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EQUIPMENT PRICES

	MASS STORAGE (Continued)	Purchase Price	Monthly Maint.	Rental (1-year lease)*	Rental (5-year lease)*
5040-95	8430/8433/8450 Control; controls up to eight 8450 disc drives; may be expanded to control up to eight 8430/8433 disc drives by adding F2836-00 capability or may be expanded to control up to sixteen 8450 disc drives by adding F2719-01 capability; F2836-00 and F2719-01 are mutually exclusive	102,000	400	2,700	1,800
5040-93	8430/8433/8450 Dual Control (two controls for 8450 disc drives); may be expanded to control up to eight 8430/8433 disc drives by adding F2836-00 capability, or may be expanded to control up to sixteen 8450 disc drives by adding F2719-01 capability; F2836-00 and F2719-01 are mutually exclusive	176,448	700	5,015	3,260
F2836-00	8430/8433 Capability; provides attachment of up to eight 8430 or 8433 disc drives to the 5040-95 control in addition to the basic eight 8450 disc drives	2,400	10	60	42
F2719-01	8450 Capability; provides attachment of an additional eight 8450 disc drives to the 5040-95 control	7,680	40	160	120
	INPUT/OUTPUT UNITS				
0861-00	Uniservo 12 Master Tape Unit; 9-track; 1600 bpi; 68.32 KB/second	18,336	165	382	325
F0934-99	Simultaneous Single-Density Feature for 0861-00; requires 2 controls	4,080	21	85	75
F0934-01	Simultaneous Dual-Density Bi-Model Feature for 0861-00; requires F0934-99 and F0935-00 to give simultaneous access to dual density bi-modal (7- or 9-track) slaves attached to same master unit; control units each require F0826-00 and F1028-95	4,608	21	96	80
F0935-00	Dual Density Bi-Modal Feature for 0861-00; control must have F0823-99; if 7-track slaves are present, control must also have F1028-95	2,688	15	56	48
0861-01	Uniservo 12 Slave Tape Unit; 9-track; 1600 bpi; 68.32 KB/second (3 slaves may be used with 1 master unit)	14,688	113	306	260
0861-04	Uniservo 12 Master Tape Unit; 7-track; 200, 556, or 800 bpi; 8.54, 23.74, or 34.16 KB/second	15,936	165	332	280
F0934-98	Simultaneous Single-Density Feature for 0861-04; requires 2 control units which each must contain F0823-99	4,080	21	85	75
F1041-00	7- to 9-track Conversion Feature for 0861-04; converts to 0861-00	2,448	0	51	44
F1041-01	Simultaneous 7- to 9-Track Conversion Feature for 0861-04 with F0934-98; converts to 0861-00 with F0934-99	2,448	0	51	44
0861-05	Uniservo 12 Slave Tape Unit; 7-track; 200, 556, or 800 bpi; 8.54, 23.74, or 34.16 KB/second (3 slaves may be used with 1 master unit)	13,056	113	272	230
F1042-00	7- 9-Track Conversion Feature for 0861-05; converts to 0861-01	1,632	0	34	30
5017-99	Uniservo 12 Non-Simultaneous Control for up to 16 Uniservo 12 drives; 9-track; 1600 bpi	26,448	139	605	440
5017-00	Uniservo 12/16 Non-Simultaneous Control for up to 16 Uniservo 12 and/or 16 drives; 9-track; 1600 bpi	28,560	152	655	476
F1131-99	Uniservo 16 Capability for 5017-99	2,112	12	44	37
F1029-99	Simultaneous Single-Density Access for 5017-99; provides second control module	16,896	82	352	300
F1029-00	Simultaneous Single-Density Access for 5017-00; provides second control module	18,960	95	395	335
F0823-99	7-Track NRZI, for 5017-00 or 5017-99 OSC PIO	5,760	24	120	100
F1028-95	Bi-Modal (7- or 9-Track) NRZI for 5017-00 or 5017-99 with F0826-00	4,176	15	87	70
F1028-96	Bi-Modal (7- or 9-Track) NRZI for 1017-00 or 5017-99 with F0823-99	4,176	15	87	70
F0825-00	Non-Simultaneous Dual Channel Feature for 5017-00 or 5017-99	4,416	24	92	74
0862-00	Uniservo 16 Magnetic Tape Unit; 9-track; 1600 bpi; 192 KB/second; requires 5034-00 control	22,032	156	505	370
0862-02	Uniservo 16 Magnetic Tape Unit; 7-track; 200, 556, or 800 bpi; 24, 66.72, or 96 KB/second; requires 5034-00 or 5017-00 control	22,032	156	505	370
F0936-99	Simultaneous Feature for 0862-00 or 0862-02; requires 2 controls	914	0	21	17
F0937-00	Dual-Density Feature for 0862-00; control(s) must contain F0826-00 or F1028-96	2,284	0	51	40
F1040-00	7- to 9-Track Non-Simultaneous Conversion Feature (for 0862-02); converts to 0862-00	0	0	0	0
5045-99	Uniservo 14 Control; includes control and cabinet space for 2 Uniservo 14 Magnetic Tape Units	21,168	120	441	355
5045-02	Auxiliary Cabinet; for 1 or 2 additional Uniservo 14 Magnetic Tape Units	1,296	5	27	22
F0823-99	7-Track NRZI; for 5045-99 Control	5,760	24	120	100
F0825-00	Dual-Channel; permits nonsimultaneous operation on 2 channels of 1 processor or 1 channel on each of 2 processors	4,416	24	92	74
F0826-00	9-Track NRZI; permits 9-track phase-encoded operation	5,760	24	120	100
F1028-96	Adds 9-track NRZI to F0823-99 or F1753-99	4,176	15	87	70
F1028-95	Adds 7-track NRZI plus data conversion to F0826-00	4,176	15	87	70
F1028-92	Adds 7-track NRZI native mode plus data conversion to F0826-00	3,654	11	82	66
F1753-99	Provides capability to add 7-track tape units to 5045-99 control	5,760	20	120	96

*Rental prices do not include equipment maintenance.

UNIVAC 90/60, 90/70, and 90/80

EQUIPMENT PRICES

	INPUT/OUTPUT UNITS (Continued)	Purchase Price	Monthly Maint.	Rental (1-year lease)*	Rental (5-year lease)*
0870-03	Uniservo 14 9-track PE Magnetic Tape Unit	14,880	87	310	250
0870-05	Uniservo 14 7-track NRZI Magnetic Tape Unit	14,880	87	310	250
F2194-00	Dual Density; adds 9-track NRZI to Uniservo 14 PE Magnetic Tape Unit; requires F0826-00 in control	1,200	6	25	20
F2194-02	Converts 0870-05 7-track NRZI Magnetic Tape Unit into 9-track PE ($106 field installation charge)	—	—	—	—
F2194-03	Converts 0870-05 7-track NRZI Magnetic Tape Unit into 9-track PE and NRZI	1,200	6	25	20
0864-00	Uniservo 20 Magnetic Tape Unit; 9-track; 1600 bpi, 320 KB/second	27,696	186	635	460
F1510-00	Dual Access and Simultaneous Feature for 0864-00; requires 2 controls	2,448	13	51	41
5034-00	Uniservo 20 Non-Simultaneous Control for up to 16 9-track; 1600 bpi requires 2 controls for dual access	36,720	134	840	610
F0823-98	7-Track NRZI for 5034-00; adds bi-modal 7- or 9-track capability to control; may not be used with F0826-99	5,544	22	113	90
F0826-99	9-Track NRZI for 5034-00; adds dual-density 800 or 1600 bpi to control; may not be used with F0823-98	6,552	22	133	105
F1028-97	Bi-Modal (7- or 9-track) NRZI for 5034-00 with F0826-99	4,536	13	92	75
F1028-98	Bi-Modal (7- or 9-track) NRZI for 5034-00 with F0823-98	5,544	22	113	90
5042-00	Uniservo 30 Series Control; controls up to eight dual density (GCR/PE) Uniservo 32 and/or Uniservo 34 Tape Units	55,392	288	1,170	865
F2131-00	9-Track NRZI; enables read or write operation in 9-track NRZI mode at a density of 800 bpi	3,648	19	76	57
F2132-99	7 Track NRZI; enables read or write operation in 7-track NRZI mode at densities of 800, 556, or 200 bpi	1,824	10	38	29
F2135-00	Dual Channel; provides an additional I/O interface for the 5042-00 Control	6,000	32	125	94
F2137-00	16 Drive Addressing; adds 16 drive addressing capabilty; must be added to all the control units in the subsystem	960	5	20	15
0872-00	Uniservo 30; 9-track Phase Encoded/NRZI Tape Unit	34,800	181	780	545
0872-02	Uniservo 30; 7 Track Non-Return to Zero (NRZI) Tape Unit	34,800	181	780	545
F2123-00	7 to 9-Track Conversion; converts 7-track Type 0872 Tape Unit to 9-track Type 0872 Tape Unit	3,774	0	79	59
0873-00	Uniservo 32; 9-Track Dual-Density Group Coded Recorded/Phase Encoded Tape Unit	31,584	164	725	495
0873-02	Uniservo 34; 9-Track Dual-Density Group Coded Recorded/Phase Encoded Tape Unit	36,192	188	830	565
F2125-00	Tape Speed Conversion; converts a Uniservo 32 Tape Unit (0873-00) at 75 ips to a Uniservo 34 Tape Unit (0873-02) at 125 ips	4,608	24	96	72
0874-00	Uniservo 36; 9-Track Dual-Density Group Coded Recorded/Phase Encoded Tape Unit	38,880	202	890	605
0604-99	Card Punch and Control; 250 cpm	26,640	170	555	470
F0875-00	Read/Punch Feature for 0604-99	7,152	68	149	130
0716-95	Card Reader and Control; 600 cpm	12,192	73	254	190
0716-99	Card Reader and Control; 1000 cpm	15,504	121	323	275
F1487-00	Short Card Feature; 51 columns	1,968	13	41	30
F1487-01	Short Card Feature; 66 columns	1,968	13	41	30
F1488-00	Validity Check Feature	816	0	17	13
F1498-00	Alternate Stacker Fill Feature	528	0	11	8
F1530-99	Dual Translate; additional ASCII translator	1,104	5	23	17
0770-00	Printer, 800 lines per minute	56,304	268	1,173	940
0770-02	Printer, 1400 lines per minute	64,896	351	1,352	1,080
0770-04	Printer, 2000 lines per minute	86,686	447	2,220	1,445
F1533-00	160 Print Positions	4,416	19	92	74
F1534-00	Expanded Character Set Control (required for other than 1536-00 or -01 Print Cartridges)	2,880	5	60	48
F1536-00	48-character alphanumeric Business	462	—	22	18
F1536-01	48-character alphanumeric Scientific	462	—	22	18
F1537-00	94-character ASCII	462	—	22	18
F1537-03	64-character universal ISO OCR-B	462	—	22	18
F1537-04	64-character universal OCR H-14	462	—	22	18
F1537-05	58-character COBOL-FORTRAN-Business	462	—	22	18
F1537-06	177-character international	462	—	22	18
F1537-09	24-character Numeric	462	—	22	18
F1537-11	68-character universal OCR-A	462	—	22	18
F1537-12	68-character universal OCR-B	462	—	22	18
F1537-13	68-character universal 77L	462	—	22	18
2703-00	Optical Document Reader; 300 dpm	47,664	281	993	—
F1108-00	600-dpm Speed Upgrade for 2703-00	12,000	48	250	—
F1163-00	Modulus 10 Check Digit for 2703-00	1,104	5	23	—
F1106-00	Mark Read—EBCDIC for 2703-00	9,024	56	188	—
F1106-01	Mark Read—ASCII for 2703-00	9,024	56	188	—
F1149-00	Punch Card Read Feature for 2703-00; requires F1106-00 or -01	3,024	13	63	—
F1154-00	Validity Check Feature for 2703-00	528	0	11	—

*Rental prices do not include equipment maintenance.

UNIVAC 90/60, 90/70, and 90/80

EQUIPMENT PRICES

		Purchase Price	Monthly Maint.	Rental (1-year lease)*	Rental (5-year lease)*
DATA COMMUNICATIONS SUBSYSTEMS					
F1395-00	Voice-Grade Communications Interface for Series 90 Processor; coordinates a BSC line and a 201A, 201B, 202C, or 202D-type modem at up to 19,000 bits/second	768	5	16	12
F1395-01	Telpak Communications Interface for Series 90 Processor; coordinates BSC line and a 301B, 303B, 303C, or 303D-type modem	2,064	5	43	32
8577-02	DCS Cabinet; provides power supply and housing for up to 4 DCS-1 or 1C	2,976	5	62	47
F1000-00	Line Terminal-1 for DCS-1; controls 1 duplex line	4,799	18	113	85
8575-00	Line Terminal Control-4 for DCS-4; controls 4 duplex lines	12,432	55	259	195
8575-01	Line Terminal Control-16 for DCS-16; controls 14 duplex lines	26,208	117	546	410
F1357-01	Line Terminal Control 1C for binary synchronous; not supported by UNIVAC software	6,432	33	134	100
NOTE: Numerous line terminals, communications interfaces, and optional features enable the above controls to accommodate a wide range of communications facilities and equipment.					
MULTI-CHANNEL COMMUNICATIONS CONTROLLER					
8579-86	Multi-Channel Communications Controller (MCC 1); includes processor with 32K bytes of storage expandable to 65K bytes, real-time clock, power protect, interface to CPU multiplexer channel, operator console with CRT and keyboard, and Scanner 1 for attachment of up to 16 half or full duplex lines	52,416	193	1,092	820
8579-85	Multi-Channel Communications Controller (MCC 1A); same as MCC 1 except includes Scanner 1 expansion F2262-99	61,872	226	1,289	966
8579-84	Multi-Channel Communications Controller 2 (MCC 2); includes processor with 32K bytes of storage expandable to 65K bytes, real-time clock, power protect, interface to CPU multiplexer channel, operator console with CRT and keyboard, and Scanner 2 for control of 16 half or full duplex communications lines, expandable to a maximum of 59 half duplex or 29 full duplex lines	78,816	284	1,642	1,230
F2262-99	MCC 1 to MCC 1A Expansion (MCC 1 is prerequisite)	9,456	33	197	150
F1800-01	Manual Channel Switch (provides capability to switch an MCC between multiplexer channels of two Series 90 host processors)	4,305	14	83	62
F1793-00	16K-Byte Storage Expansion (maximum of two per MCC)	10,300	32	250	195
F2264-00	16-Port Parameter Module (provides MCC 2 with high-speed register storage for up to 16 half duplex or 8 full duplex lines; maximum of one per MCC 2; excludes F2264-01 and -02)	1,584	5	33	25
F2264-01	64-Port Parameter Module (provides MCC 2 with high-speed register storage for up to 59 half duplex or 29 full duplex lines; maximum of one per MCC 2; excludes F2264-00 and -02)	2,496	8	52	39
F2264-02	128-Port Parameter Module (provides MCC 2 with high-speed register storage for up to 59 half duplex or 29 full duplex lines; maximum of one per MCC 2; excludes F2264-00 and -01)	3,456	11	72	54
F2263-00	Line Adapter Chassis (expands number of line adapter positions of MCC 2 from 32 to 64; maximum of one per MCC 2)	2,360	9	59	44
F2263-01	Line Adapter Chassis Expansion (expands number of line adapter positions of MCC 2 from 64 to 96 or from 96 to 128)	1,120	4	28	21
F1825-02	Line Indicator—Type II (provides visual display of line activity on up to 16 half duplex or 8 full duplex lines; maximum of eight per MCC 2)	440	2	11	9
F1801-01	Line Base II (provides interface and control for up to 16 line adapters on MCC 2; maximum of seven per system)	600	3	15	11
F1796-00	Dual Dial Adapter—Type 1 (provides interface to two Bell 801 ACU's; maximum of four per MCC 1 or eight per MCC 1A)	872	5	23	18
F1798-01	Line Adapter—Asynchronous Type 1; contains two full duplex or half duplex serial modem interfaces (RS-232-C and CCITT-V24); attaches to MCC 1 or MCC 1A	630	4	18	14
F1799-00	Line Adapter—Synchronous Type 1; contains two full duplex or half duplex serial modem interfaces (RS-232-C and CCITT-V24) for MCC 1 or MCC 1A	900	4	24	19
F1799-01	Line Adapter—Synchronous Type 1 (compatible with MIL 188 B/C)	900	4	24	19
F1814-00	Wide-Bank Adapter—Type 1 (provides capability to connect two synchronous full duplex or half duplex lines for operation at 19.2, 40.8, or 50 kilobits per second)	1,743	11	45	35
F1866-00	Active Line Indicator I (displays the line activity on data sets connected to the MCC 2 or MCC 2A; includes capacity for 16 displayed lines; maximum of one per MCC 1 or two per MCC 1A)	528	2	12	9
F1828-00	Asynchronous Line Adapter—Type II (provides full duplex or half duplex interface to asynchronous data sets conforming to RS-232-C and CCITT-V24 and V28)	600	6	15	13
F1828-01	Asynchronous Line Adapter—Type II (same as F1828-01 but also provides a reverse channel of up to 5 bps asynchronous for Bell 202 type modems)	760	7	19	16

*Rental prices do not include equipment maintenance.

UNIVAC 90/60, 90/70, and 90/80

EQUIPMENT PRICES

MULTICHANNEL COMMUNICATIONS CONTROLLER (Continued)		Purchase Price	Monthly Maint.	Rental (1-year lease)*	Rental (5-year lease)*
F1828-02	Asynchronous Line Adapter—Type II (same as F1828-00 but also provides a supervisory channel of up to 150 bps asynchronous)	920	8	23	20
F1829-00	Asynchronous Line Adapter—Type II (provides a full duplex or half duplex interface for compliance with MIL-STD-188C low-level interface)	600	6	15	13
F1826-00	Synchronous Line Adapter—Type II (provides a full duplex or half duplex interface to synchronous data sets conforming to RS-232-C and CCITT-V24 and V28)	760	7	19	16
F1826-01	Synchronous Line Adapter—Type II (same as F1826-00 but also provides a supervisory channel of up to 150 bps asynchronous)	1,160	8	29	25
F1827-00	Synchronous Line Adapter—Type II (provides a full duplex or half duplex interface for compliance with MIL-STD-188C low-level interface)	760	7	19	16
F1832-00	Asynchronous Relay Line Adapter—Type II (provides an asynchronous full duplex or half duplex interface optionally compatible with either 20-75 MA neutral or 10-40 polar telegraph lines)	600	6	15	13
F1830-00	Widebank Line Adapter—Type II (provides capability to connect one synchronous full duplex or half duplex line for operation at 19.2, 40.8, or 50 kilobits per second; used with AT&T 300 Series Data Sets)	920	8	23	20
F1831-00	Dial Adapter, Single—Type II (provide interface to one Bell 801 ACU)	600	6	15	13
F1834-00	Widebank Line Adapter (same as F1830-00 but conforms to CCITT V35)	920	8	23	20
F1836-00	Telex Line Adapter	600	6	15	13
F1835-00	TWX Line Adapter	600	6	15	13
F1840-00	Telex Adapter—International	720	6	15	13

*Rental prices do not include equipment maintenance.

SOFTWARE PRICES

		Monthly Rental
6504-00	UNIS 90 OS/4 Master Data Processor	75
6504-02	Inventory Management	75
6504-04	UNIS 90 OS/4 Production Planning and Scheduling and Work Order Management; requires 6504-00 and 6504-02 program products	125
6530-00	APL/90	200