

The 8510 A from Terak Corporation features a DEC-compatible Q bus, RT/11 software compatibility, and supports DEC's network architecture. The 8510/A offers 64K words of memory, floppy disk storage, serial printers, 8 communications lines and asynchronous communications to 19.2K bps. With one CRT, keyboard, and graphics capability, the 8510/A sells for \$7,850.

What's available today in the fast-moving minicomputer marketplace? What are the significant features of these machines? How can you tell whether a minicomputer will fit into your own data processing plans? And, if so, which of the many available models represents the best overall choice for you? This report is designed to answer these questions and bring you up to date on the rapidly advancing state of the art in minicomputers.

Dynamism and proliferation continue in the world of the minicomputer. We hear daily of a continual stream of new products entering the marketplace, with hardware and software that take on many names. We hear of minicomputers, microcomputers, programmable controllers, microprogrammable data entry units, intelligent terminals, accounting machines, large-scale programmable calculators, etc. We also regularly hear of old-line peripheral device and terminal manufacturers announcing their entry into the "minicomputer business" as they add programmable logic and memory to their formerly unintelligent, hard-wired devices.

The net result of all these happenings is, more often than not, confusion—at least when one tries to grasp the meaning or direction of the industry in any overall sense. The confusion may well be compounded when one sets out to satisfy a known in-house need and wonders where to begin looking for a specific minicomputer that will satisfy that need at the best available price.

This report on Minicomputer Specifications can cut through a lot of this confusion by providing a convenient way to scan quickly a comprehensive list of available minicomputers, together with their primary specifications and prices.

This report presents the salient characteristics of 246 minicomputers from 63 vendors. Prices and capabilities of these machines span a wide range, so prospective users should carefully check the details of this report and the accompanying comparison charts.

The comparison charts that follow can be effectively used to complete a comprehensive, first-level search of the minicomputer universe in just a few minutes. For example, if you want a minicomputer but know you can't pay more than \$5,000 for the basic CPU and memory, then you can quickly scan across the charts noting the entry "Price of CPU, power supply, front panel, and minimum memory in chassis" and jotting down the name and model number of each minicomputer that applies. Or, your requirements may be for a minicomputer that has a BASIC programming language in addition to removable disk pack storage. A similar quick scan across the entries called "Disk pack/cartridge drives" and "Compilers" will produce a complete list of those minicomputers that satisfy both requirements.

PLEASE NOTE that a similar presentation of the characteristics of minicomputers with a strong orientation toward business data processing applications is contained in the report immediately following this one. It is called "All About Small Business Computers" (70C-010-30a). Thus, to assure that your search will be complete, we suggest that you also scan that report because, as you know, categorical descriptions and definitions in the area of minicomputers can be difficult. What you may consider to be a small business computer, someone else may call a minicomputer, pure and simple. To be sure, therefore, we suggest you quickly scan both sets of charts.

Once your search has been narrowed in the manner described above, your task may be completed, or you may then want to know the full details about the minicomputers whose names you've extracted. If the latter is the case, then simply turn to the Index of this service and locate the detailed system report, which contains price data (down to the feature and option level), complete hardware and software descriptions, and our independent analysis of where each minicomputer really fits in the marketplace.

Whenever you seek more information on a minicomputer system that is not yet covered in the full report format in the Computers section of this service, such as might happen if the minicomputer were just announced, please contact us directly via the Inquiry Service and get the facts you need by telephone or mail.

A significant aspect of any evaluation and procurement cycle is to gather information about how well the product

has worked out for other customers. True, you are not likely to find someone with exactly your processing requirements or company/information set-up, but there will be similar elements. An important first step in gathering this information is presented in report 70C-010-40 "User Ratings of Minicomputers and Small Business Computers". This summary of the experience of hundreds of users with their minicomputers and small business computers will not replace the need for you to talk with existing users, but it will provide you with important insights about the strengths and weaknesses of the popular systems.

#### THE COMPARISON CHARTS

The key functional characteristics of 246 commercially available minicomputers from 63 manufacturers are presented in the accompanying comparison charts. Nearly all of the information in the charts was supplied and/or verified by the manufacturers during the months of November and December 1979; their close cooperation with the Datapro Research staff in the preparation of these charts is greatly appreciated.

The chart entries and their significance to potential minicomputer users are explained in the following paragraphs, together with some useful guidelines for selecting the most suitable minicomputer for your application.

#### Word Length

Probably the single most important distinguishing characteristic of a minicomputer is its word length, bits; i.e., the number of bits (binary digits) that can be stored in or retrieved from main storage during a single cycle. In general, the longer the word length, the greater the efficiency and accuracy of a computer's internal operations—and the higher its price tag. Most of the minicomputers currently on the market have a 16-bit word length; this size neatly accommodates two 8-bit bytes (characters) and has been shown to yield an attractive balance between economy and performance for many applications. Other widely used models have word lengths of 8, 12, 18, 24, or 32 bits. The 8-bit minicomputers are suitable for many functions where low cost is more important than high precision or sophisticated instruction repertoires—and they can be particularly effective when extensive manipulation of 8-bit bytes must be performed. Entries also indicate parity and error correction bits when applicable.

#### **Number of Workstations Supported**

A very important consideration for many users who are considering the acquisition of a minicomputer is the number of workstations it can support. Workstations, in this case, can mean most any type of device which can input and/or receive data from the minicomputer. When the minicomputer is used in a business environment, for instance, the workstation would normally be a data processing device or terminal, but in a manufacturing or distribution environment the workstation could be a sensor or transmission unit that simply transmits signals back to the minicomputer for processing.

#### Main Storage

The storage type generally falls into one of two basic categories, magnetic core or semiconductor memory. Magnetic core storage has been widely used for more than a decade, and has proved to be fast, flexible, and reliable. Semiconductor memories began to appear in commercially available minicomputers late in 1970, and most minicomputer makers are now using semiconductor memory in their new products. It is clear that the demand for higher performance at lower cost, together with continuing improvements in semiconductor technology, have accelerated the trend toward the use of semiconductor memories.

Two types of semiconductor memories appear in the charts, MOS (metal oxide semiconductor) and bipolar (bipolar transistor). MOS is decidedly more popular because of its compactness and price. However, bipolar technology, a type of transistor-transistor logic, offers a classic trade-off-higher speed at the expense of more space and greater power consumed, as well as greater cost.

The cycle time, microseconds/word for a storage device is the minimum time interval that must elapse between the starts of two successive accesses to any one storage location. Though cycle time ranks with word length as one of the most significant individual indicators of a computer's performance potential, it is definitely not safe to assume that the computer with the fastest cycle time will be the best overall performer in a particular application. Other parameters that have an important effect on a minicomputer's performance include the flexibility and power of its instruction repertoire, the number of storage cycles it requires to execute each instruction, its input/ output capabilities, etc.

Access time, microseconds/word is the actual elapsed time between the CPU's request for data and the time when that data is received (read). In core memory, the access time is usually one-half the cycle time; semiconductor memories do not display a similar relationship.

Our comparison charts show the amount of main storage available for each computer in terms of the minimum capacity and maximum capacity, expressed in words. In the great majority of cases, storage is available in all the usual binary increments of capacity. Thus, if a computer has minimum and maximum storage capabilities of 4,096 and 32,768 words, respectively, it's safe to assume that capacities of 8,192 and 16,384 words are also available.

It is important to choose the right storage capacity; for nonmultiprogramming systems, that usually means enough storage to hold your largest program and all associated subroutines and data, but not too much more than that. It's also wise to make sure that your computer's



main storage capacity can be expanded if necessary, preferably by simply plugging in an additional storage module.

Parity checking is a standard feature of some minicomputers and an extra-cost option for others. In still other cases, the manufacturers maintain—with some justification—that the reliability of modern magnetic core and semiconductor memories is so high that parity checking is an unnecessary luxury unless absolute accuracy is a must. Parity checking requires the addition of one more bit to each main storage location. This added bit is set to the appropriate value (0 to 1) whenever a word is written into main storage and checked each time the word is read out; the technique permits detection of most, though not all, read and write errors.

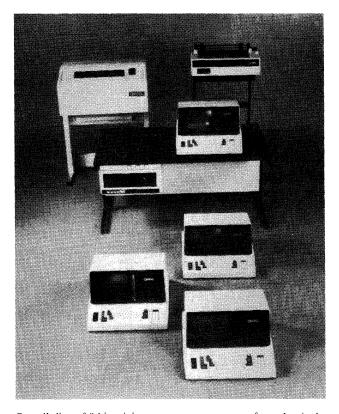
Error correction is a rather new feature which is beginning to appear in some of the recent minicomputer offerings. This feature involves appending five or six check bits to each word of memory. The check bits, called a Hamming code, and special algorithms allow a system to detect and correct single-bit errors, and also to detect a fair proportion of the multiple-bit errors that occur.

Storage protection is a feature that prevents unauthorized writing in certain areas of main storage. The protection can be accomplished by hardware means, software means, or a combination of both. Though unnecessary in simple dedicated systems, an effective storage protection scheme is an essential element in multiprogramming and timesharing environments.

#### **Central Processor**

Although there are many variations in their internal architecture, the great majority of currently available minicomputers are parallel, binary processors with singleaddress instructions and fixed word lengths of 8, 12, 16, 18, 24, or 32 bits.

The number of directly addressable words of main storage is an important characteristic that may require some explanation if you're investigating minicomputers for the first time. The problem is that the short word lengths impose serious limitations upon the number of bits that can be assigned to hold the address part of each instruction. A typical 16-bit minicomputer instruction might consist of three parts: operation code, address mode field, and the address itself. If 6 bits are assigned to hold the operation code (permitting up to 64 distinct operations) and 2 bits are used to designate the addressing mode (permitting specification of indexing and/or indirect addressing), then only 8 bits are left to hold the address field. Since these 8 bits permit direct addressing of only 256 distinct memory locations, it is clear that other means will need to be employed to access most regions of the computer's main storage. The most common solutions to the problem are the use of multi-word instructions, indexing, and/or indirect addressing.



Qantel's line of 8-bit minicomputer systems ranges from the singlestation 210 to the 1450-2 system that can support up to 64 workstations, 1024K bytes of memory, and 1200 million bytes of disk storage. Also available are floppy disk drives, magnetic tape units, serial printers, and line printers with speeds up to 600 lpm. Basic system prices range from \$11,950 to \$69,900.

Since indirect addressing is so prominent, it deserves a short explanation. Indirect addressing is an address modification technique in which the address part of an instruction specifies a storage location that contains another address rather than the desired operand itself. This second address may in turn be either the address of the desired operand or another indirect address; the latter case is called multi-level indirect addressing. Indirect addressing permits the use of an entire word to hold an operand address. It can also simplify programming and speed up execution times in some applications by making it possible to change the effective address of numerous instructions by altering the indirect address in a single storage location. Each level of indirect addressing, however, usually requires one additional storage cycle of execution time.

Control storage is an indication of the microprogrammability of the minicomputer. Microprogrammability is a trait that enables the vendor and/or the user to tailor a minicomputer's internal processing capabilities to suit his particular needs. In place of conventional hard-wired logic, a microprogrammed computer uses sequences of microinstructions, usually stored in a special read-only memory (ROM), programmable read-only memory (PROM), or bipolar read-only memory (BROM) unit, to define the effects of each instruction in its repertoire. In some cases the microprograms can be altered by the user

himself, while in others they are accessible only to the vendor. Microprogrammability can greatly increase the flexibility of a minicomputer, but its presence may involve a trade-off in terms of reduced performance or increased price. Entries here indicate both the type and the size of central storage.

Although it is undeniably dangerous to make inferences about a computer's overall performance capability on the basis of instruction execution times, our charts show the basic add time, microseconds to give a first-level indication of fixed-point arithmetic speeds. In general, the indicated add times are the times required to retrieve a one-word operand from main storage and add it to another operand already contained in an accumulator, with no indexing or indirect addressing. Comparisons based on add times can easily be misleading, however, because of differences in word lengths and instruction repertoires.

Hardware multiply/divide facilities are standard in some minicomputers and optional in others. When no hardware facilities are present, multiplication and division must be performed by means of programmed subroutines at a significant reduction in execution speeds. Many minicomputer applications, however, impose little or no need for multiplication or division operations, and in these cases the hardware facilities would be superfluous.

Hardware floating-point facilities are not included in the standard instruction repertoires of most of the currently available minicomputers, despite the fact that floating-point arithmetic is highly desirable, if not essential, in many scientific applications. Where available, these facilities can dramatically reduce the execution times for certain programs by eliminating the need for time-consuming floating-point subroutines.

Hardware byte manipulation is the ability to conveniently process information expressed in the 8-bit character codes which are rapidly becoming an industry standard. Obviously, most of the 8-bit minicomputers are effective byte manipulators, and many of the 16-bit machines offer special instructions that permit either half of a word to be addressed and processed as an 8-bit byte.

Battery backup is a feature unique to minicomputers with semiconductor memory, which is volatile and requires refreshing at regular intervals to retain the data that has been written into it. In the event of a power failure, the contents of memory would be lost if the regulator power supply were not backed up by the battery pack.

An interesting solution to this problem with semiconductor memories is furnished by Computer Talk, Inc., whose battery backup feature causes the contents of memory to be recorded on the system disk if a power failure occurs. When power is restored, memory can be recreated by copying from the disk.

A real-time clock or timer is another essential element in most "time-conscious" systems. A real-time clock enables

the program to determine the time of day, while an interval timer usually indicates the amount of time that has elapsed since the occurrence of some significant event. In many cases the timer can trigger an interrupt signal when a predetermined interval of time has elapsed.

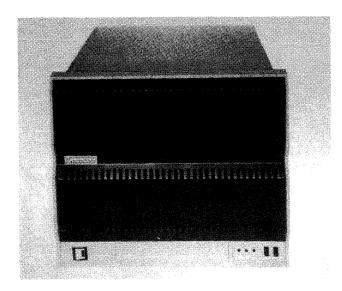
#### Input/Output Control

A direct memory access channel (DMA) permits direct transfer of I/O data between main storage and a peripheral controller. When a DMA channel is used, the I/O data bypasses the computer's main hardware registers, and the I/O operation proceeds independently of program control once it has been initiated by the program. In minicomputers that lack a DMA channel, I/O data transfers are generally carried out under direct program control, with each word being transferred by way of the processor's registers. Generally speaking, the DMA channel has two significant advantages over program-controlled I/O: it can accommodate higher I/O data rates, and it causes far less interference with internal processing operations. Regardless of the type of I/O control they employ, most minicomputers can accommodate multiple I/O devices and include appropriate facilities for addressing the desired device.

Maximum I/O rate, words/sec is a measure of each computer's potential ability to transfer data to and from peripheral devices or other external sources. In machines equipped with a DMA channel, the maximum I/O rate frequently equals the cycling rate of the main storage unit. These maximum I/O rates, however, can be quite deceptive in the case of minicomputers. In general, their storage capacities are limited, their capabilities for simultaneous input/output operations are restricted, and fairly complex programming is associated with I/O operations. For all these reasons, I/O data rates approaching the indicated maximum rates can usually be handled only in short bursts, if at all.

An effective program interrupt facility is a requirement for virtually all applications of a real-time nature. An interrupt is a signal that causes temporary suspension of normal program execution so that the particular condition that caused the interrupt can be dealt with. Interrupts fall into two basic categories: internal and external. Internal interrupts are usually triggered by conditions such as a memory parity error, an illegal instruction, or a power failure. External interrupts usually indicate that a particular peripheral device requires attention or has completed an I/O operation. An interrupt usually results in automatic storage of the current contents of the instruction counter, followed by a transfer of control to a software routine that determines the cause of the interrupt and initiates the appropriate action.

The number of external interrupt levels provides a reasonable indication of the power of a minicomputer's interrupt system. It shows the number of different external devices whose interrupt signals can be identified by the processor—though it should be noted that this identification process



Dataram Corporation's BM-2 combines DEC's LSI-11 processor with up to 4 million bytes of Dataram's bulk storage. With 64K bytes of main memory and 512K bytes of bulk storage, the BM-2 sells for \$11,645.

may require a fairly complex and time-consuming sequence of instructions. Many of the minicomputers offer additional external interrupt levels as extra-cost options, and in these cases our charts show the available range, from minimum to maximum.

#### **Communications Capabilities**

Communications capabilities enable some of the small business computers to function as "intelligent terminals" in data communications networks. An interface equips the small computer to send and receive data over a commoncarrier communications link, usually to a larger central computer installation. The small computer's internal processing and storage capabilities enable it to do some data processing locally and to handle a variety of code translation, editing, and control functions in connection with the data communications activities.

Maximum no. of lines indicates how many communications lines can be handled by a particular system. The types of lines are specified in the next two entries.

Synchronous and asynchronous have entries of standard, optional, or no, indicating their availability, and also a notation as to the speed of each line in bits per second (bps). Most entries will be of the type "to 4800 bps," indicating one or more transmission speeds up to a maximum of 4800 bps.

Protocols supported indicates the type of communication protocols accommodated by hardware and software for the model.

#### Peripheral Equipment

The comparison charts summarize the standard peripheral devices that are available for each minicomputer. (Full details on the specifications and prices of more than 900 peripheral and memory products can be found in the Peripherals section of DATAPRO REPORTS ON MINI-COMPUTERS. In addition, the individual minicomputer system reports in the Computers section include coverage of all the important peripheral devices offered with each minicomputer.)

Users who are accustomed to larger general-purpose computer systems will find that the term "standard peripheral device" often has a somewhat different meaning when used by a minicomputer manufacturer. Since comparatively few of the minicomputer makers produce their own peripheral equipment, the indicated availability of a given type of device may simply mean that an appropriate interface is available to couple the computer with a peripheral unit supplied by some other manufacturer. In many instances the minicomputer manufacturer buys the peripheral device from the peripheral manufacturer and supplies an appropriate interface for his minicomputer. Datapro has made every effort to include only the peripheral devices that are physically supplied by the minicomputer vendors; therefore, prospective buyers should ask these questions about each item of peripheral equipment they will need:

- Has it actually been installed and used with the computer of interest?
- If so, what has the users' experience been?
- What software support is available?
- Who will provide service for the device, and under what conditions?

The inclusion of mass storage devices (magnetic disk units) can greatly increase the data storage and processing capabilities of a minicomputer system. Disk units enable millions of characters of information to be constantly accessible to the computer. Moreover, any desired record can be retrieved, updated, and re-recorded on the disk, usually within a fraction of a second.

By replacing or augmenting slower, less flexible file storage media such as punched cards, paper tape, or magnetic ledger cards, disk units can enable small computers to handle applications and processing volumes that would otherwise be impossible. The principal disadvantages of disk units are their comparatively high costs and the software complexities that are encountered by users who attempt to harness their full potential. One or both of these considerations will make disk units impractical for many small computer buyers, despite the obvious appeal of diskoriented data processing.

The diskette, or "floppy disk," is an innovation that can significantly reduce the cost of disk-oriented data

processing. The diskette itself consists of a flexible Mylar disk, about 8 inches in diameter, that is permanently housed in a plastic envelope. It can serve as an input/output and/or random-access storage medium that is considerably smaller in capability and slower in performance than conventional disk units—but also far lower in cost. Introduced by IBM in 1972, diskettes and diskette drive units are now being produced by dozens of vendors and are finding their way into numerous small computer systems, such as the IBM System/32 and Burroughs B 80. Recent enhancements to the floppy disk concept include more concentrated data storage and "flippies" (floppy disks that utilize both sides of the diskette), allowing more data to be stored on-line.

The other, more conventional types of mass storage devices, cartridge and disk pack drives, provide access to far more data and at significantly faster rates. Unfortunately, they also carry price tags several times higher than their floppy counterparts. Most of these units employ cartridges or disk packs that can easily be removed from the drive units and interchanged in much the same manner as magnetic tape reels.

Some cartridge-type units either use nonremovable media or use two cartridges, one fixed and the other removable. Nonremovable disks impose two important limitations. First, the system's file storage capacity is effectively limited to the amount of information that can be stored on-line. Second, disk dumps to create backup files for efficient restart procedures in case of catastrophe are not available to the user.

Interchangeable disks, conversely, provide great flexibility and make it practical to use small computers effectively for both sequential and random data processing applications. In sequential applications, files of virtually unlimited size can be handled through the use of multiple disk packs or cartridges.



The System 730 from Basic Four represents the high-end of Basic Four's line of small business computers. The basic System 730 is priced at \$95,000 and includes 96K bytes of memory, 150 million bytes of disk storage, a 300 lpm printer, and four VDT's. The system can be expanded to include up to 256K bytes of memory and 300 million bytes of disk storage, and support up to 32 VDT's.

Fixed-head (head-per track) disk and drum units can provide much faster access to on-line data than any other type of mass storage device. The reason is that there is no loss of time due to head positioning because a head is provided for each track. The only delay is rotational delay (latency), or the time required for the desired data to move under the read/write head. But the price of this type of equipment is higher than that of the preceding varieties, and less data can be stored on-line. Fixed-head devices are used when data bases are relatively small and very rapid access to the information is required.

Floppy disk (diskette) drives indicates whether floppies are available for a particular minicomputer and the minimum and maximum on-line capacities that are offered.

Disk pack/cartridge drives signifies whether one or the other, or both, types of devices can be interfaced to the system and the minimum and maximum on-line capacities available.

Drum/fixed-head disk storage informs the reader as to the availability of a drum or head-per-track (fixed-head) disk drive and the minimum and maximum on-line capacities offered.

The indicated maximum storage capacities are shown in thousands (K) or millions (M) of bytes and may be the capacity of a single disk or the total capacity of two or more (typically, four to eight) drives that can be connected to one controller. It is difficult to imagine minicomputer users wanting more disk storage, but if an I/O slot is open, theoretically, another controller and its associated drives can be added to most systems.

Magnetic tape cassettes and cartridges offer increased convenience in that they can be transported and stored with little fear of damaging the data that has been recorded. What's more, price tags for cassette and cartridge drives are significantly lower than those of the more conventional reel-to-reel variety, but once again the trade-off of slower transfer rates and reduced on-line storage must be accepted. The charts indicate the availability of magnetic tape cassettes/cartridges and magnetic tape, ½-inch drives and their associated transfer rates in characters per second (cps) or thousands of bytes per second (KBS).

Serial (character-at-a-time) printers are enjoying increased popularity with the prolific growth of the minicomputer marketplace. The main reason is price; serial printers can provide excellent-quality hard-copy reports for far less money than the line-at-a-time printers used with larger computers. However, for users who require faster printing capabilities, line printers are also available for many systems. Serial printers generally range in speed from about 30 to 600 or more characters per second (cps), while line printers operate at speeds of 100 to 2000 or more lines per minute (lpm). The user who needs faster printed output can obviously get it, but he must be willing to pay the higher price tag associated with the line printers.

Data communications interface describes the minicomputer's capabilities, if any, to send and receive data over a common-carrier communications link. Depending on the configuration, a minicomputer can be programmed to function as an intelligent terminal communicating with a larger host computer, or the mini can act as the host computer communicating with other terminals in a network, the chart entry indicates whether an interface is available and gives the range of data rates or the maximum data rate in bits per second (bps).

CRT indicates the availability of a CRT display unit and describes its standard screen size in characters per line and number of lines per screen (e.g., 80 char. x 24 lines).

Other standard peripheral units lists the additional peripheral devices that are available for each system. Typical entries include analog/digital (A/D) converters, paper tape readers, paper tape punches, plotters, etc.

#### Software

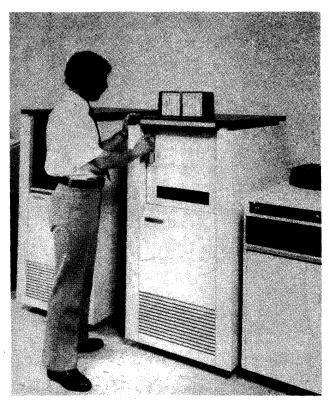
A critically important area to be evaluated is software the programming packages and languages used to program the computer and thereby direct its operations. It is important that you carefully investigate the available software. This investigation should include the operating systems, programming languages, preprogrammed utility packages such as sorts and file maintenance, and application packages such as payroll, inventory control, general ledger, etc. Prospective buyers should carefully note whether the software they will require is included in the cost of the system or offered at extra cost.

Vendors' claims and promises concerning the availability and capability of software should be carefully checked. This is particularly true of software that has been announced but not yet released. Vendors have frequently failed to live up to their marketing publicity.

An assembler is a special-purpose program that uses the computer's power to facilitate the preparation of other programs. It enables the programmer to write his own program in a simplified format that uses mnemonic operation codes and symbolic operand addresses. The assembler program then converts these symbolic instructions into their machine-language equivalents, producing computer programs ready for loading and execution. Entries here indicate the availability of an assembler or, in some cases, a macro assembler.

A macro assembler is another software tool to aid the programmer and make his job a little easier. Macro routines can be called by the programmer and copied right into his program. This saves the programmer from having to recode the routine each time it is used and also eliminates the possibility of keying errors when that part of the program is entered. As usual, there is a price to pay: the use of macros usually wastes memory space.

Entries in this section of the charts indicate whether an assembler, a macro assembler, or both are available.



The Model 5000 from BTI is a packaged system for use in a timesharing environment. The BTI 5000 can support up to 32 workstations and features automatic, remote computer-to-computer fault diagnosis, which can be carried out even if the customer's system has halted. With 29 megabytes of disk storage, battery backup, and a cartridge tape drive, the BTI 5000 sells for \$29,950.

A compiler is a software tool designed to shift part of the program preparation task from the user to the computer itself by converting programs written in a simplified, procedure-oriented language into machine-language object programs. Compilers are now used in virtually all large and medium-scale computer installations because of their demonstrated ability to slash programming costs and they are becoming increasingly available for minicomputers. This trend is possible because of the more powerful central processors now being used, since compilation is an intricate process that requires more storage space and processing power than the earlier minicomputers provided. Where compilers are offered, however, they frequently limit the programmer to restricted subsets of the standard programming languages and/or require the use of a larger computer to perform the compilation process.

Entries in this section of the charts may include COBOL (COmmon Business Oriented Language), RPG (Report Program Generator), FORTRAN (FORmula TRANslator), BASIC (Beginners All-purpose Symbolic Instruction Code), ALGOL (ALGOrithmic Language), or proprietary languages that are available from a vendor for use on a particular system, and indicate the availability of those compilers for each minicomputer. The key word of warning here is that if you use a language that is unique to a vendor, you will be faced >

with a big problem if someday you decide to change vendors. Your investment in software will be lost, since the programs will not operate on any other system.

An operating system facilitates the operation of a computer by handling functions such as: (1) scheduling, loading, and supervising the execution of programs; (2) allocating storage and I/O devices; (3) initiating and controlling I/O operations; (4) analyzing interrupt signals and dealing with errors; (5) handling communications between the system and its human operator; and (6) controlling multiprogramming or time-sharing operations.

Typical entries describing the available operating systems include "batch," which means that the system processes one or more jobs sequentially and requires all data to be supplied before initiation (communication between operator and system is not permitted once the job has begun); "interactive," which means that the system allows data, parameters, etc., to be entered as the job is executing; "real-time," which means that the system responds to external demands on a priority basis; or "time-sharing," which means that the system allows multiple users to access the system and share all its resources at the same time.

Language implemented in firmware and operating system implemented in firmware tell the reader whether or not the language processor and/or the operating system are contained in microcode. The entries stipulate "fully," "partially," or "no" to indicate the extent of firmware implementation. An advantage to the user is that a language and/or operating system implemented in firmware frees up more memory space for the user's programs and data. Also, the microcode is usually inaccessible to the user (generally contained in read-only memory), eliminating any possible tampering with the language processor or operating system and reducing chances for error. A third advantage derived from firmware implementation is the ability to create more sophisticated and complex system functions at the hardware level. Microcode routines can be substituted for often-used subroutines, thereby increasing system performance.

#### Pricing and Availability

The comparison charts show the price of CPU, power supply, front panel, and minimum memory in chassis along with the memory size in parentheses. Price of memory increment stipulates the costs of various sizes (when available) of memory increments, with the actual sizes in parentheses.

(Completely detailed pricing data is provided with each minicomputer system report in the Computers section of this service. Detailed pricing on any minicomputer which is not covered in the in-depth report format can be obtained directly from the Datapro analysts by using the Datapro Inquiry Service.)

If you'll need two or more minicomputers, it's also worth noting that most of the manufacturers offer sizeable discounts from their list prices on orders for multiple computers. Discounts of up to 40 percent are not unusual on large orders.

Date of first delivery indicates when the first production model of each minicomputer was delivered ( or is scheduled to be delivered) to a customer.

Number installed to date shows how many systems of each type had been delivered to customers as of approximately November, 1979. All figures were supplied by the manufacturers themselves.

#### Comments

This final entry on the comparison charts is used to explain or amplify the preceding entries and to provide other pertinent information about each system's hardware, software, pricing, or applications.

#### MINICOMPUTER MANUFACTURERS

Listed below, for your convenience in obtaining additional information, are the full names, addresses, and telephone numbers of the 63 suppliers whose products are listed in the comparison charts that follow.

Anderson-Jacobson, Inc., 521 Charcot Avenue, San Jose, California 95131. Telephone (408) 263-8520.

**Applied Data Processing, Inc.,** 33 Bernhard Road, North Haven, Connecticut 06473.. Telephone (203) 787-4107.

**Applied Systems Corporation,** 26401 Harper Avenue, St. Clair Shores, Michigan 48081. Telephone (313) 779-8700.

**Basic/Four Corporation**, 14101 Myford Road, Tustin, California 92680. Telephone (714) 731-5100.

**BRD (Bainbridge Research & Development), Inc.,** 12715A Miller Road, N.E., Bainbridge Island, Washington 98110. Telephone (206) 842-4777.

**BTI Computer Systems, Inc.,** 870 West Maude Avenue, Sunnyvale, California 94086. Telephone (408) 733-1122.

**Burroughs Corporation,** Burroughs Place, Detroit, Michigan 48232. Telephone (313) 972-7000.

Cado Systems Corporation, 2771 Toledo Drive, Torrance, California 90503. Telephone (213) 320-9660.

Cascade Data, Inc., 6300 28th Street, S.E., Grand Rapids, Michigan 49506. Telephone (616) 942-1420.

**CDA (Computer Data Access), Inc.,** 1373 Broad Street, Clinton, New Jersey 07011. Telephone (201) 473-4700.

Century Computer Corporation, 2339 Stanwell Circle, Concord, California 94520. Telephone (415) 798-8000.

Compal Computer Systems, 6300 Variel Avenue, Suite E, Woodland Hills, California 91367. Telephone (213) 992-4425.

Computer Automation, Inc., 18651 Von Karman Avenue, Irvine, California 92713. Telephone (714) 833-8830.





The F-85 from Durango Systems Inc., supports 64K bytes of memory, up to 24 million bytes of cartridge disk storage, and up to 5 workstations. A basic single-station F-85 with 64K bytes of main memory, two 473K-byte diskette drives, CRT, keyboard, and 165 cps printer is priced at \$12,983. The F-85 is an integrated desktop small business system that is available with a variety of packaged application software.

Computer Design Systems, Inc., 8085 Wayzata Boulevard, Minneapolis, Minnesota 55426. Telephone (612) 545-2855.

Computer Hardware, Inc., 4111 North Freeway Boulevard, Sacramento, California 95834. Telephone (916) 929-2020.

Computer Talk Inc., P.O. Box 100, Idledale, Colorado 80453. Telephone (303) 697-5485.

Computervision Corporation, 201 Burlington Road, Route 62, Bedford, Massachusetts 01730. Telephone (617) 275-1800.

Control Data Corporation, 4400 Computer Drive, Westboro, Massachusetts 01581. Telephone (617) 366-8911.

**Datapoint Corporation,** 9725 Datapoint Drive, San Antonio, Texas 78284. Telephone (512) 690-7000.

**Dataram Corporation,** Princeton-Hightstown Road, Cranbury, New Jersey 08512. Telephone (609) 799-0071.

**Digital Equipment Corporation,** 129 Parker Street, Maynard, Massachusetts 01754. Telephone (617) 897-5111.

**Digital Scientific Corporation,** 11455 Sorrento Valley Road, San Diego, California 92121. Telephone (714) 453-6050.

**Digital Systems Corporation,** P.O. Box 158, Walkersville, Maryland 21793. Telephone (301) 845-4141.

Dimis, Inc., 1060 Highway 35, Middletown, New Jersey 07748. Telephone (201) 671-1011.

**Display Data Corporation,** Executive Plaza IV, Hunt Valley, Maryland 21031. Telephone (301) 667-9211.

Durange Systems, Inc., 3003 North First Street, San Jose, California 95134. Telephone (408) 946-5000.

Four-Phase Systems, Inc., 10700 North DeAnza Boulevard, Cupertino, California 95014. Telephone (408) 255-0900.

Functional Automation, Inc., 3 Graham Drive, Nashua, New Hampshire 03060. Telephone (603) 888-1905.

General Robotics Corporation, 55-57 North Main Street, Hartford, Wisconsin 53027. Telephone (414) 673-6800.

Harris Corporation, Computer Systems Division, 2101 West Cypress Creek Road, Fort Lauderdale, Florida 33309. Telephone (305) 974-1700.

**Hewlett-Packard, Data Systems Division,** 11000 Wolfe Road, Cupertino, California 95014. Telephone (408) 257-7000.

**Hewlett-Packard, GSD Division,** 19447 Prunridge Avenue, Cupertino, California 95014. Telephone (408) 725-8111.

Honeywell Information Systems, Inc., 200 Smith Street, Waltham, Massachusetts 02154. Telephone (617) 890-8400.

**IBM Corporation,** General Systems Division, P.O. Box 2150, N.E., Atlanta, Georgia 30301. Telephone (404) 238-2000.

Jacquard Systems, 1639 11th Street, Santa Monica, California 90404. Telephone (450-6784.

MCM Computers Ltd., P.O. Box 310, 133 Dalton Street, Kingston, Ontario, Canada K7L 4W2. Telephone (613) 544-9860.

Melcom Business Systems, Inc., 2200 West Artesia Boulevard, Suite 101, Compton, California 90220. Telephone (213) 979-6055.

Microdata Corporation, 17481 Red Hill Avenue, Irvine, California 92705. Telephone (714) 540-8341.

Modular Computer Systems, Inc., 1650 West McNab Road, Fort Lauderdale, Florida 33310. Telephone (305) 974-1380.

Mylee Digital Sciences, Inc., 155 Weldon Parkway, Maryland Heights, Missouri 63043. Telephone (314) 567-3420.

Nanodata Corporation, One Computer Park, Buffalo, New York 14203. Telephone (716) 631-6000.

NCR Corporation, Main and K Streets, Dayton, Ohio 45479. Telephone (513) 449-2000.

New England Digital Corporation, Main Street, Norwich, Vermont 05055. Telephone (802) 649-5183.

Northern Telecom Systems Corporation, P.O. Box 1222, Minneapolis, Minnesota 55440. Telephone (612) 932-8000.

Northrop Data Systems, 1160 Sandhill Avenue, Carson, California 90746. Telephone (213) 637-1533.



Olivetti Corporation of America, 500 Park Avenue, New York, New York 10022. Telephone (212) 371-5500.

Perkin Elmer Computer Systems Division, 2 Crescent Place, Oceanport, New Jersey 07757. Telephone (201) 229-6800.

Point 4 Computer Corporation, 2659 McCabe Way, Irvine, California 92714. Telephone (714) 556-4242.

Prime Computer, Inc., 40 Walnut Street, Wellesley Hills, Massachusetts 02181. Telephone (617) 237-6990.

Qantel Corporation, 3525 Breakwater Avenue, Hayward, California 94545. Telephone (415) 783-3410.

Raytheon Data Systems Company, 1415 Boston-Providence Turnpike, Norwood, Massachusetts 02062. Telephone (617) 762-6700

Rolm Corporation, 4900 Old Ironsides Drive, Santa Clara, California 95050. Telephone (408) 988-2900.

Sperry Univac Division, Sperry Rand Corporation, P.O. Box 500, Blue Bell, PA 19424. Telephone (215) 542-4011.

Sperry Univac Minicomputer Operations, P.O. Box C-19504, 2722 Michelson Drive, Irvine, California 92713. Telephone (714) 833-2400.

STC, Inc., Nine Brook Avenue, Marywood, New Jersey 07607. Telephone (201) 845-0500.

Systems Engineering Laboratories, Inc., 6901 West Sunrise Boulevard, Fort Lauderdale, Florida 33313. Telephone (305) 587-2900

Tandem Computers, Inc., 19333 Vallco Parkway, Cupertino, California 95014. Telephone (408) 996-6000.

Terak Corporation, 14405 North Scottsdale Road, Scottsdale, Arizona 85254. Telephone (602) 991-1580.

**Texas Instruments, Inc.,** P.O. Box 2909, Austin, Texas 78769. Telephone (512) 250-7309.

Wang Laboratories, Inc., 836 North Street, Tewksbury, Massachusetts 08176. Telephone (617) 459-5000.

Warrex Computer Corporation, 1780 Jay Ell Drive, Richardson, Texas 75081. Telephone (214) 699-8400.

Xerox Corp., 440 Oakmead Parkway, Sunnyvale, California 94086. Telephone (408) 733-2300.

MANUFACTURER AND MODEL	Anderson Jacobson 1500	Applied Data Processing Resource/100	Applied Systems Corporation ASC/80	Basic Four System 200	Basic Four System 410
WORD LENGTH, BITS	8	16	8, 16	8-bit byte	8-bit byte
NO. WORKSTATIONS SUPPORTED	4	16		2	8
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection CENTRAL PROCESSOR	Core, MOS 1.0/0.8 64K/64K No No No	Core 0.8/0.4 65K/256K No No	MOS 1.0/0.5 4K/12BK Optional Optional Optional	MOS 0.60/0.40 40K/64K bytes Standard No No	MOS 0.60/0.40 40K/128K bytes Standard No No
No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K bytes ROM 4.0 No No Standard No	256K No .80 Optional Optional No Optional Optional	64K PROM; 64K max. 1.0 Optional Optional Sandard Optional Standard	64K bytes ROM; 1K x 16 bits 7.4 No No Standard Standard Standard	128K bytes ROM; 1K x 16 bits 7.4 No No Standard Standard Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 606K 15	Standard 1.1M 16	Optional 50K 8 optional	Standard 1M 8	Standard 1M 8
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	8 No Std.; 300-9600 bps None	15 No Std.: 1200 baud 2780	16, 32 Opt.; to 50K bps Opt.; to 9600 bps IBM-Bisync; DECnet (RPQ)	1 Opt., 9600 bps Std., 9600 bps Bisync	8 Opt., 9600 bps Std., 9600 bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	None None No	No 2780 No	_ _ _	BFBIN 2780/3780 No	BFBIN   2780/3780   No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	640K-2.56M bytes Cart.; 10M-20M bytes	No Both; 10MB, 320MB	.24M-2M bytes Optional	No Fixed; 10-20M bytes	No Fixed; 14-42MB byte
Drum/fixed head disk storage	No	No	Optional; 10-100M bytes	No	No
Magnetic tape cassettes/cartridges	No	No	A/R optional	Std.; 2.3m bytes	Std.; 9.2MB
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 30-960 cps 125-300 lpm 300-9600 cps 1920 characters No	120K Yes; 120-180 cps Yes; 300-600 lpm Yes; 19.2K Yes; 1998 char. None	Optional 30/180 cps A/R optional To 19.2K bps 64 x 16 std.; 80 x 24 Plotters, graphic CRT, A/D-D/A I/O	10 KBS 120 cps; 160 cps Opt. Opt. 150 cpm 1200 bps 80 char. x 24 lines	10 KBS 120 cps; 160 cps opt 150, 300, 600 lpm 1200 bps 80 char. x 24 lines —
SOFTWARE Assembler	Yes	Yes	Yes; macro assembler	No	No
Compilers	BASIC	BASIC		Business BASIC	Business BASIC
Operating system	Multiprogramming	Time-sharing	PASCAL, PI/M Optional	Multi-user interactive	Multi-user
Language implemented in firmware Operating system implemented in firmware	No Partially	No No	Optional Optional	No Partially	No Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu-	18,150 (32K bytes)	Contact vendor	1,900 (basic system)	24,990 (40 Kbytes) 260	32,500 (40Kbytes) 280
ration above for on-site contract, \$ Discounts available Price of memory increment, \$	 1,800 (32K bytes)	_	 250 (8K bytes)		
Date of first delivery Number installed to date	July 1977 Over 200	June 1976 NA	1977 NA	1978 9000 (all models)	1978 900 (all models)
COMMENTS		Resource/100 is a minicomputer-based business data processing system. It is marketed with applications software	Modular computer system designed for general applications and special business, communications, and real-time/control operations	Price includes 40KB memory, 10MB fixed disk, 120 cps printer, 2.3MB mag- netic tape cartridge drive, and one VDT; 8K bytes (\$1,500) also available	Price includes 40KB memory, 14M fixed disk, 120 cps printer, 9.2MB mag- netic tape cartridge drive, and one VDT; 64K bytes (\$4,100) also available

MANUFACTURER AND MODEL	Basic Four System 610	Basic Four System 730	BRD Dolphin	BRD Porpoise	BTI 5000
WORD LENGTH, BITS	8-bit byte	8-bit byte	8-bit byte	8-bit byte	16
NO. WORKSTATIONS SUPPORTED	16	32	1	1	32
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.60/0.40 64K/192K bytes Standard No No	MOS 0.60/0.40 96K/256K bytes Standard No No	MOS 0.60 4K/32K bytes Standard No No	MOS 0.60 4K/32K bytes Standard No No	MOS and core 0.60 64K/64K bytes Standard Yes, with MOS Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K ROM; 1K x 16 bits 7.4 No No Standard Standard Standard	64K ROM; 1K x 16 bits 7.4 No No Standard Standard Standard	4K to 8K EPROM; 14K 5.0 Standard No Standard No No	4K to 8K EPROM; 12K 5.0 Standard No Standard No No	NA PROM; 98K bits 20 Standard Standard Standard Standard Standard Standard
NPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1M 8	Standard 1M 8	Standard 1M None	Standard 1M None	Standard 616K NA
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	16 Opt., 9600 bps Std., 9600 bps Bisync	32 Opt., 9600 bps Std., 9600 bps Bisync	1 No Std.; 1200 bps Programmable	1 No Std.; 300 bps Programmable	32 No Std.; to 9600 bps Asynch
Network architectures supported RJE terminals emulated IBM 3270 emulation	BFBIN 2780/3780 No	BFBIN 2780/3780 No			No No No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Pack; 35 M-300M	No Pack; 150-300M bytes	1.2MB; dual dr. std. No	622KB; dual dr. std. No	No Non-remov. pack, 29MB to 392MB
Drum/fixed head disk storage	No	No	No No	No	Cart; 10MB
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	Opt.; 9.2 bytes 10 KBS 80, 120, 160 cps 150, 300, 600 lpm 1200 bps 80 char. x 24 lines	Opt.; 9.2MB 10 KBS 80, 120, 160 cps 150, 300, 600 lpm 1200 bps 80 char. x 24 lines	No 45-200 cps No 300-1200 bps 24 x 80 No	No 30-55 cps No 300 bps 24 x 80 No	Optional No 300, 600, 900 lpm 9600 bps; asynch 24 x 80; char.
SOFTWARE Assembler	No	No	B.A.L.	B.A.L.	No
Compilers	Business BASIC	Business BASIC	ALPHABASE	ALPHABASE	BASIC
Operating system	Multi-user	Multi-user	Real-time	Real-time	Time-sharing
Language implemented in firmware Operating system implemented in firmware	No Partially	No Partially	B.A.L./fully Fully	B.A.L./fully Fully	Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$	51, 400 (64K bytes)	95,000 (96K bytes)	\$18,000 to \$25,000 No	\$10,000 to \$15,000	29,950 345
Monthly maint, of basic configuration above for on-site contract, \$ Discounts available	424	766 	Educ. (15%)	Educ. (15%)	Quantity
Price of memory increment, \$  Date of first delivery Number installed to date	2,240 (32KB) 1978	2,240 (32KB) 1978 9000 (all models)	\$400 (4K bytes) July 1977 125	\$400 (4K bytes) January 1978 25	None August 1978 650 (all models)
COMMENTS	9000 (all models) Price includes 64KB memory, 35MB disc drive & pack w/op. sys., 160 cps printer, and one VDT (desk/worktable); 64K bytes (\$4,100), 128K bytes (\$6,600) also available	Price includes	Entry-level small business system; price also includes dual floppy disk drives, workstation cabinet, and desk as standard; software packages available for most business applications	Entry-level small business system; price also includes dual floppy disk drives, workstation, cabinet, and desk as standard; software packages available for most business applications	Packaged system includes non-removable and/or pack disk drives, cartridge magnetic tape drives, reel-to-reel tape drives and line printers are standard options; up to 32 users supported; price is for minimum system (I

MANUFACTURER AND MODEL	ВТІ	Burroughs	Burroughs	Burroughs	Burroughs
WANDFACTORER AND MODEL	8000	B80	B90	B720/B730	B770 Series
WORD LENGTH, BITS	16	8-bit byte	8-bit byte	64	16
NO. WORKSTATIONS SUPPORTED	128	4	8	9	
MAIN STORAGE Storage type Cycle/access time Min /Max. capacity, words Parity checking Error correction Storage protection	Core 0.67 256K/8M bytes Standard No Standard	MOS 1.0/0.5 60K/128K bytes Standard No Standard	MOS	MOS 1.0/0.5 32K/96K bytes Standard No Standard	Core, MOS 1/0.4, 0.63 32K/64K Standard No Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	PROM 3.5 Standard Standard Standard Standard Standard Standard	ROM; 4K bytes	ROM; 4K bytes No	ROM; 3.5K bytes 0.43 No No Standard No No	RAM; 32K bytes No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	4 to 32 10M		Standard — —	Standard 2M bytes —	Standard 2M bytes
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	128 No Std.; to 19,200 bps Asynch	4 To 9600 bps To 9600 bps BDLC	2 To 9600 bps To 9600 bps BDLC, async, bisync	22 To 9600 bps To 9600 bps	To 9600 bps To 9600 bps
Network architectures supported RJE terminals emulated IBM 3270 emulation	No No No	None No	BNA 	— IBM 3780 No	— IBM 3780 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives Drum/fixed head disk storage	No Pack; 33MB to 8,000MB No	243K-6M bytes Cartridge; 4.6-27.6M bytes No	243K-6M bytes Cartridge; 4:6-9.2M bytes Fixed; 9.4 to 37.6M	243K-1.5M bytes Cartridge; 4.6-27.6M bytes No	243K bytes Cartridge; 4.6-27.6M bytes No
Magnetic tape cassettes/cartridges	No	Cassette; 1 KBS	bytes Cassette; 1 KBS	Cassette; 1 KBS	Cassette; 1 KBS
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	200 KBS (9-TK) No 300, 600, 900 lpm 19.2 bps; asynch 24 x 80; char. None	No 60, 180 cps 160, 250 lpm 9600 bps 80 char. x 24 lines	No 90, 120 cps 64-650 lpm To 9600 bps 28 lines x 80 char.	10 KBS 60 cps 85-400 lpm 9600 bps 80 char. x 24 lines Card punch, card reader/punch	10 KBS No 85-750 lpm 9600 bps No Up to 2 data com- munications proc- essors; reader/punch
SOFTWARE Assembler	Assembler	No	No	No	Assembler
Compilers	BASIC, FORTRAN, COBOL, PASCAL	COBOL, RPG, NDL, MPL	COBOL, RPG, MGP	COBOL, RPG, AEL	COBOL, RPG, NDL,
Operating system	Time-sharing, batch	Interactive	Multiprogramming	Real-time	Batch, real-time
Language implemented in firmware Operating system implemented in firmware	No Partially	Fully Fully	Fully Fully	Fully Fully	Fully Fully
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	86,750 650 Quantity 9,000 (128K bytes)	18,030 — Leases	7,900 56 Leases 1,050 (64K byte)	Contact vendor  Leases	Contact vendor  Leases
Date of first delivery Number installed to date	April 1980 NA	April 1976 Over 4000	December 1979 NA	March 1973 Over 3000	1974
COMMENTS	Packaged system for interactive and multistream batch workload; variable resource bus architecture accommodates up to 8 processors, together with multiple memory modules and peripheral processors.	Offers the technology of Burroughs larger computers	Growth path to the Burrough's L Series.	System price includes console printer; AEL and COBOL or RPG pro- grams can run con- currently	Systems and com- munications proc- essors; not all models allow all features presented

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MANUFACTURER AND MODEL	Burroughs B800 Series	Burroughs B1700 Series	Burroughs B1720 Series	Burroughs B1800 Series	Burroughs B1900
WORD LENGTH, BITS	64, 16	8-bit byte	64	8-bit byte	8-bit byte
NO. WORKSTATIONS SUPPORTED	4-10	_	-	32	32
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS; bipolar 1.0/0.5 32K/150K bytes Standard No Standard	MOS 1.5/1.0 24K/128K bytes Standard No Standard	MOS; bipolar 1.0/0.67 48K/256K bytes Standard No Standard	MOS 1.7-2.0 96K/1024K bytes Standard No Standard	MOS; bipolar 0.25 128K/2M bytes No Standard Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	RAM; to 48K  No Standard	No No No No No	ROM; to 8K bytes No No No	ROM; 4K bytes	Cache; 8K bytes
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 2M bytes —	Optional — —	Optional — —		   
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	7 To 9600 bps To 9600 bps BDLC	4 To 9600 bps To 9600 bps Bisync	4 To 9600 bps To 9600 bps Bisync	32 Opt.; to 50,000 bps Opt.; to 9600 bps Bisync, BDLC, BNA	32 Opt.; to 50,000 bps Opt.; to 9600 bps BDLC, Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	 IBM 3780 No	— HASP, IBM/360;/3 No	HASP, IBM/360;/3 No	HASP, IBM 360/370	BNA HASP, IBM 360/370 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives Drum/fixed head disk storage	2M bytes Both; 4.6-130.4M bytes Fixed-head; 9.4-65.6M	No Both; 2.3-697.6M bytes Fixed-head; 1.9M	No Both; 2.3-697.6M bytes Fixed-head; 1.9-70M	486K bytes Both; 4.6-697M bytes No	243K-6M bytes Pack; 1608M bytes
Magnetic tape cassettes/cartridges	bytes Cassette; 3 KBS	bytes Cassette, 1 KBS	bytes Cassette; 1 KBS	Cassette; 1 KBS	Cassette; 1 KBS
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	10 KBS 120 cps 160-750 lpm 9600 bps 80 char. x 24 lines Card punch; card reader/punch; DDES	10-120 KBS No 85-1040 lpm 9600 bps 80 char. x 24 lines Card punch/reader units	10-120 KBS No 85-1040 lpm 9600 bps 80 char. x 24 lines Card punch/reader units	10-120 KBS No 400-1500 lpm 9600 bps 80 char. x 24 lines Card punch/reader units	20-120 KBS No 85-1500 lpm To 50,000 bps Card units, MICR
SOFTWARE Assembler	No	No	No	No	No
Compilers		COBOL, FORTRAN RPG, BASIC, UPL,	COBOL, FORTRAN, RPG, BASIC, UPL,		BASIC, COBOL, MIL, SDL, RPG, FORTRAN
Operating system	Batch, real-time	NDL Batch, real-time, time-sharing	NDL Batch, real-time, time-sharing	Batch, real-time, time-sharing	77 Batch, real-time, time-sharing
Language implemented in firmware Operating system implemented in firmware	Fully Fully	Fully Fully	fully Fully	Fully Fully	Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	35,045 (32K bytes) 198 Leases 1,020 (8K bytes)	Contact vendor	Contact vendor	Contact vendor	71,500 410 5,750 (262K bytes)
Date of first delivery Number installed to date	Second qtr. 1978 NA	Third qtr. 1972 NA	Second qtr. 1973 NA	May 1977 Over 350	First qtr. 1980 NA
COMMENTS			4K to 8K bipolar storage is available for control memory		

MANUFACTURER AND MODEL	Burroughs L9000 Series	Cado Systems Corporation System 20	Cado Systems Corporation System 20/IV	Cado Systems Corporation System 40	Cado Systems Corporation System 40/IV
WORD LENGTH, BITS	64	8-bit byte	8-bit byte	8-bit byte	8-bit byte
NO. WORKSTATIONS SUPPORTED	None	1	2	2	2
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 1.5/1.2 4K/48K bytes Standard No Standard	MOS 2.5/0.75 6K/10K bytes No No No	MOS 1.3/0.4 20K/52K bytes Standard No	MOS 2.5/0.75 5K/9K bytes No No No	MOS 1.3/0.4 20K/52K bytes Standard No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	RAM; 8K bytes	10K PROM; 1-2K, 1-1K 6.0 (5 digits) No No Standard No No	52K PROM; 4K 3.9 (5 digits) No No Standard No Standard	9K ROM; 2K 6.0 (5 digits) No No Standard No No	52K PROM; 4K 3.9 (5 digits) No No Standard No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	_ _ _	Standard 1M bps None	Standard 1M bps 3	Standard 1M bps None	Standard 1M bps 3
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported		 Up to 9600 bps Up to 9600 bps 	 Up to 9600 bps Up to 9600 bps 	 Up to 9600 bps Up to 9600 bps 	Up to 9600 bps Up to 9600 bps —
Network architectures supported RJE terminals emulated	_	 IBM 2770, 2780/ 3780	 IBM 2770, 2780/ 3780	  IBM 2770, 2780/  3780	 IBM 2770, 2780/ 3780
IBM 3270 emulation		No	No	No	No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No No	1.2 to 3.6M bytes Cart.; 9.5 to 19M bytes	1.2 to 3.6M bytes Cart.; 9.5 to 19M bytes	1.2 to 3.6M bytes Cart.; 9.5 to 19M bytes	1.2 to 3.6M bytes Cart.; 9.5 to 19M bytes
Drum/fixed head disk storage	No	Fixed media; 15M bytes	Fixed media; 15M	Fixed media; 15M bytes	Fixed media; 15M bytes
Magnetic tape cassettes/cartridges	Cassette; 1 KBS	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	10 KBS 60, 90, 120, 150 cps 90-250 lpm 9600 bps 32 char. x 8 lines Mag. ledger card reader	NA 150 cps No 9600 bps 80 char. x 24 lines None	NA 150 cps 3000 lpm 9600 bps 80 char. x 24 lines None	NA 45 cps 300 lpm 9600 bps 80 char. x 24 lines None	NA 45 cps 300 lpm 9600 bps 80 char. x 24 lines None
SOFTWARE Assembler	Assembler	No	No	No	No
Compilers	COBOL	Basic (CADOL)	Basic (CADOL)	Basic (CADOL)	Basic (CADOL)
Operating system	_	Real-time	Real-time, Multi-task	Real-time	Real-time, multi-task
Language implemented in firmware Operating system implemented in firmware	Fully —	Partially Partially	Partially Partially	Partially Partially	Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available	17,490 — Leases	13,995 — Quantity	19,845 — Quantity	17,500 130 Quantity	25,075 — Quantity
Price of memory increment, \$  Date of first delivery	— June 1975	895 (4K bytes) January 1978	1,800 (32K bytes) June 1978	895 (4K bytes) April 1976	1,800 (32K bytes) June 1978
Number installed to date  COMMENTS	Six models L 9300, L 9400, and L 9500 with 60-cps printer, L 9700, L9800, and L 9900 with 90-cps printer, L 9500 and L 9900 have mag ledger capability	200+	60+	600+	60+

MANUFACTURER AND MODEL	Cascade Data Concept II	Cascade Data Concept III	Cascade Data Concept IV	CDA Inc. DG IT Series	CDA Inc. DG MP/100 Series
WORD LENGTH, BITS	16	16	8	16	16
NO. WORKSTATIONS SUPPORTED	16	16	1 per application	5	16
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds	Core 1.0/0.35 16K/64K Standard No No 32K No 8.8 Standard	MOS 0.5/0.5 32K/64K No No No 64 No 7.5 (word)	MOS 0.6/0.2 16K/60K No No No 64K bytes PROM; to 2K bytes 2.0 (byte) Optional	MOS .96/.50 32K/32K No No No 1024 No .84 Optional	MOS .96/.50 32K/32K No No No 1024 No .84 Optional
Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	No Standard Optional Optional	No Standard Optional Standard	Standard Standard No No	No No Optional Standard	No No Optional Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 413K 0	Standard 413K 0	Optional 750K 0	Standard 2M bytes 16	Standard 2M bytes 16
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	 Standard Standard 	 Standard Standard 	Standard Standard 	5 Optional (1) Optional (4) Optional	16 Optional (6) Optional (16) Optional
Network architectures supported RJE terminals emulated IBM 3270 emulation	None None No	None None No	None None No	NA 2780/3780 Yes	NA 2780/3780 YES
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Cartridge; 40MK bytes	1.2M bytes Cartridge; 40M bytes	4.8M bytes No	630K bytes Cartridge; 10-20M bytes	630K bytes Cartridge; 10-20M bytes
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	30-60 KBS 55 cps 125-600 lpm 9600 bps 80 char. x 24 lines Paper tape reader and punch	30-60 KBS 55 cps 125-600 lpm 9600 bps 80 char. x 24 lines Paper tape reader and punch, card reader	No 60 cps 125-600 lpm 19.2K bps 80 char. x 24 lines Paper tape reader and punch	No (1) 80, 160 cps No Yes Yes	No (2) 80, 160 cps No Yes Yes
SOFTWARE Assembler	Macro assembler	Macro assembler	Macro assembler, BASIC	Assembler and macro assembler	Assembler and macro assembler
Compilers	RPG	RPG	No	ALGOL, BASIC, FORTRAN	ALGOL, BASIC FORTRAN
Operating system  Language implemented in firmware Operating system implemented in firmware	Batch, real-time, time-sharing No No	Batch, real-time, time-sharing No No	Batch, real-time Partially Partially	Real-time No No	Real-time No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$	22,200 (32K bytes)	26,900 (16K bytes)	16,900 (32K bytes) 	Contact vendor	Contact vendor
Discounts available Price of memory increment, \$	1,200 (16K bytes)	2,700 (32K bytes)	1,500 (16K bytes)	Quantity NA	Quantity NA
Date of first delivery Number installed to date	January 1970 300	November 1977 50	September 1978 25	March 1979 10	April 1979 5
COMMENTS	Operating system provides 2 partitions; system price includes 32KB CPU, 5MB disk, console CRT, and keyboard	Operating system provides 45 partitions; system price includes 32KB CPU, 5MB disk, and 2,000 char. display	Applications compatible with concept II and III; system price includes two application software packages, 32KB CPU, 2.4MB floppy disk, and 2,000 char. display	Turnkey systems for automotive parts distribution using Data General package hardware featuring MicroNova CPU; expandable	Turnkey systems for automotive parts distribution using Data General package hardware featuring MP/100 CPU; expandable
	кеуроага	ural. uispidy	packages, 32KB CPU, 2.4MB floppy disk, and 2,000 char.	CPU; expandable	

MANUFACTURER AND MODEL	CDA Inc. DG Nova Series	Century Computer 300	Century Computer 400	Century Computer 700	Century Computer 900
WORD LENGTH, BITS	16	8, 16	8, 16	8, 16	8, 16
NO. WORKSTATIONS SUPPORTED	16	4	8	20	32
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS .40/.50 64K/128K No No Optional	MOS .4/.2 32K/64K bytes No Standard No	MOS .4/2 64K/256K bytes No Standard No	MOS 4/2 96K/256K bytes No Standard No	MOS .4/.2 160K/512K bytes No Standard No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	1024 No .40 Optional No No Optional Standards	64K bytes 4K x 48 1.4 (16 bits) Standard Standard Standard No Standard	64K bytes 4K x 48 1.4 (16 bits) Standard Standard Standard No Standard	64K bytes 4K x 48 1.4 (16 bits) Standard Standard Standard No Standard	64K bytes 4K x 48 1.4 (16 bits) Standard Standard Standard No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 2M bytes 16	Standard 1.6M bytes 15	Standard 1.6M bytes 15	Standard 1.6M bytes 15	Standard 1.6M bytes 15
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	16 Optional (16) Optional (16) Optional	4 Opt.; 9600 bps Std.; 19,200 bps Bisync	8 Opt.; 9600 bps Std.; 19,200 bps Bisync	20 Opt.; 9600 bps Std.; 19,200 bps Bisync	32 Opt.; 9600 bps Std.; 19,200 bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	NA 2780/3780 Yes	No	— No	No	No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	630K bytes Cart; 10-20M bytes Pack; 96M bytes No	No Both; (10) 320K bytes No	No Both, (10) 320K bytes	No Both; (10) 640K bytes No	No Both; (10) 1200K bytes No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	Yes (8) 80, 160 cps No Yes Yes	36 KBS 165 cps 150 lpm 9600 cps 1920 characters	36 KBS 165 cps 150 lpm 9600 bps 1920 characters	36 KBS 165 cps 150 lpm 9600 bps 1920 characters	36 KBS 165 cps 150 lpm 9600 bps 1920 characters
SOFTWARE Assembler Compilers	Assembler and macro assembler ALGOL, BASIC FORTRAN	Assembler and macro assembler BASIC	Assembler and macro assembler BASIC	Assembler and macro assembler BASIC	Assembler and macro assembler BASIC
Operating system	Real-time	Real-time	Real-time	Real-time	Real-time
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	Contact vendor 350  Quantity 6,000 (128K)	16,500 Contact vendor OEM	26,500 Contact vendor	34,000 Contact vendor OEM	42,500 Contact vendor OEM
Date of first delivery Number installed to date	October 1979	June 1975 NA	June 1975 NA	June 1975 NA	June 1975 NA
COMMENTS	Turnkey systems for automotive parts distribution using Data General package hardware featuring Nova CPU; expandable	May be upgraded to next size model as the customer needs more capacity	Additional work- stations available	Additional work- stations available	Additional work- stations available

MANUFACTURER AND MODEL	Compal 8100	Compal 8200	Compal 9000	Computer Automation Naked Milli LSI-3/05	Computer Automation Naked Mini LSI-2 Series
WORD LENGTH, BITS	8	8	16	16	16 + 2
NO. WORKSTATIONS SUPPORTED	1	1	15	1	1
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 1.6/0.4 56K/56K No No No	MOS 1.6/0.4 56K/56K No No No	MOS .96/.16 32K/64K No No	Core, MOS 0.98-1.6/0.5-0.8 512/8K No No No	Core, MOS 0.85-1.2/0.4-0.6 8K/512K Optional No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K No 3.0 No No Yes No Optional	64K No 5.6 No No Yes No Optional	32K No 2.4 Standard No Yes Optional Standard	128 ROM; 512 x 24 bits 6.25 (2 digits) No No Standard Optional Optional	32K ROM; 512 x 56 bits 4.12, 2.06 Standard No Standard Optional Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	No 250K bytes 8	No 250K bytes 9	Yes 3072K bytes 16	Standard 250K 1	Standard 1 M 3
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	3 Std.; 110-9600 bps Std.; 110-9600 bps Async, Bisync	3 Std.; 110-9600 bps Std.; 110-9600 bps Async, Bisync	16 Opt.; 1200-9600 bps Std.; 110-9600 bps Async, Bisync	4 Opt.; 9600 bps Opt.; 9600 bps	4 Opt.; 9600 bps Opt.; 9600 bps
Network architectures supported RJE terminals emulated IBM 3270 emulation	 2780/3780 No	 2780/3780 No	 2780/3780 No	  -  -	
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives	Std.; 630K-1.2M	Std.; 630K-1.2M	Opt.; 315-630K bytes	243-972K bytes	243-972K bytes
Disk pack/cartridge drives	No	No	Cartridge; 10-20M bytes	Cartridge; 4.92- 19.68M bytes	Cartridge; 4.92- 19.68M bytes
Drum/fixed head disk storage	No Ont appearts	Opt.; 9-27M bytes	Opt.; 12.5-25M bytes Both optional	No No	No No
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch	Opt. cassette	No 150 cps	Optional 60-180 cps	20 KBS 100, 165 cps	20 KBS 100, 165 cps
Serial printer Line printer Data communications interface CRT Other supported peripheral units	150 cps No 110-9600 bps 1024 characters	No 110-9600 bps 1920 characters	240-900 lpm 110-9600 bps 1920 character	No To 9600 bps 80 char. x 24 lines Paper tape units	No To 9600 bps 80 char. x 24 lines Paper tape units
SOFTWARE Assembler	Assembler and	Asembler and	Assembler and	Macro assembler	Macro assembler
Compilers	macro assembler BASIC, COBOL,	macro assembler BASIC, COBOL,	macro assembler BASIC, FORTRAN,	FORTRAN	FORTRAN, BASIC
Operating system	FORTRAN, PASCAL Real-time	FORTRAN, PASCAL Real-time	PASCAL Real-time	Real-time	Batch, real-time,
Language implemented in firmware Operating system implemented in firmware	No Partially	No Partially	No No	No No	Multi-tasking No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel,	10,995	11,995	19.995	825 (Alpha)	2,540 (2/10 Alpha)
and minimum memory in chassis, \$ Monthly maint, of basic configu-	10,995	60	167		2,865 (2/20 Alpha)
ration above for on-site contract, \$ Discounts available Price of memory increment, \$	None NA	None NA	None 1,850 (32K bytes)	Quantity —	Quantity
Date of first delivery Number installed to date	October 1976 400	November 1979 NA	September 1979	January 1975 NA	July 1973 NA
COMMENTS	Price includes CPU, disk, CRT, and printer; Compal specializes in complete turnkey business systems	Price includes turnkey computer systems with one application software package	Price includes turnkey computer systems with one application software package	ROM/EPROM & RAM/ROM/PROM are available in combination; ROM, PROM, EROM available in max. capacities of 8K, 2K, & 4K words, respectively	ROM/EPROM & RAM/ROM/PROM are available in combination; ROM, PROM, EROM available in max. capacities of 8K, 2K, & 4K words respectively

MANUFACTURER AND MODEL	Computer Automation Naked Mini 4 Family	Computer Automation Scout Naked Mini 4/04	Computer Automation Syfa System 200	Computer Automation Syfa System 300	Computer Automation Syfa System 1000
WORD LENGTH, BITS	16	16	16	16	16
NO. WORKSTATIONS SUPPORTED	4	1	4	8	32
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core, MOS 0.55i-0.85/0.3-0.4 4K/64K Optional No No	MOS 1.0/786 16K/64K words No No	MOS 0.7/0.5 64K/64K bytes Standard No No	MOS 0.7/0.5 64K/304K bytes Standard No No	MOS 0.7/0.5 64K/304K bytes Standard No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K None 1.5-3.0 Standard Optional Standard Optional Standard	64K Up to 32K bytes 3.40 Standard Optional Standard Optional Standard	32K ROM; 512 x 56 bits 4.12, 2.06 Standard No Standard Optional Optional	32K ROM; 512 x 56 bits 4.12, 2.06 Standard No Standard Optional Optional	32K ROM; 512 x 56 bits 4.12, 2.06 Standard No Standard Optional
Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Optional 115K 4	Standard 3.8M 3	Standard NA NA	Standard NA NA	Standard NA NA
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	8 No Opt.; 50-19,200 bps	1 per interface No Opt.; 19.2K bps No	5 Std.; 4800 bps Std.; 9600 bps Bisync	9 Std.; 4800 bps Std.; 9600 bps Bisync	34 Opt.; 9600 bps Std.; 9600 bps Bisync, SDLC
Network architectures supported RJE terminals emulated IBM 3270 emulation	_ _ _	DDCMP No No	IBM 3780 Optional	IBM 3780 Optional	SNA IBM 3780, 3790 Optional
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	(4) 243K-972K bytes Both; 5-1200M bytes	(1-4) 1M-4M bytes No	No Both; (8) 1760M bytes	No Both; (8) 1760M bytes	No Both; (8) 1760M bytes
Drum/fixed head disk storage	No	No	No 	No	No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	20 KBS No 60-165 lpm To 9600 bps 80 char. x 24 lines Paper tape units; A/D & D/A con-	No — Yes 19.2K bytes 80 char. x 24 lines A/D & D/A, relay digital I/O	No 150 cps Optional Yes Yes, 2 —	No 150 cps Optional Yes Yes, 2	Yes Opt.: 150 cps Std.; 600 lpm Yes Yes, 8
SOFTWARE Assembler	verters Assembler &	Assembler	No	No	No
Compilers	Macro assembler FORTRAN, BASIC	FORTRAN IV,	SYBOL	SYBOL	SYBOL
Operating system	Batch, real-time	COBOL Real-time	Time-sharing	Time-sharing	Time-sharing
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$	995 (4/10 Alpha) 4,080 (4/90 Alpha) —	1,010 (32KB RAM) NA	29,950 349	36,000 369	102,500 840
Discounts available Price of memory increment, \$	Quantity —	Quantity 625 (32K bytes)	NO NA	No —	No —
Date of first delivery Number installed to date	June 1977 NA	January 1980 NA	NA NA	NA NA	1976 250
COMMENTS	All processors include powerfail, auto restart, auto load, and real-time clock capabilities as standard features	Each SCOUT board has on the board a self testing diagnostic feature and functions called ISOLITE which execute at power up and under program control	Basic configuration includes 64K bytes, 32MB disk, two CRT's, five slot chassis, 150 cps printer, 3780 communications	Basic configuration includes 64K bytes, 32MB disk, two CRT's, nine slot chassis, 150 cps printer, power supply, 3780 com- munications	Basic configuration include 128K bytes, 8 port multiplexer, nine slot chassis, power supply, eight CRT's, two 32MB disk, 600 lpm printer, 3780 communications

MANUFACTURER AND MODEL	Computer Designed Systems Adviser IV/700	Computer Designed Systems Adviser IV/800	Computer Designed Systems Adviser IV/900	Computer Hardware Inc. 2130	Computer Hardware Inc. 3230
WORD LENGTH, BITS	16 + 2	16 + 2	32 + 4	16	16
NO. WORKSTATIONS SUPPORTED	32	32	128	32	32
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core, MOS ,50, 80/,04 16K/512K Optional Optional Optional	Core, MOS .50, .80 / 04 150, .80 / 04 10, .512K Optional Optional Optional	MOS .35, .68/.03 32K/1024K Optional Optional Optional	MOS, core 0.8/0.25 8K/2,000K Standard Optional Standard	MOS 1.6/0.25 8K/64K Standard No Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K ROM; 10K x 32 bits 1.05 Standard Optional Standard Optional Optional	64K ROM; 10K x 32 bits 1.05 Standard Optional Standard Optional Optional	0.4 Standard Optional	64K No 1.6 Standard Optional No No Optional	64K No 2.7 Standard Optional No No Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.6M 1-125	Standard 1.6M 1-125	Standard 2.91 M	Standard 1.25M 8	Standard 1.25M 8
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported  Network architectures supported RJE terminals emulated	32 Opt.; 9600 bps Opt.; 9600 bps 2780, 3780, SNA/ SDLC SNA-opt. 2780/3780	32 Opt.; 9600 bps Opt.; 9600 bps 2780, 3780, SNA / SDLC SNA-opt. 2780/3780	128 Opt.; 9600 bps Opt.; 9600 bps 2780, 3780, SNA / SDLC SNA-opt. 2780/3780	32 Opt.; 50-9600 bps Opt.; 50-9600 bps Bisync None IBM 2780/3780	32 Opt.; 50-9600 bps Opt.; 50-9600 bps Bisync None IBM 2780/3780
IBM 3270 emulation PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Both; 2.4G bytes	Optional No Both; 2.4G bytes	Optional No Both; 4.8G bytes	No No Pack; 1600M bytes	No No Pack; 1600M bytes
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	No	No	No		_
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	120 KBS 200 cps 300-1200 lpm To 9600 bps 80 x 24 A/D-D/A conv., plotters, graphics	120 KBS 200 cps 300-1200 lpm To 9600 bps 80 x 24 A/D-DA conv., plotters, graphics	120 KBS 200 cps 300-1200 lpm To 9600 bps 80 x 24 A/D-D/A conv., plotters, graphics	Yes No 300, 600 lpm To 4800 bps; synch. 80 char. x 24 lines Card, PT, plotter	Yes No 300, 600 lpm To 4800 bps; synch. 80 char. x 24 lines Card, PT, plotter
SOFTWARE	Macro-assembler	Macro-assembler	Macro-assembler	Assembler & macro	Assembler & macro
Assembler Compilers		PASCAL. COBOL.		assembler COBOL, FORTRAN,	assembler COBOL, FORTRAN.
Operating system		BASIC, FORTRAN Batch, real-time,		RPG Batch, time-sharing	RPG Batch, time-sharing
Language implemented in firmware Operating system implemented in firmware	multi-task, interactive Partially Partially		multi-task, interactive Partially	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$ Date of first delivery	59,000 (64K) 5,400 Quantity 18,000 (64K) October 1977	82,000 (64K) 5,400 Quantity 18,000 (64K) October 1977	100,000 (64K) 5,400 Quantity 18,000 (64K) November 1978	32,000 (16K bytes)  Contact vendor Contact vendor 1,500 (16K bytes) June 1974	15,000 (16K bytes) Contact vendor Contact vendor 1,500 (16K bytes) April 1976
Number installed to date	NA	NA .	NA .	NA	NA .
COMMENTS	Single source responsibility, field upgradable, virtual mem., min. terminal degradation under load, turnkey systems avail., inter- active, direct pro- cessing system	Single source responsibility, upgradable, virtual degradation, turnkey avail., interactive, direct processing system	Single source responsibility, virtual mem., turnkey, interactive, direct processing system		

MANUFACTURER AND MODEL	Computer Hardware Inc. 4210	Computer Hardware Inc. 4250	Computer Hardware Inc. 4800	Computer Talk Model 400	Computer Talk Model 407
WORD LENGTH, BITS	16	16	16	16	16
NO. WORKSTATIONS SUPPORTED	4	16	4	256	256
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.47/0.3 4K/26K Standard No Optional	MOS 0.47/0.3 4K/1024K Standard Optional Optional	Bipolar dynamic	MOS 0.5, 0.3/0.3, 0.15 4K/512K Optional Optional See comments	MOS 0.5, 0.3/0.3, 0.15 4K/512K Optional Optional See comments
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	32K No 4.662 Standard No Standard No Optional	64K PROM; 11,520 bits 3.5 Standard No Standard No Optional	32K No — Standard No Standard No Standard	32K; 512K PROM; 768 words 1.0 Standard Standard Standard Standard Standard with date	32K; 512K PROM; 768 words 1.0 Standard Standard Standard Standard Standard with date
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard — 8	Standard — 16	Standard  7	Standard 1M 1-256	Standard 1M 1-256
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	4 Opt.; 50-9600 bps Opt.; 50-9600 bps Bisync	16 Opt.; 50-9600 bps Opt.; 50-9600 bps Bisync	4 Opt.; 50-9600 bps Std.; 50-9600 bps Bisync	256 Opt.; 50-9600 bps Opt.; 50-9600 bps Async, Bisync, SDLC	256 Opt.; 50-9600 bps Opt.; 50-9600 bps Async, Bisync, SDLC
Network architectures supported RJE terminals emulated IBM 3270 emulation	None IBM 2780/3780 No	None IBM 2780/3780 Yes	None IBM 2780/3780 No	None Most RJE terminals Yes	None Most RJE terminals Yes
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	Yes No	Yes Cart.; 3M or 10M bytes	1-4M bytes No	110K-10,240K bytes Both; 1.2M-1 billion bytes	110K bytes Both; 1.2M-1 billion bytes
Drum/fixed head disk storage  Magnetic tape cassettes/cartridges	No	No	No Cassette; 10 ips	Moving-head; 2.5M bytes 30-800 cps; 4 KBS	Moving-head; 2.5M bytes 30-800 cps; 4 KBS
Magnetic tape cassettes/ cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 30-180 cps 300 lpm 9600 bps 80 char. x 24 lines None	No 30-180 cps 300 lpm 9600 bps 80 char. x 24 lines None	No No 84 Ipm 19.2K bps 1920 characters	5-120 KBS 10-200 cps 220-600 lpm 50-9600; 56K 96 char. x 32 lines Digitizers, plotters, factory automation equipment	50-120 KBS 10-200 cps 300 lpm 50-9600; 56K 96 char. x 32 lines Digitizers, plotters factory automation equipment
SOFTWARE Assembler	Assembler	Macro assembler	2-pass	Assembler and macro assembler	Assembler and macro assembler
Compilers	FORTRAN	BASIC, COBOL, FORTRAN	BASIC, COBOL FORTRAN	BASIC, FORTRAN, APL	BASIC, FORTRAN, APL
Operating system	Real-time	Real-time	Real-time	Batch, real-time, time-sharing	Batch, real-time, time-sharing
Language implemented in firmware Operating system implemented in firmware	No No	No No	No Partially	Partially Partially	Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel,	13,200	37,800	8,850	24,950 (4K MOS)	31,500 (4K MOS)
and minimum memory in chassis, \$ Monthly maint, of basic configu-	Contact vendor	Contact vendor	Contact vendor	168	_
ration above for on-site contract, \$ Discounts available Price of memory increment, \$	Contact vendor 960 (8K bytes)	Contact vendor 2,160 (16K bytes)	Contact vendor 1,500 (32K bytes)	Volume 1,100 (4K bytes)	Volume 1,100 (4K bytes)
Date of first delivery Number installed to date	October 1977 NA	January 1978 NA	October 1979 NA	May 1975 NA	January 1978 NA
COMMENTS	Price includes CPU, two 250K bytes diskettes, cassette, 60 cps printer, oper- ating system, and time system appli- cation	Price includes 96K bytes of ECC memory, a 10M byte disk cartridge, cassette, CRT, 60 cps printer, DX10 operating system, FORTRAN compiler, sort/merge, and time system appli- cation	Price includes 64K bytes memory, 84 lpm printer, 40 char. display key- board, two 5-inch diskettes (328K bytes), two RS232 operating system, BASIC, Assembler, and time/attendance application	Storage protection std. by memory partition and opt. by page; mapping to 512K opt.; 4K PROM opt.; on low power, memory is stored on disk; price includes CRT, light pen, modem, 1.2M-byte disk, arith. & I/O processors, & battery pack	Expanded Model 400 with additional features; disk ex- panded to 2.5M bytes, 300-lpm x 132 printer and mini-floppy disk for I/O

Computer Talk Model 408	Corporation CGP-100	Control Data Cyber 18 Series	Data General Eclipse C/150	Data General Eclipse C/350
16	16	16 + 2	16 + 5	16 + 5
256		_	64	64
MOS 0.5, 0.3/0.3, 0.15 4K/512K Optional Optional See comments	MOS 0.7/0.4 32K/512K Standard None Optional	MOS .75/0.3 16K/256K bytes Standard Optional Standard	Core, MOS 0.8, 0.5, 0.7/0.4 64K/512K No Standard Standard	Core, MOS 0.8, 0.7/0.5 32K/1024K No Standard Standard
32K; 512K PROM; 768 words 1.0 Standard Standard Standard Standard Standard Standard with date	32K PROM; 60 x 512W 0.9 Standard Optional No No Standard	64K ROM/RAM; 8K 0.95 Standard No Standard No Standard	32K ROM; 2K x 56 bits 0.6 Standard No Standard Standard Standard	32K ROM; 2K x 56 bits 0.6 Standard Standard Standard No Standard
Standard 1 M 1-256	Standard 0.7M 16	Standard 1.2M 16	Standard 1.25M 16	Standard 1.25M/5.0M 16
256 Opt.; 50-9600 bps Opt.; 50-9600 bps Async., Bisync., SDLC		Opt.; 4800 bps Opt.; 9600 bps None	Opt.; 56000 bps Opt.; 9600 bps Bisyn., X.25	
Most RJE terminals Yes		2780/3780, HASP No	2780/3780, HASP Yes	X.25 2780/3780, HASP Yes
110K-10240K bytes Both; 1.2M-1 billion bytes	256K-4M bytes Pack; 1.2 billion bytes	280K-560K bytes Both; 4-1440M bytes	315K-2.5M bytes Pack & cartridge; 10-1520M bytes	315K-2.5M bytes Pack & cartridge; 10-1520M bytes Fixed-head; 1-16M
bytes			bytes No	bytes No
50-120 KBS 10-200 cps 300 lpm 50-9600; 56K 96 char. x 32 lines Digitizers, plotters, factory automation equipment	30-75 KBS 165 cps 340 lpm 9600 bps 80 char. x 24 lines Graphic displays, plotters, digitizers	80K bps 180 cps 300, 600, 900 lpm Up to 9600 bps 1920 characters Opt. digital & analog subsystems	10-72 KBS 10-180 cps 240-900 lpm 56,000 bps 80 char. x 24 lines Modular digital & analog data control & acq. subsys. opt.	10-72 KBS 10-180 cps 240-900 lpm 56,000 bps max. 80 char. x 24 lines Modular digital & analog data control & acq. subsys. opt.
Assembler and	Assembler	Macro assembler	Assembler &	Assembler & macro assembler
BASIC, FORTRAN,	FORTRAN, TPL, PEP	FORTRAN, COBOL,	COBOL, IDEA, RPG II,	COBOL, IDEA, RPG II,
Batch, real-time, time-sharing Partially Partially	Multi-sharing, multi-tasking No No	Batch, real-time, time-sharing No	Batch, real-time, time-sharing No	Batch, real-time, time-sharing No
30,500 (4K MOS)	Contact vendor	13,700-15,300	28,500 (128K bytes) 225	49,500 (128K bytes) 330
Volume 1,100 (4K bytes)	Contact vendor	Quantity 3,000 (16K words)	Various type 6,000 (64K bytes)	Various types 6,000 (64K MOS)
January 1978 NA	November 1977 100+	May 1976 NA	February 1979 100 over	October 1978 300
Expanded Model 400 with additional features: disk ex- panded to 2.5M bytes, 300-1pm x 132 printer and mini-cassette for I/O	Extensive 3-D interactive CAD/CAM design application software; 24-slot high-resolution chassis; microdiagnostic and bootstrap diagnostic facilities; 100-amp power supply; desk console	System includes RPG System 3 com- patibility; AUTRAN package available for process control; comm. package available for IBM 2780/3780, HASP, and CDC 200 UT	Includes X.25 Net- working, AZ-text, word processing, ANSI '74, virtual COBOL	Includes virtual COBOL ANSI '74, highest Level 2 implementation; std. features include extended floating-point functions, plus a commercial instruc- tion set
	MOS 0.5, 0.3/0.3, 0.15 4K/512K Optional Optional See comments  32K; 512K PROM; 768 words 1.0 Standard	MOS 0.5, 0.3/0.3, 0.15 4K/512K Optional Optional See comments  32K; 512K PROM; 768 words 1.0 32K Standard Optional No	MOS 0.5. 0.3./0.3, 0.15 4/K/512K Optional Optional See comments  32K. 512K PROM; 768 words 10 Standard	MCS

MANUFACTURER AND MODEL	Data General Eclipse M/600	Data General Eclipse S/130	Data General Eclipse S/140	Data General Nova 4C	Data General Nova 4S
WORD LENGTH, BITS	16 + 5	16 + 5	16 + 5	16	16
NO. WORKSTATIONS SUPPORTED	64	64	64	64	64
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core, MOS 0.8/0.5 32K/1024K No Standard Standard	Core, MOS 0.8, 0.5, 0.7/0.4 16K/512K No Standard Standard	MOS 0.2/0.4 64K/512K No Standard Standard	MOS .40 16K/32K No No	MOS 
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	32K ROM; 2K x 56 bits 0.6 Standard Standard Standard No Standard	64K PROM/RAM; 224 B 0.6 Standard No Optional Optional Standard	32K ROM; 2K x 56 bits 0.2 Standard Optional Standard Standard Standard	1K No O.2 Optional No Standard Optional Optional	1K No O.2 Optional Optional Standard Optional Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.25M / 5.0M 16	Standard 1.25M 16	Standard 1.25M 16	Standard 1M 16	Standard 1M 16
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	Opt.; 56000 bps Opt.; 9600 bps Bisync., X.25	Opt.; 56000 bps Opt.; 9600 bps Bisync., X.25		128 Opt.; (32) 56K bps Opt.;(128)19200 bps Bisync., X.25	128 Opt.; (32) 56K bps Opt.;(128)19200 bps Bisync., X.25
Network architectures supported RJE terminals emulated IBM 3270 emulation	X.25 2780/3780, HASP Yes	X.25 2780/3780, HASP Yes	X.25 2780/3780, HASP Yes	XODIAC, IBM BSC 2780/3780, HASP II No	XODIAC, IBM BSC 2780/3780, HASP II No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	315K-2.5M bytes Pack & cartridge 10-6080M bytes	315K-2.5M bytes Pack & cartridge; 10-15 20M bytes	315K-2.5M bytes Pack & cartridge; 10-1520M bytes	Yes Yes	Yes Yes
Drum/fixed head disk storage	Fixed-head; 1-16M bytes	Fixed-head; 1-16M bytes	Fixed-head; 1-16M bytes	Yes	Yes
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 10-72 KBS 10-180 cps 240-900 lpm 56,000 bps max. 80 char. x 24 lines Modular digital & analog data control	No 10-72 KBS 10-180 cps 240-900 lpm 56,000 bps 80 char. x 24 lines Modular digital & analog data control	No 10-72K bps 10-180 cps 240-900 lpm 56,000 bps 80 char. x 24 lines Modular digital & analog data control	No Yes Yes Yes Yes Yes Digital & analog, data control sub-	No Yes Yes Yes Yes Yes Digital & analog, data control sub-
SOFTWARE Assembler	& acq. subsys. opt. Assembler &	& acq. subsys. opt. Assembler &	& acq. subsys. opt.  Assembler &	system Yes	yes Yes
Compilers Operating system	macro assembler COBOL, IDEA, RPG II, FORTRAN, PL/1 Batch, real-time,	macro assembler FORTRAN, BASIC, ALGOL, PL/1 Batch, real-time,	macro assembler COBOL,IDEA,RPG II, BASIC, FORTRAN Batch, real-time,	BASIC, FORTRAN, ALGOL Real-time, RDOS,	BASIC, FORTRAN, ALGOL Real-time, RDOS,
Language implemented in firmware Operating system implemented in firmware	time-sharing No No	time-sharing No No	time-sharing No No	multi-tasking No No	multi-tasking No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$	80,000 (256K bytes) 470	16,500 (128K bytes) 105	16,500 (128K bytes) 105	2,800 (32K bytes) 45	5,800 (32K bytes) 56
Discounts available Price of memory increment, \$	Various type 8,000 (256K bytes)	Various type 4,500 (32K core)	Various types 5,000 (128K bytes)	<u>-</u>	
Date of first delivery Number installed to date	April 1978 250	February 1975 1000+ (all models)	NA NA	NA NA	NA NA
COMMENTS	Includes virtual COBOL ANSI '74, highest Level 2 implementation; I/O processor with 64KB for handling low-speed character-oriented data movement	256 56-bit words of writable control store optionally available	X.25 Networking, AZ-text, word processing		

MANUFACTURER AND MODEL	Data General Nova 4X	Datapoint 1100	Datapoint 1150	Datapoint 1170	Datapoint 1500
WORD LENGTH, BITS	16	8-bit byte	8-bit byte	8-bit byte	8-bit byte
NO. WORKSTATIONS SUPPORTED	64				
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS  64K/128K No No No	MOS 1.6/0.6 4K/16K bytes No No	MOS 0.8/0.3 24K/32K bytes Standard Standard Standard	MOS 0.8/0.3 48K/56K bytes Standard Standard Standard	MOS 0.65/0.3 32K/64K bytes Standard Standard No/
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	1K No 0.2 Optional Optional Standard Optional Standard	16K bytes No 4.8 No No Standard No Optional	24K bytes ROM; 4K bytes 1.4 No No No No No	48K bytes ROM; 4K bytes 1.4 No No Standard No No	60K bytes ROM; 4K bytes 1.8 No No No No
Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1M 16	No 195K —	No 114K —	No   114K 	No 250K —
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	128 Opt.; (32) 56K bps Opt.;(128)19,200 bps Bisync, X.25		  		— Up to 9600 bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	XODIAC, IBM BSC 2780/3780, HASP II No	 _ _		<u>-</u>	  BM 3780 
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	Yes Yes	256K-1M bytes No	512K-1M bytes No	512K-1M bytes No	512K bytes No
Drum/fixed head disk storage	Yes	No	No	No	No
Magnetic tape cassettes/cartridges	No	Cassette; 352 cps	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	Yes Yes Yes Yes Yes Yes Digital & analog, data control sub- system	9.6-20 KBS 120 300, 600 lpm Up to 9600 bps 80 char. x 12 lines	9.6-20 KBS 80-160 cps 300, 600 lpm Up to 9600 bps 80 char. x 12 lines	9.6-20 KBS 80-160 cps 300, 600 lpm Up to 9600 bps 80 char. x 12 lines	No 80-160 cps No Up to 4800 bps 80 char. x 24 lines
SOFTWARE Assembler	Yes	Yes	Yes	Yes	No
Compilers Operating system	BASIC, FORTRAN, ALGOL Real-time, RDOS, multitasking	BASIC, RPG PLUS, SCRIBE, DATABUS, DATA FORM Batch	DATABUS, MULTI- FORM, BASIC, RPG- PLUS Batch	DATABUS, MULTI- FORM, DATA- SHARE, RPGPLUS Batch, time-sharing	DATABUS, DATA- FORM, RPGPLUS, DATASHARE Batch, stand-alone Multi-tasking
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	10,400 (128K bytes) 78  5,000 (128K bytes)		14,480 76  125 (8K bytes)	15,980 78  125 (8K bytes)	6,550  
Date of first delivery Number installed to date	NA NA	January 1974 NA	August 1976 NA	July 1977 NA	October 1977 NA
COMMENTS			Price includes 24K bytes and two diskette drives	Price includes 48K bytes and two diskette drives	Price includes 64K bytes and 1M byte dual diskette

MANUFACTURER AND MODEL	Datapoint 1800	Datapoint 2200	Datapoint 5500	Datapoint 6600	Dataram BM-1
WORD LENGTH, BITS	8-bit byte	8-bit byte	8-bit byte	8-bit byte	16
NO. WORKSTATIONS SUPPORTED					
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection CENTRAL PROCESSOR No. of directly addressable words	MOS 0.63 64K/64K bytes Standard Standard Standard	MOS 1.6/0.4 4K/16K bytes No No No	MOS 0.89/0.3 48K/48K bytes Standard No Standard	MOS 0.6/0.2 120K/120K bytes Standard Standard Standard	Core, MOS 1.2/1.2 120K/120K bytes No No No
Control storage  Add time, microseconds	ROM/RAM; 4KB/ 60KB 3.8	No 4.8	ROM; 4K bytes	ROM; 4K bytes	ROM, 1K; PROM, 1K
Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	No Standard No; auto restart Standard	No No Standard No Optional	No No Standard No Optional	Standard No Standard No No	Optional Optional Standard No Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard Instrdependent 4	No 195K —	No 114K —	No 125K —	Standard 833K Variable
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	— — Bisync, TTY		  		
Network architectures supported RJE terminals emulated IBM 3270 emulation					_ _ _
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	1 M bytes (dual-den.) No	256K-1M bytes Both; 2.4-50M bytes	256K-1M bytes Both; 2.4-200M bytes	No Both; 2.5-200M bytes	No No
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	No	Cassette; 352 cps	Cassette; 352cps	Cassette; 352 cps	No
Magnetic tape, ½-inch	560-1600 bpi;	9.6-20 KBS	9.6-20 KBS	9.6-20 KBS	No
Serial printer Line printer Data communications interface CRT Other supported peripheral units	78.9 trk. 80-160 cps 300, 600, 900 lpm Up to 9600 bps 80 char. x 24 lines Single-density disk storage, serial print- ers, belt printers	120 cps 300, 600 lpm Up to 9600 bps 80 char. x 12 lines	120 cps 300, 600 lpm Up to 9600 bps 80 char. x 12 lines —	80-160 cps 300, 600 lpm Up to 9600 bps 80 char. x 12 lines	No No No No 1-megabyte BULK- CORE storage is standard
SOFTWARE Assembler	Macro assembler	Yes	Yes	Yes	Assembler,
Compilers Operating system	COBOL, BASIC, RPG- PLUS, DATABUS, DATASHARE Batch, interactive,	BASIC, RPG PLUS, SCRIBE, DATABUS, DATAFORM Batch, time-sharing	BASIC, RPG PLUS, SCRIBE DATABUS, DATAFORM Batch, time-sharing	BASIC, RPG PLUS, SCRIBE, DATABUS, DATAFORM Batch, time-sharing	Macro assembler NA Batch, real-time
Language implemented in firmware Operating system implemented in firmware	real-time, virtual No Partially	No No	No No	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	10,550 103 —	Contact vendor	Contact vendor	Contact vendor	8,795 (64KB memory + 256KB bulk core) NA Quantity 660 (32KB)
Date of first delivery Number installed to date	August 1978 NA	April 1972 NA	December 1974 NA	July 1977 NA	November 1978 NA
COMMENTS	Price includes 64K bytes, 1M byte dual diskette, one CRT, and one com- munications inferface				

MANUFACTURER AND MODEL	Dataram BM-2	Digital Equipment PDP-8/A	Digital Equipment PDP-11/03	Digital Equipment PDP-11/04	Digital Equipment PDP-11/34A
WORD LENGTH, BITS	16	12	16	16 + 2	16 + 2
NO. WORKSTATIONS SUPPORTED	_	MARKET .	_	_	
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core, MOS 1.2/1.2 8K/128K bytes No No	Core; MOS 1.2, 1.5; 2.4, 0.6 8K/128K No No No	Core, MOS 1.2 4K/32K No No No	Core, MOS 0.98; 0.725/0.51 16K/32K Standard No No	Core, MOS 0.98; 0.725/0.51 16K/124K Standard No Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer INPUT/OUTPUT CONTROL	128K ROM, 1K; PROM, 1K 3.5 Optional Optional Standard No Optional	256	32K ROM; PROM; 1K 3.5 Optional Optional Standard No Optional	32K — 3.17 Optional Optional Standard Optional Standard	32K 
Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 833K Variable	Standard 74K 1-64	Standard 833K Variable	Standard 2048K Variable	Standard — Variable
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported Network architectures supported	   	20 No To 9600 bps	Up to 1M bps Up to 9600 bps DDCMP, DNA	Up to 1M bps Up to 9600 bps DDCMP, DNA DECnet Control Data, Univac	Up to 1M bps Up to 9600 bpos DDCMP, DNA DECnet
RJE terminals emulated IBM 3270 emulation PERIPHERAL EQUIPMENT	_	Any RS-232C 	Control Data, Univac	Control Data, Univac	Control Data, Univac
Floppy disk (diskette) drives Disk pack/cartridge drives Drum/fixed head disk storage	No No No	128-2M (6-bit) Cartridge; 3.2-12.8M (6-bit) No	256-512 bytes No No	256-512K bytes Cart. & pack; 2.5- 1048M bytes Fixed-head; 512K-	256-512K bytes Cart. & pack; 2.5- 1408M bytes Fixed-head; 512K-
Magnetic tape cassettes/cartridges	No	Cassette; 562 cps	No	8M bytes Cassette; 562 cps	8M bytes Cassette; 562 cps
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT	No No No No No	10-36 KBS 180 cps 230 lpm 110-71K bps	No 180 cps No 50-56,000 bps	10-72 KBS 30-180 cps 230-1200 lpml 50-56,000 bps	10-72 KBS 30-180 cps 230-1200 lpm 50-56,000 bps
Other supported peripheral units	4-megabyte BULK SEMI is standard	A/D converter, paper tape reader, paper tape punch	Serial line and par- allel line controllers	Paper tape reader; paper tape punch	Paper tape reader; paper tape punch
SOFTWARE Assembler Compilers	Assembler, macro assembler BASIC, FORTRAN	Assembler & macro assembler BASIC, DIBOL, ALGOL, FOCAL	Assembler & macro assembler BASIC, FORTRAN	Assembler & macro assembler BASIC, FORTRAN, FOCAL	Assembler & macro assembler BASIC, FORTRAN, COBOL, FOCAL
Operating system  Language implemented in firmware Operating system implemented in	Batch, real-time No No	Batch, real-time, time-sharing No No	Batch, real-time No No	Batch, real-time, time-sharing No No	Batch, real-time, time-sharing No No
firmware  PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available	11,645 (64KB mem. + 512KB BULK SEMI) NA	3,960-11,000 (8K) 44-99	1,995 (8K MOS) 37	3,995 (16K MOS) 54	9,050 (32K MOS) 87
Price of memory increment, \$	660 (32KB)  December 1979	1,850 (8K bytes) September 1974	625 (8K bytes)	1,700 (16K bytes) July 1975	2,200 (32K bytes) March 1976
Number installed to date	NA	Over 40,000	Over 29,000	Over 9100	Over 750
COMMENTS		Also available in packaged version called Datasystem 310	Packaged version of LSI-11 micro- computer; instruc- tion set equivalent to PDP-11/40	Successor to PDP-11/05 and 11/10; upgradable to PDP-11/34 status	Uses similar technology to PDP-11/ O4; includes memory management for greater addressing capability; packaged version called Datasystem 530 is also available

MANUFACTURER AND MODEL	Digital Equipment PDP-11/35 & 11/40	Digital Equipment PDP-11/44	Digital Equipment PDP-11/60	Digital Equipment PDP-11/70	Digital Scientific 4030/40
WORD LENGTH, BITS	16+2	16 + 2	16 + 2	16 + 2	16 + 2
NO. WORKSTATIONS SUPPORTED	_	_	_	_	_
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer INPUT/OUTPUT CONTROL Direct memory access channel	Core 0.98/0.36 8K/124K Optional No Optional  32K No 1.07 Optional Optional Standard No Optional	MOS/cache 0.48, 0.96/0.48 256K/1 M bytes No Standard Standard 32K No 0.87 Standard Optional Standard Optional Standard	Core, MOS 0.98/— 0.98/— 32K/256K Standard Standard (MOS) Standard  32K RAM; 1K words 2.2 Standard	Core 0.98/0.36 64K/1024K Standard No Standard  32K — 0.30-1.20 Standard Optional Standard No Standard	Core 0.9/0.5 8K/128K Standard No Standard  64K ROM; 4K words 2.9 Standard Optional No No Optional
Maximum I/O rate, words/sec. No. of external interrupt levels	2M Variable	1M		2.9M Variable	1M 6, 16
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported  Network architectures supported RJE terminals emulated	— Up to 1M bps Up to 9600 bps DDCMP, DNA  DECnet Control Data, Univac	Up to 1M bps Up to 9600 bps DDCMP, DNA  DECnet Control Data, Univac	Up to 1M bps Up to 9600 bps DDCMP, DNA  DECnet Control Data, Univac	Up to 1M bps Up to 9600 bps DDCMP, DNA  DECnet Control Data, Univac	
IBM 3270 emulation  PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	256-512K bytes Cart. & pk.; 2.5- 1408M bytes Fixed-head; 512-8M bytes	256-512K bytes Both; 2.5-1408M bytes Fixed-head; 512K- 8M bytes	256-512 bytes Cart. & pk.; 2.5- 1408M bytes Fixed-head; 512- 8M bytes	256-512 bytes Cart. & pk.; 2.5- 1408M bytes Fixed-head; 512-8M bytes	Yes Both; 1-160M bytes Fixed; 1-2M bytes
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	Cassette; 562 cps 10-72 KBS 30-180 cps 230-1200 lpm 50-56,000 bps 80 char. x 24 lines Paper tape reader, paper tape punch	Cassette; 562 cps 10-72 KBS 30-180 cps 230-1200 lpm 50-56,000 bps 80 char. x 24 lines Paper tape units	Cassette; 562 cps 10-72 KBS 30-180 cps 230-1200 lpm 50-56,000 bps 80 char. x 24 lines Paper tape reader, paper tape punch	Cassette; 562 cps 10-72 KBS 30-180 cps 230-1200 lpm 50-56,000 bps Paper tape reader, paper tape punch	30, 60 KBS 180 cps 300 to 1000 lpm Up to 19,200 bps 24 x 80 char. Paper tape reader/ punch, XY plotter,
SOFTWARE Assembler	Accombine 8	Assembler &	Assembler &	Assembler &	digital/analog I/O Assembler &
Compilers Operating system Language implemented in firmware Operating system implemented in firmware	Assembler & macro assembler BASIC, FORTRAN, COBOL, FOCAL Batch, real-time, time-sharing No No	macro assembler BASIC, FORTRAN, COBOL, APL, CORAL Batch, real-time, time-sharing No	macro assembler BASIC, FORTRAN,	macro assembler BASIC, FORTRAN, COBOL, FOCAL Real-time, inter., time-sharing No	macro assembler COBOL, RPG II, APL, BASIC, FORTRAN Real-time, time- sharing, multiprog. Partially No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	19,800 — 2,200 (32K core)	23,900 (256K bytes) 6,000 (256K bytes)	35,700 (32K core) 6,650 (64K core)	63,000 (128K core)  18,590 (128K core)	33,850 (4030); 42,285 (4040) — — 4,000 (8KB core)
Date of first delivery	NA	June 1980	June 1977	NA	1970
Number installed to date  COMMENTS	NA PDP-11/35 is an OEM version of the PDP-11/40; pack- aged version is called Datasystem 350 based on PDP- 11/40	NA Optional CIS proc- essor & 1M byte memory increment (\$20,000) available; enhanced main-table features and an intellignet console subsystem	Includes user- accessible micropro- gramming; error- correcting memory	NA Uses same technology as PDP- 11/45 and includes 2048 bytes of cache memory for increased performance; disk storage & mag tape periphs. avail. in packaged system called Datasystem 570	Over 240 (both models) Also features process control monitoring

WORD LENGTH, BITS  NO. WORKSTATIONS SUPPORTED  MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection  CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware ptote manipulation Battery backup Real-time clock or timer  INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels  COMMUNICATIONS Maximum number of lines	MOS 0.5/0.3 4K/32K Standard No Standard  32K PROM 1.44 Standard Optional No No Optional  Standard  Standard	16 + 2 16  Core, MOS 0.9; 0.5/0.5; 0.3 8K/64K Standard No Standard  32K PROM 1.44 Standard Optional No No Optional	16 + 2 32  Core, MOS 0.9; 0.5/0.5; 0.3 64K/1M Standard No Standard  64K PROM 1.44 Standard Optional No Optional Optional	8 to 20 15 MOS .50/.50 96K/128K bytes Standard Optional  128K PROM; 512 x 40 .30 Standard No Standard Optional	8 to 20 60  MOS .50/.50 128K/1024K bytes Standard Standard Optional  1024K PROM; 1024 x 40 .30 Standard No
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection  CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	0.5/0.3 4K/32K Standard No Standard  32K PROM 1.44 Standard Optional No No Optional Standard 2M	Core, MOS 0.9; 0.5/0.5; 0.3 8K/64K Standard No Standard  32K PROM 1.44 Standard Optional No No	Core, MOS 0.9; 0.5/0.5; 0.3 64K/1M Standard No Standard 64K PROM 1.44 Standard Optional No	MOS .50/.50 96K/128K bytes Standard Standard Optional 128K PROM; 512 x 40 .30 Standard No Standard	MOS ,50/,50 128K/1024K bytes Standard Standard Optional 1024K PROM; 1024 x 40 ,30 Standard No
Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection  CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels  COMMUNICATIONS	0.5/0.3 4K/32K Standard No Standard  32K PROM 1.44 Standard Optional No No Optional Standard 2M	0.9; 0.5/0.5; 0.3 8K/64K Standard No Standard 32K PROM 1.44 Standard Optional No No	0.9; 0.5/0.5; 0.3 64K/1M Standard No Standard 64K PROM 1.44 Standard Optional No	.50/.50 96K/128K bytes Standard Standard Optional 128K PROM; 512 x 40 .30 Standard No Standard	.50/.50 128K/1024K bytes Standard Standard Optional 1024K PROM; 1024 x 40 .30 Standard No
No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels COMMUNICATIONS	PROM 1.44 1.44 Standard Optional No No Optional Standard 2M	PROM 1.44 Standard Optional No No Optional	PROM 1.44 Standard Optional No Optional	PROM; 512 x 40 .30 Standard No Standard	PROM; 1024 x 40 .30 Standard No
Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels COMMUNICATIONS	2M	Standard		Standard	Standard Optional Standard
	1	1M-2M 6	Standard 1M-2M	Standard 200K 15	Standard 200K 60
Synchronous Asynchronous Protocols supported Network architectures supported RJE terminals emulated				15 Std.; to 15,000 bps Std.; to 9,600 bps Programmable None	120 Std.; to 15,000 bps Std.; to 9,600 bps Programmable None
IBM 3270 emulation PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	Yes Cart.; 1-5M	Yes Both; 1-160M bytes	Yes Both; 1-160M bytes	No No Cartridge; 27M bytes/drive	No No Pack; 80M bytes/drive
Drum/fixed head disk storage	No	Fixed; 1-2M bytes	Fixed; 1-2M bytes	No	No
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 30, 60 KBS 180 cps 300, 600 lpm Up to 19,200 bps 24 x 80 char. Paper tape I/O, plotter	No 30, 60 KBS 180 cps 300, 600 lpm Up to 19,300 bps 24 x 80 char. Paper tape reader/ punch, XY plotter	No 30, 60 KBS 180 cps 300, 600 lpm Up to 19,200 bps 24 x 80 char. Paper tape reader/ punch, XY plotter	No 1600 bpi No 300, 600, 900 lpm 110 to 9600 bps 80 char. x 24 lines 15 port async., multiplexer, 360/370 interface	No 1600 bpi No 300, 600, 900 lpm 110 to 9600 bps 80 char. x 24 lines 15 port async., multiplexer, 360/370 interface
SOFTWARE Assembler Compilers Operating system	Assembler & macro assembler COBOL, RPG II, BASIC, FORTRAN Batch	Assembler & macro assembler COBOL, RPG II, APL, BASIC, FORTRAN Batch, time-sharing	Assembler & macro assembler COBOL, RPG II, APL BASIC, FORTRAN Batch, time-sharing,	Yes RPG II, BASIC/5,	Yes  RPG II, BASIC/5, PL/G, COBOL Time-sharing
Language implemented in firmware Operating system implemented in firmware	No No	No No	multiprogramming Partially No	Partially Partially	Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	18,000   1,000 (4KB MOS)	24,500 222 Quantity 1,800/2,000 (8KB)	39,600 433 Quantity 1,800/2,000 (8KB)	28,700 200 On request 3,400 (32K bytes)	44,930 315 On request 6,200 (64K bytes)
Date of first delivery Number installed to date	NA NA	NA NA	NA NA	June 1979	August 1976 30
COMMENTS	Intelligent RJE or local batch for applications requiring high-speed calculations; expandable to Model 5020	Up to 8 concurrent users in a mixed conversation and batch mode; expandable to Model 5030	Up to 32 concurrent users in a mixed conversation and batch mode	In-cabinet, on-site upgrades available on all configurations; Galaxy/3 is a multiple microprocessor system; DMA channel and communications interface are both microprocessor-based	In-cabinet, on-site upgrades available on all configurations; Galaxy/5 is a multiple microprocessor system; DMA channel and communications interface are both microprocessor-based

MANUFACTURER AND MODEL	Dimis, Inc. Total 100 (70)	Dimis, Inc. Total 100 (30)	Display Data Corporation In * Sight	Durango System Inc. F-85	Four-Phase IV/40
WORD LENGTH, BITS	16	16	8	8-bit byte	24
NO. WORKSTATIONS SUPPORTED	50	27	32	5	16
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer INPUT/OUTPUT CONTROL Direct memory access channel	MOS 250/250 128K/4096K Standard Standard Standard 64K No 0.2 Standard	MOS 250/250 128K/512K Standard Standard Standard 64K No 0.3 Standard	Core; semiconductor 1.00/0.35 32K/128K	MOS 0.50/0.25 64K/64K Standard No No 64K bytes EROM; 2-8K 1.33 No No Standard Optional Standard	MOS 2.0 24K/96K bytes Standard No No 96K bytes ROM; 1K x 48 bits 16 Standard Standard Standard Standard Tstandard
Maximum I/O rate, words/sec. No. of external interrupt levels	4-8M To 128	2-8M To 128	_	750K 8	125K 8
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported  Network architectures supported RJE terminals emulated	32 Optional Std.; to 9600 bps Programmable	32 Optional Std.; to 9600 bps Programmable	32 No Std.; 9600 bps Async, X3.25 None	5 Opt.; (1) to 9600 bps Opt.; (4) to 9600 bps Bisync 	16 Up to 9600 bps Up to 2400 bps — IBM 360/370
IBM 3270 emulation	No	No	No	No	Under COBOL
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	Optional Both; 4-200M bytes	Optional Both; 4-200M bytes	No Cart.; 80M bytes	473K-1890K bytes Cart.; 12-24M bytes	354K bytes Cart.; 2.5-10M bytes
Drum/fixed head disk storage	Optional	Optional	No	No	10-20M bytes
Magnetic tape cassettes/cartridges	No	No	Opt.; 10, 20 KBS	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No Optional 600 lpm 36 KBS 24 x 80 char. A/D, D/A conver- tors, discrete I/O and memory	No Optional 600 lpm 36 KBS 24 x 80 char. A/D, D/A conver- tors, discrete I/O and memory		No 165 cps; 9 pin No Up to 9600 bps 24 lines x 80 char.	No 55 cps 120-1000 lpm Up to 9600 bps 80 char. x 24 lines None
SOFTWARE Assembler Compilers	Assembler and macro assembler FORTRAN	Asssembler and macro assembler FORTRAN	Yes No	No Star BASIC	Yes COBOL, RPG
	Batch, real-time	Batch, real-time		Batch, real-time,	IDOS, DOS
Operating system  Language implemented in firmware Operating system implemented in firmware	No No	No No	Fully No	multiprogramming No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	153,000 — — 36,000 (512K bytes)	98,000   10,500 (128K bytes)	29,600 297 Quantity	12,983 107 	37,440 (24K bytes) 166 
Date of first delivery	December 1978	June 1974 18*	January 1974 1,000	December 1978 NA	June 1973 10,000 (all sys.)
Number installed to date  COMMENTS	Three CRT's stand- ard; package includes staff & mgmt., train- ing & conversion support	Three CRTI's stand- ard; package includes staff & mgmt, train- ing & conversion support; *includes compatible Mod- comp II	Specialists in complete turnkey systems, support, forms, & maintenance for selected businesses	Totally integrated desktop small business system; emphasis on packaged applications software; system price includes two 473K-byte diskette drives, CRT, keyboard, & printer; does not include system software (\$550)	System price also includes 4 CRT's. 2.5-megabyte disk drive, and bisynch communications controller

MANUFACTURER AND MODEL	Four-Phase IV/50	Four-Phase IV/90	Four-Phase IV/70	Functional Automation F6401	Functional Automation F6420
WORD LENGTH, BITS	24	24	24	64	8, 32
NO. WORKSTATIONS SUPPORTED	24	32	32	0	8
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 2.0 24K/94K bytes Standard No No	MOS 0.8 96K/384K bytes Standard Standard No	MOS 2.0 24K/96K bytes Standard No No	MOS 0.25/0.25 32K/2048K words Optional Optional Standard	MOS 0.5/0.5 256K/16,384K Optional Optional Standard
CENTRAL PROCESSOR  No. of directly addressable words Control storage  Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	96K bytes ROM; 1K x 48 bits 16 Standard Standard Standard — Standard	96K bytes ROM; 1K x 48 bits 12 Standard Standard Standard — Standard	96K bytes ROM; 1K x 48 bits 16 Standard Standard Standard — Standard	16,384K bytes ROM/PROM 0,25 No No Standard Optional No	16,384K bytes ROM/EPROM 3.0 No No No Optional Standard
NPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	No 125K 8	No 125K 8	No 125K 8	Standard 2.66M bps None	No 8-9600 baud None
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	24 Up to 9600 bps Up to 2400 bps	32 Up to 9600 bps Up to 2400 bps Bisync, SDLC	32 Up to 9600 bps Up to 2400 bps	1 Std.; 8M bps No FABUS	8 Std.; 8M bps Std.; 8-19,200 bps FABUS
Network architectures supported RJE terminals emulated IBM 3270 emulation	 IBM 360/370 Under COBOL	 IBM 360/370 Under COBOL		FABUS None No	FABUS None No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	354K bytes Cart.; 2.5M-10M bytes	354K bytes Pack & Cartridge; 2.5M-270M bytes	354K bytes Pack & cartridge; 2.5M-270M bytes	_	_
Drum/fixed head disk storage	12.5M bytes	10-20M bytes	10-20M bytes	-	_
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No No 55 cps 120-1000 lpm 9600 bps 80 char. x 24 lines None	No 10, 60 KBS 55 cps 120-1000 lpm Up to 9600 bps 80 char. x 24 lines None	No 10, 60 KBS 55 cps 120-1000 lpm Up to 9600 bps 80 char. x 24 lines None		Cart.; 9600 baud  300 lpm (8) 19,200 bps 3,168 chars.
SOFTWARE Assembler	Yes	Yes	Yes	Assembler/loader	Yes
Compilers	COBOL, RPG	COBOL, RPG	COBOL, RPG	None	FASL
Operating system	IDOS, DOS	IDOS, DOS	IDOS, DOS	Real-time	Real-time, multi-
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	Partially Fully	user Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	69,330 349 (approx.)	1,930/month (42 month lease) —	72,315 (48K bytes)	30,902 — Quantity, 20%	20,402 — Quantity, 20% —
Date of first delivery Number installed to date	April 1976 10,000 (all sys.)	July 1977 10,000 (all sys.)	February 1971 10,000 (all sys.)	1980 0	November 1979
COMMENTS		System price also includes 12 CRT's 2.5-megabyte disk drive, and 9-track magnetic tape drive	System price also includes 12 CRT's 2.5-megabyte disk drive, and 9-track magnetic tape drive	Interfaces with other computers in modular system; intended for OEM market	Interfaces via FABUS to other computes in network; intended for OEM market

MANUFACTURER AND MODEL	Functional Automation F6440	General Robotics Polaris	General Robotics Gemini	General Robotics Tristar	General Robotics Pegasus
WORD LENGTH, BITS	8, 32	16	16	16	16
NO. WORKSTATIONS SUPPORTED	1	Variable	Variable	Variable	Variable
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection  CENTRAL PROCESSOR No. of directly addressable words	MOS 0.5/0.5 256K/16,384K Optional Optional Standard	MOS 0.45/0.30 32K/32K No No No	MOS 0.45/0.30 32K/32K No No No	MOS 0.45/0.30 32K/32K No No No	MOS 0.45/0.30 32K/32K No No No
Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	ROM/EPROM 3.0 No No No Optional Standard	PROM; 512 x 16 3.5 Standard Standard Standard No Standard	PROM; 512 x 16 3.5 Standard Standard Standard No Standard	PROM; 512 x 16 3.5 Standard Standard Standard No Standard	PROM; 512 x 16 3.5 Standard Standard Standard No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.25M bps None	Standard 833K Variable	Standard 833K Variable	Standard 833K Variable	Standard 833K Variable
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	2 Std.; 8M bps Std.; 300/9600 bps FABUS	Variable Optional Standard —	Variable Optional Standard	Variable Optional Standard	Variable Optional Standard
Network architectures supported RJE terminals emulated IBM 3270 emulation	FABUS None No	DECnet IBM 2780	DECnet IBM 2780	DECnet IBM 2780 —	DECnet IBM 2780 —
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	 Pack; 80/640M bps	1M byte Optional	2.5M bytes Optional	3.8M bytes Optional	Optional Optional
Drum/fixed head disk storage	_	No	No	No	No
Magnetic tape cassettes/cartridges	Cart.; 9600 baud	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units		No 110 cps No Optional 480 characters None	No Optional No Optional Optional None	No Optional No Optional Optional None	No Optional No Optional Optional None
SOFTWARE Assembler	Yes	Assembler &	Assembler &	Assembler &	Assembler &
Compilers	FASL	macro assembler FORTRAN, BASIC,	macro assembler FORTRAN, BASIC, APL, DIBOL	macro assembler FORTRAN, BASIC, APL, DIBOL	FORTRAN, BASIC, APL, DIBOL
Operating system  Language implemented in firmware	Real-time Partially	APL, DIBOL Batch, real-time, time-sharing No	Batch, real-time, time-sharing No	Batch, real-time, time-sharing No	Batch, real-time, time-sharing No
Operating system implemented in firmware	Partially	No	No	No	No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$	24,280	12,000	9,500	11,000	17,000
Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	Quantity, 20%	OEM	Yes	Yes	Yes —
Date of first delivery Number installed to date	1980 NA	January 1978	January 1978 100	June 1978 200	November 1977 150
COMMENTS	Interfaces via FABUS to other computers in network; intended for OEM market	Complete desktop LSI-11 computer system with key- board, screen, printer, CPU, and disks in self- contained unit	Based on the DEC LSI-11	Based on the DEC LSI-11	Based on the DEC LSI-11

MANUFACTURER AND MODEL	Harris Slash 6	Harris 100	Harris 500	Harris 800	Hewlett-Packard General Systems Division HP 250
WORD LENGTH, BITS	24, 48	24, 48	24, 48	24, 48	16
NO. WORKSTATIONS SUPPORTED		Appl. dependent	Appl. dependent	Appl. dependent	6
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.45/0.30 48K/768K bytes No Standard Standard	MOS 0.45/0.30 192K/768K bytes No Standard Standard	MOS 0.40/0.29 192K/3072K bytes No Standard Standard	MOS 0.40/0.29 384K/3072K bytes No Standard Standard	MOS 0.833 32K/64K bytes Standad No Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	96K bytes No 0.6 Standard Optional Standard Optional Optional	96K bytes No 0.6 Standard Optional Standard Optional Optional	3072K bytes No NA Standard Optional Standard Optional Optional	3072K bytes No NA Standard Standard Standard No Optional	2K — 1.6 No No Standard No No
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Optional To 19M bps 8-24	Optional To 19M bps 8-24	Optional To 19M bps 16-48	Optional To 19M bps 16-72	Standard 1.2M bytes 2.0
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	  	32 Opt.; 56K bps Opt.; 19.2K bps Async, bisync	64 Opt.; 56K bps Opt.; 19.2K bps Async, bisync	128 Opt.; 56K bps Opt.; 19.2K bps Async, bisync	5 No Opt.; 110-9600 bps None
Network architectures supported RJE terminals emulated IBM 3270 emulation		None See Comments Yes	None See Comments Yes	None See Comments Yes	None None No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	No Opt.; 10.8M-4.86 bytes No	No Opt/; 40M-4.86 bytes No	No Opt.; 40M-4.8G bytes No	No Opt.; 40M-4.8G bytes No	3 x 3.6M bytes Fixed; 10M bytes Cartridge; 10M bytes No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape easieries annuages  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	469K bps 180 cps 240-900 lpm 56K bps 1920 characters Printer/plotters	No 30, 180 cps 400 lpm Up to 9600 bps 80 char. x 24 lines —			
SOFTWARE Assembler	Macro assembler	Macro assembler	Macro assembler	Macro assembler	No
Compilers Operating system	FORTRAN IV, BASIC, FORGO, SNOBOL Real-time, batch		FORTRAN IV & 77, APL, COBOL, RPG II Batch, real-time,	FORTRAN IV & 77, APL, COBOL, RPG II Batch, real-time,	Business BASIC
Language implemented in firmware Operating system implemented in firmware	No No	time-sharing No No	time-sharing No No	time-sharing No No	Interpretive See Comments See Comments
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	17,900 (48K bytes)	45,000 (192K bytes) Special quote Yes 2,400 (48K bytes)	99,500 (192K bytes) Special quote Yes 28,800 (1,9M bytes)	155,200 (384K bytes) Special quote Yes 28,800 (1.9M bytes)	17,000 90 OEM, volume 1,050 (32K bytes)
Date of first delivery	December 1976	First qtr. 1977	First qtr. 1979	First qtr. 1980	September 1978
Number installed to date COMMENTS	NA 	RJE terminals emulated; 2780/ 3780, HASP work- station, UT-200, U-1004	RJE terminals emulated; 2780/ 3780, HASP work- station, UT-200, U-1004	RJE terminals emulated; 2780/ 3780, HASP work- station UT-200, U-1004	

MANUFACTURER AND MODEL	Hewlett-Packard General Systems Division HP 300 Model A	Hewlett-Packard General Systems Division HP 300 Model B	Hewlett-Packard HP 1000 M Series	Hewlett-Packard HP 1000 E Series	Hewlett-Packard HP 1000 F Series
WORD LENGTH, BITS	16	16	16 + 1	16 + 1	16 + 1
NO. WORKSTATIONS SUPPORTED	16	16	56	56	56
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.5/0.43 128K/512M bytes Standard Standard Standard	MOS 0.5/0.43 128K/512M bytes Standard Standard Standard	MOS 0.65 32K/1024K bytes Standard Optional Optional	MOS 0.60, 0.35 32K/1024K bytes Standard Optional Optional	MOS 0.35 32K/2048K bytes Standard Optional Optional
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	640M ROM; 6K x 32 1.98 Standard Standard Standard Standard Standard	64M ROM; 6K x 32 1.98 Standard Standard Standard Standard Standard	2K ROM/RAM; 4K 1.9 Standard Firmware Standard Optional Optional	2K ROM/RAM; 16K 0.91 Standard Firmware Standard Optional Optional	2K ROM/RAM; 16K 0.91 Standard Firmware Standard Optional Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.2M bytes 0.5	Standard 1.2M bytes 0.5	Optional 616K 50	Optional 1140K 50	Optional 1140K 50
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	16 No Opt.; to 9600 bps None	16 No Opt.; to 9600 bps None	56 Opt.; to 500K bps Opt.; to 2.5M bps Bisync, WASP	56 Opt.; to 500K bps Opt.; to 2.5M bps Bisync, WASP	56 Opt.; to 500K bps Opt.; to 2.5M bps Bisync, WASP
Network architectures supported RJE terminals emulated IBM 3270 emulation	None None No	None None No	DS/1000-3000 IBM 2780 No	DS/1000-3000 IBM 2780 No	DS/1000-3000 IBM 2780 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	1M byte Opt.; 80-480M bytes	1M byte Opt.; 20-360M bytes	0.5-2M bytes Both; to 960M bytes	0.5-2M bytes Both; 960M bytes	O.5-2M bytes Both; to 960M bytes
Drum/fixed head disk storage	Std.; 12M bytes	No	No	No	No
Magnetic tape cassettes/cartridges	No	No	Yes	Yes	Yes
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units		No 180 cps 400 lpm 9600 bps 80 char. x 24 lines Integrated display systems with win-	20-72 KBS 180 cps 300-600 lpm 300-2.5M bps Graphic devices, meas. & control	20-72 KBS 180 cps 300-600 lpm 300-2.5M bps Graphic devices, meas. & control	20-72 KBS 180 cps 300-600 lpm 300-2.5M bps Graphic devices, meas. & control
SOFTWARE	dows & soft keys	dows & soft keys	processor	processor	processor
Assembler	SL/300	SL/300	Assembler & micro assembler	Assembler & micro assembler	Assembler & micro assembler
Compilers	1	BASIC, RPG	FORTRAN, BASIC	FORTRAN, BASIC	FORTRAN, BASIC
Operating system  Language implemented in firmware Operating system implemented in firmware	Batch, multi-task, multiprogramming Partially Partially	Batch, multi-task, multiprogramming Partially Partially	Real-time, DBMS time-sharing No No	Real-time, DBMS, time-sharing Partially Partially	Real-time, DBMS time-sharing Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	35,000 160 OEM, volume 2,500 (64K words)	45,000 225 OEM, volume 2,500 (64K words)	6,950 (64K bytes) 71 OEM & end-user qty. 1.400 (32K bytes)	8,700 (64K bytes) 74 OEM & end-user qty. 1,400 (32K bytes)	11,750 (64K bytes) 102 OEM & end-user qty. 1,700 (16K bytes)
Date of first delivery Number installed to date	December 1978 NA	December 1978 NA	May 1974 NA	November 1976 NA	July 1978 NA
COMMENTS			M-Series processor supports DS/1000, high-level networking software; factory data capture software (DATACAP /1000) supported; M-Series also available as a board computer	HP1000 Model 20 & Model 40 pack- aged systems include E-Series; DS/1000 & DATA- CAP/1000 support; E-Series also available as board computer	HP1000 Model 25 & Model 45 packaged systems include F-Series; DS/1000 & DATACAP/100 support; F-Series scientific instruction set provides high performance transcendentals; optional vector instruction set provides high performance matrix operations

MANUFACTURER AND MODEL	Hewlett-Packard HP 3000 Series 30	Hewlett-Packard HP 3000 Series 33	Hewlett-Packard HP 3000 Series III	Honeywell Level 6 Model 23	Honeywell Level 6 Model 33
WORD LENGTH, BITS	16	16	16	16 + 2	16 + 2, + 6
NO. WORKSTATIONS SUPPORTED	32	32	64	12	No practical limit
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS .86/.43 256K/1024K bytes Standard Standard Standard	MOS .86/.43 256K/1024K bytes Standard Standard Standard	MOS .70/.35 256K/2048K bytes Standard Standard Standard	MOS 1.0 16K/64K words Standard No No	MOS 0.65 or 0.55 16K/64K words Standard Optional No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	32K (64K bytes) ROM; 10K x 32 bits — Standard Standard Standard Standard Standard Standard	32K (64K bytes) ROM; 10K x 32 bits — Standard Standard Standard Standard Standard Standard	32K (64K bytes) ROM; 10K x 32 bits Standard Standard Standard Standard Standard	64K ROM; 1K x 48 bits 3.5 Standard No Standard Optional Standard	64K ROM; 512 x 56 bits 1.9 Standar No Standard Optional Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1M 105	Standard 1M 105	Standard 2.86M 124	Standard 900KW 64	Standard 3MW 64
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	Sync.; 2 Opt.; (2) 56K bps Std.; (4) 9600 bps Bisync	Sync.; 7 Opt.; (7) 56K bps Std.; (8) 9600 bps Bisync	Sync.; 9 Opt.; (2) 2.5M bps Std.; (16) 2400 bps Bisync	12 (any mixture) Opt.; 50-9600 bps Opt.; 50-9600 bps Bisync, VIP, TTY	160 (any mixture) Opt.; 50-72,000 bps Opt.; 50-19,200 bps Bisync, VIP, HDLC,
Network architectures supported RJE terminals emulated IBM 3270 emulation	HP-DSN 2780/3780 No	HP-DSN 2780/3780 No	HP-DSN HASP2,JES 2-3,ASP No	 2780/3780, HASP, Yes	 2780/3780, HASP, Yes
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	1.18M bytes Cart.; 20M bytes pack; 50, 120M bytes No	1.18M bytes Cart.; 20M bytes pack; 50, 120M bytes No	No Pack; 50, 120M bytes No	4 x 256/512K Cart.; 4 x 26/80MB No	4 x 256/512K Cart.;8x10/26/80MB pack; 8x67/256MB No
Magnetic tape cassettes/cartridges	110K bps	110K bps	110K bps	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	75K bps 180 cps 400 lpm 56K bps 1920 characters Graphics terminal, 4-color plotter	75K bps 180 cps 400 lpm 56K bps 1920 characters Graphics terminal, 4-color plotters	75K bps 180 cps 400-1000 lpm 56K bps 1920 characters PT, graphic terminal, 4-color plotters	No 120, 160 cps 300, 600, 900 lpm 50-9600 bps 960,1920,2000 char.	25-120 KBS 120, 160 cps 300, 600, 900 lpm 50 bps/72KB 960,1920,2000 char. MICR
SOFTWARE Assembler	No	No	No	Assembler and macro preprocessor	Assembler and macro preprocessor
Compilers	COBOL, RPG, SPL, BASIC, FORTRAN	COBOL, RPG, SPL, BASIC, FORTRAN	COBOL, RPG, SPL, BASIC, FORTRAN	COBOL, FORTRAN, RPG	COBOL, FORTRAN, RPG
Operating system  Language implemented in firmware Operating system implemented in firmware	Batch, time-sharing, transaction Partially Partially	Batch, time-sharing, transaction Partially Partially	Batch, time-sharing, transaction Partially Partially	Multiprogramming, trans. processing No No	Multiprogramming, trans. processing No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	28,525 (256K bytes) 360 Quantity, OEM 5,000 (256K bytes)	37, 275 (256K bytes) 395 Quantity, OEM 5,000 (256K bytes)	75,875 (256K bytes) 534 Quantity, OEM 3,750 (256K bytes)	4,800 52 Yes 1,500 (16K words)	7,275 77 Yes 875 (8K words)
Date of first delivery Number installed to date	October 1979 3,500 (all sys.)	October 1978 3,500 (all sys.)	June 1978 3,500 (all sys.)	1978 NA	1976 NA
COMMENTS	Entry level HP3000 system; operates as a stand-alone system or as a station in a distributed processing network	Mid-range; for use in organizations where several functional areas require efficient local processing; communication with other systems as well as terminals throughout the organization	Top-line; highest performance of all HP3000's; simultaneously handle transaction processing, data communication, online program development and batch processing		Field upgradable to all higher models; replaces 34 & 36 which are no longer actively marketed

MANUFACTURER AND MODEL	Honeywell Level 6 Model 43	Honeywell Level 6 Model 47	Honeywell Level 6 Model 53	Honeywell Level 6 Model 57	Honeywell Level 62
WORD LENGTH, BITS	16 + 2, + 6	16 + 2, + 6	16 + 2, + 6	16 + 2, + 6	8-bit byte
NO. WORKSTATIONS SUPPORTED	No practical limit	No practical limit	No practical limit	No practical limit	_
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.65 or 0.55 16K/1024K words Standard Optional Optional	MOS 0.65 or 0.55 16K/1024K words Standard Optional Optional	MOS 0.65 or 0.55 16K/1024K Standard Optional Standard	MOS 0.65 or 0.55 16K/1024K Standard Optional Standard	MOS 1.0/0.5 96K/992K bytes Standard Yes Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	1024K ROM: 1K x 64 bits 1.0 Standard Optional Standard Optional Standard	1024K ROM: 1K x 64 bits 1.0 Standard Optional Standard Optional Standard	1024K ROM; 1K x 64 bits 0,7 Standard Optional Standard Optional Standard	1024K ROM; 1K x 64 bits 0.7 Standard Optional Standard Optional Standard	992K bytes ROM; to 30K bytes Varies Standard Optional Standard No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 3MW 64	Standard 3MW 64	Standard 3M 64	Standard 3M 64	Standard 1.587M 1-14
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	160 (any mixture) Opt.; 50-72,000 bps Opt.; 50-19,200 bps Bisync, VIP, HDLC	160 (any mixture) Opt.; 50-72,000 bps Opt.; 50-19,200 bps Bisync, VIP, HDLC TTY	160 (any mixture) Opt.; 50-72,000 bps Opt.; 50-19,200 bps Bisync, HDLC, VIP	160 (any mixture) Opt.; 50-72,000 bps Opt.; 50-19,200 bps Bisync, HDLC, VIP	Async (4); sync (2) Up to 9600 bps Up to 19,200 bps Bisync, HDLC
Network architectures supported RJE terminals emulated IBM 3270 emulation	— 2780-3780, HASP, Yes	— 2780/3780, HASP Yes		— 2780∕3780, HASP Yes	-  -  -
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	4 x 256/512K Cart.;8x10/26/80MB pack; 8x67/256MB No	4 x 256/512K Cart.;8x10/26/80MB pack; 8x67/256MB No	4 x 256/512K Cart.;(8)10/26/80MB pack; (8) 67/256MB No	4 x 255/512K Cart.;(8)10/26/80MB pack; (8) 67/256MB No	256/512K bytes Pack; 40-1,800M bytes No
Magnetic tape cassettes/cartridges	No	No	No	No	700 bps
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	25-120 KBS 120, 160 cps 300, 600, 900 lpm 50 bps/72KB 960,1920,2000 char. MICR	25-120 KBS 120, 160 cps 300, 600, 900 lpm 50 bps/72KB 960,1920,2000 char. MICR	25-120 KBS 120, 160 cps 300, 600, 900 lpm 50bps/72KB 960,1920,2000 char. MICR	25-120 KBS 120, 160 cps 300, 600, 900 lpm 50 bps/72KB 960,1920,2000 char. MICR	10-60 KBS 30/120 cps 100-1600 lpm To 9600 bps 80 char. x 12 lines Card punch
SOFTWARE Assembler Compilers	Assembler and macro preprocessor COBOL, FORTRAN,	Assembler and macro preprocessor COBOL, FORTRAN,	Assembler and macro assembler COBOL, FORTRAN,	Assembler and macro assembler COBOL, FORTRAN,	No COBOL, FORTRAN,
Operating system  Language implemented in firmware Operating system implemented in	RPG Multiprogramming, time-sharing No No	RPG Multiprogramming, time-sharing No No	RPG Multiprogramming, time-sharing No No	RPG Multiprogramming, time-sharing No No	RPG Batch, real-time, time-sharing No Partially
firmware  PRICING & AVAILABILITY Price of CPU, power supply, frt panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	10,325 114 Yes 875 (8K words)	22,275 227 Yes 2,250 (32K words)	22,175 174 Yes 875 (8K words)	46,975 334 Yes 2,250 (32K words)	33,192 160 
Date of first delivery	1977	1978	1978	1978	November 1974
Number installed to date COMMENTS	NA Field upgradable to all higher models; writable control store optional	NA Field upgradable to Model 57; writ- able control store optional; includes high-speed commer- cial instructions (decimal arithmetic, etc.)	NA Field upgradable to Model 57; writ- able control store optional	NA Writable control store optional; in- cludes high-speed commercial instruc- tions (decimal arith- metic, etc.)	Over 1000 CPU is available with four different performance levels

MANUFACTURER AND MODEL	IBM Series / 1	IBM System/3	IBM System/32	IBM System/34	IBM System/38
WORD LENGTH, BITS	16	8-bit byte	8-bit byte	8-bit byte	8-bit byte
NO. WORKSTATIONS SUPPORTED	Variable			64 (remote)	40
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 2.1, 0.8, 0.6 16K/256K bytes Standard No Model 4955 only	Core, MOS 1.52 8K/512K bytes Standard Std. (Model 15) Std. (Model 15)	MOS 0.6/0.250 16K/32K bytes Standard No No	MOS 0.6 32K/128K bytes Standard No No	MOS 1.1, 0.6 512K/1536K bytes No Standard No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K bytes No NA No Optional Standard Optional Optional	64K bytes No 24.32 No No Standard No Optional	32K bytes ROM; 4K bytes 150.8 (5 digits) No No Standard No	32K bytes	512K bytes 4K-8K words 
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard — 256	Standard 658K 5 (Model 8, 10, 12) 8 (Model 15)	Standard 889K 4	Standard  	Standard 2.5M bytes NA
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	— Up to 56,000 bps Up to 9600 bps Async, Bisync	Up to 50,000 bps Bisync	Up to 7200 bps	Up to 9600 bps SNA/SDLC	4 (remote) Up to 9600 bps — SDLC
Network architectures supported RJE terminals emulated IBM 3270 emulation	SNA 2780, 3870, HASP Yes	 360/370, HASP II Yes		System/34 No	System/370 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	492-606K bytes Non-removable cart.; 9.3-258M bytes No	243K bytes/drive Both; 2.5-506M bytes No	243-303K bytes Non-removable cart; 3.2-13.7M bytes No	303K bytes Non-removable cart.; 8.6-27.1M bytes No	24M bytes Non-removable pack; 2,673M bytes No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 120 cps 80 to 414 lpm To 9600 bps 24 x 80 char. Sensor I/O	20-80 KBS 85, 115 cps 100-1100 lpm Up to 50K bps 40 char. x 12 lines MICR reader/sorter, optical mark reader	No 40-80 cps 50-155 lpm Up to 7200 bps 40 char. x 6 lines Magentic card reader	No No 160, 300 lpm Up to 4800 bps 960 or 1920 char. Punched card input	800 bpi 40, 80, 120 cps 44-650 lpm — 960 or 1920 char. Card unit
SOFTWARE Assembler	Macro assembler	No	Macro assembler	Yes	No
Compilers	FORTRAN, PL/1,	BASIC, RPG II,	RPG II, FORTRAN	RPG II, FORTRAN	RPG III
Operating system	COBOL Real-time, multi-	COBOL, FORTRAN Batch, time-sharing	Batch (one-program)	Interactive	Interactive, batch
Language implemented in firmware Operating system implemented in firmware	tasking Partially Partially	No No	No Partially	Partially Partially	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	4,360 76  1,170 (16K bytes)	11,310 (8K bytes) 43 2,950 (4K bytes)	23,490 160  878 (8K bytes)	26,300 145 Educational (10%) 1,175 (16K bytes)	70,210 341 5,000 (256K bytes)
Date of first delivery Number installed to date	_ NA	December 1970 54,000 +	March 1975 15,000 +	December 1977 6,000 (estimated)	August 1979 NA
Number installed to date  COMMENTS	Offered on a purchase-only basis; eleven different CPU models	Six different model lines currently avail- able	Entry-level business computer; strong emphasis on packaged applications software; system price also includes 3.2M-byte fixed disk drive, diskette drive, CRT, keyboard, and 40-cps unidirectional printer	Similar to System/ 32, but features more processing power, larger memory, larger disk capacity, and multiple independent workstations	Most powerful computer offered by IBM's General Systems Division, available in 48 packaged models

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MANUFACTURER AND MODEL	IBM 5100	IBM 5100	IBM 8100	Jacquard J100	Jacquard J500
WORD LENGTH, BITS	8-bit byte	8-bit byte	8-bit byte	16	16
NO. WORKSTATIONS SUPPORTED	1	1	24	16	NA
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.533/0.330 16K/64K bytes Standard No	MOS 0.53/0.33 16K/64K bytes Standard No	MOS 0.8, 1.5 256K/512K bytes Standard No Standard	MOS 1.5/3.0 48K/64K No No No	MOS .50 64K/64K No No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K bytes ROM: 180K x 9 bits 1000 (approx.) Standard Standard Standard No	64K bytes ROM; 18K x 9 bits 1000 (approx.) Standard Standard Standard No	256K bytes	256 ROM; 512 words 8.0 No No No No Standard	256 PROM; 28K bytes 5.28 No No No No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 500K 3	Standard 500K 3	Standard 	Standard 500K 1	Standard 750K 1
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	_ _ _ _	1 1200 to 4800 bps 134-5,300 bps	19 600 to 19,200 bps  SDLC, BSC, SNA	19 Opt.; to 4800 bps Opt.; to 4800 bps Async, Bisync	2 Std.; to 9600 bps Std.; to 9600 bps Async, Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	  -  -	  BM/370, 2741  No	Most IBM Yes	None 2780/3780 Yes	None 2780/3780 Yes
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No No	303K-4.8M bytes No	Up to 962K bytes Up to 320M bytes	(2) 25CK bytes Both; 12-320M bytes	(2) 256K, 512K bytes Cartridge; 12-96M bytes
Drum/fixed head disk storage	No	No		No	No
Magnetic tape cassettes/cartridges	Cartridge; 2.85 KBS	Cartridge; 2.85 KBS	20-160 KBS	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 80, 120 cps No Up to 300 bps 64 char. x 16 lines RS232C interface available for non- IBM peripherals	No No Up to 9600 bps 64 char. x 16 lines RS232C, IEEE interfaces available for non-IBM	40-120 cps 120-400 lpm Up to 19,200 bps Up to 3440 char. RS232C, V.35 interfaces available	10-72K bps 45 cps 300 lpm Up to 4800 bps 1920 characters	No 45 cps 300 lpm, 150 cps Up to 9600 bps 1920 characters
SOFTWARE Assembler	No	peripherals No	Yes	Yes	Yes
Compilers	BASIC, APL	BASIC, APL	COBOL, FORTRAN	BASIC, Data-Rite	BASIC, Data-Rite
Operating system	Batch (one-program)	Batch (one-program)	Batch, interactive	Time-sharing	Time-sharing
Language implemented in firmware Operating system implemented in firmware	Fully Fully	Fully Fully	No No	No No	No. No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$	6,285	8,475 (16K bytes)	24,000	19,900	10,200
Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available	63.50	45 Educational (10%)	122  -	172 Oty., dollar vol, educ.	92 Oty., dollar vol., educ.
Price of memory increment, \$	1,175 (16K bytes)	1,175 (16K bytes)	2,250 (128K bytes)	2,400 (16K bytes)	NA
Date of first delivery Number installed to date	September 1975 NA	February 1978 NA	August 1979 NA	August 1975 NA	NA NA
COMMENTS	Portable computer weighing 50 pounds; system price also includes cartridge tape drive, CRT, and BASIC language interpreter	Features floppy disk and/or magnetic tape storage, and approximately two to three times the internal computing power of the 5100	Price includes 256K bytes, 1M byte diskette storage, 29M bytes disk storage, 8 I/O hard- ware interrupt levels, and instruc- tions set	Includes dual floppy disk	Includes dual floppy disk, disk controller, printer & controller, two communications controllers

MANUFACTURER AND MODEL	MCM Computers MCM/800	MCM Computers MCM / 900	Melcom Business Systems Inc. Mitsubishi 8018	Melcom Business Systems Inc. Mitsubishi 8038	Microdata Reality Series 2000
WORD LENGTH, BITS	8 + 1	8	8	16	16
NO. WORKSTATIONS SUPPORTED	1	1	5	27	8
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	1.2	MOS 0.3 8K bytes/24K bytes No No No	MOS 0.5/0.32 48K/96K bytes Standard No No	MOS 0.6/0.26 128K/512K bytes Standard No Standard	Core 1.0 16K/64K bytes No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	16K ROM; 32K bytes — No Standard Standard Standard No	24K ROM; 40K bytes — No Standard Standard No No	64K bytes ROM; 1.5K bytes 900 (12 digits) Standard No Standard No No	64K bytes ROM; 12K bytes 37.75 (5 digits) Standard Optional Standard No Standard	58K bytes No — Standard Optional Standard No No
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	No  None	No  None	No 40K bps 1	Std.; high-speed 3.3M bps	Standard 40,000 bytes
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	1 No Up to 1200 bps None	1 No Standard No	1 Opt.; to 9600 bps Opt.; 200-9600 bps BSC	32 Opt.; to 9600 bps Opt.; 300-9600 bps BSC	8 Opt.; to 9600 bps No bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	None None No	No No No	<u>-</u>	-  -  -	— HASP, 360/370 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	250K-2M bytes No	250K-1M bytes Cartridge	512K-2M bytes Cart.; 10-40M bytes	256K-2M bytes Both; 10-40M bytes	No Cart.; to 20 M bytes
Drum/fixed head disk storage	No ·	No	No	No	No
Magnetic tape cassettes/cartridges	Cassette, 810 cps	No	Cassette; 6K bps	Cassette; 6K bps	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 45 cps No To 1200 bps 80 char. x 24 lines GP interface; pro- grammable RS-232C interface	No 45-180 cps 300 lpm To 4800 bps 80 char. x 24 lines GP1B interface; pro- grammable RS-232C interface	No 120 cps 110 lpm 200-9600 bps 1024 characters Auto ledger/feed, PT reader/punch	20-40K bytes 200 cps 110/600 lpm 300-19,200 bps 2,000 characters Auto ledger/feed, PT reader/punch	20-40 KBS 120-165 cps 150, 300, 600 lpm To 9600 bps 80 char. x 24 lines None
SOFTWARE Assembler	No	No	Yes	Yes	Yes
Compilers	No	No	BASIC, COBOL	BASIC, COBOL,	ENGLISH, DATA/
Operating system  Language implemented in firmware Operating system implemented in firmware	Virtual memory, interactive Fully Fully	Virtual memory Fully interpretive Fully	Batch real-time, multi-user Fully Fully	RPG, Progress Batch, real-time, multi-user No Partially	BASIC, PROC Interactive, multi-user Partially Fully
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	9,200 (8K bytes) 1,600 (8K bytes)	8,700  	18,900 128  800 (8K bytes)	43,000 287 	32,500 290  3,500 (16K bytes)
Date of first delivery Number installed to date	July 1976 1150 +	October 1978	February 1979 12,000 + (all models)	November 1979 12,000 + (all models)	December 1977 NA
COMMENTS	MSI implementation	CPU: it features APL firmware & virtual memory oper. sys. and is MCM 800- compatible	Includes CPU (48K bytes), serial printer, keyboard / CRT, dual floppy drive	Includes CPU (128K bytes), serial printer, keyboard/CRT, 10M byte cartridge disk	Packaged systems includes 16KB core memory, magnetic tape, 10MB disk drive, 165 cps printer, and 1 CRT

MANUFACTURER AND MODEL	Microdata Reality Series 4000	Microdata Reality Series 6000	Microdata Reality Series 8000	Modular Computer Systems Inc. Classic 7810/3140	Modular Computer Systems Inc. Classic 7830/7835
WORD LENGTH, BITS	16	16	16	16	16
NO. WORKSTATIONS SUPPORTED	32	32	32	32	96
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core 1.0 16K /64K bytes No 	Core, MOS 1.0, 0.8 32K/128K bytes Standard (MOS)	Core, MOS 1.0, 0.8 128K/512K bytes Standard (MOS)	MOS .6/.6 64K/128K bytes Standard No Optional	MOS .125/.250 128K/2048K bytes Standard Standard Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	58K bytes No  Standard Optional Standard No No	122K bytes No — Standard Optional Standard No	504K bytes No — Standard Optional Standard No No	128K bytes No .90 Standard No Standard Optional Standard	2048K bytes No .30 Standard Optional/Standard Standard Optional Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 40,000 bytes	Standard 40,000 bytes	Standard 40,000 bytes —	Standard 500K bytes Up to 128	Standard 5,124.8 K bytes Up to 128
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	32 Opt.; to 9600 bps No Bisync	32 Opt.; to 9600 bps No Bisync	32 Opt.; to 9600 bps No Bisyn	256 FDX Opt.; 48-230.4K bps Opt.; 50-19.2K bps SDLC/HDLC, Bisync	256 FDX Opt.; 48-230.4K bps Opt.; 50-19.2K bps SDLC/HDLC, Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	— HASP, 360/370 No	HASP, most IBM No	— HASP, 360/370 No	MAXNET HASP, 2780/3780, —	MAXNET HASP, 2780/3780, —
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Cart.; to 40M bytes	No Cart.; to 40M bytes	No Cart.; to 40M bytes	315-630K bytes Both; 2.5-256M	315-630K bytes Both; 2.5-256M bytes
Drum/fixed head disk storage	Fixed; to 50 M bytes	Fixed; to 200M bytes	Fixed; to 514M bytes	Fixed; (3).5-2M bytes	Fixed; (3).5-2M bytes
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	20-40KBS 120-165 cps 150, 300, 600 lpm To 9600 bps 80 char. x 24 lines None	20-40KBS 120-165 cps 150, 300, 600 lpm To 9600 bps 80 char. x 24 lines None	20-40KBS 120-165 cps 150, 300, 600 lpm To 9600 bps 80 char. x 24 lines None	(7) 36-409.6K bytes (4) 30-440 cps (5) 280-1000 lpm 50-2000K bps 24 x 80 char. A/D & D/A converts, card inputs, others	(7) 36-409 6K bytes (4) 30-440 cps (5) 280-1000 lpm 50-200K bps 24 x 80 char. A/D & D/A converts, card inputs, others
SOFTWARE Assembler	Yes	Yes	Yes	Assembler and	assembler and
Compilers	ENGLISH, DATA/	ENGLISH, DATA/	ENGLISH, DATA/	macro assembler COBOL, FORTRAN,	macro assembler COBOL, FORTRAN,
Operating system  Language implemented in firmware Operating system implemented in firmware	BASIC, PROC, Interactive, multi- user Partially Fully	BASIC, PROC, Interactive, multi- user Partially Fully	BASIC, PROC, Interactive, multi- user Partially Fully	CORAL 66, TOTAL, Batch, real-time, time-sharing No No	CORAL 66, TOTAL, Batch, real-time, time-sharing No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	38,550 280  3,500 (16K bytes)	61,250 420 	84,975 580 	8150 85  1,000 (32K bytes)	23,800/29,500 155/192 
Date of first delivery Number installed to date	November 1973 NA	November 1973 NA	October 1979 NA	May 1979 NA	September 1979 NA
COMMENTS	Packaged system in- cludes 16KB core memory, magnetic tape, 20MB disk drive, 165 cps printer, and 1 CRT	Packaged system includes 32KB core memory, magnetic tape, 50MB disk drive, 165 cps printer, and 1 CRT	Packaged system includes 256KB MOS memory, magnetic tape, 128MB disk		-

MANUFACTURER AND MODEL	Modular Computer Systems Inc. Classic 7860	Modular Computer Systems Inc. Classic 7870	Modular Computer Systems Inc. Modcomp II	Modular Computer Systems Inc. Modcomp IV/35	Mylee Digital Sciences 3000
WORD LENGTH, BITS	16	16	16 + 1	16 + 1	16
NO. WORKSTATIONS SUPPORTED	128	128			16
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer INPUT/OUTPUT CONTROL Direct memory access channel	Standard Standard MOS Standard  8192K bytes No .20 Standard Standard Standard Standard Optional Standard Standard	MOS .125/.250 512K/4096K bytes — Standard Standard 8192K bytes No .20 Standard Standard Standard Standard Standard Standard	Core 0.8/0.4 32K/128K Standard No Optional  128K bytes No 0.8 Standard Optional Standard Optional Standard No Optional Standard No Optional	Core 0.5/0.4 64K/1024K Standard No Standard  128K bytes No 0.56 Standard Optional Standard No Standard No Standard No Standard No Standard No Standard Mo Standard Mo Standard	MOS 0.8 12K/143K No No No No Standard Yes No Standard Yes No Standard
Maximum I/O rate, words/sec. No. of external interrupt levels  COMMUNICATIONS  Maximum number of lines Synchronous	To 96K bytes Up to 128 256 FDX Opt.; 48-230.4K bps	To 96K bytes Up to 128 256 FDX Opt.; 48-230.4K bps	Up to 128	Up to 128	1-18 15 Opt.; to 9600 bps
Asynchronous Protocols supported	Opt.; 50-19.2K bps	Opt.; 50-10.2K bps SDLC/HDLC, Bisync		 _	Opt.; 1200 bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	MAXNET HASP, 2780/3780 —	MAXNET HASP, 2780/3780 	MAXNET  	MAXNET — —	 2780/3780 _
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	315-630K bytes Both; 2.5-256M bytes	315-630K bytes Both; 2.5-256M bytes	315-630K bytes Both; 2.5-256M bytes	315-630K bytes Both; 2.5-256M bytes	Yes Cart.; 16-64M bytes
Drum/fixed head disk storage	Fixed; (3).5-2M bytes	Fixed; (3).5-2M bytes	Fixed; (3).5-2M bytes	Fixed; (3).5-2M bytes	No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	(7) 36-409.6K bytes (4) 30-440 cps (5) 280-1000 lpm 50-200K bps 24 x 80 char. A/D & D/A converts, card inputs, others	(7) 36-409 6K bytes (4) 30-440 cps (5) 280-1000 lpm 50-200K bps 24 x 80 char. A/D & D/A converts, card inputs, others	(7) 36-409 6K bytes (4) 30-440 cps (5) 280-1000 lpm 50-200K bps 24 x 80 char. A/D & D/A con- verters, printer, plot- ter, color graphic CRT	(7) 36-409.6K bytes (4) 30-440 cps (5) 280-1000 lpm 50-200K bps 24 x 80 char. A/D & D/A con- verters, printer, plot- ter, color graphics CR1	No 165 cps 300 lpm 9600 bps 32 char. x 11 lines None
SOFTWARE Assembler  Compilers  Operating system  Language implemented in firmware Operating system implemented in firmware	Assembler and macro assembler COBOL, FORTRAN, CORAL 66, TOTAL Batch, real-time, time-sharing No	Assembler and macro assembler COBOL, FORTRAN, CORAL 66, TOTAL, Batch, real-time, time-sharing No	Assembler and macro assembler COBOL, FORTRAN, CORAL 66, TOTAL, Batch, real-time time-sharing No No	Assembler and macro assembler COBOL, FORTRAN CORAL 66, TOTAL, Batch, real-time, time-sharing No No	No ACE Real-time Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	38,150 242 	61,500 382 	13,400 (64K bytes) — — — 8,250 (512K bytes)	48,200 (128K bytes) — — 17,950 (128K bytes)	28,995 (56K bytes) 9%  2,000 (32K bytes)
Date of first delivery Number installed to date	April 1978	October 1978	March 1971 NA	June 1974 NA	May 1976 175
COMMENTS					System price also in- cludes a CRT (32 x 11 or 24 x 80), 16MB of disk storage, a 165- cps printer, system software, and an in- ventory control appli- cations package

MANUFACTURER AND MODEL	Nanodata QM/1	NCR 499	NCR 8130	NCR 8150	NCR 8200
WORD LENGTH, BITS	18 + 2	16 + 1	16 + 2	16 + 2	16 + 2
NO. WORKSTATIONS SUPPORTED	Appl. dependent		1	4	
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core 0.75-125/0.35 16K/1024K Standard Optional Optional	Core 1.2/0.65 12K/32K Standard No No	MOS 0.6 48K/64K bytes Standard No Optional	MOS 0.6 48K/256K butes Standard No Optional	Core 1.2/0.65 32K/128K bytes Standard No No
CENTRAL PROCESSOR  No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	256K RAM; 40KX 18 0.75 Standard Standard Standard Optional Optional	ROM, 64K words 1.7 milliseconds Standard No No No No	32K ROM, 4K bytes — No Standard Optional Optional	32K ROM, 4K bytes — No Standard Optional Optional	— No 24 (8 digits) Standard No Standard No No
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Optional 1M 2,048	Standard 833K 8	Standard 866K bytes 16	Standard 866K bytes 16	Standard 833K 8
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	Appl. dependent Optional Optiona Bisync, Async		1 Opt.; to 9600 bps No Bisync	Opt.; to 9600 bps No Bisync	Opt.; to 9600 bps Opt.; to 9600 bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	See Comments	IBM 2780/3780	  	 	— IBM 2780 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Both; 12-60M bytes	No Cart.; 4.9-9.8MB	512K-4096K bytes No	250K bytes 5 to 40M bytes	No Cart.; to 39.2M bytes
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	Cartridge; 2.5M bytes	Cassette; 750 bpi	Cassette; 750 bpi	Both	Cassette; 750 cps
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	200KBS 200-1000 cps 600-1250 lpm Up to 50K bps Yes IBM 360 & Univac 1100 compatible channel	No 75, 130 cps 55-300 lpm 300-9600 bps No PT, mag ledger card	No 110 cps 50-200 lpm To 9600 bps 512, 1920 char.	No 110 cps 50-200 lpm To 9600 bps 512, 1920 char.	No 50, 70, 125 cps See Comments 1200, 9600 bps 80 char. x 24 lines
SOFTWARE	Assembler & macro	Neat/AM	No	No	No
Assembler	assembler PASCAL, APL/SV,	No	COBOL, BASIC	COBOL, BASIC	NEAT/3, COBOL
Compilers	See Comments See Comments	No	Interactive	Interactive	Batch, multipro-
Operating system  Language implemented in firmware Operating system implemented in firmware	Yes No	No No	No No	No No	gramming No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	176,000 — None 4,960 (16K words)	17,900 (12K bytes) 83  1,100 (2K bytes)	10,700 119 _: 600 (16K bytes)	18,300 170  1,000 (32K bytes)	Available only used  — — — —
Date of first delivery Number installed to date	1975 20	February 1976 NA	March 1978 NA	March 1978 NA	September 1974 NA
COMMENTS	Existing emulators include IBM 360/370, 7094; Univac 1106; DEC 11/05, 11/40; DG Nova; CDC 160A; Delco 352; RCA 234SCP, UYK-7, -20; and	Replacement for NCR 399	Price includes proc- essor (48K bytes), CRT, flexible disk (1M byte), and printer (50 lpm)	Price includes processor (48K bytes), CRT, cartridge disk (2.5M bytes), fixed disk (5M bytes), and printer (50 lpm)	Line printers, 50, 70 and 125 lpm matrix, 200, 300 and 600 lpm band
	microprocessor; emulation lab soft- ware provided; both vertical and hori- zontal control store				

rd Standa No Option 64K No ———————————————————————————————————	128K bytes ard nal lard lard to 9600 bps to 9600 bps	16 + 2 24  MOS 0.8 0.8 64K/512 bytes Standard No Optional  No Standard No Standard No Standard No Standard No No Standard No Opt.; to 9600 bps Opt.; to 9600 bps Bisync  IBM 2780 No  243-486K bytes 54M to 324M bytes No Cassette; 450K	MOS 0.8 96K/512M bytes Standard No Optional  No Standard No Standard No Standard No Standard No Opt; to 9600 bps Sisync  BM 2780 No 243-486K bytes Fixed & removable; 20M-364M bytes	8 + 1  Thin film 0.8 16K/32K bytes Standard No No  No 59 (5 digits) No Standard Standard No No  Standard 40K & 108K 2  600-50,000 bps 45-2400 bps  No Pack; to 33.5M bytes No
MOS 0.8 64K /1 Standa No Option    rd	lard lard lard lard to 9600 bps to 9600 bps c	MOS 0.8 64K/512 bytes Standard No Optional  Standard No Standard No Standard No Standard No No  Standard 833K 8  Opt.; to 9600 bps Opt.; to 9600 bps Bisync IBM 2780 No  243-486K bytes 54M to 324M bytes No	MOS 0.8 96K/512M bytes Standard No Optional  No Standard No Standard No No Standard Standard Standard Standard Standard Standard No No Standard Sta	0.8 16K/32K bytes Standard No No  No 59 (5 digits) No Standard Standard Standard Standard V No No  Standard 40K & 108K 2  600-50,000 bps 45-2400 bps  No Pack; to 33.5M bytes
6K bytes rd 6BK bytes rd 6BK bytes rd 6BK bytes 6BK byte	lard lard lard lard to 9600 bps to 9600 bps c	O.8 64K/512 bytes Standard No Optional  No Standard No Standard No Standard No No Standard S33K 8  Opt.; to 9600 bps Opt.; to 9600 bps Bisync  IBM 2780 No 243-486K bytes 54M to 324M bytes No	O.8 96K/512M bytes Standard No Optional	0.8 16K/32K bytes Standard No No  No 59 (5 digits) No Standard Standard Standard Standard V No No  Standard 40K & 108K 2  600-50,000 bps 45-2400 bps  No Pack; to 33.5M bytes
No	lard to 9600 bps to 9600 bps c 2780 -1024K bytes to 80M bytes	Standard No Standard No No Standard 833K 8  Opt.; to 9600 bps Opt.; to 9600 bps Bisync IBM 2780 No 243-486K bytes 54M to 324M bytes No	Standard No Standard No No Standard 833K 8  Opt.; to 9600 bps Opt.; to 9600 bps Bisync Bisync BM 2780 No 243-486K bytes Fixed & removable; 20M-364M bytes	59 (5 digits) No Standard Standard No No Standard 40K & 108K 2  600-50,000 bps 45-2400 bps
833K 8 9 9600 bps Opt.; to Opt.; to Opt.; to Opt.; to Bisync 780 IBM 2' No 1024K bytes 250K- Cart.; to No te; 450K Casset bytes	to 9600 bps to 9600 bps c 2780 -1024K bytes to 80M bytes	833K 8	833K 8  —Opt.; to 9600 bps Opt.; to 9600 bps Bisync —IBM 2780 No  243-486K bytes Fixed & removable; 20M-364M bytes	40K & 108K 2 600-50,000 bps 45-2400 bps No Pack; to 33.5M bytes
0 9600 bps   Opt.; to Bisync	to 9600 bps c 2780 -1024K bytes to 80M bytes	Opt.; to 9600 bps Bisync — IBM 2780 No 243-486K bytes 54M to 324M bytes No	Opt.; to 9600 bps Bisync — IBM 2780 No 243-486K bytes Fixed & removable; 20M-364M bytes	No Pack; to 33.5M bytes
1024K bytes   250K-Cart.; 1	-1024K bytes to 80M bytes	No 243-486K bytes 54M to 324M bytes No	No 243-486K bytes Fixed & removable; 20M-364M bytes	Pack; to 33.5M bytes
o 39.2M Cart.; to No Casset bytes	to 80M bytes	54M to 324M bytes	Fixed & removable; 20M-364M bytes	Pack; to 33.5M bytes
te; 450K Casset	ette; 450K	]	C	No
bytes	ette; 450K	Cassette; 450K	C 7EO	
KBS 10-20 s 173 cp 00 lpm 100-30 9600 bps 1200,		bytes 10-20 KBS 173 cps 50-900 lpm 1200, 9600 bps 24 x 80 char.	Cassette; 750 cps 10-20 KBS 173 cps 200-900 lpm Up to 9600 bps 24 x 80 char.	Cassette; 750 cps 10-40 KBS 6 cps 125-900 lpm 45-50,000 bps 24 x 80 char. PT; MICR/OCR
No		No	No	No
3, COBOL NEAT	/3, COBOL	NEAT/3, COBOL	NEAT/3, COBOL	BASIC, COBOL,
	, multi- amming	Batch, multi- programming No No	Batch, multi- programming No No	FORTRAN, NEAT/3 Batch, multi- programming No No
29,92!	5	21,555	77,812	47,000 (16K bytes)
119		85	Included for 5-years	347
16K bytes) 1,000	(16K bytes)	4,000 (64K bytes)	]-	3,500 (16K bytes)
977 March NA	n 1977	Fourth qtr. 1979 NA	NA NA	December 1970 NA
			Price includes 96K bytes memory, three CRT's, one 200 lpm printer, 5 years of hardware maintenance, one-time licensing fee for IMOS II, COBOL. '74, and utilities	System price also includes line printel 8 4 MB disk drive, and card reader, no longer manufactured, available only in used or used-refurbished units
	29,92 119 	29,925 119 — (16K bytes) 1,000 (16K bytes) 977 March 1977	29,925 21,555 119 85 ————————————————————————————————————	29,925 21,555 77,812 Included for 5-years  1,000 (16K bytes) 77,812 Included for 5-years  4,000 (64K bytes)  Fourth qtr. 1979 NA  Price includes 96K bytes memory, three CRT's, one 200 lpm printer, 5 years of hardware main- tenance, one-time licensing fee for IMOS II, COBOL

MANUFACTURER AND MODEL	NCR Century 75	NCR Century 100	NCR Century 101	NCR Century 151	New England Digital Able / 40
WORD LENGTH, BITS	8 + 1	8 + 1	8 + 1	8 + 1	16
NO. WORKSTATIONS SUPPORTED	<u> </u>	_	_	<b> </b> -	NA
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core 1.2/0.65 16K/64K bytes Standard No	Thin film 0.8 16K/32K bytes Standard No No	Core 1.2/0.6 16K/128K bytes Standard No Optional	MOS 0.75 (1 or 2 bytes) 64K/128K bytes Standard No Optional	Static MOS 0.5 (avg.)/0.5 (avg.) 16K/64K No No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer  INPUT/OUTPUT CONTROL Direct memory access channel	No 25 (5 digits) Optional Standard Standard Optional Standard	No 59 (5 digits) No Standard Standard No No	No 28.8 (5 digits) Optional Standard Standard Optional Standard	No 18.0 (5 digits) Standard No Standard No Optional	64K 16 x 256 0.25 Optional Optional Standard Optional Standard
Maximum I/O rate, words/sec. No. of external interrupt levels	120K & 416K 8	40K & 108K 2	120K & 416K 9	120K & 545K 9	2M 12
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	 600-50,000 bps 45-2400 bps 	 600-50,000 bps 45-2400 bps 	 600-50,000 bps 45-24000 bps 	 600-50,000 bps 45-2400 bps 	64 Optional 300-38.4K bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation		-  -  -			NED WORK IBM 2780 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Cart.; to 9.8M bytes	No Pack; 8.4-33.5M bytes	No Pack; 8.4-381.6M bytes	No Pack; 8.4-381.6M bytes	180K bytes No
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	No	Cassette; 750 cps	Cassette; 750 cps	Cassette; 750 cps	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	10-320 KBS 6 cps 200-450 lpm 45-50,000 bps Interface only PT; MICR/OCR	10-80 KBS 6 cps 450-3000 lpm 45-50,000 bps 80 char. x 24 lines Paper tape units; MICR/OCR units	10-320 KBS 6 cps 450-3500 lpm 45-50,000 bps 80 char. x 24 lines Paper tape units; MICR/OCR units	10-320 KBS 6 cps 450-3500 lpm 45-50,000 bps 80 char. x 24 lines Paper tape units; MICR/OCR units	No 30,120 cps 300 lpm 300-38,400 bps 1920 char. Plotter, graphic CRT, 16 channel AID & quad D/A, digital I/O
SOFTWARE Assembler	No	No	No	No	Yes
Compilers Operating system	BASIC, COBOL, FORTRAN, RPG Batch, multi- programming	COBOL, BASIC, FORTRAN, NEAT/3 Batch, multi- programming	COBOL, BASIC, FORTRAN, NEAT/3 Batch, multi- programming	COBOL, BASIC, FORTRAN, NEAT/3 Batch, multi- programming	XPL, PASCAL, BASIC Real-time
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	No No	No Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$	56,850 (16K bytes) 367	71,500 (16K bytes) 458	69,520 (16K bytes)	119,925 (64K bytes) 521	7,950
Discounts available Price of memory increment, \$	5,000 (8K bytes)	3,500 (16K bytes)	5,000 (8K bytes)	20,000 (64K bytes)	Educ., qty. 1,000 (8K words)
Date of first delivery Number installed to date	May 1976 NA	March 1963 NA	August 1972 NA	February 1975 NA	June 1977 NA
COMMENTS	System price also includes a card reader, line printer, disk drive, TTY and cabinet; can be upgraded to Century 101	System price also includes line printer, 8.4-MB disk drive, and card reader, no longer manufactured; available only in used or used-refurbished units	System price also includes line printer, 8.4-MB disk drive, and card reader		Includes minifloppy drives, RTC, APL, and serial port

MANUFACTURER AND MODEL	New England Digital Able ⁄ 60	Northern Telecom Systems Corp. 405	Northern Telecom Systems Corp. 410	Northern Telecom Systems Corp. 440	Northern Telecom Systems Corp. 445
WORD LENGTH, BITS	16	8	8	8	8
NO. WORKSTATIONS SUPPORTED	NA	2	1	8	8
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Static MOS 0.5 (avg.)/0.5 (avg.) 16K/64K No No No	MOS 0.25/0.25 48K/64K bytes 	MOS 0.50/0.25 40K/64K bytes 	MOS 0.50/0.25 24K/64K bytes —	MOS 0.25/0.25 64K/256K bytes 
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K 16 x 256 0.25 Optional Optional Standard Optional Standard			5.5    	
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Optional 2M 12	  	   		  
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	64 Optional 300-38.4K bps Bisync	2 Opt.; 600-9600 bps Opt.; 37.5-1200 bps Bisync, SDLC	2 Opt.; 600-4800 bps Opt.; 37.5-1200 bps Bisync, SDLC	2 Opt.; 600-9600 bps Opt.; 37.5-1200 bps Bisync, SDLC	3 Opt.; 600-9600 bps Opt.; 37.5-1200 bps Bisync, SDLC
Network architectures supported RJE terminals emulated IBM 3270 emulation	NEDWORK IBM 2780 No	IBM/SNA Several Yes	None Several No	None Several No	None Several Yes
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	1.2M bytes 24M bytes	.5-1.0M bytes No	256K bytes Cartridge; 5M bytes	256K bytes No	256K bytes Pack; 4-74.5M
Drum/fixed head disk storage	No	No	No	To 20M bytes	bytes 5, 10, 20M bytes
Magnetic tape cassettes/cartridges	No	No	Cartridge; 1000 cps	Cartridge; 1000 cps	Cartridge; 12K cps
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	800 bpi 30,120 cps 300 lpm 300-38,400 bps 1920 char. Plotter, graphic CRT, 16 channel A/D & quad D/A, digital I/O	To 180 cps 300 or 600 lpm 	To 180 cps 300 lpm 	To 180 cps 300 lpm Standard	To 180 cps 300 to 600 lpm 2000 characters
SOFTWARE Assembler	Yes	No	No	No	No
Compilers  Operating system  Language implemented in firmware Operating system implemented in firmware	XPL, PASCAL, BASIC Real-time No Partially	COBOL, BASIC, TAL 2000 Multiprogramming, multi-task No No	COBOL, BASIC, TAL-2 Multiprogramming, multi-task No	COBOL, BASIC, TAL-2 Multiprogramming, multi-task No No	COBOL, BASIC, TAL-2, -2000 Multiprogramming, multi-task No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	9,650  Educ., qty. 1,000 (8K words)	6,150 234 Quantity 800 (16K)	23,960 546 Quantity 1,160 (8K)	21,240 528 Quantity 1,160 (8K)	20,680 517 Quantity 2,000 (32K)
Date of first delivery	April 1978	August 1978	May 1976 NA	May 1976 NA	May 1978 NA
Number installed to date COMMENTS	Includes 8-inch floppy drives, RTC, APL, and serial port	Designed for trans- action proc. in dis- tributed or stand- alone environments; industry application software packages are available through distributors	Designed for trans- action processing in distributed or stand- alone environments; industry application software packages are avail. through distributors		Six remote work- stations on-line con- currently unlimited in time-sharing

MANUFACTURER AND MODEL	Northrop Data Systems BDS Series 500	Northrop Data Systems BDS Series 1000	Northrop Data Systems BDS Series 2000	Northrop Data Systems BDS Series 4000	Olivetti 2030 FDV
WORD LENGTH, BITS	Variable 8-32	Variable 8-32	Variable 8-32	16	8-bit byte
NO. WORKSTATIONS SUPPORTED	2	4	8	32	1
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core 1.0/NA 24K/32K No No No	Core 1.0/NA 24K/32K No No	Core 1.0/NA 32K/32K No No No	MOS 1.0/NA 64K/512K No No No	MOS 1/2.3 4K/16K bytes Standard No Optional
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	24K PROM, ROM; 4KB 9,68 (7 digits) Standard No Standard Standard No	24K PROM, ROM; 4KB 9.68 (7 digits) Standard No Standard Standard	32K PROM, ROM; 4KB 9,68 (7 digits) Standard No Standard Standard No	64K PROM, ROM; 4KB 9.68 (7 digits) Standard No Standard Standard No	— RAM 6.0 Standard No Standard No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1M 2; 128	Standard 1M 2; 128	Standard 1M 2; 128	Standard 1M 2; 128	No 250K bps 1
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	1 No Std.; 1,200 bps —	4 No Std.; 1,200 bps	8 No Std.; 1,200 bps	32 Opt.; 9,600 bps Opt.; 9,600 bps IBM 2780	1 Standard No Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	_ _ _	 	_ _ _	IBM IBM 2780 No	None None No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No 5MB, 10MB	No 10MB	No 20MB, 40MB	No 20MB, 500MB	Standard No
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	No	No	No	No	Cassette; 1000 bps
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	Opt.; 20 kbs Opt.; 100 cps 150 lpm 1200 bps Std.; 24 x 80 char.	Opt.; 20 kbs Opt.; 100 cps 150 lpm 1200 bps Std.; 24 x 80 char.	Opt.; 20 kbs Opt.; 100 cps 150 lpm 1200 bps Std.; 24 x 80 char.	Std.; 20 kbs Opt.; 100 cps 150 lpm 9600 bps Std.; 24 x 80 char.	No Std.; 1000 cps Opt.; 200 cps 1200-9600 bps Alphanumeric Magnetic card, auto front feed
SOFTWARE Assembler	Yes	Yes	Yes	Yes	Assembler
Compilers		_	_	BASIC	
Operating system	Real-time	Real-time	Real-time	Reality Oper. Sys.,	Batch
Language implemented in firmware Operating system implemented in firmware	Partially Partially	Partially Partially	Partially Partially	virtual, English Rpt. Partially Partially	Fully Fully
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$	\$29,500	\$34,920	\$40,355	\$52,295	\$14,950
Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment. \$	\$250  NA	\$283  NA	\$372  NA	\$355 _ \$3,900/\$6,900	\$91  650 (4K bytes)
· · · · · · · · · · · · · · · · · · ·	1977	1972	1973	1975	January 1979
Date of first delivery Number installed to date COMMENTS	Over 200 (all models) Price includes CRT and printer	Over 200 (all models) Price includes CRT and printer	Over 200 (all models) Price includes CRT and printer	Over 200 (all models) Price includes CRT and printer; memory increment price is for 32K and 64K, respectively	NA

MANUFACTURER AND MODEL	Olivetti 2030 MDV	Olivetti BCS 3030	Olivetti P6060	Perkin-Elmer Sixteen 10	Perkin-Elmer Sixteen 20
WORD LENGTH, BITS	8-bit byte	16	_	16 + 1	16 + 1
NO. WORKSTATIONS SUPPORTED	1	1	1	4	16
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 1/2.3 4K/16K bytes Standard No Optional	MOS	MOS 16K/48K bytes No No	MOS 1.0/NA 8K/32K Standard No No	MOS 0.825/NA 16K/131K Standard No Optional
CENTRAL PROCESSOR No. of directly addressable words Centrol storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer  INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels  COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported Network architectures supported RJE terminals emulated IBM 3270 emulation	ROM 6.0 Standard No Standard No Standard  No 1  Standard  No 1  No	3,500 No	No No No Optional No Optional No Opt.; 75-1920 bps None None None No	32K ROM 1.0 Optional No Standard Optional Standard  Standard  1M 1-255  Std.; to 9600 bps IBM 2780/3780	32K ROM 0.825 Optional Optional Standard Optional Standard 1.21 M 1-255 Std.; to 9600 bps IBM 2780/3780
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives Drum/fixed head disk storage	No No No	256K-1024K bytes Cart.; 10-20M bytes; Nonrem.; 2.5-20MB No	256K-1024K bytes Cart.; 10-20M bytes; Nonrem.; 2.5-20MB No	Yes, 1-4 Both, 10-4800 MB No	Yes, 1-4 Both, 10-4800 MB No
Magnetic tape cassettes/cartridges	Cassette; 1000 bps	Cassette; 1000 bps	Cassette; 1000 bps	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No Std.; 1000 cps Opt.; 200 cps 1200-9600 bps Alphanumeric Magnetic card, auto front feed	800-1600 bpi 90-175 cps 300-600 lpm To 9600 bps 80 char. x 24 lines None	800-1600 bpi 30-175 cps Optional To 9600 bps 80 char. x 24 lines Paper tape reader and punch	Yes, 36-120 KB Yes, 30-180 cps Yes, 300-600 lpm Yes, to 19.2K bps 24 x 80 char. A/D and D/A, digital I/O	Yes, 36-120 KB Yes, 30-180 cps Yes, 300-600 lpm Yes, to 19.2K bps 24 x 80 char. A/D and D/A, digital I/O
SOFTWARE Assembler	Assembler	Macro assembly	No	Assembler, macro	Assembler, macro
Compilers	_	Mini PL/1,	BASIC	assembler BASIC, extended	assembler BASIC, extended
Operating system	Batch	RPG II Interactive, batch	Interactive, batch	FORTRAN IV Batch, real-time, multi-tasking	FORTRAN IV Batch, real-time, multi-tasking
Language implemented in firmware Operating system implemented in firmware	Fully Partially	No No	Partially Partially	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	9,095 54 650 (4K bytes)	11,000 56  900 (8K bytes)	9,250 59  900 (8K bytes)	\$5,400 (8K words) \$40 Quan., \$ vol., educat. See comments	\$9,000 (16K words) \$70 Quan., \$ vol., educat. See comments
Date of first delivery Number installed to date	January 1979 NA	March 1978 NA	January 1977 NA	January 1979 30	March 1979 45
COMMENTS			Desktop computer features integrated 80-cps/80-col. thermal printer, single floppy disk drive display, 16K user memory, and full typewriter keyboard with BASIC keywords and operating system commands	\$1,600 (8K words), \$2,000 (16K words)	\$2,000 (16K words), \$3,000 (32K words), \$4,500 (64K words)

MANUFACTURER AND MODEL	Perkin-Elmer Sixteen 30	Perkin-Elmer Model 7/32 CII	Perkin-Elmer Model 8/32	Perkin-Elmer Model 3220	Perkin-Elmer Model 3240
WORD LENGTH, BITS	16 + 6	32 + 2	32 + 2	32 + 7	32 + 7
NO. WORKSTATIONS SUPPORTED	16	_	_	_	_
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.750/NA 16K/131K No Standard Optional	Core 0.750/NA 32K/500K Optional No Optional	Core 0.3/NA 62K/500K Standard No Standard	MOS 0.34/NA 131K/500K No Standard Standard	MOS 0.25/NA 131K/8M No Standard Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	32K ROM 0.750 Standard Optional Standard Standard Standard	500K ROM — Standard Optional Standard No Standard	500K ROM — Standard Optional Standard No Optional	500K ROM — Standard Optional Standard Standard Standard	8M ROM — Standard Optional Standard Standard Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.33M 1-255	Standard 1M 1-1023	Standard 1.5M 1-1023	Standard 2M 1-1023	Standard 10M 1-1023
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported		<u>-</u>			_ _ _
Network architectures supported RJE terminals emulated IBM 3270 emulation	 IBM 2780/3780 _	  			
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	Yes, 1-4 Both, 10-4800 MB	No Both, 10-8400 MB	No Both, 10-8, 400 MB	No Both, 10-8, 400 MB	No Both, 10-38, 400 MB
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	Yes, 36-120 KB Yes, 30-180 cps Yes, 300-600 lpm Yes, to 19.2K bps 24 x 80 char. A/D and D/A, digital I/O	Yes, 36-120 KB Yes, 30-180 cps Yes, 300-600 lpm yes, to 19.2K bps 24 x 80 char. A/D and D/A, digital I/O	Yes, 36-120 KB Yes, 30-180 cps Yes, 300-600 lpm Yes, to 19.2K bps 24 x 80 char A/D and D/A, digital I/O	Yes, 36-120 KB Yes, 30-180 cps Yes, 300-600 lpm Yes, to 19.2K bps 24 x 80 char. A/D and D/A, digital I/O	Yes, 36-120 KB Yes, 30-180 cps Yes, 300-600 lpm Yes, to 19.2 bps 24 x 80 char. A/D and D/A, digital I/O
SOFTWARE Assembler	Assembler, macro	Assembler, macro	Assembler, macro	Assembler, macro	Assembler, macro
Compilers	assembler BASIC, extended	assembler BASIC, extended	assembler BASIC, COBOL, RPG	assembler BASIC, COBOL, RPG	assembler BASIC, COBOL, RPG
Operating system	FORTRAN IV Batch, real-time, multi-tasking	FORTRAN IV Batch, real-time, multi-tasking	II, FORTRAN IV Batch, real-time, multi-tasking	II, FORTRAN IV Batch, real-time, multi-tasking	II, FORTRAN IV Batch, real-time, multi-tasking
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	No No	No.
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	\$11,500 (16K words) \$80 Quan., \$ vol., educat. See comments	\$12,045 (32K words) \$160 Quan., \$ vol., educat. \$6,550 (32K words)	\$51,900 (65K words) \$500 _ \$19,000 (65K words)	\$33,500 (131K words) \$300 — \$8,000 (131K	\$85,000 (131K words) \$540
Date of first delivery Number installed to date	February 1979 35	NA NA	June 1975 Over 2,000	words) May 1979 125	\$8,000 (131K words) September 1979
COMMENTS	\$2,000 (16K words), \$3,000 (32K words), \$4,500 (64K words)				10
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	16, 32	16, 32	16, 32	16, 32
128 (32	32	63	63	63
recommended) MOS 0.4/0.2 32K/64K Optional No Optional	MOS; bipolar cache 0.75/0.54 256K/1024K bytes Standard Standard Std.; 3 levels	MOS; bipolar cache 0.75/0.54 512K/2048K bytes Standard Standard Std.; 3 levels	MOS; bipolar cache 0.75/0.54 512K/4096K bytes Standard Standard Std.; 3 levels	MOS; bipolar cache 0.75/0.54 512K/8192K bytes Standard Standard Std.; 3 levels
64K *ROM 0.40 *Optional Optional *Optional Optional No	64K 4K x 64 1.1 Standard Standard Standard Optional Standard	64K 4K x 64 1.1 Standard Standard Standard Optional Standard	64K 5K x 64 1.1 Standard Standard Standard Optional Standard	64K 7K x 64 0.5 Standard Standard Standard Optional Standard
Std. & high-speed 1.1M 1-16	Standard 2.5M bytes 64	Standard 2.5M bytes 64	Standard 2.5M bytes 64	Standard 8M bytes 64
128 (32 recom.) Std.; 56,000 bps Std.; 19,200 bps SDLC	Async. (32); Sync (4) Std.; to 56K bps Std.; to 9600 bps HASP, 2780/3780	Async. (63); Sync (8) Std.; to 56K bps Std.; to 9600 bps HASP, 2780/3780	Async. (63); Sync (8) Std.; to 56K bps Std.; to 9600 bps HASP, 2780/3780	Async.(63); Sync. (8, Std.; to 56K bps Std.; to 9600 bps HASP, 2780/3780
None IBM 2780/3780 No	Primenet X.25 HASP, 2780/3780 Emulate & support	Primenet X.25 HASP, 2780/3780 Emulate & support	Primenet X.25 HASP, 2780/3780 Emulate & support	Primenet X.25 HASP, 2780/3780 Emulate & support
No No	512K-2M bytes Both; 12-2400M	512K-2M bytes Both; 12-24OOM	512K-2M bytes Both; 12-2400M bytes	512K-2M bytes Both; 12-2400M bytes
No	Fixed-head; 512K- 1M bytes	Fixed-head; 512K- 1M bytes	Fixed-head; 512K- 1M bytes	Fixed-head; 512K- 1M bytes
No No No 56,000 bps No No	To 488K bps 300 lpm To 1000 lpm To 56K bps 80 char. x 24 lines PT, card reader/ punch, printer/	To 488K bps 300 lpm To 1000 lpm To 56K bps 80 char. x 24 lines PT, card reader/ punch, printer/	To 488K bps 300 lpm To 1000 lpm To 56K bps 80 char. x 24 lines PT, card reader/ punch, printer/ plotter	To 488K bps 300 lpm To 1000 lpm To 56K bps 80 char. x 24 lines PT, card reader/ punch, printer/ plotter
Assembler & macro assembler Business BASIC  Real-time, interactive, time-sharing No	Macro & micro assembler BASIC, FORTRAN, COBOL, RPG II, Multi-user, virtual memory Partially	Macro & micro assembler BASIC, FORTRAN, COBOL, RPG II, Multi-user, virtual memory Partially Partially	Macro & micro assembler BASIC, FORTRAN, COBOL, RPG II, Multi-user, virtual memory Partially Partially	Macro & micro assembler BASIC, FORTRAN, COBOL, RPG II, Multi-user, virtual memory Partially Partially
5,540 — — 1,760 (32K bytes)	65,000 (450 QMB) 73,000 (450 HMB) 500 (450 QMB) 590,450 (450 HMB) Volume	80,000 (550 HMB) 578/550 (HMB) Volume 40,000 (1M byte)	105,000 (650 HMB) 685/650 (HMB) Volume 40,000 (1M byte)	130,000 (750 HMB) 149,000 (750 1MB) 785 (750 HMB) 965 (750 1M byte) Volume 40,000 (1M byte)
March 1979 250-300	1979 77	1979 201	1979 11	1979 55
*Starred features not available until third quarter 1980; Point 4 formerly known as Educa- tional Data Systems	Virtual memory management system permits addressing up to 32M bytes per user; 2K-byte cache memory std.; 2 to 1 memory interleaving std.	Virtual memory management system permits addressing up to 32M bytes per user; 2K-byte cache memory std.; 2 to 1 memory interleaving std.	Virtual memory management system permits addressing up to 32M bytes per user, 2K-byte cache memory std.; 2 to 1 memory interleaving std.	Virtual memory management systel permits addressing up to 32M bytes per user; 16K-byte cache memory std.; 2 to 1 memory interleaving std.
	32K/64K Optional No Optional  64K *ROM 0.40 *Optional Optional Optional Optional No  Std. & high-speed 1.1M 1-16  128 (32 recom.) Std.; 56,000 bps Std.; 19,200 bps SDLC  None IBM 2780/3780 No	32K/64K Optional No Optional Standard Standard Standard Std.; 3 levels  64K *ROM 0.40 1.1 *Optional Optional Optional Optional Optional No Std. & high-speed 1.1M 1-16  Std. & high-speed 1.1M 1-16  Std.; 56,000 bps Std.; 19,200 bps Std.; 10,9600 bps HASP, 2780/3780 No  No No No S12K-2M bytes Both; 12-2400M bytes No No No S12K-2M bytes Both; 12-2400M bytes No No To 488K bps 300 lpm To 1000 lpm To 56K bps No No No To 488K bps 300 lpm To 1000 lpm To 56K bps No No No No Real-time, inter- active, time-sharing No No S1CROTRAN, COBOL, RPG II, Multi-user, virtual memory Partially  5,540  65,000 (450 QMB) 590/450 (450 HMB) Volume  Virtual memory management system permits addressing up to 32M bytes per user; 2K-byte cache memory std.; 2 to 1 memory	256K-7024K bytes Standard St	22K./64K   Optional   Standard   Optional   Optional   Optional   Optional   Optional   Optional   Optional   Optional   Standard   Standard   Standard   Standard   Optional   Optional

MANUFACTURER AND MODEL	Qantel 210	Qantel 950	Qantel 960/965	Qantel 970/975	Qantel 1400
WORD LENGTH, BITS	8	8	8	8	8
NO. WORKSTATIONS SUPPORTED	1	16	16	32	64
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.8 16K/32K bytes Standard No No	MOS 1.5 48K/48K Standard No No	MOS 1.5 48K/64K Standard No No	MOS 0.8 64K/256K Standard No	MOS 1.1 40K / 128K Standard No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K bytes ROM; 26K bytes 22 No No Standard No	48K bytes ROM; 32K bytes 18 No No Standard No Optional	64K bytes ROM; 32K bytes 8 No No Standard No Optional	256K bytes ROM: 32K bytes 4 Standard No Standard NA Optional	128K bytes ROM; 32K bytes 8 No No Standard No Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	No — None	Standard 667K 1	Standard 909K 1	Standard 375K 1	Standard 909K 1
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	 No 110-50K bps	110-50K bps 110-50K bps	110-50K bps 110-50K bps	110-50K bps 110-50K bps	 110-50K bps 110-50K bps 
Network architectures supported RJE terminals emulated IBM 3270 emulation	_ _ _				
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	1.3-5.2 MB No	No Cart.; 6-36 MB	1.3-5.2 MB Cart.; 6-75 MB	1.3-5.2 MB Cart.; 12-300 MB	1.3-5.2 MB Cart.; 12-48 MB
Drum/fixed head disk storage	No	No	No	No	No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 45-120 cps 300 lpm 1200 bps 64 char. x 27 lines None	36-72 KBS 45-120 cps 300-600 lpm To 50K bps 64 char. x 27 lines None	36-72 KBS 45-120 cps 300-600 lpm To 50K bps 64 char. x 27 lines None	36-72 KBS 45-120 cps 300-600 lpm To 50K bps 64 char. x 27 lines None	36-72 KBS 45-120 cps 300-600 lpm Up to 50K bps 64 char. x 27 lines None
SOFTWARE   Assembler	Yes	Yes	Yes	Yes	Yes
Compilers	QIC (BASIC)	QIC (BASIC)	QIC (BASIC)	QIC (BASIC)	QIC (BASIC)
Operating system	Time-sharing	Time-sharing	Time-sharing	Time-sharing	Time-sharing
Language implemented in firmware Operating system implemented in firmware	Partially Partially	Partially Partially	Partially Partially	Partially Partially	Partially Partially
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$ Date of first delivery Number installed to date  COMMENTS	11,950 105 Quantity 1,450 (16K bytes) 4th qtr. 1977 Over 2000 (all models)	27,900 245 1,450 (8K bytes) 1st qtr. 1975 Over 2000 (all models) System 950 price includes 48K bytes,	39,500 (48K bytes) 330 2,450 (16K bytes) 3rd qtr. 1976 Over 2000 (all models) Model 965 price includes 48K bytes,	59,900 482 2,950 (32K bytes) 1st qtr. 1979 Over 2000 (all models) Model 975 price includes 64K bytes,	43,900 335  2,450 (32K bytes) 2nd qtr. 1977 Over 2000 (all models) System 1400 price includes 40K bytes,
		6M bytes disk drive, 55 cps printer, and one CRT	magnetic tape drive, 24M bytes fixed disk, 120 cps printer, and one CRT	25M byte sealed disk drive, magnetic tape drive, 50-100 Ipm printer, and one CRT	12M bytes disk drive, 300 lpm printer, and one CRT

MANUFACTURER AND MODEL	Qantel 1 400-2	Qantel 1450	Qantel 1450-2	Raytheon RDS-500	Raytheon RDS-7500
WORD LENGTH, BITS	8	8	8	16 + 2	16 + 2
NO. WORKSTATIONS SUPPORTED	64	64	64	_	_
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 1.1 48K/128K Standard No No	MOS 0.8 64K/1024K Standard No	MOS 0.8 128K/1024K Standard No No	Core or MOS 0.70/0.45 16K/64K Standard Standard (MOS)	MOS 0.70/0.45 32K/128K Standard Standard
CENTRAL PROCESSOR  No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	128K bytes ROM: 32K bytes 8 No No Standard No Optional	1024K bytes ROM: 32K bytes 4 Standard No Standard — Optional	1024K bytes ROM: 32K bytes 4 Standard No Standard  Optional	64K No 1.4 Standard Optional Standard Optional Optional	64K No 1. 4 Standard Optional Standard Optional Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 909K 1	No  1	No	Standard 2M 16	Standard 2M 16
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	110-50K bps 110-50K bps	 110-50K bps 110-50K bps 		_ _ _ _	_ _ _
Network architectures supported RJE terminals emulated IBM 3270 emulation	_ _ 				_
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	1.3-5.2 MB Fixed, moving heads; 25-600 MB No	1.3-5.2 MB Fixed, moving heads; 25-600 MB	1.3-5.2 MB Fixed, moving heads; 25-600 MB No	No Both; 2.56-1200M bytes Fixed-head; 770K-	No Both; 2.56-1200M bytes Fixed-head; 770K-
Magnetic tape cassettes/cartridges	No	No	No	3.08M bytes Cassette	3.08M bytes Cassette
Magnetic tape cassettes/ carringes  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	36-72 KBS 45-120 cps 300-600 lpm Up to 50K bps 64 char. x 27 lines None	36-72 KBS 45-120 cps 300-600 lpm Up to 50K bps 64 char. x 27 lines None	36-72 KBS 45-120 cps 300-600 lpm Up to 50K bps 64 char. x 27 lines None	58.6K bps 10-165 cps 300-1250 lpm 14.2K bps 1920 characters Appollo array, plotters, A/D & D/A converters	58.6K bps 10-165 cps 300-1250 lpm 19.2K bps 1920 characters Appollo array, plotters, A/D & D/A converters
SOFTWARE Assembler	Yes	Yes	Yes	Macro assembler	Macro assembler
Compilers	QIC (BASIC)	QIC (BASIC)	QIC (BASIC)	FORTRAN	FORTRAN
Operating system	Time-sharing	Time-sharing	Time-sharing	Batch, real-time, multi-programming	Batch, real-time, multi-programming
Language implemented in firmware Operating system implemented in firmware	Partially Partially	Partially Partially	Partially Partially	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$	64,900	44,900	69,900	19,800 (32K core)	17,100 (32K MOS)
Monthly maint, of basic configu- ration above for on-site contract, \$ Discounts available	485	335	485	5,400 (16K core)	_
Price of memory increment, \$	2,450 (32K bytes)	2,950 (32K bytes)	2,950 (32K bytes)	_	-
Date of first delivery Number installed to date	2nd qtr. 1977 Over 2000 (all	1st qtr. 1979 —	1st qtr. 1979 —	February 1973 Over 800	1980 NA
COMMENTS	models) System 1400-2 price includes 48K bytes, 25M bytes disk drive, magnetic tape drive, 300 lpm printer, and two CRT's	System 1450 price includes 128K bytes, 12M bytes disk drive, 300 lpm printer, and one CRT	System 1450-2 price includes 128K bytes, 25M bytes disk drive, magnetic tape drive, 300 lpm printer, and two CRT's	Apollo array processor can per- form 22 specialized array operations	Multiprocessing system capability

MANUFACTURER AND MODEL	Rolm 1602B (AN/UYK-19)	Rolm 1603A (AN/UYK-12)	Rolm 1606 (AN/UYK-19)	Rolm 1650 (AN/UYK-19)	Rolm 1664 (AN/UYK-19)
WORD LENGTH, BITS	16	16	16	16	16
NO. WORKSTATIONS SUPPORTED	2	2	10	12	2
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection  CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	Core 1.0 16K/64K No No No No Standard Optional Standard No Optional	Core 1.2 16K/32K No No No No So No So So So Standard No Optional	Core 1.0 16K/1024K No No Standard  64K ROM; 4K x 36 bits 1.0 Standard No Standard No Optional	Core 1.0/0.5 16K/32K No No Optional  32K PROM; 1K x 52 bits 1.05 Standard Optional Standard No Optional	Core 1.0/0.5 16K/64K No No Optional  64K ROM; 4K x 32 bits 1.0 Standard Standard Standard No Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 666K 16	Standard 768K 16	Standard 1M 16	Standard 666K 16	Standard 1 M 1 6
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	56  19.2K baud 	56  19.2K baud	56  19.2K baud	12  19.2K baud	56 — 19.2K baud
Network architectures supported RJE terminals emulated IBM 3270 emulation					None None No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage  Magnetic tape cassettes/cartridges	Yes Cartridge; 5-10M bytes Fixed-head; 2M bytes No	Yes Cartridge; 5-10M bytes Fixed-head; 2M bytes No	0.6-1.2M bytes Cartridge & Pack.; 5-160M bytes Fixed-head; 0.5-4M bytes No	Yes Cartridge; 5-10M bytes Fixed-head; 2M bytes No	Yes Cartridge; 5-10M bytes Fixed-head; 2M bytes No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	60 KBS 60 cps 1100 lpm 20K bps 80 char. x 24 lines Paper tape units, D/A & A/D conver., NTDS 1533	60 KBS 60 cps 1100 lpm 20K bps 80 char. x 24 lines Paper tape units, D/A & A/D converters	60 KBS 60 cps 11 00 lpm 20K bps 80 char. x 24 lines Paper tape units, D/A & A/D con- verters, NTDS 1533	60 KBS 60 cps 1100 lpm 20K bps 80 char. x 24 lines PT, D/A & A/D units, NTDS, 1533	60 KBS 60 cps 1100 lpm 20K bps 80 char. x 24 lines Paper tape units, D/A & A/D converters
SOFTWARE Assembler Compilers	Assembler & macro assembler ALGOL, BASIC, FORTRAN	Assembler & macro assembler ALGOL, BASIC, FORTRAN	Assembler & macro assembler ALGOL, BASIC, FORTRAN	Assembler & macro assembler ALGOL, BASIC, FORTRAN	Assembler & macro assembler ALGOL, BASIC, FORTRAN
Operating system  Language implemented in firmware Operating system implemented in	Batch, real-time No No	No No	Batch, real-time  No No	No No	No No
firmware  PRICING & AVAILABILITY  Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available  Price of memory increment, \$	25,250 — GSA, OEM, qty. 7,000 (16K words)	13,400 — GSA, OEM, qty. 6,000 (16K words)	43,900 — GSA, OEM, qty. 7,000 (16KW)	26,250 — GSA, OEM, qty. 7,000 (16K words)	39,450 — OEM, GSA, qty. 7,000 (16K words)
Date of first delivery Number installed to date	1977 Approx. 500	1976 90	1978 100	1976 NA	1976 100
COMMENTS	Qualified to Mil-E-5400 & Mil-E-16400 specif.; ATR chassis; micro- programmed militarized CPU	Qualified to Mil-E-5400 & Mil-E-16400 specif.; ATR chassis; low- priced, faster version of previously offered Model 1603, Model compatible with DG Nova	Qualified to Mil-E-16400; sys- tem used on Navy DPEWS (AN/SLO- 32); same as 1666 except for floating- point capability	Designed to meet Mil-E-5400 & Mil-E-16400 specif., half ATR version of Rolm 1602-A	Designed to meet Mil-E-5400 & Mil-E-16400 specif., ATR chassis, tri- processor militarized computer, upward- compatible with other Rolm com- puters

MANUFACTURER AND MODEL	Rolm 1666 (AN/UYK-19)	Rolm MSE/30 MIL-SPEC Eclipse	Sperry Univac BC/7 600	Sperry Univac BC/7 700	Sperry Univac BC/7 800
WORD LENGTH, BITS	16	16	8	8	8
NO. WORKSTATIONS SUPPORTED	10	48	2	4	6
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection  CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup	Core 1.0/0.5 16K/1024K No No Optional  64K ROM; 4K x 36 bits 1.0 Standard Standard Standard No	Core 1.0/0.5 32K/1024K No No Standard  32K ROM 0.5 Standard Standard Standard Standard No	MOS 1.0/0.5 48K bytes/64K bytes Standard No No 64K bytes 4K bytes 106 (5 digits) — Standard No Standard	MOS 1.0/0.5 48K bytes/64K bytes Standard No No 64K bytes 4K bytes 106 (5 digits) — Standard No Standard	MOS 1.0/0.5 128K/128K bytes Standard No No 64K bytes 4K bytes 106 (5 digits) — Standard No Standard
Real-time clock or timer INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Optional Standard 1M 16	Optional Standard 800,000 16	Standard 1M bytes 5	Standard 1M bytes 5	Standard 1M bytes 5
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	56  19.2K baud	 10 lines 48 x 19.2K baud 			
Network architectures supported RJE terminals emulated IBM 3270 emulation	None None No	— IBM 3780 Yes		 	  -  -
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	Yes; 0.6-1.2MB Pack & Cartrdige; 5-160M bytes Fixed-head; 0.5-	2.4M bytes Pack; 8 x 536M bytes Fixed; 2 x 4M bytes	2M-6M bytes No	2M-6M bytes Cartridge; 5M-40M bytes No	2M-6M bytes Cartridge; 10M-40M bytes No
Magnetic tape cassettes/cartridges	4.0M bytes	Cartridge	No	No	No
Magnetic tape cassettes/carringes  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	60 KBS 60 cps 1100 lpm 20K bps 80 char. x 24 lines Paper tape units, D/A & A/D conver-	Yes Yes Yes Yes Yes A/D, D/A, MIL-Std 1553A, NTDS	No 200 cps 125 lpm 9600 bps 80 char. x 24 lines Punched card reader	20, 40 KBS 200 cps 125-600 lpm 9600 bps 80 char. x 24 lines Punched card reader	20, 40 KBS 200 cps 125-600 lpm 9600 bps 80 char. x 24 lines Punched card reader
SOFTWARE Assembler	ters, NTDS, 1533 Assembler &	Assembler &	No	No	No
Compilers	macro assembler ALGOL, BASIC,	macro assembler ALGOL, BASIC,	RPG II, ESCORT	RPG II, ESCORT	RPG II, ESCORT
Operating system	FORTRAN Batch, real-time	PL/1, FORTRAN, Time-sharing, real-	Interactive, batch	Interactive, batch	Interactive, batch
Language implemented in firmware Operating system implemented in firmware	No No	time, batch No No	No No	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	48,900 — OEM, GSA, qty. 7,000 (16K words)	135,000  OEM, GSA, qty. 7,000 (16K words)	22,400 187  900 (16K bytes)	21,800 161  900 (16K bytes)	29.400 193 —
Date of first delivery Number installed to date	1977 140	NA NA	April 1977	Alpril 1977 NA	July 1978 NA
COMMENTS	Qualified to Mil-E-5400 & Mil-E-16400; Std.	Includes 128K words memory, I/O chassis, pro- cessor, and front panel			

MANUFACTURER AND MODEL	Sperry Univac BC/7 900	Sperry Univac V77-200	Sperry Univac V77-400	Sperry Univac V77-600	Sperry Univac V77-800
WORD LENGTH, BITS	8	16	16	16	16
NO. WORKSTATIONS SUPPORTED	8	-	_	<u> </u> -	_
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.67/0.33 (approx.) 262K/262K bytes Standard No No	MOS 0.66/0.56 8K/32K Optional No Optional	MOS 0.66/0.56 8K/1024K Optional No Std. w/megamap	MOS 0.66/0.56 16K/1024K Optional Yes Standard	MOS 0.60/0.375 64K/1024K No Yes Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K bytes 4K bytes   Standard No Standard	32K ROM; 512 x 24 2.31 Standard No Standard Optional; 1.5 hrs. Standard	32K ROM 2.64 Standard Optional Standard Optional; 8 hrs. Standard	2048 WCS 0.66-2.15 Standard Optional Standard Optional Standard	2048 WCS 0.45 Standard Optional Standard Optional Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1M bytes 5	Standard 319K 8-64	Standard 1.5M 8-64	Standard 1.51M 8-64	Standard 2.65K 8-64
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	2000-9600 bps No X.25	50KB 9600 bps	50KB 9600 bps	50KB 9600 bps UDLC/SDLC	50KB 9600 bps UDLC/SDLC
Network architectures supported RJE terminals emulated IBM 3270 emulation	Many IBM	HASP + 1004	 HASP + 1004 	Univac DCA HASP + 1004 SDLC/BISYNC	Univac DCA HASP + 1004 SDLC/BISYNC
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage	2M-6M bytes Cartridge; 10M- 40M bytes	Yes Both; 10M-40M	Yes Both; 10M-1.6B	No Both; 10M-1.6B	Yes Both; 10M-1.6B
_	No	No	No	No	No
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	20, 40 KBS 200 cps 125-600 lpm 9600 bps 80 char. x 24 lines Punched card reader	120 kbs 200 cps 300-600 lpm 50K bytes — IEEE-488	120 kbs 200 cps 300-600 lpm 50K bytes — IEEE-488	120 kbs 200 cps 300-600 lpm 50K bytes — IEEE-488	120 kbs 200 cps 300-600 lpm 50K bytes 
SOFTWARE	No.	data acquisition  Assembler, macro	data acquisition  Assembler, macro	data acquisition  Assembler, macro	data acquisition  Assembler, macro
Assembler Compilers	No   RPG II, ESCORT	assembler	assembler	assembler FORTRAN IV, RPG II,	assembler FORTRAN IV, RPG II,
Operating system	Interactive, batch	PASCAL Batch, real-time	COBOL, PASCAL Batch, real-time	COBOL, PASCAL Batch, real-time,	COBOL, PASCAL Batch, real-time,
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	multi-tasking No No	multi-tasking No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	40,100 263 3,500 (64K bytes)	5,350 (8K words) 1,350 (8K words)	7,850 (8K words) 1,350 (8K words)	13,950 (16K words) 2,900 (16K words)	33,000 (128K words) 5,000/9,000
Date of first delivery Number installed to date	Second qtr. 1980 NA	NA NA	NA NA	December 1976 NA	July 1979 NA
COMMENTS				Price includes cabinet; power supply and memory at lower prices than chassis level components	See V77-600; memory increment prices for 64K words and 128K words, respectively
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MANUFACTURER AND MODEL	STC Systems Ultimacc 2000	STC Systems Ultimacc 3000	STC Systems Ultimacc 4000	STC Systems Personna-data	Systems Engineering Laboratories 32/30A
WORD LENGTH, BITS	16	16	16	16	32 + 7
NO. WORKSTATIONS SUPPORTED	3	20	20	20	16
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core 0.7/0.35 32K/48K Optional No No	Core 0.7/0.35 32K/256K Optional No No	Core 0.7/0.35 32K/256K Optional No No	Core 0.7/0.35 32K/256K Optional No No	MOS 0.6/0.3 32K, 64K/256K No Standard Standard
CENTRAL PROCESSOR  No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	256 No 0.7 Optional No No Optional Standard	256 No 0.7 Optional No No Optional Standard	256 No 0.7 Optional No No Optional Standard	256 No 0.7 Optional No No Optional Standard	128KI PROM/ROM; 4096 0.6/1.2 Standard Standard Standard Standard Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.1M 16	Standard 1.1M 16	Standard 1.1M 16	Standard 1.1M 16	Standard 6.67M 16
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	Opt.; 1200 bps Opt.; 9600 bps Bisync	Opt.; 1200 bps Opt.; 9600 bps Bisync		Opt.; 1200 bps Opt.; 9600 bps Bisync	16 Opt.; to 9600 bps Opt.; 38.4 bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	 IBM 2780/3780 Yes	 IBM 2780/3780 Yes	 IBM 2780/3780 Yes	 IBM 2780/3780 Yes	HASP terminals
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Pack; 12-24M bytes	No Pack; 32-320M bytes	No Pack; 32-320M bytes	No Pack; 12-320M bytes	No Both; 10-1200MB
Drum/fixed head disk storage	No	No	No	No	Fixed-head; 5-40MB
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	60 KBS 165 cps 150-900 lpm 300-2400 bps 24 x 80 char.	No 60 KBS 165 cps 150-900 lpm 300-2400 bps 24 x 80 char.	No 60 KBS 165 cps 150-900 lpm 300-2400 bps 24 x 80 char.	No 60 KBS 165 cps 150-900 lpm 300-2400 bps 24 x 80 char.	No 36K-1.2M bytes 34O cps 30O-900 lpm 40K bps 1920 characters A/D, D/A, Digital I/O, high-speed data interface
SOFTWARE Assembler	Yes	Yes	Yes	Yes	Assembler & macro assembler
Compilers	BASIC	BASIC	BASIC	BASIC	FORTRAN, COBOL,
Operating system	Real-time	Real-time	Real-time	Real-time	Real-time, inter-
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	No No	active, multi-batch No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	7,800  3,500 (32K bytes)	17,700   3,500 (32K bytes)	Contact vendor	17,700 — — 3,500 (32K bytes)	25,100 (128K bytes) 200 See 32/57 Comments 9,300 (128K bytes)
Date of first delivery	1976	1976	1980	1976	September 1979
Number installed to date COMMENTS	Sold only as integral part of turnkey system	50 Sold only as integral part of turnkey system	NA Sold only as integral part of turnkey system	Sold only as integral part of turnkey system	Single Chassis System, memory map, 16MB addressing capability, multiprocessor configurations. Total DBMS, instrumenter I/II, scientific accelerator, plotters and graphics

MANUFACTURER AND MODEL	Systems Engineering Laboratories 32/55	Systems Engineering Laboratories 32/57	Systems Engineering Laboratories 32/77	Systems Engineering Laboratories 32/75	Systems Engineering Laboratories VPS 3200
WORD LENGTH, BITS	32 + 7	32 + 7	32 + 7	32 + 4	32 + 7
NO. WORKSTATIONS SUPPORTED	16	64	64	64	64
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	Core 0.6/0.3 8K/256K Standard No Standard	MOS 0.6/0.3 64K/256K No Standard Standard	MOS 0.6/0.3 64K/4096K No Standard Standard	MOS o.6/0.3 32K/2048K Standard No Standard	MOS 0.6/0.3 64K/4096K No Standard Standard
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	128K PROM 0.6/1.2 Standard Standard Standard No Standard	128K PROM/ROM 0.6/1.2 Standard Standard Standard Optional Standard	128K PROM/ROM 0.6/1.2 Standard Standard Standard Optional Standard	128 PROM/ROM 0.6/1.2 Standard Standard Standard No Standard	128K PROM/ROM See Comments Standard Standard Standard Optional Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 6.67M 16-112	Standard 6.67M 16-112	Standard 6.67M 16-112	Standard 6.67M 16-112	Standard 6.67M 16-112/192
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	16 Opt.; to 40.8K bps Opt.; 38.4K bps	64 Opt.; to 9600 bps Opt.; 38.4 bps Bisync	64 Opt.; to 9600 bps Opt.; 38.4 bps Bisync	64 Opt.; to 9600 bps Opt.; 38.4 bps Bisync	64 Opt.; to 40.8K bps Opt.; 38.4 bps
Network architectures supported RJE terminals emulated IBM 3270 emulation	HASP terminals	HASP terminals	HASP terminals	HASP terminals	HASP terminals
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives  Drum/fixed head disk storage  Magnetic tape cassettes/cartridges	No Both; 10-1200M bytes Fixed-head; 4-16M bytes No	No Both; 10M-19.2G bytes Fixed-head; 5-40M bytes No	No Both; 10M-19.2G bytes Fixed-head; 5-40M bytes No	No Both 10M-19G bytes Fixed-head; 5-40M bytes No	No Both; .1-19.26B bytes Fixed-head; 5-40M bytes No
Magnetic tape cassettes/ cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	36K-1.2M bps 34O cps 30O-900 lpm 40K bps 1920 characters A/D, D/A, digital I/O, high-speed data interface	36K-1.2M bytes 340 cps 300-900 lpm 40K bps 1920 characters A/D, D/A, Digital I/O, high-speed data interface	36-1.2M bytes 340 cps 300-900 lpm 40K bps 1920 characters A/D, D/A, Digital I/O, high-speed data interface	36-1.2M bytes 340 cps 340-900 lpm 40K bps 1920 characters A/D & D/A, Digital I.O, high-speed data interface	36K-1.2M bps 340 cps 300-900 lpm 40K bps 1920 characters A/D, D/A, digital I/O, high speed data interface
SOFTWARE Assembler Compilers	Asembler & macro assembler FORTRAN, COBOL,	Assembler & macro assembler FORTRAN, COBOL,	Assembler & macro assembler FORTRAN, COBOL, BASIC	Assembler & macro assembler FORTRAN, COBOL, BASIC	Assembler & macro assembler FORTRAN, COBOL,
Operating system	BASIC Batch, real-time	BASIC   Real-time, inter-   active, multi-batch	Real-time, inter- active, multi-batch	Real-time, inter- active, multi-batch	Real-time, interactive, multi-/vector batch
Language implemented in firmware Operating system implemented in firmware	No No	FORTRAN RTL (part.)	FORTRAN RTL (part.)	FORTRAN RTL (part.)	FORTRAN RTL (part.) No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$ Discounts available Price of memory increment, \$	53,900 (128K bytes) 375 See 32/57 Cmnts. 6,300 (8K words)	39,500 (256K bytes) 295 See Comments 12,500 (256K bytes)	46,300 (256K bytes) 330 See 32/57 Cmnts. 13,500 (256K bytes)	72,300 (128K bytes) 470 See 32/57 Cmnts. 6,300 (32K bytes)	79,0000 710 See 32/57 Cmnts. 13,500 (256K bytes)
Date of first delivery Number installed to date	October 1975 425	April 1979	June 1978 275	January 1978 325	
COMMENTS	Single or double cabinet systems; multiprocessor configurations; total DBMS; instrumenter I/II, plotters/graphics	Discounts are based on projected point values for equipment purchased during the term of the agreement; OEM, volume end-user, and educational discounts are available	4MB memory in double cabinet, memory map, 16MB addressing capability, multi-processor configurations, TOTAL DBMS, instrumenter I/II, scientific accelerator, internal processing unit, plotters and graphics	Double cabinet system, memory map, 16MB addressing capability, multi-processor configurations, TOTAL DBMS, instrumenter. I/II, scientific accelerator, internal processing unit, plotters and graphics.	Includes a 32/77 CPU for scalar arithmetic and a VPU for vector arithmetic; the VPU can perform two 32-bit floating-point adds and one floating-point multiply in 420 nanoseconds; software includes SNAP II vector processing executive and array processing

MANUFACTURER AND MODEL	Systems Engineering Laboratories VPS 3300	Systems Engineering Laboratories VPS 6400	Tandem Computers T16/1102	Tandem Computers T16/1403	Terak Corporation 8510/a
WORD LENGTH, BITS	32 + 7	32 + 4	16 + 1	16 + 1	16
NO. WORKSTATIONS SUPPORTED	64	64	256	256	8
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.6/0.3 64K/4096K No Standard Standard	MOS 0.6/0.3 64K/4096K No Standard Standard	Core 0.8/0.5 32K/256K Standard No Standard	MOS 0.5/0.5 32K/256K No Standard Standard	MOS RAM 1.2/1.2 64K/64K Standard No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	128K PROM/ROM; 4096 See Comments Standard Standard Optional Standard	128K PROM/ROM; 4096 See Comments Standard Standard Standard Optional Standard	128K PROM; 4K x 32 bits 0.5 Stnadard Optional Standard No Standard	128K PROM; 4K x 32 bits 0.5 Standard Optional Standard Standard Standard	64K  3.5 Standard Standard Standard No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 6.67M 16-112/192	Standard 6.67M 16-112/192	Standard NA 16	Standard NA 16	Standard  2
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	64 Opt.; to 40.8K bps Opt.; 38.4K bps	64 Opt.; to 40.8K bps Opt.; 38.4K bps	256 Opt.; to 80K bps Opt.; 50-19.2K bps —	256 Opt.; to 80K bps Opt.; 50-19.2K bps —	8 Optional Std.; 19.2K bps Several
Network architectures supported RJE terminals emulated IBM 3270 emulation	 HASP terminals 	HASP terminals	NCP 2780/3780, 360/370 —	NCP 2780/3780,360/370 —	DECnet None Optional
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Both; 1-19.268 bytes	No Both; .1-19.26B bytes	No Pack & cartridge; 10M-24M bytes	No Pack & cartridge; 10M-24M bytes	Yes No
Drum/fixed head disk storage	Fixed-head; 5-40M bytes	Fixed-head; 5-40M bytes	No	No	No
Magnetic tape cassettes/cartridges	No	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	36K-1.2M bps 340 cps 300-900 lpm 40K bps 1920 characters A/D, D/A, digital I/O, high-speed data interface	36K-1.2M bps 340 cps 300-900 lpm 40K bps 1920 characters A/D, D/A, digital I/O, high speed data interface	36-120KBS Yes 300-1500 lpm 50-80K bps 80 char. x 24 lines None	36-120 KBS Yes 300-1500 lpm 50-80K bps 80 char. x 24 lines None	No 60, 180 cps No 19.2K bps 1920 characters Plotters, digitizers
SOFTWARE Assembler Compilers	Assembler & macro assembler FORTRAN, COBOL, BASIC	Assembler & macro assembler FORTRAN, COBOL, BASIC	Assembler, macro assembler COBOL, TAL, FORTRAN	Assembler, macro assembler COBOL, TAL, FORTAN	Assembler & macro assembler BASIC, FORTRAN, PASCAL
Operating system  Language implemented in firmware Operating system implemented in firmware	Real-time, inter- active, multi-1 vector FORTRAN RTL (part.) No	Real-time, interac- tive, multi-/vector FORTRAN RTL (part.) No	Multiprocessing, multiprog., virt. Partially Partially	Multiprocessing, multiprog., virt. Partially Partially	Real-time No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu- ration above for on-site contract, \$	85,000 765	137,500	20,400 106	22,000 136	7,850 NA
Discounts available Price of memory increment, \$	See 32/57 Cmnts. 13,500 (256K bytes)	See 32/57 Cmnts. 17,000 (16K x 64-bit)	8,000 (64K bytes)	7,200	Qty., educational NA
Date of first delivery Number installed to date	_	_ _	May 1976 250 + (processors)	May 1976 250 + (processors)	April 1977 Over 700
COMMENTS	Includes a 32/77 CPU for scalar arithmetic and a VPU for vector arithmetic; the VPU can perform four 32-bit floating-point adds and two floating-point multiplications in 420 nanoseconds; software includes SNAP II vector	Includes a 32/77 CPU for scalar arithmetic and a VPU for vector arithmetic; the VPU can per-form form two 64-bit floating point adds and one floating-point multiply in one microsecond; software includes SNAP II vector processing executive and array	Multiprocessor system containing from 2 to 16 CPU's for fault-tolerance; all	Multiprocessor system containing from 2 to 16 CPU's for fault-tolerance; all system components are dual-ported; CPU's have dual buses	Packaged system includes CRT, keyboard, graphic, processor (full memory), etc.; Full DEC "Q" bus and RT/11 software compatibility

MANUFACTURER AND MODEL	Texas Instruments 960B	Texas Instruments 980B	Texas Instruments 990/4	Texas Instruments 990/5
WORD LENGTH, BITS	16 + 6	16 + 6	16 + 1	16 + 1
NO. WORKSTATIONS SUPPORTED	_	-	See Comments	See Comments
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation	MOS 0.75 8K/64K No Standard Standard 64K ROM; 256 x 16 bits 3.6 Optional No	MOS 0.75 8K/64K No Standard Standard 64K ROM; 256 x 16 bits 1.75 Standard No Standard	MOS 0.67/0.67 4K/28K Standard No No 32K No 4.7 Standard No Standard	MOS 0.50/0.50 16K/32K Standard No No 32K No 3.5 Standard No Standard
Battery backup Real-time clock or timer	Optional Optional	Optional Optional	Optional Standard	Optional Standard
NPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.3M 3-2048	Standard 1M 4-32	No 1.5M M	Standard M 16
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	Up to 9600 bps Up to 9600 bps Bisync	1 to 256 Up to 9600 bps No —	See Comments Std.; to 9600 bps Standard Bisync	See Comments Std.; to 9600 bps Standard Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	None IBM 360/370 No	None Any RS-232C/20mA No	 IBM 2780/3780 No	— IBM 2780/3780 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	No Cartridge & pack; 2.28-392M bytes	No Cartridge & pack; 2.28-392M bytes	242-968K bytes No	242K-4M bytes 10M-200M bytes
Drum/fixed head disk storage	No	No	No	No
Magnetic tape cassettes/cartridges  Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	Cassette; 120 cps 30 KBS 30-330 cps No 110-9600 bps 80 char. x 24 lines Process control interfaces, A/D &	Cassette; 120 cps 30 KBS 30-330 cps No 110-9600 bps 80 char. x 24 lines Paper tape units	Cassette; 120 cps  No 180 cps 300-600 lpm 75-9600 bps 1920 char. PROM programmer, A/D & D/A con-	Cassette; 120 cps 30-60 KBS 180 cps 300-600 lpm 75-9600 bps 1920 char. PROM programmer, A/D & D/A converter
SOFTWARE Assembler Compilers	D/A converters  Assembler & macro preprocessor FORTRAN	Assembler & macro preprocessor FORTRAN, BASIC	verters Yes FORTRAN	Yes FORTRAN, BASIC
Operating system  Language implemented in firmware Operating system implemented in firmware	Single-user, real- time, multiprgrming. No No	Single-user, real- time, multilprgrming. No No	Real-time, multi- task No No	Real-time, multi-task No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint, of basic configu- ration above for on-site contract, \$	4,850 (8K words)	5,650 (8K words) 95	1,925 (4K words)	3,400 (16K words)
Discounts available Price of memory increment, \$	1,400 (8K MOS)	1,400 (8K MOS)	2,050 (16K words)	750 (16K words)
Date of first delivery Number installed to date	May 1974 Over 4,100	May 1974 Over 4,400	March 1976 NA	April 1979 NA
COMMENTS	Heavily supported for process control applications	3.5. 4.50	Based on TI's TMS 990 microprocessor; num. of work- stations & lines are a function of application	Based on TI's TMS 990 microprocessor; num. of work- stations & lines are a function of application & memory sizes
			a function of	a function of application &

MANUFACTURER AND MODEL	Texas Instruments 990 / 10	Texas Instruments 990/12	Wang PCS II	Wang 2200 VP/MVP
WORD LENGTH, BITS	16+6	16 + 6	8-bit byte	8-bit byte
NO. WORKSTATIONS SUPPORTED	See Comments	See Comments	1	4-8
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parror correction Storage protection	MOS 0.67/0.67 64K/1,048K No Standard Standard	MOS/cache .74, .15/.50, .15 128K/1,048K No Standard Standard	MOS 1.6 8K/32K bytes No No No	MOS 0.6 16K/256K bytes No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	32K No 3.6 Standard No Standard Optional Standard	32K No .552 Standard Standard Standard No Standard	No ROM; 425K bytes 800 Standard Standard Standard No Optional	No ROM; 48K words 13 Standard Standard Standard No Optional
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 3M 16	Standard 3M 16	No 10K None	No 100K None
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	See Comments Std.; to 9600 bps Standard Bisync	See Comments Std.; to 9600 bps Standard Bisync	 Optional Up to 9600 bps Bisync	9 Up to 9600 bps Up to 9600 bps Bisync
Network architectures supported RJE terminals emulated IBM 3270 emulation	 IBM 2780/3780 Yes	 IBM 2780/3780 Yes		=
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	242K-4M bytes 10M-800M bytes	242K-4M bytes 10M-800M bytes	89-178K bytes No	262-786K bytes Cartridge 12-20M bytes
Drum/fixed head disk storage	No	No	No	No
Magnetic tape cassettes/cartridges	Cassette; 120 cps	Cassette; 120 cps	Cassette; 326 bps	Cassette; 326 bps
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	30-60 KBS 180 cps 300-600 lpm 75-9600 bps 1920 char. Prom programmer A/D & D/A converters	30-60 KBS 180 cps 300-600 lpm 75-9600 bps 1920 char. Prom programmer A/D & D/A converters	No 200 cps 600 lpm To 9600 bps 64 char. x 16 lines Plotter	10 KBS 200 cps 600 lpm To 9600 bps 64 char. x 16 lines Paper tape reader, paper tape punch, card punch, lotter
SOFTWARE Assembler	Assembler &	Assembler &	No	No
Compilers	macro assembler FORTRAN, BASIC,	macro assembler FORTRAN, BASIC,	BASIC, BASIC-2	BASIC, BASIC-2
Operating system	COBOL, PASCAL, RPG II Real-time, multi-task	COBOL, PASCAL, RPG II Real-time, multi-task	None	None
Language implemented in firmware Operating system implemented in firmware	No No	No No	Fully NA	Fully ·
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu-	14,675 (64K words)	29,050 (128K words)	4,800 50	9,000 (16K bytes)
ration above for on-site contract, \$ Discounts available Price of memory increment, \$	6,250 (128K words)	6,250 (128K words)		
Date of first delivery Number installed to date	March 1976	September 1979 NA	March 1977 25,000 + (all mod.)	November 1977 25,000 + (all mod.)
COMMENTS	MSI implementation of 990 instruction set; Disk Oper. Sys.; num. of workstations & lines are a function of application & memory sizes	SCHOTTKY implementa- tion of 990 instruction set; num. of worksta- tions & line are a function of application & memory sizes	Portable computer weighing 62 lbs. Information taken from report dated April 1979	Information taken from report dated April 1979

MANUFACTURER AND MODEL	Wang 2200T	Wang 2200 VS	Warrex Centurion 100	Warrex Centurion 200
WORD LENGTH, BITS	8-bit byte	32	8	8
NO. WORKSTATIONS SUPPORTED	4	32	2	4
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 1.6 16K/32K bytes No No No	MOS/cache 0.66 128K/2048K bytes Standard Standard Standard	MOS 0.8/0.2 16K/64K No No No	MOS 0.8/0.2 16K/64K No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	No ROM; 425K words 800 Standard Standard Standard No No	512K bytes Standard Standard No Optional	64K PROM; 7,512 x 8 3.0 No No No No Standard	64K PROM; 7,512 x 8 3.0 No No No No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	No 10K None	Standard — 5	Standard 1.25M 0-16	Standard 1.25M 0-16
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	3 Up to 9600 bps Up to 9600 bps Bisync	16 No Up to 9600 bps Bisync	2 No Std.; 300 bps None	4 No Std.; 300 bps None
Network architectures supported RJE terminals emulated IBM 3270 emulation		2780/3780, HASP No	None None No	None None No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	262-786K bytes Cartridge; 12-20M bytes	3154K bytes Removable; to 90M bytes	600K-3.6M bytes No	No Both; 10.4-20.8M bytes
Drum/fixed head disk storage	No	Fixed; 288M bytes	No	No
Magnetic tape cassettes/cartridges	Cassette; 326 bps	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	10 KBS 200 cps 250 lpm To 9600 bps 64 char. x 16 lines Paper tape reader, paper tape punch, card punch, plotter	120 KBS 30, 120, 200 cps 250-600 lpm To 9600 bps 80 char. x 16 lines None	No 65-600 cps 125-500 lpm No 80 char. x 24 lines Any RS-232C	No 65-600 cps 125-600 lpm No 80 char. x 24 lines Any RS-232C
SOFTWARE Assembler	No	Assembler &	Yes	Yes
Compilers	BASIC, BASIC-2	macro assembler BASIC, COBOL, RPG II,	CPL SMART	CPL SMART
Operating system	None	PL/1, FORTRAN Interactive, virtual-	Time-sharing	Time-sharing
Language implemented in firmware Operating system implemented in firmware	Fully NA	storage, multi-user Fully Partially	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configu-	5,000 (16K bytes)	19,000 (128K bytes)	6,339	12,766
ration above for on-site contract, \$ Discounts available				_
Price of memory increment, \$	2,200 (16K bytes)	7,000 (128K bytes)	2,060 (32K)	2,060 (32K)
Date of first delivery Number installed to date	February 1975 25,000 + (all mod.)	December 1977 NA	First qtr. 1979 150	Second qtr. 1979 100
COMMENTS	Also available in packaged systems WCS-20 & WCSI-30. Information taken from report dated April 1979	Packaged systems includes 128KB memory, one 308KB floppy disk, 7-slot chassis, cabinets, operating system, resource management software, and choice of one language	English oriented JCL; large selection of applications	English oriented JCL; large selection of applications; spooled sprint

MANUFACTURER AND MODEL	Warrex Centurion III	Warrex Centurion Series 6000	Xerox Diablo 3000	Xerox Diablo 3200
WORD LENGTH, BITS	8	8	8 + parity	8 + parity
NO. WORKSTATIONS SUPPORTED	8	32	1	9
MAIN STORAGE Storage type Cycle/access time Min./Max. capacity, words Parity checking Error correction Storage protection	MOS 0.8/0.2 32K/64K No No	MOS 0.8/0.2 64K/256K Standard No Standard	MOS .41/.25 32K/64K Standard No No	MOS .488/.30 24K/64K Standard No No
CENTRAL PROCESSOR No. of directly addressable words Control storage Add time, microseconds Hardware multiply/divide Hardware floating point Hardware byte manipulation Battery backup Real-time clock or timer	64K PROM; 7,512 x 8 3.0 No No No No No Standard	64K PROM; 14, 1024 x 18 1.6 Standard No Standard No Standard	64K ROM; 2K 16.7 (6 digits) No No Standard No Standard	64K ROM; 1K 23.9 (6 digits) No No Standard No Standard
INPUT/OUTPUT CONTROL Direct memory access channel Maximum I/O rate, words/sec. No. of external interrupt levels	Standard 1.25M 0-16	Standard 1.25 0-16	Standard 1.5M 8	Standard 1 M 8
COMMUNICATIONS Maximum number of lines Synchronous Asynchronous Protocols supported	4 No Std.; 300 bps None	8 Opt.; 1.2-9.6K bps Std.; 300 bps Bisync	O No No None	9 Opt.; up to 9600 bps Opt.; up to 9600 bps None
Network architectures supported RJE terminals emulated IBM 3270 emulation	None None No	None IBM 2780/3780 No	None None No	None IBM 2780 No
PERIPHERAL EQUIPMENT Floppy disk (diskette) drives Disk pack/cartridge drives	600K-3.6M bytes Both; 10.4-81.6M bytes	600K-3.6M bytes Both; 10.4-72,000M bytes	1M-2.5M bytes No	1M-5M byte Cartridge; 10-20M bytes
Drum/fixed head disk storage	No	No	No	No
Magnetic tape cassettes/cartridges	No	No	No	No
Magnetic tape, ½-inch Serial printer Line printer Data communications interface CRT Other supported peripheral units	No 65-600 cps 125-600 lpm ivo 80 char. x 24 lines Any RS-232C	72 KBS 65-600 cps 125-600 lpm 1.2-9.6K bps 80 char. x 24 lines Any RS-232C	No 40, 45, or 200 cps No No 1920 characters	No 40, 45, or 200 cps No Up to 9600 bps 1920 characters
SOFTWARE Assembler	Yes	Yes	Global assembler	Global assembler
Compilers	CPL SMART	CPS SMART, BASIC,	DACL* (English-like	DACL (English-like
Operating system	Time-sharing	COBOL Time-sharing	compiler) Batch, interactive	compiler) Batch, interactive, time-sharing
Language implemented in firmware Operating system implemented in firmware	No No	No No	No No	No No
PRICING & AVAILABILITY Price of CPU, power supply, frt. panel, and minimum memory in chassis, \$ Monthly maint. of basic configuration above for on-site contract, \$ Discounts available Price of memory increment, \$	15,652   2,060 (32K)	19,712 — — 2,060 (32K)	15,950 (w/printer) On-call only  Various	18,950 (w/printer) On-call only  Various
Date of first delivery	1975 900 +	Third qtr. 1979	October 1979 NA	December 1976 NA
Number installed to date  COMMENTS	English oriented JCL; large selection of applications; spooled print	English oriented JCL; large selection of applications; print sub-systems; CRT security, disk security, dynamic memory allocation; up to 64 partitions	*DACL compiler language is a high- level English like language source statement compiler	Diablo systems are manufactured by Xerox and distributed worldwide; in the U.S. it is exclusively distributed by Shasta General Systems