

# LSI11

## FIS DIAGNOSTIC MD-11-DVKAC-A

EP-DVKAC-A-DL-A  
COPYRIGHT © 1976  
FICHE 1 OF 1

OCT 1976  
**digital**  
MADE IN USA

The image displays a grid of 128 small diagnostic tables, arranged in 16 rows and 8 columns. Each table contains technical data, likely related to the LSI11 processor, including memory addresses and status indicators. The tables are organized into a structured layout, with each cell containing a small table of data. The data appears to be diagnostic information for the LSI11, possibly related to memory or control logic. The tables are arranged in a grid that covers most of the page, with a large blank area on the right side.

A small table located in the bottom right corner of the page, containing a few lines of data. It appears to be a continuation of the diagnostic information or a summary table.

DVAKCA MACY11 27 732 24-AUG-76 15:37  
 DVAKCA.SAC TABLE OF CONTENTS

5290	SWITCH OPTIONS AND ASSIGNMENTS
5300	ACT11 WORKS
5304	VECTOR A-D, STACKS, ANSWER AREA, AND SETUP ROUTINE
5309	RPT MACY = X-ETABLE
5340	RPT P. ENTER BLOCK
7106	STARTING OF THE PROGRAM
7144	FADD TEST SECTION
7161	TEST FLOATING ADD INSTRUCTION WITH UNDERFLOW
7166	TEST FLOATING ADD INSTRUCTION WITH OVERFLOW
7171	FSUB TEST SECTION
7183	TEST FLOATING SUB. INSTRUCTION WITH UNDERFLOW
7187	TEST FLOATING SUB. INSTRUCTION WITH OVERFLOW
7191	FML TEST SECTION
7204	TEST FLOATING MUL. INSTRUCTION WITH UNDERFLOW
7208	TEST FLOATING MUL. INSTRUCTION WITH OVERFLOW
7212	FDIV TEST SECTION
7221	TEST FLOATING DIV. INSTRUCTION WITH UNDERFLOW
7225	TEST FLOATING DIV. INSTRUCTION WITH OVERFLOW
7229	TEST FLOATING DIV. INSTRUCTION FOR DIVIDE BY ZERO
7235	TEST OF ALL FIS AT ONCE
7240	ADDRESS ERROR TEST
7248	INTERUPT ABORT TEST SECTION
7258	END OF PASS ROUTINE
7268	SCOPE ROUTINE
7270	PUSH AND POP SUBROUTINES
7445	HLT ROUTINE (ERROR TYPEOUT)
7451	USER ERROR ROUTINE
7463	OCTAL WORD & ADDRESS TYPERS
7465	POWER DOWN AND UP ROUTINES
7467	ASCIZ TYPE OUT ROUTINE

CO1

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52  
DVKACA.SRC

SEQ 0002

5046

DVKACA.SRC

5048  
5049  
5055  
5056  
5057  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
5058  
5065  
5069  
5083  
5084  
5098  
5131  
5136  
5141  
5149  
5180  
5225  
5278  
5282  
5283  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
5284  
5285  
5286  
5287  
5288  
5289  
5290  
5291  
5292  
5293  
5294  
5295

000001  
160000

```

ABS
.MCALL SEOP,HEADER,SACT11,.SAPTBL5,.SAPTHDR,.SETUP,STARS
.MCALL PUSH,POP,.SPOWER
.TITLE DVKACA
.*COPYRIGHT (C) AUGUST 1975
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY PERVEZ ZAKI
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE POP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZQAC-B),JULY 11,1975.
.*
$TN=1
$SWR=160000      ;;HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYP0UT

```

SWITCH	USE
8	LOOP ON TEST IN SW<7:0>
9	LOOP ON ERROR
10	1 - BELL ON ERROR
11	INHIBIT ITERATIONS
12	INHIBIT TRACE TRAP
13	INHIBIT ERROR TYPEOUTS
14	LOOP ON TEST
15	HALT ON ERROR

```

ERROR MESSAGE FORMAT:
ERRNM  ADR  PSW  SP  ANS1 ANS2 ANS3 ANS4 ANS5 ANS6

WHERE ERRNM= ERROR NUMBER
      ADR = ADDRESS OF "HLT" INSTRUCTION + 2
      PSW = PROCESSOR STATUS WORD
      SP = STACK POINTER
      ANS1 THRU ANS6 = DATA OFF THE STACK(S)
NOTE: ANS1 THRU ANS6 ARE NOT ALWAYS TYPED, DEPENDING ON THE
      NUMBER ADDED TO THE "HLT". "HLT" ALONE TYPES NONE.
      "HLT+1" TYPES ANS1, "HLT+2" TYPES ANS1 AND ANS2, ETC.

```

```

5296      104000      HLT=      ENT
5297      000000      R0=      X0
5298      000001      R1=      X1
5299      000002      R2=      X2
5300      000003      R3=      X3
5301      000004      R4=      X4
5302      000005      R5=      X5
5303      000005      TTY=     X5
5304      000006      SP=      X6
5305      000007      PC=      X7
5306      000024      PWRVFC= 24
5307      104400      SCOPE=   TRAP
5308      100000      SW15=   100000
5309      040000      SW14=   40000
5310      020000      SW13=   20000
5311      010000      SW12=   10000
5312      004000      SW11=   4000
5313      002000      SW10=   2000
5314      001000      SW09=   1000
5315      000400      SW08=   400
5316      000004      TYPE=   IOT
5317      000001      N=      1
5318      000001      SF=     1
5319      ;*****
5320
5321      000000      .=      0      ;TRAP CATCHER FROM 0 - 776
5322
5323      ;*****
5324
5325      .SBTTL ACT11 HOOKS
5326      ;HOOKS REQUIRED BY ACT11
5327      $SVPC=.      ;SAVE PC
5328      .=46
5329      SENDAD      ;;1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP
5330      .=52
5331      .WORD 0      ;;2)SET LOC.52 TO ZERO
5332      .=$SVPC      ;; RESTORE PC

```

```

5337
5338      000400      . =      400
5339      ;*****
(1)      .SBTTL  APT MAILBOX-ETABLE
(1)
(1)
(1)
(1)
(1)
(1)      000400      .EVEN
(1)      000400      000000      $MAIL:      :: APT MAILBOX
(1)      000402      000000      $MSGTY: .WORD  AMSGTY  :: MESSAGE TYPE CODE
(1)      000404      000000      $FATAL: .WORD  AFATAL  :: FATAL ERROR NUMBER
(1)      000406      000000      $TESTN: .WORD  ATESTN  :: TEST NUMBER
(1)      000410      000000      $PASS:  .WORD  APASS   :: PASS COUNT
(1)      000412      000000      $DEVCT: .WORD  ADEVCT  :: DEVICE COUNT
(1)      000414      000000      $UNIT:  .WORD  AUNIT   :: I/O UNIT NUMBER
(1)      000416      000000      $MSGAD: .WORD  AMSGAD  :: MESSAGE ADDRESS
(1)      000420      000000      $MSGLG: .WORD  AMSGLG  :: MESSAGE LENGTH
(1)      000420      000      $ETABLE:      :: APT ENVIRONMENT TABLE
(1)      000421      000      $ENV:      .BYTE  AENV   :: ENVIRONMENT BYTE
(1)      000422      000000      $ENVH:      .BYTE  AENVH  :: ENVIRONMENT MODE BITS
(1)      000424      000000      $SWREG: .WORD  ASWREG  :: APT SWITCH REGISTER
(1)      000426      000000      $USWR:  .WORD  AUSWR   :: USER SWITCHES
(1)
(1)      $CPUOP: .WORD  ACPUOP :: CPU TYPE, OPTIONS
(1)      ;*
(1)      ;*      BITS 15-11=CPU TYPE
(1)      ;*      11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
(1)      ;*      11/70=06, PD0=07, 0=10
(1)      ;*
(1)      ;*      BIT 10=REAL TIME CLOCK
(1)      ;*      BIT 9=FLOATING POINT PROCESSOR
(1)      ;*      BIT 8=MEMORY MANAGEMENT
(1)      000430      $ETEND:
(1)      .MEXIT
5340      ;*****
(1)      .SBTTL  APT PARAMETER BLOCK
(1)      ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
(1)      ;*****
(1)      000430      .SX=      :: SAVE CURRENT LOCATION
(1)      000024      =24      :: SET POWER FAIL TO POINT TO START OF PROGRAM
(1)      000024      200      :: FOR APT START UP
(1)      000044      =44      :: POINT TO APT INDIRECT ADDRESS PNTR.
(1)      000044      $APTHDR :: POINT TO APT HEADER BLOCK
(1)      000430      =.SX      :: RESET LOCATION COUNTER
(1)      ;*****
(1)      ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-POP11 DIAGNOSTIC
(1)      ;INTERFACE SPEC.
(1)
(1)      000430      $APTHD:
(1)      000430      000000      $SHIBTS: .WORD  0      :: TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
(1)      000432      000400      $MBAOR: .WORD  $MAIL  :: ADDRESS OF APT MAILBOX (BITS 0-15)
(1)      000434      000003      $STSM:  .WORD  3      :: RUN TIM OF LONGEST TEST
(1)      000436      000005      $PASTM: .WORD  5      :: RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
(1)      000440      000000      $UNITM: .WORD      :: ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
(1)      000442      000014      .WORD  SETEND-$MAIL/2 ;; LENGTH MAILBOX-ETABLE(WORDS)
(1)      000430      =      $APTHD
5341
5342
HLTADS:

```

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-4  
 DVKACA.SRC APT PARAMETER BLOCK

```

5343          000432          HLTADS+2
5344 000432          SPSW:  =      ;PROCESSOR STATUS WORD
5345          000434          =      SPSW+2
5346 000434          SSP:   =      ;STACK POINTER
5347          000436          =      SSP+2
5348 000436          ANS1:  =      ;FIRST ANSWER (SEE CODE)
5349          000440          =      ANS1+2
5350 000440          ANS2:  =      ANS2+2
5351          000442          =      ANS2+2
5352 000442          ANS3:  =      ANS3+2
5353          000444          =
5354 000444          ANS4:  0
5355 000446          ANS5:  0
5356 000450          ANS6:  0
5357 000452          000000 000000 0,0,0,0 ;NON-%6 STACK BUFFER
          000460          000000
5358 000462          000000 ERRORS: 0
5359 000464          000244 FISVEC: 244 ;FIS TRAP VECTOR ADDRESS
5360 000466          000246 FISLVL: 246
5361 000470          000000 LADS: 0
5362 000472          006412 000 RETURN: .ASCIZ <12><15> ;RETURN AND LINEFEED
5363 000475          015 020012 020040 SPACE: .ASCIZ <15><12> ;RETURN AND 3 SPACES
          000502          000
5364 000503          000 SICNT: .BYTE 0
5365          .EVEN
5366 000504          000007 SBELL: .WORD 7 ;RING A BELL
5367 000506          000000 SAVTPS: 0 ;LOC TO SAVE TELEPRINTER STATUS
5368 000510          000000 STACK0: 0 ;NON-%6 STACK NORMAL LIMIT
5369 000512          000000 STACK2: 0
5370 000514          000000 STACK4: 0
5371 000516          000000 STACK6: 0
5372 000520          000000 000000 000000 STACK8: 0,7,0,0,0 ;NON-%6 STACK BUFFER
          000526          000000
5373 000532          000000 STAK10: 0
5374          000511 STACK1 = STACK0+1
5375 000534          000000 TEMP: 0
5376 000536          000000 TIMES: 0
5377 000540          000000 TYPCNT: 0
5378 000542          000006 YESRT: RTT ;RETURN FROM TRACE TRAP
5379 000544          000000 .PR: 0 ;COUNT AND SWITCH
5380 000546          000064 TTYOUT: 64
5381 000550          177564 STPS: 177564 ;TTY PRINTER STATUS REG.
5382 000552          177566 STPB: 177566 ;TTY PRINTER BUFFER REG.
    
```

# H01

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-5  
 DVKACA.SRC APT PARAMETER BLOCK

SEQ 0007

```

7104 ;*****
7105
7106 .SBTTL STARTING OF THE PROGRAM
7107
7108
7109      000200      200
7110 000200 012767 000001 000330      =      MOV      #1,TIMES      ;NUMBER OF ITERATIONS IN THE FIRST PASS=1
7111 000206 012700 000410      MOV      #SDEVCT,R0      ;PREPARE TO INITIALIZE THE PROGRAM
7112 000212 005040      2$:      CLR      -(R0)
7113 000214 022700 000400      CMP      #SMAIL,R0
7114 000220 001374      BNE      2$
7115
7116 000222 000167 000352      RESTRT: JMP      BEGIN      ;JUMP TO STARTING ADDRESS OF PROGRAM
7117
7118
7119
7120      000600      600
7121      =
7122 000600 012706 000600      BEGIN: MOV      #BEGIN, SP      ;INITIALIZE STACK POINTER
7123 000604 012737 000542 000014      MOV      #YESRT, @#14      ;SET TRACE TRAP VECTOR
7124 000612 012737 016346 000020      MOV      #STYPE, @#20      ;SET UP VECTOR 20
7125 000620 012737 016206 000024      MOV      #SPWRDN, @#24      ;SERVICE POWER DOWN ROUTINE FOR ANY FUTURE
7126                                     ;POWER DOWN
7127 000626 012700 000030      MOV      #30, R0      ;SET R0 TO VECTOR 30
7128 000632 012720 015642      MOV      #HLTS, (0)+      ;SET EMT VECTOR
7129 000636 012720 000340      MOV      #340, (0)+
7130 000642 012720 015074      MOV      #SCOPES, (0)+      ;SET TRAP VECTOR
7131 000646 012710 000340      MOV      #340, (0)
7132 000652 012737 000006 000004 1$:      MOV      #6, @#4      ;RESTORE TIME-OUT VECTOR
7133 000660 132737 000001 000420      BITB      #1,@#SENV      ;ARE WE UNDER APT ?
7134 000666 001410      BEQ      2$      ;IF NOT THEN GO TO 2$
7135 000670 012700 000554      MOV      #STPB+2,R0      ;OTHERWISE SET FOR THE OTHER SLU
7136 000674 012740 176566      MOV      #176566,-(R0)
7137 000700 012740 176564      MOV      #176564,-(R0)
7138 000704 012740 000074      MOV      #74,-(R0)
7139 000710 005067 177470      2$:      CLR      $TESTN
7140 000714 005067 177550      CLR      LAOS      ;CLEAR LOOP ADDRESS
7141
7142

```































DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-19  
 DVKACA.SRC TEST FLOATING ADD INSTRUCTION WITH UNDERFLOW

SEQ 0021

```

(1)          :TEST 16:      FADD (LSI-11 FLOATING ADD INSTRUCTION)
(1)          :          100200,000000 + 000377,177777 ==> UNDERFLOW
(1)          :          PS(ON STACK) = 012,      STACK POINTER = R3
(1)          :*****
(1)          :
(1) 003224 104400          SCOPE
(1) 003226 004567 012132  TST16: JSR      R5,      PUSHR   ; PUSH 4 WORDS ONTO R3 STACK, SET PRIORITY
(1) 003232 000377 177777   .WORD    000377,177777 ; SECOND OPERAND ON TOP
(1) 003236 100200 000000   .WORD    100200,000000 ; FIRST OPERAND ON BOTTOM
(1) 003242 000157          .WORD    157             ; PROCESSOR PRIORITY LEVEL
(1) 003244 003276 000000   .WORD    ISR16, 000      ; FIS TRAP VECTOR
(1) 003250 012703 000510   MOV      #STACK0,R3    ; SET UP R3 AS STACK POINTER
(1)
(1) 003254 000240          NOP
(1) 003256 075003          FADD     R3             ; FLOATING ADD ON THE R3 STACK
(1)
(1) 003260 004767 012132  RTA16: JSR      PC,      POPR    ; POP THE "ANSWER"
(1) 003264 010367 175144   MOV      R3,      $SP     ; SAVE STACK POINTER (R3)
(1) 003270 104002          HLT+2    ; FIS TRAP DIDN'T OCCURE!
(3) 003272 000103          103      ; THE ERROR NUMBER IS 103
(1) 003274 000462          BR       END16
(1)
(1) 003276 004767 012144  ISR16: JSR      PC,      POPER   ; POP ALL DATA OFF THE STACKS
(1) 003302 010367 175126   MOV      R3,      $SP     ; SAVE STACK POINTER (R3)
(1) 003306 105767 175120   TSTB    $PSW           ; CHECK PS AFTER FIS TRAP
(1) 003312 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003314 104000          HLT     ; PS AFTER FIS TRAP NOT EQUAL TO 000
(3) 003316 000104          104      ; THE ERROR NUMBER IS 104
(1)
(1) 003320 022767 000510 175106  CMP     #STACK0,$SP    ; CHECK THE STACK POINTER (R3)
(1) 003326 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003330 104000          HLT     ; STACK POINTER (R3) NOT EQUAL TO #STACK0
(3) 003332 000105          105      ; THE ERROR NUMBER IS 105
(1)
(1) 003334 022767 003260 175074  CMP     #RTA16,ANS1    ; CHECK FIS TRAP RETURN ADDRESS
(1) 003342 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003344 104001          HLT+1   ; FIS TRAP AT WRONG ADDRESS
(3) 003346 000106          106      ; THE ERROR NUMBER IS 106
(1)
(1) 003350 022767 000012 175062  CMP     #012,ANS2     ; CHECK PS BEFORE FIS TRAP
(1) 003356 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003360 104002          HLT+2   ; PS AT FIS TRAP TIME NOT 012
(3) 003362 000107          107      ; THE ERROR NUMBER IS 107
(1)
(1) 003364 022767 000377 175050  CMP     #000377,ANS3   ; CHECK DATA FROM THE STACK
(1) 003372 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003374 104004          HLT+4   ; DATA ON STACK (000377) CHANGED
(3) 003376 000110          110      ; THE ERROR NUMBER IS 110
(1)
(1) 003400 022767 177777 175036  CMP     #177777,ANS4   ; CHECK DATA FROM STACK
(1) 003406 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003410 104004          HLT+4   ; DATA ON STACK (177777) CHANGED
(3) 003412 000111          111      ; THE ERROR NUMBER IS 111
(1)
(1) 003414 022767 100200 175024  CMP     #100200,ANS5   ; CHECK DATA FROM STACK
(1) 003422 001402          BEQ     .+6             ; BRANCH IF OK

```







```

(1) 003644 022767 000200 174574      CMP      #000200,ANS5 ;CHECK DATA FROM STACK
(1) 003652 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 003654 104006                      HLT+6    124         ;DATA ON STACK (000200) CHANGED
(3) 003656 000124                      124         ;THE ERROR NUMBER IS 124
(1)
(1) 003660 005767 174564      TST      ANS6        ;CHECK DATA FROM STACK
(1) 003664 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 003666 104006                      HLT+6    125         ;DATA ON STACK (000000) CHANGED
(3) 003670 000125                      125         ;THE ERROR NUMBER IS 125
(1)
(1) 003672 122767 000017 174504 END17: CMPB     #17,    $TESTN ;CHECK THE TEST NUMBER
(1) 003700 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 003702 104000                      HLT      126         ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 003704 000126                      126         ;THE ERROR NUMBER IS 126
(1)
(1)

```







```

(1) 004332 022767 077652 174106      CMP      #077652,ANS5 ;CHECK DATA FROM STACK
(1) 004340 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 004342 104006                      HLT+6    ;DATA ON STACK (077652) CHANGED
(3) 004344 000150                      150      ;THE ERROR NUMBER IS 150
(1)
(1) 004346 022767 125253 174074      CMP      #125253,ANS6 ;CHECK DATA FROM STACK
(1) 004354 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 004356 104006                      HLT+6    ;DATA ON STACK (125253) CHANGED
(3) 004360 000151                      151      ;THE ERROR NUMBER IS 151
(1)
(1) 004362 122767 000021 174014 END21: CMPB     #21,    $TESTN ;CHECK THE TEST NUMBER
(1) 004370 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 004372 104000                      HLT      ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 004374 000152                      152      ;THE ERROR NUMBER IS 152
(1)
(1)

```









7176  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)

\*\*\*\*\*  
 TEST 25: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)  
 000425,052525 - 000252,125252 = 000200,000000  
 PS = 200, STACK POINTER = R4  
 \*\*\*\*\*

```

005026 104400                SCOPE
(1) 005030 004567 010330  TST25: JSR    R5,      PUSHR   ;PUSH 4 WORDS ONTO R4 STACK, SET PRIORITY
(1) 005034 000252 125252      .WORD  000252,125252   ;SECOND OPERAND ON TOP
(1) 005040 000425 052525      .WORD  000425,052525   ;FIRST OPERAND ON BOTTOM
(1) 005044 000217      .WORD  217              ;PROCESSOR PRIORITY LEVEL
(1) 005046 015634 000340      .WORD  TRAPER, 340      ;FIS TRAP VECTOR
(1) 005052 012704 000510      MOV    #STACK0,R4      ;SET UP STACK POINTER
(1)
(1) 005056 000240                NOP
(1) 005060 075014                FSUB   R4                ;FLOATING SUBTRACT ON THE R4 STACK
(1)
(1) 005062 004767 010330      JSR    PC,      POPR    ;POP THE ANSWER
(1) 005066 010467 173342      MOV    R4,      $SP    ;SAVE "STACK POINTER"
(1) 005072 122767 000200 173332  CMPB   #200,    $PSW    ;CHECK PS (EXCEPT T BIT)
(1) 005100 001402      BEQ    .+6          ;BRANCH IF OK
(1) 005102 104000      HLT    ;PS NOT EQUAL TO 200
(3) 005104 000172      HLT    172          ;THE ERROR NUMBER IS 172
(1)
(1) 005106 022767 000514 173320  CMP    #STACK4,$SP    ;CHECK THE STACK POINTER (R4)
(1) 005114 001402      BEQ    .+6          ;BRANCH IF OK
(1) 005116 104000      HLT    ;STACK POINTER (R4) NOT EQUAL TO #STACK4
(3) 005120 000173      HLT    173          ;THE ERROR NUMBER IS 173
(1)
(1) 005122 022767 000200 173306  CMP    #000200,ANS1   ;CHECK FIRST HALF OF ANSWER
(1) 005130 001402      BEQ    .+6          ;BRANCH IF OK
(1) 005132 104002      HLT+2 ;ANS1 NOT EQUAL TO 000200
(3) 005134 000174      HLT    174          ;THE ERROR NUMBER IS 174
(1)
(1) 005136 005767 173276      TST   ANS2          ;CHECK SECOND HALF OF ANSWER
(1) 005142 001402      BEQ    .+6          ;BRANCH IF OK
(1) 005144 104002      HLT+2 ;ANS2 NOT EQUAL TO 000000
(3) 005146 000175      HLT    175          ;THE ERROR NUMBER IS 175
(1)
(1) 005150 122767 000025 173226 END25: CMPB   #25,      $TESTN  ;CHECK THE TEST NUMBER
(1) 005156 001402      BEQ    .+6          ;BRANCH IF OK
(1) 005160 104000      HLT    ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 005162 000176      HLT    176          ;THE ERROR NUMBER IS 176
  
```



7178  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)

\*\*\*\*\*  
 TEST 27: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)  
 035152,125252 - 043125,052525 = 143125,052524  
 PS = 010, STACK POINTER = R3  
 \*\*\*\*\*

```

005320 104400
005322 004567 010036
005326 043125 052525
005332 035152 125252
005336 000147
005340 015634 000340
005344 012703 000510

005350 000240
005352 075013

005354 004767 010036
005360 010367 173050
005364 122767 000010 173040
005372 001402
005374 104000
005376 000204

005400 022767 000514 173026
005406 001402
005410 104000
005412 000205

005414 022767 143125 173014
005422 001402
005424 104002
005426 000206

005430 022767 052524 173002
005436 001402
005440 104002
005442 000207

005444 122767 000027 172732 END27:
005452 001402
005454 104000
005456 000210
    
```

```

SCOPE
TST27: JSR   R5,    PUSH4   ;PUSH 4 WORDS ONTO R3 STACK, SET PRIORITY
        .WORD 043125,052525 ;SECOND OPERAND ON TOP
        .WORD 035152,125252 ;FIRST OPERAND ON BOTTOM
        .WORD 147           ;PROCESSOR PRIORITY LEVEL
        .WORD TRAPER, 340    ;FIS TRAP VECTOR
        MOV   #STACK0,R3    ;SET UP STACK POINTER

NOP
FSUB    R3                  ;FLOATING SUBTRACT ON THE R3 STACK

JSR    PC,    POPR         ;POP THE ANSWER
MOV    R3,    $SP         ;SAVE "STACK POINTER"
CMPB  #010,   $PSW        ;CHECK PS (EXCEPT T BIT)
BEQ   .+6,          ;BRANCH IF OK
HLT   .+6            ;PS NOT EQUAL TO 010
                        ;THE ERROR NUMBER IS 204

CMP    #STACK4,$SP        ;CHECK THE STACK POINTER (R3)
BEQ   .+6,          ;BRANCH IF OK
HLT   .+6            ;STACK POINTER (R3) NOT EQUAL TO #STACK4
                        ;THE ERROR NUMBER IS 205

CMP    #143125,ANS1       ;CHECK FIRST HALF OF ANSWER
BEQ   .+6,          ;BRANCH IF OK
HLT+2 .+6            ;ANS1 NOT EQUAL TO 143125
                        ;THE ERROR NUMBER IS 206

CMP    #052524,ANS2       ;CHECK SECOND HALF OF ANSWER
BEQ   .+6,          ;BRANCH IF OK
HLT+2 .+6            ;ANS2 NOT EQUAL TO 052524
                        ;THE ERROR NUMBER IS 207

CMPB  #27,    $TESTN      ;CHECK THE TEST NUMBER
BEQ   .+6,          ;BRANCH IF OK
HLT   .+6            ;WRONG TEST! PC MUST HAVE FOULED UP.
                        ;THE ERROR NUMBER IS 210
    
```



K03

7180

(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)

\*\*\*\*\*  
TEST 31: FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)  
135152,125252 - 143325,052525 = 043325,052525  
PS = 200, STACK POINTER = R5  
\*\*\*\*\*

TST31: SCOPE  
JSR R5, PUSHR ; PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY  
; SECOND OPERAND ON TOP  
.WORD 143325,052525  
; FIRST OPERAND ON BOTTOM  
.WORD 135152,125252  
; PROCESSOR PRIORITY LEVEL  
.WORD 357  
; FIS TRAP VECTOR  
.WORD TRAPER, 340  
; SET UP STACK POINTER  
MOV #STACK0, R5  
  
NOP  
FSUB R5 ; FLOATING SUBTRACT ON THE R5 STACK  
  
JSR PC, POPR ; POP THE ANSWER  
MOV R5, \$SP ; SAVE "STACK POINTER"  
CMPB #200, \$PSW ; CHECK PS (EXCEPT T BIT)  
BEQ .+6 ; BRANCH IF OK  
HLT ; PS NOT EQUAL TO 200  
216 ; THE ERROR NUMBER IS 216  
  
CMP #STACK4, \$SP ; CHECK THE STACK POINTER (R5)  
BEQ .+6 ; BRANCH IF OK  
HLT ; STACK POINTER (R5) NOT EQUAL TO #STACK4  
217 ; THE ERROR NUMBER IS 217  
  
CMP #043325, ANS1 ; CHECK FIRST HALF OF ANSWER  
BEQ .+6 ; BRANCH IF OK  
HLT+2 ; ANS1 NOT EQUAL TO 043325  
220 ; THE ERROR NUMBER IS 220  
  
CMP #052525, ANS2 ; CHECK SECOND HALF OF ANSWER  
BEQ .+6 ; BRANCH IF OK  
HLT+2 ; ANS2 NOT EQUAL TO 052525  
221 ; THE ERROR NUMBER IS 221  
  
END31: CMPB #31, \$TESTN ; CHECK THE TEST NUMBER  
BEQ .+6 ; BRANCH IF OK  
HLT ; WRONG TEST! PC MUST HAVE FOULED UP.  
222 ; THE ERROR NUMBER IS 222





```

(1) 006304 022767 000425 172134      CMP      #000425,ANS5 ;CHECK DATA FROM STACK
(1) 006312 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 006314 104006                      HLT+6    ;DATA ON STACK (000425) CHANGED
(3) 006316 000237                      237      ;THE ERROR NUMBER IS 237
(1)
(1) 006320 022767 052525 172122      CMP      #052525,ANS6 ;CHECK DATA FROM STACK
(1) 006326 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 006330 104006                      HLT+6    ;DATA ON STACK (052525) CHANGED
(3) 006332 000240                      240      ;THE ERROR NUMBER IS 240
(1)
(1) 006334 122767 000033 172042 END33: CMPB     #33,      $TESTN ;CHECK THE TEST NUMBER
(1) 006342 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 006344 104000                      HLT      ;WRONG TEST! PC MUST HAVE FOJLED UP.
(3) 006346 000241                      241      ;THE ERROR NUMBER IS 241
(1)
(1)
    
```







7193

(1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)

\*\*\*\*\*  
 TEST 35: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)  
 000000,000000 \* 000000,000000 = 000000,000000  
 PS = 004, STACK POINTER = R4  
 \*\*\*\*\*

006606 104400  
 006610 004567 006550  
 006614 000000 000000  
 006620 000000 000000  
 006624 000111  
 006626 015634 000340  
 006632 012704 000510  
 006636 000240  
 006640 075024  
 006642 004767 006550  
 006646 010467 171562  
 006652 122767 000004 171552  
 006660 001402  
 006662 104000  
 006664 000254  
 006666 022767 000514 171540  
 006674 001402  
 006676 104000  
 006700 000255  
 006702 005767 171530  
 006706 001402  
 006710 104002  
 006712 000256  
 006714 005767 171520  
 006720 001402  
 006722 104002  
 006724 000257  
 006726 122767 000035 171450 END35:  
 006734 001402  
 006736 104000  
 006740 000260

TST35: SCOPE  
 JSR RS, PUSHR ; PUSH 4 WORDS ONTO R4 STACK, SET PRIORITY  
 .WORD 000000,000000 ; SECOND OPERAND ON TOP  
 .WORD 000000,000000 ; FIRST OPERAND ON BOTTOM  
 .WORD 111 ; PROCESSOR PRIORITY LEVEL  
 .WORD TRAPER, 340 ; FIS TRAP VECTOR  
 MOV #STACK0, R4 ; SET UP STACK POINTER  
 NOP  
 FMUL R4 ; FLOATING MULTIPLY ON THE R4 STACK  
 JSR PC, POPR ; POP THE ANSWER  
 MOV R4, \$SP ; SAVE "STACK POINTER"  
 CMPB #004, \$PSW ; CHECK PS (EXCEPT T BIT)  
 BEQ .+6 ; BRANCH IF OK  
 HLT ; PS NOT EQUAL TO 004  
 254 ; THE ERROR NUMBER IS 254  
 CMP #STACK4, \$SP ; CHECK THE STACK POINTER (R4)  
 BEQ .+6 ; BRANCH IF OK  
 HLT ; STACK POINTER (R4) NOT EQUAL TO #STACK4  
 255 ; THE ERROR NUMBER IS 255  
 TST ANS1 ; CHECK FIRST HALF OF ANSWER  
 BEQ .+6 ; BRANCH IF OK  
 HLT+2 ; ANS1 NOT EQUAL TO 000000  
 256 ; THE ERROR NUMBER IS 256  
 TST ANS2 ; CHECK SECOND HALF OF ANSWER  
 BEQ .+6 ; BRANCH IF OK  
 HLT+2 ; ANS2 NOT EQUAL TO 000000  
 257 ; THE ERROR NUMBER IS 257  
 CMPB #35, \$TESTN ; CHECK THE TEST NUMBER  
 BEQ .+6 ; BRANCH IF OK  
 HLT ; WRONG TEST! PC MUST HAVE FOULED UP.  
 260 ; THE ERROR NUMBER IS 260













7200 (1)				*****		
(1)				TEST 43:	FSUB (LSI-11 FLOATING SUBTRACT INSTRUCTION)	
(1)				104000 105004 - 104000 104000 = 100401,000000		
(1)				PS = 210, STACK POINTER = PC		
(1)				*****		
(1)	007664	104400		SCOPE		
(1)	007666	004567	005644	TST43: JSR	RS, PUSH7	; PUSH 4 WORDS ONTO STACK, SET PRIORITY
(1)	007672	007716		.WORD	STK43	; TOP OF STACK
(1)	007674	104000	104000	.WORD	104000, 104000	; SECOND OPERAND ON TOP
(1)	007700	104000	105004	.WORD	104000, 105004	; FIRST OPERAND ON BOTTOM
(1)	007704	000252		.WORD	252	; PROCESSOR PRIORITY LEVEL
(1)	007706	015634	000340	.WORD	TRAPER, 340	; FIS TRAP VECTOR
(1)				NOP		
(1)	007712	000240		FSUB	PC	; FLOATING SUBTRACT ON FOLLOWING 4 WORDS
(1)	007714	075017		104000		; SHOULD CONTAIN 104000
(1)	007716	104000		104000		; SHOULD CONTAIN 104000
(1)	007720	104000		104000		; BEFORE FSUB, 104000; AFTER, 100401
(1)	007722	104000		105004		; BEFORE FSUB, 105004; AFTER, 000000
(1)	007724	105004				
(1)	007726	004767	005634	JSR	PC, POP7	; POP THE ANSWER
(1)	007732	122767	000210	CMPB	#210, \$PSW	; CHECK PS (EXCEPT T BIT)
(1)	007740	001402		BEQ	+.6	; BRANCH IF OK
(1)	007742	104000		HLT		; PS NOT EQUAL TO 210
(3)	007744	000312		312		; THE ERROR NUMBER IS 312
(1)						
(1)	007746	022767	104000	CMP	#104000, ANS1	; CHECK FIRST HALF OF INPUT DATA (STK43)
(1)	007754	001402		BEQ	+.6	; BRANCH IF OK
(1)	007756	104002		HLT+2		; ANS1 NOT EQUAL TO 104000
(3)	007760	000313		313		; THE ERROR NUMBER IS 313
(1)						
(1)	007762	022767	104000	CMP	#104000, ANS2	; CHECK SECOND HALF OF INPUT DATA (STK43+2)
(1)	007770	001402		BEQ	+.6	; BRANCH IF OK
(1)	007772	104002		HLT+2		; ANS2 NOT EQUAL TO 104000
(3)	007774	000314		314		; THE ERROR NUMBER IS 314
(1)						
(1)	007776	022767	100401	CMP	#100401, ANS3	; CHECK FIRST HALF OF ANSWER
(1)	010004	001402		BEQ	+.6	; BRANCH IF OK
(1)	010006	104004		HLT+4		; ANS3 NOT EQUAL TO 100401
(3)	010010	000315		315		; THE ERROR NUMBER IS 315
(1)						
(1)	010012	005767	170426	TST	ANS4	; CHECK SECOND HALF OF ANSWER
(1)	010016	001402		BEQ	+.6	; BRANCH IF OK
(1)	010020	104004		HLT+4		; ANS4 NOT EQUAL TO 000000
(3)	010022	000316		316		; THE ERROR NUMBER IS 316
(1)						
(1)	010024	122767	000043	END43: CMPB	#43, \$TESTN	; CHECK THE TEST NUMBER
(1)	010032	001402		BEQ	+.6	; BRANCH IF OK
(1)	010034	104000		HLT		; WRONG TEST! PC MUST HAVE FOULED UP.
(3)	010036	000317		317		; THE ERROR NUMBER IS 317
(1)						
(1)						



```

(1) ;TEST 45: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
(1) ; 024252,125252 * 114100,000000 ==> UNDERFLOW
(1) ; PS(ON STACK) = 212, STACK POINTER = RO
(1) ;*****
(1)
(1) 010214 104400 SCOPE
(1) 010216 004567 005142 TST45: JSR R5, PUSHR ;PUSH 4 WORDS ONTO RO STACK, SET PRIORITY
(1) 010222 114100 000000 .WORD 114100,000000 ;SECOND OPERAND ON TOP
(1) 010226 024252 125252 .WORD 024252,125252 ;FIRST OPERAND ON BOTTOM
(1) 010232 000305 .WORD 305 ;PROCESSOR PRIORITY LEVEL
(1) 010234 010266 000057 .WORD ISR45, 057 ;FIS TRAP VECTOR
(1) 010240 012700 000510 MOV #STACK0,RO ;SET UP RO AS STACK POINTER
(1)
(1) 010244 000240 NOP
(1) 010246 075020 FMUL RO ;FLOATING MULTIPLY ON THE RO STACK
(1)
(1) 010250 004767 005142 RTA45: JSR PC, POPR ;POP THE "ANSWER"
(1) 010254 010067 170154 MOV RO, $SP ;SAVE STACK POINTER (RO)
(1) 010260 104002 HLT+2 ;FIS TRAP DIDN'T OCCURE!
(3) 010262 000326 326 ;THE ERROR NUMBER IS 326
(1) 010264 000463 BR END45
(1)
(1) 010266 004767 005154 ISR45: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
(1) 010272 010067 170136 MOV RO, $SP ;SAVE STACK POINTER (RO)
(1) 010276 122767 000057 170126 CMPB #057, $PSW ;CHECK PS AFTER FIS TRAP
(1) 010304 001402 BEQ .+6 ;BRANCH IF OK
(1) 010306 104000 HLT ;PS AFTER FIS TRAP NOT EQUAL TO 057
(3) 010310 000327 327 ;THE ERROR NUMBER IS 327
(1)
(1) 010312 022767 000510 170114 CMP #STACK0,$SP ;CHECK THE STACK POINTER (RO)
(1) 010320 001402 BEQ .+6 ;BRANCH IF OK
(1) 010322 104000 HLT ;STACK POINTER (RO) NOT EQUAL TO #STACK0
(3) 010324 000330 330 ;THE ERROR NUMBER IS 330
(1)
(1) 010326 022767 010250 170102 CMP #RTA45, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 010334 001402 BEQ .+6 ;BRANCH IF OK
(1) 010336 104001 HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 010340 000331 331 ;THE ERROR NUMBER IS 331
(1)
(1) 010342 022767 000212 170070 CMP #212, ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 010350 001402 BEQ .+6 ;BRANCH IF OK
(1) 010352 104002 HLT+2 ;PS AT FIS TRAP TIME NOT 212
(3) 010354 000332 332 ;THE ERROR NUMBER IS 332
(1)
(1) 010356 022767 114100 170056 CMP #114100,ANS3 ;CHECK DATA FROM THE STACK
(1) 010364 001402 BEQ .+6 ;BRANCH IF OK
(1) 010366 104004 HLT+4 ;DATA ON STACK (114100) CHANGED
(3) 010370 000333 333 ;THE ERROR NUMBER IS 333
(1)
(1) 010372 005767 170046 TST ANS4 ;CHECK DATA FROM STACK
(1) 010376 001402 BEQ .+6 ;BRANCH IF OK
(1) 010400 104004 HLT+4 ;DATA ON STACK (000000) CHANGED
(3) 010402 000334 334 ;THE ERROR NUMBER IS 334
(1)
(1) 010404 022767 024252 170034 CMP #024252,ANS5 ;CHECK DATA FROM STACK
(1) 010412 001402 BEQ .+6 ;BRANCH IF OK
    
```

M04

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-49  
DVKACA.SRC TEST FLOATING MUL. INSTRUCTION WITH UNDERFLOW

SEQ 0051

```

(1) 010414 104006          HLT+6          ;DATA ON STACK (024252) CHANGED
(3) 010416 000335          335           ;THE ERROR NUMBER IS 335
(1)
(1) 010420 022767 125252 170022  CMP      #125252,ANS6 ;CHECK DATA FROM STACK
(1) 010426 001402          BEQ      .+6      ;BRANCH IF OK
(1) 010430 104006          HLT+6          ;DATA ON STACK (125252) CHANGED
(3) 010432 000336          336           ;THE ERROR NUMBER IS 336
(1)
(1) 010434 122767 000045 167742 END45: CMPB    #45,    $TESTN ;CHECK THE TEST NUMBER
(1) 010442 001402          BEQ      .+6      ;BRANCH IF OK
(1) 010444 104000          HLT          ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 010446 000337          337           ;THE ERROR NUMBER IS 337
(1)
(1)

```

7210

```

(1) ;*****
(1) ;TEST 46: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
(1) ; 076452,125252 * 041500,000001 ==> OVERFLOW
(1) ; PS(ON STACK) = 002, STACK POINTER = SP
(1) ;*****
(1)
(1) 010450 104400 SCOPE
(1) 010452 004567 004534 TST46: JSR R5, PUSH5 ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
(1) 010456 041500 000001 .WORD 041500,000001 ;SECOND OPERAND ON TOP
(1) 010462 076452 125252 .WORD 076452,125252 ;FIRST OPERAND ON BOTTOM
(1) 010466 000105 .WORD 105 ;PROCESSOR PRIORITY LEVEL
(1) 010470 010516 000357 .WORD ISR46, 357 ;FIS TRAP VECTOR
(1)
(1) 010474 000240 NOP
(1) 010476 075026 FMUL SP ;FLOATING MULTIPLY ON THE STACK
(1)
(1) 010500 004767 004546 RTA46: JSR PC, POPS ;POP THE "ANSWER"
(1) 010504 104002 HLT+2 ;FIS TRAP DIDN'T OCCURE!
(3) 010506 000340 340 ;THE ERROR NUMBER IS 340
(1) 010510 012706 000600 MOV #BEGIN, SP ;RESTORE THE STACK POINTER
(1) 010514 000464 BR END46
(1)
(1) 010516 004767 004562 ISR46: JSR PC, POPES ;POP ALL DATA OFF THE STACK
(1) 010522 022706 000600 CMP #BEGIN, SP ;CHECK THE STACK POINTER
(1) 010526 001405 BEQ ISA46 ;BRANCH IF OK
(1) 010530 012706 000600 MOV #BEGIN, SP ;RESTORE THE STACK POINTER
(1) 010534 104000 HLT ;STACK POINTER FOULED UP
(3) 010536 000341 341 ;THE ERROR NUMBER IS 341
(1) 010540 000452 BR END46 ;SKIP REST OF TEST
(1)
(1) 010542 122767 000357 167662 ISA46: CMPB #357, $PSW ;CHECK PS AFTER FIS TRAP
(1) 010550 001402 BEQ .+6 ;BRANCH IF OK
(1) 010552 104000 HLT ;PS AFTER FIS TRAP NOT EQUAL TO 357
(3) 010554 000342 342 ;THE ERROR NUMBER IS 342
(1)
(1) 010556 022767 010500 167652 CMP #RTA46, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 010564 001402 BEQ .+6 ;BRANCH IF OK
(1) 010566 104001 HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 010570 000343 343 ;THE ERROR NUMBER IS 343
(1)
(1) 010572 022767 000002 167640 CMP #002, ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 010600 001402 BEQ .+6 ;BRANCH IF OK
(1) 010602 104002 HLT+2 ;PS AT FIS TRAP TIME NOT 002
(3) 010604 000344 344 ;THE ERROR NUMBER IS 344
(1)
(1) 010606 022767 041500 167626 CMP #041500,ANS3 ;CHECK DATA FROM THE STACK
(1) 010614 001402 BEQ .+6 ;BRANCH IF OK
(1) 010616 104004 HLT+4 ;DATA ON STACK (041500) CHANGED
(3) 010620 000345 345 ;THE ERROR NUMBER IS 345
(1)
(1) 010622 022767 000001 167614 CMP #000001,ANS4 ;CHECK DATA FROM STACK
(1) 010630 001402 BEQ .+6 ;BRANCH IF OK
(1) 010632 104004 HLT+4 ;DATA ON STACK (000001) CHANGED
(3) 010634 000346 346 ;THE ERROR NUMBER IS 346
(1)
    
```

```

(1) 010636 022767 076452 167602      CMP      #076452,ANS5      ;CHECK DATA FROM STACK
(1) 010644 001402                      BEQ      .+6              ;BRANCH IF OK
(1) 010646 104006                      HLT+6    ;DATA ON STACK (076452) CHANGED
(3) 010650 000347                      347          ;THE ERROR NUMBER IS 347
(1)
(1) 010652 022767 125252 167570      CMP      #125252,ANS6      ;CHECK DATA FROM STACK
(1) 010660 001402                      BEQ      .+6              ;BRANCH IF OK
(1) 010662 104006                      HLT+6    ;DATA ON STACK (125252) CHANGED
(3) 010664 000350                      350          ;THE ERROR NUMBER IS 350
(1)
(1) 010666 122767 000046 167510 END46: CMPB     #46,      $TESTM      ;CHECK THE TEST NUMBER
(1) 010674 001402                      BEQ      .+6              ;BRANCH IF OK
(1) 010676 104000                      HLT      ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 010700 000351                      351          ;THE ERROR NUMBER IS 351
(1)
(1)
    
```

7214  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)  
 (1)

```

*****
: TEST 47: FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
: 167452,125251 / 127652,125252 = 077777,177776
: PS = 000, STACK POINTER = R0
*****
    
```

```

TST47: SCOPE
        JSR RS, PUSHR ; PUSH 4 WORDS ONTO R0 STACK, SET PRIORITY
        .WORD 127652,125252 ; SECOND OPERAND ON TOP
        .WORD 167452,125251 ; FIRST OPERAND ON BOTTOM
        .WORD 111 ; PROCESSOR PRIORITY LEVEL
        .WORD TRAPER, 340 ; FIS TRAP VECTOR
        MOV #STACK0, R0 ; CHECK STACK POINTER

        NOP
        FDIV R0 ; FLOATING DIVIDE ON THE R0 STACK

        JSR PC, POPR ; POP THE ANSWER
        MOV R0, $SP ; SAVE "STACK POINTER"
        TSTB $PSW ; CHECK PS (EXCEPT T BIT)
        BEQ .+6 ; BRANCH IF OK
        HLT ; PS NOT EQUAL TO 000
        352 ; THE ERROR NUMBER IS 352

        CMP #STACK4, $SP ; CHECK THE STACK POINTER (R0)
        BEQ .+6 ; BRANCH IF OK
        HLT ; STACK POINTER (R0) NOT EQUAL TO #STACK4
        353 ; THE ERROR NUMBER IS 353

        CMP #077777, ANS1 ; CHECK FIRST HALF OF ANSWER
        BEQ .+6 ; BRANCH IF OK
        HLT+2 ; ANS1 NOT EQUAL TO 077777
        354 ; THE ERROR NUMBER IS 354

        CMP #177776, ANS2 ; CHECK SECOND HALF OF ANSWER
        BEQ .+6 ; BRANCH IF OK
        HLT+2 ; ANS2 NOT EQUAL TO 177776
        355 ; THE ERROR NUMBER IS 355

        CMPB #47, $TESTN ; CHECK THE TEST NUMBER
        BEQ .+6 ; BRANCH IF OK
        HLT ; WRONG TEST! PC MUST HAVE FOULED UP.
        356 ; THE ERROR NUMBER IS 356
    
```











# H05

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-57  
 DVKACA.SRC TEST FLOATING DIV. INSTRUCTION WITH UNDERFLOW

SEQ 0059

```

(1) ;TEST 54:          FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
(1) ;                025252,125251 / 065252,125252 ==> UNDERFLOW
(1) ;                PS(ON STACK) = 012,    STACK POINTER = R1
(1) ;                *****
(1)
(1) 011642 104400          SCOPE
(1) 011644 004567 003514  TST54: JSR      RS,      PUSHR  ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
(1) 011650 065252 125252   .WORD   065252,125252 ;SECOND OPERAND ON TOP
(1) 011654 025252 125251   .WORD   025252,125251 ;FIRST OPERAND ON BOTTOM
(1) 011660 000015         .WORD   015           ;PROCESSOR PRIORITY LEVEL
(1) 011662 011714 000300   .WORD   ISRS4, 300    ;FIS TRAP VECTOR
(1) 011666 012701 000510   MOV     #STACK0,R1   ;SET UP R1 AS STACK POINTER
(1)
(1) 011672 000240          NOP
(1) 011674 075031          FDIV    R1           ;FLOATING DIVIDE ON THE R1 STACK
(1)
(1) 011676 004767 003514  RTAS4: JSR      PC,      POPR   ;POP THE "ANSWER"
(1) 011702 010167 166526   MOV     R1,      $SP   ;SAVE STACK POINTER (R1)
(1) 011706 104002         HLT+2   ;FIS TRAP DIDN'T OCCURE!
(3) 011710 000404         404      ;THE ERROR NUMBER IS 404
(1) 011712 000464         BR      END54
(1)
(1) 011714 004767 003526  ISRS4: JSR      PC,      POPER  ;POP ALL DATA OFF THE STACKS
(1) 011720 010167 166510   MOV     R1,      $SP   ;SAVE STACK POINTER (R1)
(1) 011724 122767 000300 166500  CMPB   #300,    $PSW   ;CHECK PS AFTER FIS TRAP
(1) 011732 001402         BEQ     .+6          ;BRANCH IF OK
(1) 011734 104000         HLT     ;PS AFTER FIS TRAP NOT EQUAL TO 300
(3) 011736 000405         405      ;THE ERROR NUMBER IS 405
(1)
(1) 011740 022767 000510 166466  CMP    #STACK0,$SP   ;CHECK THE STACK POINTER (R1)
(1) 011746 001402         BEQ     .+6          ;BRANCH IF OK
(1) 011750 104000         HLT     ;STACK POINTER (R1) NOT EQUAL TO #STACK0
(3) 011752 000406         406      ;THE ERROR NUMBER IS 406
(1)
(1) 011754 022767 011676 166454  CMP    #RTAS4, ANS1  ;CHECK FIS TRAP RETURN ADDRESS
(1) 011762 001402         BEQ     .+6          ;BRANCH IF OK
(1) 011764 104001         HLT+1   ;FIS TRAP AT WRONG ADDRESS
(3) 011766 000407         407      ;THE ERROR NUMBER IS 407
(1)
(1) 011770 022767 000012 166442  CMP    #012,    ANS2  ;CHECK PS BEFORE FIS TRAP
(1) 011776 001402         BEQ     .+6          ;BRANCH IF OK
(1) 012000 104002         HLT+2   ;PS AT FIS TRAP TIME NOT 012
(3) 012002 000410         410      ;THE ERROR NUMBER IS 410
(1)
(1) 012004 022767 065252 166430  CMP    #065252,ANS3  ;CHECK DATA FROM THE STACK
(1) 012012 001402         BEQ     .+6          ;BRANCH IF OK
(1) 012014 104004         HLT+4   ;DATA ON STACK (065252) CHANGED
(3) 012016 000411         411      ;THE ERROR NUMBER IS 411
(1)
(1) 012020 022767 125252 166416  CMP    #125252,ANS4  ;CHECK DATA FROM STACK
(1) 012026 001402         BEQ     .+6          ;BRANCH IF OK
(1) 012030 104004         HLT+4   ;DATA ON STACK (125252) CHANGED
(3) 012032 000412         412      ;THE ERROR NUMBER IS 412
(1)
(1) 012034 022767 025252 166404  CMP    #025252,ANS5  ;CHECK DATA FROM STACK
(1) 012042 001402         BEQ     .+6          ;BRANCH IF OK
  
```











# M05

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-62  
DVKACA.SRC TEST FLOATING DIV. INSTRUCTION FOR DIVIDE BY ZERO

SEQ 0064

(1)	012530	022767	052525	165710	CMP	#052525,ANS5	;CHECK DATA FROM STACK	
(1)	012536	001402			BEQ	.+6	;BRANCH IF OK	
(1)	012540	104006			HLT+6		;DATA ON STACK (052525) CHANGED	
(3)	012542	000437			437		;THE ERROR NUMBER IS 437	
(1)								
(1)	012544	022767	052525	165676	CMP	#052525,ANS6	;CHECK DATA FROM STACK	
(1)	012552	001402			BEQ	.+6	;BRANCH IF OK	
(1)	012554	104006			HLT+6		;DATA ON STACK (052525) CHANGED	
(3)	012556	000440			440		;THE ERROR NUMBER IS 440	
(1)								
(1)	012560	122767	000056	165616	END56:	CMPB	#56, \$TESTN	;CHECK THE TEST NUMBER
(1)	012566	001402			BEQ	.+6	;BRANCH IF OK	
(1)	012570	104000			HLT		;WRONG TEST! PC MUST HAVE FOULED UP.	
(3)	012572	000441			441		;THE ERROR NUMBER IS 441	
(1)								
(1)								

# N05

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-63  
 DVKACA.SRC TEST FLOATING DIV. INSTRUCTION FOR DIVIDE BY ZERO

SEQ 0065

7232

```

(1) ;*****
(1) ;TEST S7: FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
(1) ;100052,052525 / 000006,123456 ==> DIVIDE BY ZERO
(1) ;PS(ON STACK) = 213, STACK POINTER = SP
(1) ;*****
(1)
(1) 012574 104400 SCOPE
(1) 012576 004567 002410 TST57: JSR RS, PUSHS ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
(1) 012602 000006 123456 .WORD 000006,123456 ;SECOND OPERAND ON TOP
(1) 012606 100052 052525 .WORD 100052,052525 ;FIRST OPERAND ON BOTTOM
(1) 012612 000357 .WORD 357 ;PROCESSOR PRIORITY LEVEL
(1) 012614 012642 000311 .WORD ISR57, 311 ;FIS TRAP VECTOR
(1)
(1) 012620 000240 NOP
(1) 012622 075036 FDIV SP ;FLOATING DIVIDE ON THE STACK
(1)
(1) 012624 004767 002422 RTA57: JSR PC, POPS ;POP THE "ANSWER"
(1) 012630 104002 HLT+2 ;FIS TRAP DIDN'T OCCURE!
(3) 012632 000442 442 ;THE ERROR NUMBER IS 442
(1) 012634 012706 000600 MOV #BEGIN, SP ;RESTORE THE STACK POINTER
(1) 012640 000464 BR ENDS7
(1)
(1) 012642 004767 002436 ISR57: JSR PC, POPES ;POP ALL DATA OFF THE STACK
(1) 012646 022706 000600 CMP #BEGIN, SP ;CHECK THE STACK POINTER
(1) 012652 001405 BEQ ISA57 ;BRANCH IF OK
(1) 012654 012706 000600 MOV #BEGIN, SP ;RESTORE THE STACK POINTER
(1) 012660 104000 HLT ;STACK POINTER FOULED UP
(3) 012662 000443 443 ;THE ERROR NUMBER IS 443
(1) 012664 000452 BR ENDS7 ;SKIP REST OF TEST
(1)
(1) 012666 122767 000311 165536 ISA57: CMPB #311, SPSW ;CHECK PS AFTER FIS TRAP
(1) 012674 001402 BEQ .+6 ;BRANCH IF OK
(1) 012676 104000 HLT ;PS AFTER FIS TRAP NOT EQUAL TO 311
(3) 012700 000444 444 ;THE ERROR NUMBER IS 444
(1)
(1) 012702 022767 012624 165526 CMP #RTA57, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 012710 001402 BEQ .+6 ;BRANCH IF OK
(1) 012712 104001 HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 012714 000445 445 ;THE ERROR NUMBER IS 445
(1)
(1) 012716 022767 000213 165514 CMP #213, ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 012724 001402 BEQ .+6 ;BRANCH IF OK
(1) 012726 104002 HLT+2 ;PS AT FIS TRAP TIME NOT 213
(3) 012730 000446 446 ;THE ERROR NUMBER IS 446
(1)
(1) 012732 022767 000006 165502 CMP #000006,ANS3 ;CHECK DATA FROM THE STACK
(1) 012740 001402 BEQ .+6 ;BRANCH IF OK
(1) 012742 104004 HLT+4 ;DATA ON STACK (000006) CHANGED
(3) 012744 000447 447 ;THE ERROR NUMBER IS 447
(1)
(1) 012746 022767 123456 165470 CMP #123456,ANS4 ;CHECK DATA FROM STACK
(1) 012754 001402 BEQ .+6 ;BRANCH IF OK
(1) 012756 104004 HLT+4 ;DATA ON STACK (123456) CHANGED
(3) 012760 000450 450 ;THE ERROR NUMBER IS 450
(1)

```





(1)								
(1)	013230	122767	000060	165146	END60:	CMPB	#60,	
(1)	013236	001402				BEQ	.+6	
(1)	013240	104000				HLT		
(3)	013242	000460				460		
(1)								

```

STESTN ;CHECK THE TEST NUMBER
        ;BRANCH IF OK
        ;WRONG TEST! PC MUST HAVE FOULED UP.
        ;THE ERROR NUMBER IS 460

```

# E06

7243

```

(1)
(1)
(1)
(1)
(1)
(1) 013244 104400
(1) 013246 012737 013336 000004 TST61: MOV #ISR61, #34 ;SET UP ADDRESS TRAP VECTOR
(1) 013254 012737 000340 000006 MOV #340, #6
(1) 013262 004567 002076 JSR R5, PUSHR ;PUSH 4 WORDS ONTO R2 STACK, SET PRIORITY
(1) 013266 070707 016161 .WORD 070707, 016161 ;SECOND OPERAND ON TOP
(1) 013272 146314 143434 .WORD 146314, 143434 ;FIRST OPERAND ON BOTTOM
(1) 013276 000143 .WORD 143 ;PROCESSOR PRIORITY LEVEL
(1) 013300 015634 000340 .WORD TRAPER, 340 ;FIS TRAP VECTOR
(2) 013304 MTPS #143 ;SET PROCESSOR STATUS
(2) 013304 106427 .WORD 106400! .C
(1) 013310 012702 177777 MOV #177777, R2 ;SET UP R2 AS STACK POINTER
(1)
(1) 013314 000240 NOP
(1) 013316 075002 FADD R2 ;FLOATING ADD ON THE R2 STACK
(1)
(1) 013320 004767 002072 RTA61: JSR PC, POPR ;POP THE "ANSWER"
(1) 013324 010267 165104 MOV R2, SSP ;SAVE STACK POINTER (R2)
(1) 013330 104002 HLT+2 ;FIS TRAP DIDN'T OCCURE!
(3) 013332 00461 461 ;THE ERROR NUMBER IS 461
(1) 013334 000464 BR END61
(1)
(1) 013336 004767 002104 ISR61: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
(1) 013342 010267 165066 MOV R2, SSP ;SAVE STACK POINTER (R2)
(1) 013346 122767 000340 165056 CMPB #340, SPSW ;CHECK PS AFTER ADR. ERR. TRAP
(1) 013354 001402 BEQ .+6 ;BRANCH IF OK
(1) 013356 104000 HLT ;PS AFTER TRAP NOT EQUAL TO 340
(3) 013360 000462 462 ;THE ERROR NUMBER IS 462
(1)
(1) 013362 022767 177777 165044 CMP #177777, SSP ;CHECK THE STACK POINTER (R2)
(1) 013370 001402 BEQ .+6 ;BRANCH IF OK
(1) 013372 104000 HLT ;STACK POINTER (R2) NOT EQUAL TO #177777
(3) 013374 000463 463 ;THE ERROR NUMBER IS 463
(1)
(1) 013376 022767 013320 165032 CMP #RTA61, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 013404 001402 BEQ .+6 ;BRANCH IF OK
(1) 013406 104001 HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 013410 000464 464 ;THE ERROR NUMBER IS 464
(1)
(1) 013412 022767 000151 165020 CMP #151, ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 013420 001402 BEQ .+6 ;BRANCH IF OK
(1) 013422 104002 HLT+2 ;PS AT FIS TRAP TIME NOT 151
(3) 013424 000465 465 ;THE ERROR NUMBER IS 465
(1)
(1) 013426 022767 070707 165006 CMP #070707, ANS3 ;CHECK DATA FROM THE STACK
(1) 013434 001402 BEQ .+6 ;BRANCH IF OK
(1) 013436 104004 HLT+4 ;DATA ON STACK (070707) CHANGED
(3) 013440 000466 466 ;THE ERROR NUMBER IS 466
(1)
(1) 013442 022767 016161 164774 CMP #016161, ANS4 ;CHECK DATA FROM STACK
(1) 013450 001402 BEQ .+6 ;BRANCH IF OK
    
```



```

7244
(1)
(1)
(1)
(1)
(1)
(1) 013522 104400
(1) 013524 012737 013614 000004 TST62: MOV #ISR62, #4 ;SET UP ADDRESS TRAP VECTOR
(1) 013532 012737 000340 000006 MOV #340, #6
(1) 013540 004567 001620 JSR RS, PUSHR ;PUSH 4 WORDS ONTO RS STACK, SET PRIORITY
(1) 013544 065432 123456 .WORD 065432,123456 ;SECOND OPERAND ON TOP
(1) 013550 037654 032107 .WORD 037654,032107 ;FIRST OPERAND ON BOTTOM
(1) 013554 000202 .WORD 202 ;PROCESSOR PRIORITY LEVEL
(1) 013556 015634 000340 .WORD TRAPER, 340 ;FIS TRAP VECTOR
(2) 013562 MTPS #202 ;SET PROCESSOR STATUS
(2) 013562 106427 .WORD 106400!..C
(1) 013566 012705 160000 MOV #160000,RS ;SET UP RS AS STACK POINTER
(1)
(1) 013572 000240 NOP
(1) 013574 075025 FMUL RS ;FLOATING MULTIPLY ON THE RS STACK
(1)
(2) 013576 RTA62: MFPS $PSW ;SAVE THE PSW
(2) 013576 106767 .WORD 106700!..C
(1) 013602 010567 164626 MOV RS, $SP ;SAVE STACK POINTER (RS)
(1) 013606 104000 HLT ;FIS TRAP DIDN'T OCCURE!
(3) 013610 000473 473 ;THE ERROR NUMBER IS 473
(1) 013612 000434 BR END62
(1)
(1) 013614 004767 001626 ISR62: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
(1) 013620 010567 164610 MOV RS, $SP ;SAVE STACK POINTER (RS)
(1) 013624 122767 000340 164600 CMPB #340, $PSW ;CHECK PS AFTER ADR. ERR. TRAP
(1) 013632 001402 BEQ .+6 ;BRANCH IF OK
(1) 013634 104000 HLT ;PS AFTER TRAP NOT EQUAL TO 340
(3) 013636 000474 474 ;THE ERROR NUMBER IS 474
(1)
(1) 013640 022767 160000 164566 CMP #160000,$SP ;CHECK THE STACK POINTER (RS)
(1) 013646 001402 BEQ .+6 ;BRANCH IF OK
(1) 013650 104000 HLT ;STACK POINTER (RS) NOT EQUAL TO #160000
(3) 013652 000475 475 ;THE ERROR NUMBER IS 475
(1)
(1) 013654 022767 013576 164554 CMP #RTA62, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 013662 001402 BEQ .+6 ;BRANCH IF OK
(1) 013664 104001 HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 013666 000476 476 ;THE ERROR NUMBER IS 476
(1)
(1) 013670 022767 000210 164542 CMP #210, ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 013676 001402 BEQ .+6 ;BRANCH IF OK
(1) 013700 104002 HLT+2 ;PS AT FIS TRAP TIME NOT 210
(3) 013702 000477 477 ;THE ERROR NUMBER IS 477
(1)
(1) 013704 122767 000062 164472 END62: CMPB #62, $TESTN ;CHECK THE TEST NUMBER
(1) 013712 001402 BEQ .+6 ;BRANCH IF OK
(1) 013714 104000 HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 013716 000500 500 ;THE ERROR NUMBER IS 500
    
```



```

7245 013720 012737 000006 000004      MOV    #6,    2#4      ;RESTORE TIME-OUT VECTOR
7246 013726 005037 000006                CLR    2#6
7250 013732 012767 000003 164576      MOV    #3,    TIMES   ;REDUCE NUMBER OF ITERATIONS
7251
(1)                                     ;*****
(1)                                     ;TEST 63:      TEST THAT FIS ABORTS PROPERLY WHEN INTERRUPTED
(1)                                     ;C35700,143235 + 000177,134543 = 035700,143235
(1)                                     ;PS = .PS,    STACK POINTER = R1
(1)                                     ;*****
(1)
(1) 013740 104400                SCOPE
(1) 013742 132737 000040 000421 TSTB3: BITB    #40, 2#SENVH
(1) 013750 001170                BNE    END63+2        ;EXIT THIS TEST IF BIT 5 OF SENVM IS HIGH
(1) 013752 013704 000546                MOV    2#TTYOUT, R4
(1) 013756 012724 014050                MOV    #ISR63, (R4)+ ;SET UP TELEPRINTER INTERRUPT VECTOR
(1) 013762 012714 000340                MOV    #340, (R4)
(1) 013766 000004 000473                TYPE,  RETURN+1      ;RETURN+1 CAN BE REPLACED WITH THE ADDRESS OF RETURN
(1)                                     ;TO TYPE CARRIAGE RETURN, LINE FEED
(1) 013772 012767 014000 164470      MOV    #.+6,  LADS    ;RESET LOOP ADDRESS
(1) 014000 004567 001360                JSR    RS,    PUSHR   ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
(1) 014004 000177 134543                .WORD 000177,134543 ;SECOND OPERAND ON TOP
(1) 014010 035700 143235                .WORD 035700,143235 ;FIRST OPERAND ON BOTTOM
(1) 014014 000143                .WORD 143            ;PROCESSOR PRIORITY LEVEL
(1) 014016 015634 000340                .WORD TRAPER, 340    ;FIS TRAP VECTOR
(1) 014022 012701 000510                MOV    #STACK0, R1   ;SET UP STACK POINTER
(1) 014026 012767 000030 164500      MOV    #30, TEMP
(1) 014034 112777 000100 164506      MOVB   #100, 2#STPS  ;SET TTY INTERRUPT ENABLE
(1)
(1) 014042 075001                RTA63: FADD   R1      ;FLOATING ADD ON THE STACK
(1) 014044 024141                CMP    -(R1), -(R1)  ;RESET THE STACK POINTER FOR NEXT PASS
(1) 014046 000775                BR     RTA63        ;REPEAT UNTIL INTERRUPTED
(1)
(1) 014050 105077 164474                ISR63: CLRB   2#STPS ;CLEAR THE INTERRUPT ENABLE
(1) 014054 022716 014042                CMP    #RTA63, (SP) ;CHECK IF INTERRUPT AT FIS INSTR.
(1) 014060 001421                BEQ   3$           ;BRANCH IF IT DID
(1) 014062 022766 014042 000004      CMP    #RTA63, 4(SP) ;CHECK FOR INTERRUPT WITH T-BIT SET
(1) 014070 001420                BEQ   4$           ;BRANCH IF IT DID
(1) 014072 112777 000015 164452 1$: MOVB   #15, 2#STPB ;CONTINUE TO TYPE "CR"
(1) 014100 105777 164444                2$: TSTB  2#STPS    ;LOOP HERE UNTILL DONE BIT COMES ON
(1) 014104 100375                BPL   2$
(1) 014106 112777 000015 164436      MOVB   #15, 2#STPB ;TYPE ANOTHER "CR"
(1) 014114 012777 000100 164426      MOV    #100, 2#STPS ;SET TTY INTERRUPT ENABLE
(1) 014122 000002                RTI
(1)
(1) 014124 004767 001316                3$: JSR    PC,    POPER ;SAVE ALL THE STUFF ON THE STACK
(1) 014130 000403                BR     5$
(1)
(1) 014132 022626                4$: CMP    (SP)+, (SP)+ ;RESET THE STACK TO IGNORE THE TRACE TRAP
(1) 014134 004767 001312                JSR    PC,    POPER1 ;POP ALL THE STUFF OFF THE STACK
(1) 014140 005746                5$: TST    -(SP)     ;SAVE PSW FOR FUTURE RTI
(1) 014142 012746 014042                MOV    #RTA63, -(SP) ;PLACE THE RTI ADDRESS BACK IN SP
(1) 014146 022706 000574                CMP    #BEGIN-4, SP ;CHECK THE STACK POINTER
(1) 014152 001407                BEQ   6$           ;BRANCH IF OK
(1) 014154 010667 164254                MOV    SP,    $SP   ;SAVE FOR TYPING
(1) 014160 012706 000574                MOV    #BEGIN-4, SP ;RESTORE THE STACK POINTER
(1) 014164 104000                HLT                ;STACK POINTER FOULED UP
    
```

```

(3) 014166 000501          501          ;THE ERROR NUMBER IS 501
(1) 014170 000457          BR          END63          ;SKIP REST OF TEST
(1)
(1) 014172 010167 164236 65:  MOV      R1,      $SP          ;SAVE STACK POINTER
(1) 014176 122767 000344 164226  CMPB    #344,    $PSW          ;CHECK PS AFTER INTERUPT
(1) 014204 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014206 104000          HLT          ;PS AFTER INTERUPT NOT EQUAL TO LVLA
(3) 014210 000502          502          ;THE ERROR NUMBER IS 502
(1)
(1) 014212 022767 000510 164214  CMP      #STACK0,$SP          ;CHECK THE STACK POINTER (R1)
(1) 014220 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014222 104000          HLT          ;STACK POINTER (R1) NOT EQUAL TO #STACK0
(3) 014224 000503          503          ;THE ERROR NUMBER IS 503
(1)
(1) 014226 022767 014042 164202  CMP      #RTA63, ANS1          ;CHECK FIS TRAP RETURN ADDRESS
(1) 014234 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014236 104001          HLT+1       ;FIS TRAP AT WRONG ADDRESS
(3) 014240 000504          504          ;THE ERROR NUMBER IS 504
(1)
(1)
(1) 014242 022767 000177 164172  CMP      #000177,ANS3          ;CHECK DATA FROM THE STACK
(1) 014250 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014252 104004          HLT+4       ;DATA ON STACK (000177) CHANGED
(3) 014254 000505          505          ;THE ERROR NUMBER IS 505
(1)
(1) 014256 022767 134543 164160  CMP      #134543,ANS4          ;CHECK DATA FROM STACK
(1) 014264 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014266 104004          HLT+4       ;DATA ON STACK (134543) CHANGED
(3) 014270 000506          506          ;THE ERROR NUMBER IS 506
(1)
(1) 014272 022767 035700 164146  CMP      #035700,ANS5          ;CHECK DATA FROM STACK
(1) 014300 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014302 104006          HLT+6       ;DATA ON STACK (035700) CHANGED
(3) 014304 000507          507          ;THE ERROR NUMBER IS 507
(1)
(1) 014306 022767 143235 164134  CMP      #143235,ANS6          ;CHECK DATA FROM STACK
(1) 014314 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014316 104006          HLT+6       ;DATA ON STACK (143235) CHANGED
(3) 014320 000510          510          ;THE ERROR NUMBER IS 510
(1)
(1) 014322 005367 164206          DEC      TEMP          ;STAY IN THE LOOP FOR 30 TIMES
(1) 014326 001261          BNE      IS
(1)
(1) 014330 022626          END63:  CMP      (SP)+, (SP)+          ;RESTORE STACK POINTER TO 500
(1) 014332 122767 000063 164044  CMPB    #63,    $TESTN          ;CHECK THE TEST NUMBER
(1) 014340 001402          BEQ          .+6          ;BRANCH IF OK
(1) 014342 104000          HLT          ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 014344 000511          511          ;THE ERROR NUMBER IS 511
(2) 014346          MTPS    #340
(2) 014346 106427          .WORD   106400!..C
(1)

```



## K06

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-73  
 DVKACA.SRC INTERUPT ABORT TEST SECTION

SEQ 0075

```

(1) 014604 010067 163624      65:  MOV      RO,      SSP      ;SAVE STACK POINTER
(1) 014610 122767 000344 163614  CMPB    #344,    SPSW    ;CHECK PS AFTER INTERRUPT
(1) 014616 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014620 104000                HLT                    ;PS AFTER INTERRUPT NOT EQUAL TO LVLA
(3) 014622 000513                513                ;THE ERROR NUMBER IS 513
(1)
(1) 014624 022767 000510 163602  CMP     #STACK0, SSP  ;CHECK THE STACK POINTER (RO)
(1) 014632 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014634 104000                HLT                    ;STACK POINTER (RO) NOT EQUAL TO #STACK0
(3) 014636 000514                514                ;THE ERROR NUMBER IS 514
(1)
(1) 014640 022767 014454 163570  CMP     #RTA64, ANS1  ;CHECK FIS TRAP RETURN ADDRESS
(1) 014646 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014650 104001                HLT+1                ;FIS TRAP AT WRONG ADDRESS
(3) 014652 000515                515                ;THE ERROR NUMBER IS 515
(1)
(1)
(1) 014654 022767 040200 163560  CMP     #040200, ANS3 ;CHECK DATA FROM THE STACK
(1) 014662 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014664 104004                HLT+4                ;DATA ON STACK (040200) CHANGED
(3) 014666 000516                516                ;THE ERROR NUMBER IS 516
(1)
(1) 014670 005767 163550      TST     ANS4          ;CHECK DATA FROM STACK
(1) 014674 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014676 104004                HLT+4                ;DATA ON STACK (000000) CHANGED
(3) 014700 000517                517                ;THE ERROR NUMBER IS 517
(1)
(1) 014702 022767 107070 163536  CMP     #107070, ANS5 ;CHECK DATA FROM STACK
(1) 014710 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014712 104006                HLT+6                ;DATA ON STACK (107070) CHANGED
(3) 014714 000520                520                ;THE ERROR NUMBER IS 520
(1)
(1) 014716 022767 070707 163524  CMP     #070707, ANS6 ;CHECK DATA FROM STACK
(1) 014724 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014726 104006                HLT+6                ;DATA ON STACK (070707) CHANGED
(3) 014730 000521                521                ;THE ERROR NUMBER IS 521
(1)
(1) 014732 005367 163576      DEC     TEMP          ;STAY IN THE LOOP FOR 30 TIMES
(1) 014736 001262                BNE     IS
(1)
(1) 014740 022626 000064 163434  END64: CMP     (SP)+, (SP)+ ;RESTORE STACK POINTER TO 500
(1) 014742 122767                CMPB    #64,      $TESTN ;CHECK THE TEST NUMBER
(1) 014750 001402                BEQ     .+6          ;BRANCH IF OK
(1) 014752 104000                HLT                    ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 014754 000522                522                ;THE ERROR NUMBER IS 522
(2) 014756                MTPS    #340
(2) 014756 106427                .WORD  106400!..C
(1)

```

L06

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-74  
DVKACA.SRC INTERUPT ABORT TEST SECTION

SEQ 0076

7253	014762	012767	000377	163546	MOV	#377,	TIMES ;SET NUMBER OF ITERATIONS TO 377
7254	014770	010477	163552		MOV	R4,	TTYOUT ;RESTORE TTY INTERUPT VECTOR
7255	014774	005014			CLR	(R4)	
7256							

```

7258 ;*****
(1) ;
(1) .SBTTL END OF PASS ROUTINE
(1) ;*INCREMENT THE PASS NUMBER ($PASS)
(1) ;*TYPE "END PASS"
(1) ;*IF THERES A MONITOR GO TO IT
(1) ;*IF THERE ISN'T JUMP TO BEGIN
(1) ;*IF IT IS DESIRED TO HAVE A BELL INDICATE THE "END OF PASS" LOCATION
(1) ;*SENDMG CAN BE CHANGED TO 7.
(1)
(1) 014776 $EOP:
(1) 014776 104400 SCOPE
(1) 015000 005267 163402 INC $PASS ;; INCREMENT THE PASS NUMBER
(1) 015004 042767 100000 163374 BIC #100000,$PASS ;; DON'T ALLOW A NEG. NUMBER
(1) 015012 005327 DEC (PC)+ ;; LOOP?
(1) 015014 000001 $EOPCT: .WORD 1
(1) 015016 003015 BGT $DOAGN ;; YES
(1) 015020 012737 MOV (PC)+,2(PC)+ ;; RESTORE COUNTER
(1) 015022 000001 $ENDCT: .WORD 1
(1) 015024 015014 $EOPCT
(1) 015026 000004 015056 TYPE , $SENDMG ;; TYPE "END PASS"
(1) 015032 $GET42:
(1) 015032 013700 000042 MOV 2#42,R0 ;; GET MONITOR ADDRESS
(1) 015036 001405 BEQ $DOAGN ;; BRANCH IF NO MONITOR
(1) 015040 000005 RESET ;; CLEAR THE WORLD
(1) 015042 004710 $ENDAD: JSR PC,(R0) ;; GO TO MONITOR
(1) 015044 000240 NOP ;; SAVE ROOM
(1) 015046 000240 NOP ;; FOR
(1) 015050 000240 NOP ;; ACT11
(1) 015052 $DOAGN:
(1) 015052 000137 000600 JMP 2#BEGIN ;; RETURN
(1) 015056 005015 047105 020104 $SENDMG: .ASCII <15><12>/END PASS/
(1) 015064 040520 051523 $ENULL: .BYTE -1,-1,0 ;; NULL CHARACTER STRING
(1) 015070 377 377 000 .EVEN
(1) 015074
7259
7264 015022 000001 $ENDCT: 1

```

```

7268 ;*****
(1) .SBTTL SCOPE ROUTINE
(1)
(1) 015074 032737 000400 000422 SCOPES: BIT #SW08,2#SSWREG ;KILL LDUB OR LOOP ON SPEC. TEST
(1) 015102 001404 BEQ 1$
(1) 015104 123767 000422 163272 CMPB 2#SSWREG,$TESTN ;ON RIGHT TEST? *SW7-0*
(1) 015112 001431 BEQ OVER$
(1) 015114 032737 040000 000422 1$: BIT #SW14,2#SSWREG ;LOOP ON TEST
(1) 015122 001023 BNE KITS
(1) 015124 032737 004000 000422 BIT #SW11,2#SSWREG ;KILL ITERATIONS
(1) 015132 001412 BEQ SVLAD$
(1) 015134 105767 163343 TSTB $ICNT
(1) 015140 001404 BEQ 2$ ;BRANCH IF FIRST
(1) 015142 126767 163370 163333 CMPB TIMES,$ICNT ;DONE?
(1) 015150 001010 BNE KITS ;BRANCH IF NOT
(1) 015152 112767 000001 163323 2$: MOVB #1,$ICNT ;FIRST ITERATION
(1) 015160 105267 163220 SVLAD$: INCB $TESTN ;COUNT TEST NUMBERS
(1) 015164 011667 163300 MOV (6),LADS ;SAVE LOOP ADDRESS
(1) 015170 000002 RTI ;RETURN
(1)
(1) 015172 105267 163305 KITS: INCB $ICNT
(1) 015176 005767 163266 OVER$: TST LADS ;FIRST ONE?
(1) 015202 001766 BEQ SVLAD$
(1) 015204 016716 163260 MOV LADS,(6) ;FUDGE RETURN ADDRESS
(1) 015210 000002 RTI ;FIXES PS
(1)

```

```

7273
7274 ;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK
7275
7276 015212 005726
7277 015214 062705 000010
7278 015220 014546
7279 015222 014546
7280 015224 014546
7281 015226 014546
7282 015230 062705 000010
7286 015234
(1) 015234 106425
7290 015236 005205
7291 015240 012577 163220
7292 015244 012577 163216
7293 015250 000115
7294
7295
7296 ;SUBROUTINE TO POP 2 WORDS OFF THE STACK
7297 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
7298
7302 015252
(1) 015252 106767
7306 015256 042767 000020 163146
7307 015264 012604
7308 015266 012667 163144
7309 015272 012667 163142
7310 015276 010667 163132
7311 015302 000114
7312
7313
7314 ;SUBROUTINE TO POP 6 WORDS OFF THE STACK.
7315 ;THE FIRST TWO WERE PUT ON BY THE ERROR TRAP,
7316 ;THE LAST FOUR WERE THE ORIGINAL INPUT DATA.
7317 ;ALSO SAVES THE PS AND STACK POINTER.
7318
7322 015304
(1) 015304 106767
7326 015310 012604
7327 015312 012667 163120
7328 015316 011667 163116
7329 015322 042767 000020 163110
7330 015330 012746 015336
7331 015334 000002
7332 015336 012667 163100
7333 015342 012667 163076
7334 015346 012667 163074
7335 015352 012667 163072
7336 015356 010667 163052
7337 015362 000114
7338
7339 ;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK
7340
7341 015364 012704 000510
7342 015370 012524
7343 015372 012524
    
```

;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK

```

PUSHS: TST (SP)+ ;POP STACK BY 1
        ADD #10, R5 ;POINT TO END OF DATA
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        ADD #10, R5 ;POINT TO END OF DATA
        MTPS (R5)+ ;SET THE PROCESSOR STATUS
        .WORD 106400!..C
        INC R5
        MOV (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
        MOV (R5)+, @FISLVL ;TRAP STATUS
        JMP (R5) ;RETURN
    
```

;SUBROUTINE TO POP 2 WORDS OFF THE STACK  
 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)

```

POPS: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
        .WORD 106700!..C
        BIC #20, $PSW ;CLEAR T-BIT
        MOV (SP)+, R4 ;SAVE RTS ADDRESS
        MOV (SP)+, ANS1 ;SAVE THE ANSWER
        MOV (SP)+, ANS2
        MOV SP, $SP ;SAVE THE STACK POINTER
        JMP (R4) ;RETURN
    
```

;SUBROUTINE TO POP 6 WORDS OFF THE STACK.  
 ;THE FIRST TWO WERE PUT ON BY THE ERROR TRAP,  
 ;THE LAST FOUR WERE THE ORIGINAL INPUT DATA.  
 ;ALSO SAVES THE PS AND STACK POINTER.

```

POPEX: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
        .WORD 106700!..C
        MOV (SP)+, R4 ;SAVE RTS ADDRESS
        MOV (SP)+, ANS1 ;SAVE RTI ADDRESS
        MOV (SP), ANS2 ;SAVE RTI STATUS
        BIC #20, ANS2 ;CLEAR THE T-BIT
        MOV #18, -(SP)
        RTI ;RESTORE THE PROCESSOR STATUS
        IS: MOV (SP)+, ANS3 ;SAVE DATA
        MOV (SP)+, ANS4
        MOV (SP)+, ANS5
        MOV (SP)+, ANS6
        MOV SP, $SP ;SAVE SP
        JMP (R4) ;RTS
    
```

;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK

```

PUSHR: MOV #STACK0, R4 ;SET R4 TO STACK
        MOV (R5)+, (R4)+ ;PUT DATA ON STACK
        MOV (R5)+, (R4)+
    
```



```

7344 015374 012524      MOV      (R5)+, (R4)+ ;
7345 015376 012524      MOV      (R5)+, (R4)+ ;
7349 015400              MTPS     (R5)+      ;SET THE PROCESSOR STATUS
(1) 015400 106425      .WORD   106400!..C
7353 015402 005205      INC      RS
7354 015404 012577 163054      MOV      (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
7355 015410 012577 163052      MOV      (R5)+, @FISLVL ;TRAP STATUS
7356 015414 000205      RTS      RS          ;RETURN
7357
7358
7359 ;SUBROUTINE TO POP 2 WORDS OFF THE STACK
7360 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
7361
7365 015416              POPR:  MFPS     $PSW      ;SAVE PROCESSOR STATUS WORD
(1) 015416 106767      .WORD   106700!..C
7369 015422 042767 000020 163002      BIC      #20, $PSW      ;CLEAR T-BIT
7370 015430 016767 163060 163000      MOV      STACK4, ANS1   ;SAVE THE ANSWER
7371 015436 016767 163054 162774      MOV      STACK6, ANS2   ;
7372 015444 000207      RTS      PC
7373
7374
7375 ;SUBROUTINE TO POP 6 WORDS OFF THE STACKS.
7376 ;THE TWO OFF THE R6 STACK WERE PUT ON BY THE ERROR TRAP
7377 ;THE FOUR OFF THE SOFTWARE STACK WERE THE ORIGINAL INPUT DATA.
7378 ;ALSO SAVES THE PS AND STACK POINTER AFTER THE FIS TRAP.
7379
7383 015446              POPER:  MFPS     $PSW      ;SAVE PROCESSOR STATUS WORD
(1) 015446 106767      .WORD   106700!..C
7387 015452 012667 000056      POPER1: MOV      (SP)+, SAVRTS ;SAVE RTS ADDRESS
7388 015456 012667 162754      MOV      (SP)+, ANS1    ;SAVE RTI ADDRESS
7389 015462 011667 162752      MOV      (SP), ANS2     ;SAVE RTI STATUS
7390 015466 042767 000020 162744      BIC      #20, ANS2     ;CLEAR THE T-BIT
7391 015474 012746 015502      MOV      #18, -(SP)
7392 015500 000002      RTI
7393 015502 016767 163002 162732 18:  MOV      STACK0, ANS3   ;RESTORE PROCESSOR STATUS
7394 015510 016767 162776 162726      MOV      STACK2, ANS4   ;SAVE DATA
7395 015516 016767 162772 162722      MOV      STACK4, ANS5   ;
7396 015524 016767 162766 162716      MOV      STACK6, ANS6   ;
7397 015532 000137      JMP      @PC)+        ;SIMULATED RTS
7398 015534 000000      SAVRTS: 0
7399
7400 ;SUBROUTINE TO PUSH 4 WORDS ONTO THE PC STACK
7401
7402 015536 012504      PUSH7: MOV      (R5)+, R4    ;SET R4 TO STACK
7403 015540 012524      MOV      (R5)+, (R4)+  ;PUT DATA ON STACK
7404 015542 012524      MOV      (R5)+, (R4)+  ;
7405 015544 012524      MOV      (R5)+, (R4)+  ;
7406 015546 012524      MOV      (R5)+, (R4)+  ;
7410 015550              MTPS     (R5)+      ;SET THE PROCESSOR STATUS
(1) 015550 106425      .WORD   106400!..C
7414 015552 005205      INC      RS
7415 015554 012577 162704      MOV      (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
7416 015560 012577 162702      MOV      (R5)+, @FISLVL ;TRAP STATUS
7417 015564 000205      RTS      RS          ;RETURN
7418
7419 ;SUBROUTINE TO POP 4 WORDS OFF THE PC "STACK"

```

```

7420 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
7421
7425 015566 POP7: MFPS SPSW ;SAVE PROCESSOR STATUS WORD
      (1) 015566 106767 WORD 106700!..C
7429 015572 042767 000020 162632 BIC 820, SPSW ;CLEAR T-BIT
7430 015600 011600 MOV (SP), RO ;GET RETURN ADDRESS
7431 015602 162700 000014 SUB 814, RO ;POINT TO TOP OF "PC STACK"
7432 015606 012067 162624 MOV (RO)+, ANS1 ;SAVE 1ST HALF INPUT DATA
7433 015612 012067 162622 MOV (RO)+, ANS2 ;SAVE 2ND HALF INPUT DATA
7434 015616 010067 162612 MOV RO, SSP ;SAVE ASSUMED END PC "STACK POINTER"
7435 015622 012067 162614 MOV (RO)+, ANS3 ;SAVE 1ST HALF OF ANSWER
7436 015626 012067 162612 MOV (RO)+, ANS4 ;SAVE 2ND HALF OF ANSWER
7437 015632 000207 RTS PC
7438
7439 ;ERRONIOUS TRAP SERVICE ROUTINE
7440
7441 015634 104000 TRAPER: HLT ;FIS SHOULDN'T HAVE TRAPED
7442 015636 000523 523 ;THE ERROR NUMBER IS 523
7443 015640 000002 RTI
7444
    
```

```

7445 ;*****
(1) .SBTTL HLT ROUTINE (ERROR TYPEOUT)
(1) 015642 032737 002000 000422 HLTS: BIT #SW10,2#SSWREG ;SHOULD IT RING THE BELL ON ERROR?
(1) 015650 001402 BEQ 1$ ;NO - SKIP
(1) 015652 000004 000504 TYPE $SPELL ;RING BELL
(1) 015656 005267 162600 1$: INC ERRORS ;COUNT THE NUMBER OF ERRORS
(1) 015662 032737 020000 000422 BIT #SW13,2#SSWREG ;SKIP TYPEOUT IF SET
(1) 015670 001023 BNE 2$ ;SKIP TYPEOUTS
(1) 015672 000004 000472 TYPE RETURN
(1) 015676 013637 000402 MOV 2(6)+,2#SFATAL ;PLACE THE ERROR NUMBER IN LOCATION SFATAL
(1) 015702 014667 162522 MOV -(6),HLTADS ;PUT ADDRESS OF INSTRUCTION ON STACK
(1) 015706 162767 000002 162514 SUB #2,HLTADS
(2) 015714 017605 000000 MOV 2(6),TTY ;TYPE 2(6) IN OCTAL
(2) 015720 004767 000124 JSR X7,PRINTR ;TYPE LEADING ZERO'S
(1) 015724 062716 000002 ADD #2,(6) ;ADJUST THE RETURN ADDRESS
(1) 015730 000004 000500 TYPE $SPACE+3
(1) 015734 004767 000046 JSR PC,ERRORS ;GO TO USER ERROR ROUTINE
(1) 015740 105767 162454 2$: TSTB $ENV ;ARE WE RUNNING UNDER APT?
(1) 015744 001403 BEQ 4$ ;IF NOT THEN GO TO 4$
(1) 015746 005237 000400 INC 2#SMSTY ;OTHERWISE INFORM APT
(1) 015752 000777 BR ;AND LOOP
(1) 015754 005737 000422 4$: TST 2#SSWREG ;HALT ON ERROR
(1) 015760 100001 BPL .+4 ;SKIP IF CONTINUE
(1) 015762 000000 HALT ;HALT ON ERROR!
(1) 015764 032737 001000 000422 BIT #SW09,2#SSWREG ;CHECK FOR INHIBIT LOOP ON ERROR
(1) 015772 001001 BNE .+4 ;SKIP IF LOOP ON ERROR
(1) 015774 000002 RTI
(1) 015776 105067 162501 CLRB $ICNT
(1) 016002 000167 177164 JMP KITS ;LOOP ON TEST UNTIL NO ERRORS

```

7446

```

7448
7449
7450
7451
7452
7453 016006 117767 162416 162524 ERRORS: MOV B @HLTADS, TYPCNT ; TYPE COUNT IS LOW BYTE OF HLT
7454 016014 062767 000002 162516 AOB @2, TYPCNT ; TYPE COUNT = X+2
7455 016022 012703 000430 MOV @HLTADS, R3 ; TOP OF DATA TO BE TYPED
7456 016026
(1) 016026 012305 ERRIS: MOV (R3)+, TTY ; TYPE (R3)+ IN OCTAL
(1) 016030 004767 000014 JSR %7, PRINTR ; TYPE LEADING ZERO'S
7457 016034 000004 000501 TYPE, SPACE+4 ; SPACE
7458 016040 105367 162474 DECB TYPCNT ; CHECK FOR DONE
7459 016044 100370 BPL ERRIS ; BRANCH IF NOT DONE
7460 016046 000207 RTS PC
7461

```

7463	016050	112767	000001	162466	PRINTR:	MOVB	#1, .PR	; SET ZERO FILL SWITCH
(1)	016056	000402				BR	.+6	; SKIP
(1)	016060	005067	162460		PRINTS:	CLR	.PR	; SUPRESS LEADING ZERO'S
(1)	016064	112767	177772	162453		MOVB	#-6, .PR+1	; SET COUNT
(1)	016072	010446				MOV	R4 -(6)	; SAVE R4
(1)	016074	012704	016176			MOV	#.PRBUF, R4	; SET POINTER TO FIRST ASCII CHAR.
(1)	016103	105014				CLRB	(4)	; CLEAR FIRST BYTE
(1)	016102	000405				BR	.PRF	; ROTATE FIRST BIT
(1)	016104	105014			.PRL:	CLRB	(4)	; CLEAR BYTE OF CHARACTER
(1)	016106	006105				ROL	TTY	; ROTATE BIT INTO C
(1)	016110	106114				ROLB	(4)	; PACK IT
(1)	016112	006105				ROL	TTY	; ROTATE BIT INTO C
(1)	016114	106114				ROLB	(4)	; PACK IT
(1)	016116	006105			.PRF:	ROL	TTY	; ROTATE BIT INTO C
(1)	016120	106114				ROLB	(4)	; PACK IT
(1)	016122	105714				TSTB	(4)	; IS IT ZERO?
(1)	016124	001402				BEQ	.+6	; SKIP INC
(1)	016126	105267	162412			INCB	.PR	; SET FILL SWITCH
(1)	016132	105767	162406			TSTB	.PR	; CHECK FILL SWITCH
(1)	016136	001402				BEQ	.+6	; SKIP BITSET
(1)	016140	152724	000060			BISB	#'0, (4)+	; MAKE INTO ASCII CHAR
(1)	016144	105267	162375			INCB	.PR+1	; INC COUNT
(1)	016150	001355				BNE	.PRL	; REPEAT
(1)	016152	022704	016176			CMP	#.PRBUF, R4	; EMPTY BUFFER?
(1)	016156	001002				BNE	.+6	; SKIP IF NOT
(1)	016160	112, 24	000060			MOVB	#'0, (4)+	; LOAD 1 ZERO
(1)	016164	105014				CLRB	(4)	; NULL TERMINATOR
(1)	016166	000004	016176			TYPE	.PRBUF	; TYPE IT
(1)	016172	012604				MOV	(6)+, R4	; RESTORE R4
(1)	016174	000207				RTS	PC	; RETURN
(1)	016176	000004			.PRBUF:	.BLKW	4	; OUTPUT BUFFER

H07

```

7465 ;*****
(1) ;
(1) .SBTTL POWER DOWN AND UP ROUTINES
(1) ;
(1) :POWER DOWN ROUTINE
(1) 016206 012737 016330 000024 $PWRDN: MOV $SILLUP,2#$PWRVEC ;; SET FOR FAST UP
(1) 016214 012737 000340 000026 MOV #340,2#$PWRVEC+2 ;; PWRUP:
(3) 016222 010046 MOV R0,-(SP) ;; PUSH R0 ON STACK
(3) 016224 010146 MOV R1,-(SP) ;; PUSH R1 ON STACK
(3) 016226 010246 MOV R2,-(SP) ;; PUSH R2 ON STACK
(3) 016230 010346 MOV R3,-(SP) ;; PUSH R3 ON STACK
(3) 016232 010446 MOV R4,-(SP) ;; PUSH R4 ON STACK
(3) 016234 010546 MOV R5,-(SP) ;; PUSH R5 ON STACK
(1) 016236 010667 000072 MOV SP,$SAVR6 ;; SAVE SP
(1) 016242 012737 016254 000024 MOV $PWRUP,2#$PWRVEC ;; SET UP VECTOR
(1) 016250 000000 HALT
(1) 016252 000776 BR -.2 ;; HANG UP
(1) ;
(1) :POWER UP ROUTINE
(1) 016254 016706 000054 $PWRUP: MOV $SAVR6,SP ;; GET SP
(1) 016260 005067 000050 CLR $SAVR6 ;; WAIT LOOP FOR THE TTY
(1) 016264 005267 000044 IS: INC $SAVR6 ;; WAIT FOR THE INC
(1) 016270 001375 BNE IS OF WORD
(3) 016272 012605 MOV (SP)+,R5 ;; POP STACK INTO R5
(3) 016274 012604 MOV (SP)+,R4 ;; POP STACK INTO R4
(3) 016276 012603 MOV (SP)+,R3 ;; POP STACK INTO R3
(3) 016300 012602 MOV (SP)+,R2 ;; POP STACK INTO R2
(3) 016302 012601 MOV (SP)+,R1 ;; POP STACK INTO R1
(3) 016304 012600 MOV (SP)+,R0 ;; POP STACK INTO R0
(1) 016306 012737 016206 000024 MOV $PWRDN,2#$PWRVEC ;; SET UP THE POWER DOWN VECTOR
(1) 016314 012737 000340 000026 MOV #340,2#$PWRVEC+2 ;; PWRUP:
(1) 016322 000004 TYPE REPORT THE POWER FAILURE
(1) 016324 016336 $PWRMG: .WORD $POWER ;; POWER FAIL MESSAGE POINTER
(1) 016326 000002 RTI
(1) 016330 000000 $SILLUP: HALT ;; THE POWER UP SEQUENCE WAS STARTED
(1) 016332 000776 BR -.2 ;; BEFORE THE POWER DOWN WAS COMPLETE
(1) 016334 000000 $SAVR6: 0 ;; PUT THE SP HERE
(1) 016336 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER"
(1) 016344 000122 .EVEN
  
```

```

7470
7471          : *          TYPE OUT ROUTINE
7472          : *          -----
7473          : *
7474          : *
7475          : *          THIS ROUTINE IS USED TO TYPE ASCIZ MESSAGES
7476          : *
7477
7478 016346 132737 000040 000421 $TYPE: BITB    #40,@#SENVH ;HAS THE CONSOLE OUTPUTS BEEN SUPPRESSED?
7479 016354 001007          BNE        3$          ;IF SO THEN RETURN FROM THE SUBROUTINE VIA 3$
7480 016356 010046          MOV        RO,-(SP) ;OTHERWISE SAVE RO
7481 016360 017600 000002          MOV        @2(SP),RO ;GET THE ADDRESS OF THE ASSCIZ STRING
7482 016364 112046          2$:        MOVB    (RO)+,-(SP) ;PUSH THE CHARACTER TO BE TYPED ONTO STACK
7483 016366 001005          BNE        4$          ;BRANCH IF IT IS NOT THE TERMINATOR
7484 016370 005726          TST        (SP)+
7485 016372 012600          MOV        (SP)+,RO ;OTHERWISE RESTORE THE STACK AND RO
7486 016374 062716 000002          3$:        ADD        #2,(SP) ;ADJUST THE RETURN PC
7487 016400 000002          RTI          ;AND RETURN
7488
7489 016402 105777 162142          4$:        TSTB    @STPS ;IS THE PRINTER AVAILABLE?
7490 016406 100375          BPL        4$          ;IF NOT THEN LOOP HERE
7491 016410 112677 162136          MOVB    (SP)+,@STPB ;OUT PUT THE CHARACTER
7492 016414 000763          BR        2$          ;AND GO BACK
7493          000001          .END

```

A	=	015074	7261	7266											
ABASE	=	000000	5339												
ACD#1	=	000000	5339												
ACD#2	=	000000	5339												
ACPUOP	=	000000	5339												
ADD#0	=	000000	5339												
ADD#1	=	000000	5339												
ADD#10	=	000000	5339												
ADD#11	=	000000	5339												
ADD#12	=	000000	5339												
ADD#13	=	000000	5339												
ADD#14	=	000000	5339												
ADD#15	=	000000	5339												
ADD#2	=	000000	5339												
ADD#3	=	000000	5339												
ADD#4	=	000000	5339												
ADD#5	=	000000	5339												
ADD#6	=	000000	5339												
ADD#7	=	000000	5339												
ADD#8	=	000000	5339												
ADD#9	=	000000	5339												
ADEVCT	=	000000	5339												
ADEVN	=	000000	5339												
RENV	=	000000	5339												
RENVN	=	000000	5339												
AFATAL	=	000000	5339												
AMADR1	=	000000	5339												
AMADR2	=	000000	5339												
AMADR3	=	000000	5339												
AMADR4	=	000000	5339												
AMAMS1	=	000000	5339												
AMAMS2	=	000000	5339												
AMAMS3	=	000000	5339												
AMAMS4	=	000000	5339												
AMSGAD	=	000000	5339												
AMSGLG	=	000000	5339												
AMSGTY	=	000000	5339												
AMTYP1	=	000000	5339												
AMTYP2	=	000000	5339												
AMTYP3	=	000000	5339												
AMTYP4	=	000000	5339												
ANS1		000436	5348	5349	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157
			7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179
			7180	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206
			7210	7214	7215	7216	7217	7219	7223	7227	7231	7232	7238*	7243	7244
			7251	7252	7308*	7327*	7370*	7388*	7432*						
ANS2		000440	5350	5351	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157
			7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179
			7180	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206
			7210	7214	7215	7216	7217	7219	7223	7227	7231	7232	7238*	7243	7244
			7309*	7328*	7329*	7371*	7389*	7390*	7433*						
ANS3		000442	5352	5353	7159	7163	7164	7168	7169	7185	7189	7200	7202	7206	7210
			7219	7223	7227	7231	7232	7243	7251	7252	7332*	7393*	7435*		
ANS4		000444	5354	7159	7163	7164	7168	7169	7185	7189	7200	7202	7206	7210	7219
			7223	7227	7231	7232	7243	7251	7252	7333*	7394*	7436*			
ANS5		000446	5355	7163	7164	7168	7169	7185	7189	7206	7210	7223	7227	7231	7232







# M07

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 53-3  
 DVKACA.SRC CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0090

POPER	015446	7163	7168	7189	7206	7223	7227	7231	7243	7244	7251	7252	7383#	
POPER1	015452	7251	7252	7387#										
POPES	015304	7164	7169	7185	7210	7232	7322#							
POPR	015416	7147	7148	7149	7152	7153	7154	7155	7158	7163	7168	7173	7174	7176
		7178	7179	7180	7181	7189	7193	7194	7195	7196	7198	7206	7214	7216
		7217	7223	7227	7231	7243	7365#							
POPS	015252	7150	7151	7156	7157	7164	7169	7175	7177	7185	7197	7210	7215	7232
		7302#												
POP7	015566	7159	7200	7202	7219	7425#								
PRINTR	016050	7445	7456	7463#										
PRINTS	016060	7463#												
PUSHR	015364	7147	7148	7149	7152	7153	7154	7155	7158	7163	7168	7173	7174	7176
		7178	7179	7180	7181	7189	7193	7194	7195	7196	7198	7206	7214	7216
		7217	7223	7227	7231	7243	7244	7251	7252	7341#				
PUSHS	015212	7150	7151	7156	7157	7164	7169	7175	7177	7185	7197	7210	7215	7232
		7276#												
PUSH7	015536	7159	7200	7202	7219	7402#								
PWRVEC=	000024	5306#	7465*											
RESTR	000222	7116#												
RETURN	000472	5362#	7251	7252	7445									
RTA16	003260	7163#												
RTA17	003506	7164#												
RTA20	003742	7168#												
RTA21	004174	7169#												
RTA33	006146	7185#												
RTA34	006404	7189#												
RTA45	010250	7206#												
RTA46	010500	7210#												
RTA54	011676	7223#												
RTA55	012134	7227#												
RTA56	012372	7231#												
RTA57	012624	7232#												
RTA61	013320	7243#												
RTA62	013576	7244#												
RTA63	014042	7251#												
RTA64	014454	7252#												
R0	=%000000	5297#	7111*	7112*	7113	7127*	7135*	7136*	7137*	7138*	7147*	7174*	7179*	7206*
		7214*	7252*	7258*	7430*	7431*	7432	7433	7434	7435	7436	7465*	7480	7481*
		7482	7485*											
R1	=%000001	5298#	7148*	7153*	7168*	7173*	7198*	7223*	7251*	7465*				
R2	=%000002	5299#	7149*	7181*	7194*	7216*	7243*	7465*						
R3	=%000003	5300#	7163*	7178*	7189*	7196*	7217*	7455*	7456	7465*				
R4	=%000004	5301#	7155*	7158*	7176*	7193*	7227*	7237*	7238*	7251*	7252*	7254	7255*	7307*
		7311	7326*	7337	7341*	7342*	7343*	7344*	7345*	7402*	7403*	7404*	7405*	7406*
		7463*	7465*											
R5	=%000005	5302#	7147*	7148*	7149*	7150*	7151*	7152*	7153*	7154*	7155*	7156*	7157*	7158*
		7159*	7163*	7164*	7168*	7169*	7173*	7174*	7175*	7176*	7177*	7178*	7179*	7180*
		7181*	7185*	7189*	7193*	7194*	7195*	7196*	7197*	7198*	7200*	7202*	7206*	7210*
		7214*	7215*	7216*	7217*	7219*	7223*	7227*	7231*	7232*	7243*	7244*	7251*	7252*
		7277*	7278	7279	7280	7281	7282*	7286*	7290*	7291	7292	7293	7342	7343
		7344	7345	7349*	7353*	7354	7355	7356*	7402	7403	7404	7405	7406	7410*
		7414*	7415	7416	7417*	7465*								
		7387#	7398#											
SAVRTS	015534	5367#												
SAVTPS	000506	5307#	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157	7158
SCOPE =	104400	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180



DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 53-5  
DVKACA.SRC CROSS REFERENCE TABLE -- USER SYMBOLS

TST20	003710	7168#							
TST21	004146	7169#							
TST22	004400	7173#							
TST23	004540	7174#							
TST24	004674	7175#							
TST25	005030	7176#							
TST26	005166	7177#							
TST27	005322	7178#							
TST3	001212	7149#							
TST30	005462	7179#							
TST31	005622	7180#							
TST32	005762	7181#							
TST33	006120	7185#							
TST34	006352	7189#							
TST35	006610	7193#							
TST36	006744	7194#							
TST37	007104	7195#							
TST4	001346	7150#							
TST40	007240	7196#							
TST41	007374	7197#							
TST42	007530	7198#							
TST43	007666	7200#							
TST44	010042	7202#							
TST45	010216	7206#							
TST46	010452	7210#							
TST47	010704	7214#							
TST5	001500	7151#							
TST50	011042	7215#							
TST51	011176	7216#							
TST52	011334	7217#							
TST53	011470	7219#							
TST54	011644	7223#							
TST55	012102	7227#							
TST56	012340	7231#							
TST57	012576	7232#							
TST6	001632	7152#							
TST60	013030	7237#							
TST61	013246	7243#							
TST62	013524	7244#							
TST63	013742	7251#							
TST64	014354	7252#							
TST7	001772	7153#							
TTY	=/000005	5303	7445#	7456#	7463#				
TTYOUT	000546	538#	7251	7252	7254#				
TYPCNT	000540	5377#	7453#	7454#	7458#				
TYPE	= 000004	5316#	7251	7252	7258	7445	7457	7463	7465
YESRT	000542	5378#	7123						
%APTHD	000430	5340#	5341						
%BELL	000504	5366#	7445						
%CPUOP	000426	5339#							
%DEVCT	000410	5339#	7111						
%ORAGN	015052	7258#							
%ENDAD	015042	5330	7258#						
%ENDCT	015022	7258#	7262						
%ENDMG	015056	7258#							
%ENULL	015070	7258#							





COMMEN	974#																	
DUMP	5132#	7445	7456															
ENDCOM	986#																	
ESCAPE	1097#																	
MFPS	5070#	7238	7244	7302	7322	7365	7383	7425										
MTPS	5085#	7237	7243	7244	7251	7252	7286	7349	7410									
MULT	3466#																	
NEWTST	1030#																	
POP	1483#	5056#	7465															
PRINT	5142#																	
PUSH	1475#	5056#	7465															
REPORT	4437#																	
SDUMP	5137#																	
SETUP	801#																	
SKIP	1131#																	
SLASH	926#																	
STARS	895#	5055#	5319	5330	5339	5340	7104	7258	7268	7445	7449	7465						
TYPBIN	1419#																	
TYPDEC	1389#																	
TYPNAM	1177#																	
TYPNUM	1356#																	
TYPCS	1309#																	
TYPOCT	1272#																	
TYPTXT	1226#																	
\$ADDER	5605#	7163	7168															
\$ADDES	5507#	7164	7169															
\$ADDR	5443#	7147	7148	7149	7152	7153	7154	7155	7158									
\$ADDS	5383#	7150	7151	7156	7157													
\$ADRER	6110#	7243	7244															
\$ALL	6881#	7237																
\$ALL2	6914#	7238																
\$DIVER	6779#	7223	7227	7231														
\$DIVES	6681#	7232																
\$DIVR	6617#	7214	7216	7217														
\$DIVS	6553#	7215																
\$FATL	5059#	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157	7158	7159	7163			
	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180	7181	7185	7189	7193			
	7194	7195	7196	7197	7198	7200	7202	7206	7210	7214	7215	7216	7217	7219	7223			
	7227	7231	7232	7238	7243	7244	7251	7252	7442									
\$FINT	6972#	7251	7252															
\$FIS7	6031#	7159	7200	7202	7219													
\$HLT	5181#	7445																
\$MULR	6451#	7206																
\$MULES	6353#	7210																
\$MULR	6289#	7193	7194	7195	7196	7198												
\$MULS	6233#	7197																
\$OCTAL	5226#	7463																
\$SCOPE	5150#	7268																
\$SUBER	5929#	7189																
\$SUBES	5831#	7185																
\$SUBR	5767#	7173	7174	7176	7178	7179	7180	7181										
\$SUBS	5707#	7175	7177															
\$SHDOC	5099#	5283																
\$SESCA	1110#																	
\$SFATL	5066#	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157	7158	7159	7163			
	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180	7181	7185	7189	7193			







RESET	7258														
ROL	7463														
ROLB	7463														
RTI	7251	7252	7268	7331	7392	7443	7445	7465	7487						
RTS	7356	7372	7417	7437	7460	7463									
RTT	5378														
SUB	7431	7445													
TRAP	5307														
TST	7147	7148	7149	7150	7151	7154	7155	7156	7159	7163	7164	7174	7176	7193	7195
	7196	7198	7200	7202	7206	7216	7217	7219	7251	7252	7268	7276	7445	7484	
TSTB	7148	7159	7163	7174	7181	7214	7251	7252	7268	7445	7463	7489			
.ABS	5049														
.ASCII	7258														
.ASCIZ	5362	5363	7465												
.BLKW	7463														
.BYTE	5339	5364	7258												
.ENABL	4														
.END	7493														
.ENDC	5057	5283	5319	5328	5330	5339	5340	7104	7147	7148	7149	7150	7151	7152	7153
	7154	7155	7156	7157	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177
	7178	7179	7180	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206
	7210	7214	7215	7216	7217	7219	7223	7227	7231	7232	7238	7243	7244	7251	7252
	7258	7268	7445	7449	7463	7465									
.EVEN	5339	5365	7258	7465											
.IF	5057	5283	5319	5328	5330	5339	5340	7104	7147	7148	7149	7150	7151	7152	7153
	7154	7155	7156	7157	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177
	7178	7179	7180	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206
	7210	7214	7215	7216	7217	7219	7223	7227	7231	7232	7238	7243	7244	7251	7252
	7258	7268	7445	7449	7463	7465									
.IFF	5319	5330	5339	5340	7104	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156
	7157	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180
	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206	7210	7214	7215
	7216	7217	7219	7223	7227	7231	7232	7238	7243	7244	7251	7252	7258	7268	7445
	7449	7465													
.IFNE	7243	7244													
.IIF	5057	5339	7258	7463											
.IRP	5328	7465													
.LIST	2	4623	5053	5054	5281	5327	5328	5335	5339	7145	7147	7148	7149	7150	7151
	7152	7153	7154	7155	7156	7157	7158	7159	7162	7163	7164	7167	7168	7169	7172
	7173	7174	7175	7176	7177	7178	7179	7180	7181	7184	7185	7188	7189	7192	7193
	7194	7195	7196	7197	7198	7200	7202	7205	7206	7209	7210	7213	7214	7215	7216
	7217	7219	7222	7223	7226	7227	7230	7231	7232	7236	7237	7238	7241	7243	7244
	7249	7251	7252	7258	7263	7267	7271	7284	7285	7286	7289	7300	7301	7302	7305
	7320	7321	7322	7325	7347	7348	7349	7352	7363	7364	7365	7368	7381	7382	7383
	7386	7408	7409	7410	7413	7423	7424	7425	7428	7442	7463	7468			
.MACRO	39	81	166	308	485	515	586	747	801	895	926	974	986	1030	1064
	1097	1110	1131	1144	1177	1226	1272	1309	1356	1389	1419	1475	1483	1535	1739
	1947	2140	2228	2353	2450	2528	2613	2827	2919	2995	3095	3223	3286	3348	3466
	3504	3568	3666	3751	3790	3853	3891	3934	4032	4080	4350	4397	4437	4513	5059
	5066	5070	5085	5099	5132	5137	5142	5150	5181	5226	5383	5443	5507	5605	5707
	5767	5831	5929	6031	6110	6233	6289	6353	6451	6553	6617	6681	6779	6881	6914
	6972														
.MCALL	5055	5056													
.MEXIT	5339														
.NLIST	1	3	5050	5052	5279	5322	5328	5333	5339	7143	7147	7148	7149	7150	7151
	7152	7153	7154	7155	7156	7157	7158	7159	7160	7163	7164	7165	7168	7169	7170

	7173	7174	7175	7176	7177	7178	7179	7180	7181	7182	7185	7186	7189	7190	7193
	7194	7195	7196	7197	7198	7200	7202	7203	7206	7207	7210	7211	7214	7215	7216
	7217	7219	7220	7223	7224	7227	7228	7231	7232	7234	7237	7238	7239	7243	7244
	7247	7251	7252	7258	7260	7265	7269	7283	7286	7287	7288	7299	7302	7303	7304
	7319	7322	7323	7324	7346	7349	7350	7351	7362	7365	7366	7367	7380	7383	7384
	7385	7407	7410	7411	7412	7422	7425	7426	7427	7442	7463	7466			
.NTYPE	7237	7238	7243	7244	7251	7252	7286	7302	7322	7349	7365	7383	7410	7425	
.PAGE	5045	5047	5336	6232	7103	7146	7147	7148	7149	7150	7151	7152	7153	7154	7155
	7156	7157	7158	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180
	7181	7185	7189	7193	7194	7195	7196	7197	7198	7199	7201	7206	7210	7214	7215
	7216	7217	7218	7223	7227	7231	7232	7233	7242	7243	7244	7251	7252	7257	7268
	7272	7445	7447	7462	7464	7469									
.REPT	4624	5323													
.SBTTL	5280	5330	5334	5339	5340	7106	7144	7161	7166	7171	7183	7187	7191	7204	7208
	7212	7221	7225	7229	7235	7240	7248	7258	7268	7270	7445	7451	7463	7465	7467
.TITLE	5057														
.WORD	5330	5339	5340	5366	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157
	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180	7181
	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206	7210	7214	7215	7216
	7217	7219	7223	7227	7231	7232	7237	7238	7243	7244	7251	7252	7258	7286	7302
	7322	7349	7365	7383	7410	7425	7465								

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

\* DVKACA/CRF=DVKACA.SML DVKACA.SRC  
 RUN-TIME: 44 56 5 SECONDS  
 RUN-TIME RATIO: 568/106=5.3  
 CORE USED: 41K (81 PAGES)

