

DMC11

FREE RUNNING TESTS
MD-11-DZDMH-B

EP-DZDMH-B-DL-A
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FICHE 1 OF 1

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The microfiche card contains a grid of frames. The first column on the left contains small, illegible text, likely identifying information for each frame. The subsequent columns contain test data, including numerical values and graphical waveforms. The waveforms appear to be digital signals, possibly representing the output of the system under test. The data is organized into a structured format, likely corresponding to different test conditions or parameters.

EOF1DZDMGCSEQ
PDP10 PAGE: 0001

00010000

770712

PDP10 411

2:HDR1DZDMHBSEQ

00010000

770712

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZDMH-B-D
PRODUCT NAME: DMC11 FREE RUNNING TESTS
DATE: MAY 1977
MAINTAINER: DIAGNOSTICS
AUTHOR: FAY BASHAW

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1. ABSTRACT

The function of the DMC11 diagnostics is to verify that the option operates according to specifications. The diagnostics verify that there are no malfunctions and the all operations of the DMC11 are correct in its environment.

Parameters must be set up to alert the diagnostics to the DMC11 configuration. These parameters are contained in the STATUS TABLE and are generated in two ways: 1) Manual Input - the operator answers questions. 2) Autosizing - the program determines the parameters automatically.

DZDMH tests the DMC11-AR and DMC11-AL micro-processors (M8200-YA and M8200-YB), or the KMC11 micro-processor (M8204). Free running tests are performed. A line unit (M8201 or M8202) must be installed. DZDMH can be used as a heat test diagnostic by manufacturing.

Currently there are five off line diagnostics that are to be run in sequence to insure that if an error should occur it will be detected at an early stage.

NOTE: Additional diagnostics may be added in the future.

The five diagnostics are:

1. DZDMC [REV] Basic W/R and Micro-processor tests
2. DZDME [REV] DDCMP Line unit tests
3. DZDMF [REV] BITSTUFF Line unit tests
4. DZDMG [REV] CROM and Jump tests
5. DZDMH [REV] Free-running tests (Heat test tape)

2. REQUIREMENTS

2.1 EQUIPMENT

Any PDP11 family CPU (except an LSI-11) with minimum 8k memory
 ASR 33 (or equivalent)
 DMC11-AR with DMC11-DA or DMC11-FA or
 DMC11-AL with DMC11-MA or DMC11-MD

2.2 STORAGE

Program will use all 8K of memory except where ABL and BOOTSTRAP LOADER reside. Locations 1500 thru 1640; contain the "STATUS TABLE" information which is generated at start of diagnostics by manual input (questions) or automatically (auto-sizing). This area is an overlay area and should not be altered by the operator.

3. LOADING PROCEEDURE

3.1 METHOD

All programs are in absolute format and are loaded using the ABSOLUTE LOADER. NOTE: if the diagnostics are on a media such as DISK, MAGTAPE, DECTAPE, or CASSETTE; follow instructions for the monitor which has been provided on that specific media.

ABSOLUTE LOADER starting address *500

MEMORY * SIZE

4k	17
8k	37
12k	57
16k	77
20k	117
24k	137
28k	157

- 3.1.1 Place address of ABS loader into switch register.
(also place 'HALT' SW up)
- 3.1.2 Depress 'LOAD ADDRESS' key on console and release.
- 3.1.3 Depress 'START KEY' on console and release (program should now be loading into CPU)

4. STARTING PROCEDURE

- a. Set switch register to 000200
- b. Depress 'LOAD ADDRESS' key and release
- c. Set SWR to zero for 'AUTO SIZING' or SWR bit0=1 for manual input (questions) or SWR bit7=1 to use existing parameters set up by a previous start or a previously run DMC11 diagnostic.
- d. Depress 'START KEY' and release. The program will type Maindec Name and program name (if this was the first start up of the program) and also the following:

MAP OF DMC11 STATUS

PC	CSR	STAT1	STAT2	STAT3
---	---	---	---	---
001500	160010	145310	177777	000000
001510	160020	145320	177777	000000

The program will type 'R' and proceed to run the diagnostic. The above is only an example. This would indicate the status table starting at add. 1500 in the program. In this example the table contains the information and status of two DMC11'S. THE STATUS TABLE MUST BE VERIFIED BY THE USER IF AUTO SIZING IS DONE. For information of status table see section 8.4 for help.

If the diagnostic was started with SW00=1 indicating manual parameter input then the following shows an example of the questions asked and some example answers:

HOW MANY DMC11'S TO BE TESTED?1

01
 CSR ADDRESS?160010
 VECTOR ADDRESS?310
 BR PRIORITY LEVEL? (4,5,6,7)?5
 DCES MICRO-PROCESSOR HAVE CRAM? (Y OR N)N
 WHICH LINE UNIT? IF NONE TYPE "N", IF M8201 TYPE "1", IF M8202 TYPE "2"?1
 IS THE LOOP BACK CONNECTOR ON?Y
 SWITCH PAC#1 (DDCMP LINE#)?377
 SWITCH PAC#2 (BM873 BOOT ADD)?377

Following the questions the status map is printed out as described above, the information in the map reflects the answers to the questions. If the diagnostic was started with SW00=0 and SW07=0 (AUTO-SIZING) then no questions are asked and only the status-map is printed out. If AUTO-SIZING is used the status information must be verified to be correct (match the hardware). if it does not match the hardware the diagnostic must be restarted with SW00=1 and the questions answered.

4.1 CONTROL SWITCH SETTINGS

SW 15 Set: Halt on error
 SW 14 Set: Loop on current test
 SW 13 Set: Inhibit error print out
 SW 12 Set: Inhibit type out/abell on error.
 SW 11 Set: Inhibit iterations. (quick pass)
 SW 10 Set: Escape to next test on error
 SW 09 Set: Loop with current data
 SW 08 Set: Catch error and loop on it
 SW 07 Set: Use previous status table.
 SW 06 Set: Halt in ROMCLK routine before clocking
 micro-processor
 SW 05 Set: Reserved
 SW 04 Set: Reserved
 SW 03 Set: Reselect DMC11's desired active
 SW 02 Set: Lock on selected test
 SW 01 Set: Restart program at selected test
 SW 00 Set: Build new status table from questions. (If SW07=0
 and SW00=0 a new status table is built by
 auto-sizing)

Switch 06 and 08-15 are dynamic and can be changed as needed while the diagnostic is running. Switches 00-03 and switch 07 are static, and are used only on starting or restarting the diagnostic.

4.1.2 SWITCH REGISTER OPTIONS (at start up)

SW 01 RESTART PROGRAM AT SELECTED TEST. It is strongly suggested that at least one pass has been made before trying to select a test, the reason being is that the program has to clear areas and set up parameters. When this switch is used the diagnostic will ask TEST NO.? Answer by typing the number of the test desired and carriage return to begin execution at the selected test.

SW 02 LOCK ON SELECTED TEST. This switch when used with SW01 will cause the program to constantly loop on the selected test. Hitting any key on the console will let it advance to the next test and loop until a key is hit again. If SW02=0 when SW01 is used. The program will begin at the selected test and continue normal operations.

SW 03 RESELECT DMC11'S DESIRED ACTIVE. Please note that a message is typed out for setting the switch register equal to DMC11's active. this means if the system has four DMC11s; bits 00,01,02,03 will be set in loc 'DMACTV' from the switch register. Using this switch(SW00) alters that location;therefore if four DMC11s are in the system ***DO NOT*** set switches greater than SW 03 in the up position. this would be a fatal error. do not select more active DMC11s than there is information on in the status table.

METHOD: A: Load address 200
 B: Start with SW 00=1
 C: Program will type message
 D: Set a switch for each DMC desired active.
 EXAMPLE: If you have 4 DMC's but only want to run the first and the last set SWR bits 0 and 3 = 1. PRESS CONTINUE
 E: Number (IF VALID) will be in data lights (excluding 11/05)
 F: Set with any other switch settings desired. PRESS CONTINUE.

4.1.3 DYNAMIC SWITCHES

ERROR SWITCHES

1. SW 12 Delete print out/bell on error.
2. SW 13 Delete error printout.
3. SW 15 Halt on the error.
4. SW 08 Goto beginning of the test(on error).
5. SW 10 Goto next test(on error).

SCOPE SWITCHES

1. SW06 Halt in ROMCLK routine before clocking micro-processor instruction. This allows the operator to scope a micro-processor instruction in the static state before it is clocked. Hit continue to resume running.
2. SW09 (if enabled by 'SCOPl') on an error; If an '*' is printed in front of the test no. (ex. *TEST NO. 10) SW09 is incorporated in that test and therefore SW09 is usually the best switch for the scope loop (SW14=0, SW10=0, SW09=1, SW08=0). If SW09 is not enableed; and there is a HARD error (constant); SW08 is best. (SW14=1,0, SW10=0, SW09=0, SW08=1). for intermitent errors; SW14=1 will loop on test regardless of error or not error. (SW14=1, SW10=0, SW09=0, SW08=1,0)
3. SW11 Inhibit interations.
4. SW14 Loop on current test.

4.2 STARTING ADDRESS

Starting address is at 000200 there are no other starting addresses for the DMC11 diagnostics. (See Section 4.0)

NOTE: If address 000042 is non-zero the program assumes it is under ACT11 or XXDP control and will act accordingly after all available DMC11's are tested the program will return to 'XXDP' or 'ACT-11'.

5. OPERATING PROCEDURE

When program is initially started messages as described in section 4.0 will be printed, and program will begin running the diagnostic

5.2 PROGRAM AND/OR OPERATOR ACTION

The typical approach should be

1. Halt on error (via SW 15=1) when ever an error occurs.
2. Clear SW 15.
3. Set SW 14: (loop on this test)
4. Set SW 13: (inhibit error print out)

The TEST NUMBER and PC will be typed out and possibly an error message (this depends on the test) to give the operator an idea as to the source of the problem. If it is necessary to know more information concerning the error report; LOOK IN THE LISTING for that TEST NUMBER which was typed out and then NOTE THE PC of the ERROR REPORT this way the EXACT FUNCTION of the test CAN BE DETERMINED.

6. ERRORS

As described previously there will always be a TEST NUMBER and PC typed out at the time of an error (providing SW 13=0 and SW 12=0). in most cases additional information will be supplied in the the error message to give the operator an indication of the error.

6.2 ERROR RECOVERY

If for some reason the DMC11 should 'HANG THE BUS' (gain control of bus so that console manual functions are inhibited) an init or power down/up is necessary for operator to regain control of cpu. If this should happen; look in location 'TSTNO' (address 1226) for the number of the test that was running at the time of the catastrophic error. In this way the operator will have an idea as to what the DMC11 was doing at the time of the error.

7. RESTRICTIONS

7.1 STARTING RESTRICTIONS

See section 4. (PLEASE)
Status table should be verified regardless of how program was started. Also it is important to use this listing along with the information printed on the TTY to completely isolate problems.

7.2 OPERATING RESTRICTIONS

The first time a DMC11 diagnostic is loaded into core and run the STATUS TABLE must be set up. This is done by manual input (SW00=1) or by autosizing (SW00=0 and SW07=0). Thereafter however the status table need not be setup by subsequent restarts or even loading the next DMC diagnostic because the STATUS TABLE is overlayed. The current parameters in the STATUS TABLE are used when SW07=1 on start up.

7.3 HARDWARE CONFIGURATION RESTRICTIONS

DMC11(MB200)- Jumper W1 must be in, and switch 7 of E76 must be in the OFF position.

KMC(MB204)- Jumper W1 must be in.

LINE UNIT(MB201)- Jumpers W1, W2, and W4 must be IN. Jumpers W3, and W5 must be OUT. SW8 of E26 must be in the ON POSITION.

LINE UNIT (MB202)- Jumper W1 must be in. SW8 of E26 must be in the OFF position.

8. MISCELLANEOUS

8.1 EXECUTION TIME

All DMC11 device diagnostics will give an 'END PASS' message (providing no errors and sw12=0) within 4 mins. This is assuming SW11=1 (DELETE ITERATIONS) is set to give the fastest possible execution. The actual execution time depends greatly on the PDP11 CPU configuration and the amount of memory in the system.

8.2 PASS COMPLETE

NOTE: EVERY time the program is started; the tests will run as if SW11 (delete iterations) was up (=1). This is to 'VERIFY NO HARD ERRORS' as soon as possible. Therefore the first pass -EACH TIME PROGRAM IS STARTED- will be a 'QUICK PASS' until all DMC11's in system are tested. When the diagnostic has completed a pass the following is an example of the print out to be expected.

```
END PASS DZDMH CSR: 175000 VEC: 0300 PASSES: 000001
ERRORS: 000000
```

NOTE: The pass count and error counts are cummlitive for each DMC11 that is running, and are set to zero only when the diagnostic is started. Therefore after an overnight run for example, the total passes and errors for each DMC11 since the diagnostic was started are reflected in PASSES: and ERRORS:.

8.4 KEY LOCATIONS

- RETURN (1214) Contains the address where program will return when iteration count is reached or if loop on test is asserted.
- NEXT (1216) Contains the address of the next test to be performed.
- TSTNO (1226) Contains the number of the test now being performed.
- RUN (1316) The bit in 'RUN' always points to the DMC11 currently being tested. EXAMPLE: (RUN) 1302/0000000001000000 Means that DMC11 no.06 is the DMC11 now running.

DMC000-DMC17
DMST00-DMST17
(1500)-(1640)

These locations contain the information needed to test up to 16 (decimal) DMC11s sequentially. they contain the CSR, VECTOR and STATUS concerning the configuration of each DMC11.

- DMACTV (1306) Each bit set in this location indicates that the associated DMC11 will be tested in turn. EXAMPLE: (DMACTV) 1276/0000000000011111 means that DMC11 no. 00,01,02,03,04 will be tested. EXAMPLE: (DMACTV) 1276/0000000000010001 Means that DMC11 no. 00,04 will be tested.
- DMCSR (1404) Contains the CSR of the current DMC11 under test.

8.4A 'STATUS TABLE' (1500-1640)

The table is filled by AUTO SIZING or by the manual parameter input (questions) as described previously. Also if desired by user; the locations may be altered by hand (toggled in) to suit the specific configuration.

The example status map shown below contains information for two DMC11'S. the table can contain up to 16 DMC11'S. Following the map is a description of the bits for each map entry

MAP OF DMC11 STATUS

PC	CSR	STAT1	STAT2	STAT3
001500	160010	145310	177777	000000
001510	160020	016320	000000	000000

Each map entry contains 4 words which contain the status information for 1 DMC11. The PC shows where in core memory the first of the 4 words is. In the example above the first DMC'S status is in locations, 1500, 1502, 1504, and 1506. The second DMC status is located at 1510, 1512, 1514, and 1516. The information contained in each 4 word entry is defined as follows:

CSR: Contains DMC11 CSR address

STAT1: BITS 00-08 IS DMC11 VECTOR ADDRESS
BIT15=1 MICRO-PROCESSOR HAS CROM
BIT15=0 MICRO-PROCESSOR HAS CROM
BIT14=1 TURNAROUND CONNECTOR IS ON
BIT14=0 NO TURNAROUND CONNECTOR
BIT13=0 LINE UNIT IS AN M8201
BIT13=1 LINE UNIT IS AN M8202
BIT12=1 NO LINE UNIT
BITS 09-11 IS DMC11 BR PRIORITY LEVEL

STAT2: LOW BYTE IS SWITCH PAC#1 (DDCMP LINE NUMBER)
HIGH BYTE IS SWITCH PAC#2 (BM873 BOOT ADD)

STAT3: BIT0=1 PERFORM FREE RUNNING TESTS ON KMC
(MUST BE SET MANUALLY. SEE TEST 1)
BIT1=0 DMC11-AR (LOW SPEED)
BIT1=1 DMC11-AL (HIGH SPEED)

8.5 METHOD OF AUTO SIZING

8.5.1 FINDING THE CONTROL STATUS REGISTER.

The auto-sizing routine finds a DMC11 as follows: It starts at address 160000 and tests all address in increments of 10 up to and including address 167760. If the address does not time out, the following is done, the first CROM address is written to a 125252 then it is read back. If it contains a -1 or 125252 or a 626 or 16520 a DMC11 or KMC11 has been found, if not, the address is updated by 10 and the search continues. A -1 indicates a DMC11 with no CROM, a 125252 indicates a KMC11 with CROM, a 626 indicates a DMC11-AL and a 16520 indicates a DMC11-AR. Further tests are performed at this point to determine which line unit, if any, is installed, if a loop-back connector is installed and various switch settings on the line unit. THIS IS WHY THE STATUS TABLE MUST BE VERIFIED BY THE USER AND IF ANY OF THE INFORMATION DOES NOT AGREE WITH THE HARDWARE THE DIAGNOSTIC MUST BE RESTARTED AND THE QUESTIONS MUST BE ANSWERED. All DMC11's in the system will be found by the auto-sizer. If it does not find a DMC11 the diagnostic must be restarted and the questions answered.

8.5.2 FINDING THE VECTOR AND BR LEVEL

The vector area (address 300-776) is filled with the instruction IOT and '+2' (next address). The processor status is started at 7 and the DMC is programmed to interrupt. The PS is lowered by 1 until the DMC interrupts, a delay is made and if no interrupt occurs at PS level 3 (because of a bad DMC11) the program assumes vector address 300 at BR level 5 and the problem should be fixed in the diagnostic. Once the problem is fixed; the program should be re-setup again to get correct vector. If an interrupt occurred; the address to which the DMC11 interrupted to is picked up and reported as the vector. NOTE: if the vector reported is not the vector set up by you; there is a problem and AUTO SIZING should not be done.

8.5 SOFTWARE SWITCH REGISTER

If the diagnostic is run on an 11/04 or other CPU without a switch register then a software switch register is used to allow user the same switch options as described previously. If the hardware switch register does not exist or if one does and it contains all ones (177777) this software switch register is used.

Control:

To obtain control at any allowable time during execution of the diagnostic the operator types a CTRL G on the console terminal keyboard. As soon as the CTRL G is recognized, by the diagnostic, the following message will be displayed:

SWR=XXXXXX NEW?

Where XXXXXX is the current contents of the software switch register in octal. The software control routine will then await operator action. At which time the operator is required to type one or more of the legal characters: 1) 0 - 7, 2) line feed(<LF>), 3) carriage return(<CR>), or 4) control-U (CTRL U). No check is made for legality. If the input character is not a <LF>, <CR>, or CTRL U it is assumed to be an octal digit.

To change the contents of the SSR the operator simply types the new desired value in octal - leading zeros need not be typed. And terminates the input string with a <CR> or <LF> depending on the program action desired as described below. The input value will be truncated to the last 6 digits typed. At least one digit must be typed on any given input string prior to the terminator before a change to the SSR will occur.

When the input string is terminated with a <CR> the diagnostic will continue execution from the point at which it was interrupted. If a <CR> is the only thing typed the program will continue without changing the SSR. The <LF> differs from the <CR> by restarting the program as if it were restarted at address 200.

If a CTRL U is typed at any point in the input string prior to the terminator the input value will be disregarded and the prompt displayed (SWR = XXXXXX NEW?).

To set the SSR for the starting switches, first load the diagnostic, then hit CTRL G, then start the diagnostic.

DZDMH LST

B02

DECDOC VER 00.04 11-JUL-77 12:36 PAGE 01 PAGE: 0014

DOCUMENT

DZDMH LST

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1666 ***** TEST 1 *****
FREE RUNNING FLAG MODE DATA TEST
TRANSMIT A MESSAGE AND VERIFY THE RECEIVED DATA
LINE UNIT LOOP IS SET FOR THIS TEST.
ALL FOLLOWING TESTS ARE FREE RUNNING AND ARE PERFORMED
ONLY ON DMC'S WITH LINE UNITS. IF YOU WISH TO PERFORM
THESE FREE RUNNING TESTS ON A KMC (NORMALLY THE FREE RUNNING TESTS
WILL FAIL ON A KMC, THE TIMER IS TOO FAST) THEN YOU MUST
MANUALLY SET BIT0 OF STAT3 IN THE STATUS MAP. ALSO THE KMC
MUST HAVE THE MICRO-CODE LOADED BY PREVIOUSLY RUNNING
DZDMG TEST 2 AND THEN LOADING AND STARTING DZDMH
WITH SWITCH 7 = 1

1857 ***** TEST 2 *****
OVERUN TEST
IN FREE RUNNING MODE SEND MESSAGE WITH NO RECEIVE
BUFFER AVAILABLE, VERIFY THAT AN OVERRUN ERROR OCCURS

1937 ***** TEST 3 *****
LOST DATA TEST
IN FREE RUNNING MODE SEND A MESSAGE LONGER THAN THE RECEIVE
BUFFER, VERIFY THAT A LOST DATA ERROR OCCURS.

2003 ***** TEST 4 *****
TRANSMIT NON-EXISTENT MEMORY TEST
IN FREE RUNNING MODE, LOAD A TRANSMIT BA THAT WILL TIME OUT
VERIFY THAT A NON-EXISTENT MEMORY ERROR OCCURS

2066 ***** TEST 5 *****
RECEIVE NON-EXISTENT MEMORY TEST
IN FREE RUNNING MODE, LOAD A RECEIVE BA THAT WILL TIME OUT
VERIFY THAT A NON-EXISTENT MEMORY ERROR OCCURS

2132 ***** TEST 6 *****
PROCESSOR ERROR TEST
IN FREE RUNNING MODE, DO A BASE TRANSFER REQUEST AFTER A
BASE HAS BEEN SET UP, VERIFY THAT A PROCESSOR ERROR OCCURS.

2192 ***** TEST 7 *****
PROCESSOR ERROR TEST
IN FREE RUNNING MODE DO A RQI WITH AN ILLEGAL IO CODE
VERIFY THAT A PROCESSOR ERROR OCCURS

2252 ***** TEST 10 *****
HALF DUPLEX TEST
IN FREE RUNNING MODE, SET HALF DUPLEX AND L U LOOP
SEND A MESSAGE AND VERIFY THAT THERE ARE NO DONES

2291 ***** TEST 11 *****

RESUME TEST
THIS TEST SENDS AND RECEIVES A BUFFER AND SHUTS DOWN THE
DMC. THEN A MASTER CLEAR IS ISSUED AND A BASE WITH RESUME
BIT SET IS GIVEN, ANOTHER BUFFER IS SENT AND RECEIVED.
DATA IS CHECKED.

2380 ***** TEST 12 *****

FREE RUNNING DATA TEST (INTERRUPT DRIVEN EXERCISER)
THIS TEST REPEATEDLY QUEUES UP 7 RECEIVE BUFFERS AND
7 TRANSMIT BUFFERS AND CHECKS DATA WHEN ALL 7 BUFFERS
ARE RECEIVED. TRANSMIT COUNTS RANGE FROM 2 TO 104.
DATA IS A BINARY COUNT PATTERN. THE RESUME FUNCTION
IS CHECKED IN THIS TEST. THIS TEST USES THE TURNAROUND CONNECTOR
IF IT IS PRESENT, OTHERWISE LINE UNIT LOOP IS SET.

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;REGISTER DEFINITIONS
;-----

000000	R0=%0	;GENERAL REGISTER
000001	R1=%1	;GENERAL REGISTER
000002	R2=%2	;GENERAL REGISTER
000003	R3=%3	;GENERAL REGISTER
000004	R4=%4	;GENERAL REGISTER
000005	R5=%5	;GENERAL REGISTER
000006	SP=%6	;PROCESSOR STACK POINTER
000007	PC=%7	;PROGRAM COUNTER

;LOCATION EQUIVALENCIES
;-----

177776	PS=177776	;PROCESSOR STATUS WORD
001200	STACK=1200	;START OF PROCESSOR STACK

;INSTRUCTION DEFINITIONS
;-----

005746	PUSH1SP=5746	;DECREMENT PROCESSOR STACK 1 WORD
005726	POP1SP=5726	;INCREMENT PROCESSOR STACK 1 WORD
010046	PUSHR0=10046	;SAVE R0 ON STACK
012600	POP R0=12600	;RESTORE R0 FROM STACK
024646	PUSH2SP=24646	;DECREMENT STACK TWICE
022626	POP2SP=22626	;INCREMENT STACK TWICE
	.EQUIV EMT,HLT	;BASIC DEFINITION OF ERROR CALL

;BIT DEFINITIONS
;-----

100000	BIT15=100000
040000	BIT14=40000
020000	BIT13=20000
010000	BIT12=10000
004000	BIT11=4000
002000	BIT10=2000
001000	BIT9=1000
000400	BIT8=400
000200	BIT7=200
000100	BIT6=100
000040	BIT5=40
000020	BIT4=20
000010	BIT3=10
000004	BIT2=4
000002	BIT1=2
000001	BIT0=1

TRAPCATCHER FOR UNEXPECTED INTERUPTS

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000000

000024 005336
000026 000340
000030 004750
000032 000340
000034 004716
000036 000340

000040 000000
000042 000000
000044 000000
000046 003522
000052 000052
000052 000000

000174 000000
000176 000000

000200 000137 002002

001000 005377 040515 047111
001025 104 041515 030461

001200

001200 177570
001202 177570

```
*****
-----
:TRAPCATCAER FOR ILLEGAL INTERRUPTS
:THE STANDARD "TRAP CATCHER" IS PLACED
:BETWEEN ADDRESS 0 TO ADDRESS 776.
:IT LOOKS LIKE "PC+2 HALT".
-----
*****

.=0
:STANDARD INTERRUPT VECTORS
-----

.=24
.PFAIL          :POWER FAIL HANDLER
340             :SERVICE AT LEVEL 7
.HLT            :ERROR HANDLER
340             :SERVICE AT LEVEL 7
.TRPSRV         :GENERAL HANDLER DISPATCH SERVICE
340             :SERVICE AT LEVEL 7

.=40
0               :SAVE FOR ACT-11 OR XXDP
0               :RETURN ADDRESS IF UNDER ACT-11 OR XXDP
0               :SAVE FOR ACT-11 OR XXDP
$ENDAD         :FOR USE WITH ACT-11 OR XXDP

.=52
0               :ACT-11 PROGRAM CHARACTERISTICS

.=174
DISPREG:0      :SOFTWARE DISPLAY REGISTER
SWREG: 0       :SOFTWARE SWITCH REGISTER

.=200
JMP .START     :GO TO START OF PROGRAM

.=1000
MTITLE: .ASCII <377><12>/MAINDEC-11-DZDMH-B/<377>
        .ASCIIZ /DMC11 FREE RUNNING TESTS/<377>

.=1200
:INDIRECT POINTERS TO SWITCH REGISTER AND LIGHT DISPLAY
-----

DISPLAY:177570
SWR: 177570
```

```

144
145
146
147
148 001204 177560 TKCSR: 177560 ; TELETYPE KEYBOARD CONTROL REGISTER
149 001206 177562 TKDBR: 177562 ; TELETYPE KEYBOARD DATA BUFFER
150 001210 177564 TPCSR: 177564 ; TELEPRINTER CONTROL REGISTER
151 001212 177566 TPDBR: 177566 ; TELEPRINTER DATA BUFFER
152
153 ;PROGRAM CONTROL PARAMETERS
154 ;-----
155
156 001214 000000 RETURN: 0 ; SCOPE ADDRESS FOR LOOP ON TEST
157 001216 000000 NEXT: 0 ; ADDRESS OF NEXT TEST TO BE EXECUTED
158 001220 000000 LOCK: 0 ; ADDRESS FOR LOCK ON CURRENT DATA
159 001222 000003 ICOUNT: 3 ; NUMBER OF ITERATIONS THAT CURRENT TEST WILL BE
160 001224 000000 LPCNT: 0 ; NUMBER OF ITERATIONS COMPLETED
161 001226 000000 TSTNO: 0 ; NUMBER OF TEST IN PROGRESS
162 001230 000000 PASCNT: 0 ; NUMBER OF PASSES COMPLETED
163 001232 000000 ERRCNT: 0 ; TOTAL NUMBER OF ERRORS
164 001234 000000 LSTERR: 0 ; PC OF LAST ERROR CALL
165
166 ;PROGRAM VARIABLES
167 ;-----
168
169 001236 000000 STRISW: 0 ; SWITCHES AT START OF PROGRAM
170 001240 000000 STAT: 0 ; DM STATUS WORD STORAGE
171 001242 000000 CLKX: 0
172 001244 000000 MASKX: 0
173 001246 000000 TEMP1: 0 ; TEMPORARY STORAGE
174 001250 000000 TEMP2: 0 ; TEMPORARY STORAGE
175 001252 000000 TEMP3: 0 ; TEMPORARY STORAGE
176 001254 000000 TEMP4: 0 ; TEMPORARY STORAGE
177 001256 000000 TEMPS: 0 ; TEMPORARY STORAGE
178 001260 000000 SAVR0: 0 ; R0 STORAGE
179 001262 000000 SAVR1: 0 ; R1 STORAGE
180 001264 000000 SAVR2: 0 ; R2 STORAGE
181 001266 000000 SAVR3: 0 ; R3 STORAGE
182 001270 000000 SAVR4: 0 ; R4 STORAGE
183 001272 000000 SAVR5: 0 ; R5 STORAGE
184 001274 000000 SAVSP: 0 ; STACK POINTER STORAGE
185 001276 000000 SAVPC: 0 ; PROGRAM COUNTER STORAGE
186 001300 000000 ZERO: 0
187 001302 000001 ONE: 1
188 001304 000000 MEMLIM: 0 ; HIGHEST LOCATION FOR NPR'S
189 001306 000001 DMACTV: .BLKW 1 ; DMC11'S SELECTED ACTIVE.
190 001310 000001 DMNUM: .BLKW 1 ; OCTAL NUMBER OF DMC11'S.
191 001312 000001 SAVACT: .BLKW 1 ; ORIGINAL ACTV DEVICES
192 001314 000001 SAVNUM: .BLKW 1 ; WORKABLE NUMBER
193 001316 000000 RUN: 0 ; POINTER TO RUNNING DEVICE.
194
195 001320 001472 .EVEN ; TABLE POINTER.
196 001322 001676 CREAM: DM.MAP-6 ; TABLE POINTER
MILK: CNT.MAP-4

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PROGRAM PARAMETERS, VARIABLES, AND TRAP CALLS.

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001324 000
001325 000
001326 000
001327 000

001330 104400
001330 003576
001332 104401
003736
104402
001334 003766
104403
001336 004050
104404
001340 004154
104405
001342 004174
104406
001344 004374
104407
001346 004434
104410
001350 004466
104411
001352 004472
104412
001354 005466
104413
001356 005436
104414
001360 005504
104415
001362 005552
104416
001364 005616

PROGRAM CONTROL FLAGS

INIFLG: .BYTE 0 ;PROGRAM INITIALIZATION FLAG
ERRFLG: .BYTE 0 ;ERROR OCCURED FLAG
LOKFLG: .BYTE 0 ;LOCK ON CURRENT TEST FLAG
QV.FLG: .BYTE 0 ;QUICK VERIFY FLAG.
;ON FIRST PASS OF EACH DMC11 ITERATIONS WILL BE
 .EVEN

;DEFINITIONS FOR TRAP SUBROUTINE CALLS
;POINTERS TO SUBROUTINES CAN BE FOUND
;IN THE TABLE IMMEDIATLY FOLLOWING THE DEFINITIONS

TRPTAB:
SCOPE=TRAP+0 ;CALL TO SCOPE LOOP AND ITERATION HANDLER
 .SCOPE
SCOPI=TRAP+1 ;CALL TO LOOP ON CURRENT DATA HANDLER
 .SCOPI
TYPE=TRAP+2 ;CALL TO TELETYPE OUTPUT ROUTINE
 .TYPE
INSTR=TRAP+3 ;CALL TO ASCII STRING INPUT ROUTINE
 .INSTR
INSTER=TRAP+4 ;CALL TO INPUT ERROR HANDLER
 .INSTER
PARAM=TRAP+5 ;CALL TO NUMERICAL DATA INPUT ROUTINE
 .PARAM
SAVOS=TRAP+6 ;CALL TO REGISTER SAVE ROUTINE
 .SAVOS
RESOS=TRAP+7 ;CALL TO REGISTER RESTORE ROUTINE
 .RESOS
CONVRT=TRAP+10 ;CALL TO DATA OUTPUT ROUTINE
 .CONVRT
CNVRT=TRAP+11 ;CALL TO DATA OUTPUT ROUTINE WITHOUT CR/LF.
 .CNVRT
MSTCLR=TRAP+12 ;CALL TO ISUE A MASTER CLEAR
 .MSTCLR
DELAY=TRAP+13 ;CALL TO DELAY
 .DELAY
ROMCLK=TRAP+14 ;CALL TO CLOCK ROM ONCE
 .ROMCLK
DATACLK=TRAP+15 ;CALL TO CLK DATA
 .DATACLK
TIMER=TRAP+16 ;CALL TO DELAY A CLOCK TICK
 .TIMER

PROGRAM PARAMETERS, VARIABLES, AND TRAP CALLS.

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248 ;DMC11 CONTROL INDICATORS FOR CURRENT DMC11 UNDER TEST
249 ;-----
250
251 001366 000000 STAT1: 0
252 001370 000000 STAT2: 0
253 001372 000000 STAT3: 0
254
255 ;DMC11 VECTOR AND REGISTER INDIRECT POINTERS
256 ;-----
257
258 001374 000000 DMRVEC: 0 ; POINTER TO DMC11 RECEIVER INTERRUPT VECTOR
259 001376 000000 DMRLVL: 0 ; POINTER TO DMC11 RECEIVER INTERRUPT SERVICE PS
260 001400 000000 DMTVEC: 0 ; POINTER TO DMC11 TRANSMITTER INTERRUPT VECTOR
261 001402 000000 DMTLVL: 0 ; POINTER TO DMC11 TRANSMITTER INTERRUPT SERVICE PS
262 001404 000000 DMCSR: 0 ; POINTER TO DMC11 CONTROL STATUS REGISTER
263 001406 000000 DMCSRH: 0 ; POINTER TO DMC11 CONTROL STATUS REGISTER HIGH BYTE.
264 001410 000000 DMCTL: 0 ; POINTER TO DMC11 CONTROL OUT REGISTER
265 001412 000000 DMFO4: 0 ; POINTER TO DMC11 PORT REGISTER(SEL 4)
266 001414 000000 DMPO6: 0 ; POINTER TO DMC11 PORT REGISTER(SEL 6)
267
268 ;TEMP STORAGE
269 ;-----
270
271 001416 000000 TEMP: 0
272 001460 .=. +40
273
274 ;DMC11 STATUS TABLE AND ADDRESS ASSIGNMENTS
275 ;-----
276
277 . =1500
278 001500 DM.MAP:
279 001500 000001 DMC00: .BLKW 1 ; CONTROL STATUS REGISTER FOR DMC11 NUMBER 00
280 001502 000001 DMS100: .BLKW 1 ; VECTOR FOR DMC11 NUMBER 00
281 001504 000001 DMS200: .BLKW 1 ; DDCMP LINE# FOR DMC11 NUMBER 00
282 001506 000001 DMS300: .BLKW 1 ; 3RD STATUS WORD
283
284 001510 000001 DMC01: .BLKW 1 ; CONTROL STATUS REGISTER FOR DMC11 NUMBER 01
285 001512 000001 DMS101: .BLKW 1 ; VECTOR FOR DMC11 NUMBER 01
286 001514 000001 DMS201: .BLKW 1 ; DDCMP LINE# FOR DMC11 NUMBER 01
287 001516 000001 DMS301: .BLKW 1 ; 3RD STATUS WORD
288
289 001520 000001 DMC02: .BLKW 1 ; CONTROL STATUS REGISTER FOR DMC11 NUMBER 02
290 001522 000001 DMS102: .BLKW 1 ; VECTOR FOR DMC11 NUMBER 02
291 001524 000001 DMS202: .BLKW 1 ; DDCMP LINE# FOR DMC11 NUMBER 02
292 001526 000001 DMS302: .BLKW 1 ; 3RD STATUS WORD
293
294 001530 000001 DMC03: .BLKW 1 ; CONTROL STATUS REGISTER FOR DMC11 NUMBER 03
295 001532 000001 DMS103: .BLKW 1 ; VECTOR FOR DMC11 NUMBER 03
296 001534 000001 DMS203: .BLKW 1 ; DDCMP LINE# FOR DMC11 NUMBER 03
297 001536 000001 DMS303: .BLKW 1 ; 3RD STATUS WORD
298
299 001540 000001 DMC04: .BLKW 1 ; CONTROL STATUS REGISTER FOR DMC11 NUMBER 04
300 001542 000001 DMS104: .BLKW 1 ; VECTOR FOR DMC11 NUMBER 04
301 001544 000001 DMS204: .BLKW 1 ; DDCMP LINE# FOR DMC11 NUMBER 04
302 001546 000001 DMS304: .BLKW 1 ; 3RD STATUS WORD
303
    
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PROGRAM PARAMETERS, VARIABLES, AND TRAP CALLS.

304	001550	000001	DMCR05: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 05
305	001552	000001	DMS105: .BLKW	1	;VECTOR FOR DMC11 NUMBER 05
306	001554	000001	DMS205: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 05
307	001556	000001	DMS305: .BLKW	1	;3RD STATUS WORD
308					
309	001560	000001	DMCR06: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 06
310	001562	000001	DMS106: .BLKW	1	;VECTOR FOR DMC11 NUMBER 06
311	001564	000001	DMS206: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 06
312	001566	000001	DMS306: .BLKW	1	;3RD STATUS WORD
313					
314	001570	000001	DMCR07: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 07
315	001572	000001	DMS107: .BLKW	1	;VECTOR FOR DMC11 NUMBER 07
316	001574	000001	DMS207: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 07
317	001576	000001	DMS307: .BLKW	1	;3RD STATUS WORD
318					
319	001600	000001	DMCR10: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 10
320	001602	000001	DMS110: .BLKW	1	;VECTOR FOR DMC11 NUMBER 10
321	001604	000001	DMS210: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 10
322	001606	000001	DMS310: .BLKW	1	;3RD STATUS WORD
323					
324	001610	000001	DMCR11: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 11
325	001612	000001	DMS111: .BLKW	1	;VECTOR FOR DMC11 NUMBER 11
326	001614	000001	DMS211: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 11
327	001616	000001	DMS311: .BLKW	1	;3RD STATUS WORD
328					
329	001620	000001	DMCR12: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 12
330	001622	000001	DMS112: .BLKW	1	;VECTOR FOR DMC11 NUMBER 12
331	001624	000001	DMS212: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 12
332	001626	000001	DMS312: .BLKW	1	;3RD STATUS WORD
333					
334	001630	000001	DMCR13: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 13
335	001632	000001	DMS113: .BLKW	1	;VECTOR FOR DMC11 NUMBER 13
336	001634	000001	DMS213: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 13
337	001636	000001	DMS313: .BLKW	1	;3RD STATUS WORD
338					
339	001640	000001	DMCR14: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 14
340	001642	000001	DMS114: .BLKW	1	;VECTOR FOR DMC11 NUMBER 14
341	001644	000001	DMS214: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 14
342	001646	000001	DMS314: .BLKW	1	;3RD STATUS WORD
343					
344	001650	000001	DMCR15: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 15
345	001652	000001	DMS115: .BLKW	1	;VECTOR FOR DMC11 NUMBER 15
346	001654	000001	DMS215: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 15
347	001656	000001	DMS315: .BLKW	1	;3RD STATUS WORD
348					
349	001660	000001	DMCR16: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 16
350	001662	000001	DMS116: .BLKW	1	;VECTOR FOR DMC11 NUMBER 16
351	001664	000001	DMS216: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 16
352	001666	000001	DMS316: .BLKW	1	;3RD STATUS WORD
353					
354	001670	000001	DMCR17: .BLKW	1	;CONTROL STATUS REGISTER FOR DMC11 NUMBER 17
355	001672	000001	DMS117: .BLKW	1	;VECTOR FOR DMC11 NUMBER 17
356	001674	000001	DMS217: .BLKW	1	;DDCMP LINE# FOR DMC11 NUMBER 17
357	001676	000001	DMS317: .BLKW	1	;3RD STATUS WORD
358					
359	001700	000000	DM.END: 000000		

PROGRAM PARAMETERS, VARIABLES, AND TRAP CALLS.

Line	Code	Value	Label	Description
360			;DMC11 PASS COUNT AND ERROR COUNT TABLE	
361			-----	
362				
363			CNT.MAP:	
364	001702		PACT00: 0	;PASS COUNT FOR DMC11 NUMBER 00
365	001702	000000	ERCT00: 0	;ERROR COUNT FOR DMC11 NUMBER 00
366	001704	000000		
367				
368	001706	000000	PACT01: 0	;PASS COUNT FOR DMC11 NUMBER 01
369	001710	000000	ERCT01: 0	;ERROR COUNT FOR DMC11 NUMBER 01
370				
371	001712	000000	PACT02: 0	;PASS COUNT FOR DMC11 NUMBER 02
372	001714	000000	ERCT02: 0	;ERROR COUNT FOR DMC11 NUMBER 02
373				
374	001716	000000	PACT03: 0	;PASS COUNT FOR DMC11 NUMBER 03
375	001720	000000	ERCT03: 0	;ERROR COUNT FOR DMC11 NUMBER 03
376				
377	001722	000000	PACT04: 0	;PASS COUNT FOR DMC11 NUMBER 04
378	001724	000000	ERCT04: 0	;ERROR COUNT FOR DMC11 NUMBER 04
379				
380	001726	000000	PACT05: 0	;PASS COUNT FOR DMC11 NUMBER 05
381	001730	000000	ERCT05: 0	;ERROR COUNT FOR DMC11 NUMBER 05
382				
383	001732	000000	PACT06: 0	;PASS COUNT FOR DMC11 NUMBER 06
384	001734	000000	ERCT06: 0	;ERROR COUNT FOR DMC11 NUMBER 06
385				
386	001736	000000	PACT07: 0	;PASS COUNT FOR DMC11 NUMBER 07
387	001740	000000	ERCT07: 0	;ERROR COUNT FOR DMC11 NUMBER 07
388				
389	001742	000000	PACT10: 0	;PASS COUNT FOR DMC11 NUMBER 10
390	001744	000000	ERCT10: 0	;ERROR COUNT FOR DMC11 NUMBER 10
391				
392	001746	000000	PACT11: 0	;PASS COUNT FOR DMC11 NUMBER 11
393	001750	000000	ERCT11: 0	;ERROR COUNT FOR DMC11 NUMBER 11
394				
395	001752	000000	PACT12: 0	;PASS COUNT FOR DMC11 NUMBER 12
396	001754	000000	ERCT12: 0	;ERROR COUNT FOR DMC11 NUMBER 12
397				
398	001756	000000	PACT13: 0	;PASS COUNT FOR DMC11 NUMBER 13
399	001760	000000	ERCT13: 0	;ERROR COUNT FOR DMC11 NUMBER 13
400				
401	001762	000000	PACT14: 0	;PASS COUNT FOR DMC11 NUMBER 14
402	001764	000000	ERCT14: 0	;ERROR COUNT FOR DMC11 NUMBER 14
403				
404	001766	000000	PACT15: 0	;PASS COUNT FOR DMC11 NUMBER 15
405	001770	000000	ERCT15: 0	;ERROR COUNT FOR DMC11 NUMBER 15
406				
407	001772	000000	PACT16: 0	;PASS COUNT FOR DMC11 NUMBER 16
408	001774	000000	ERCT16: 0	;ERROR COUNT FOR DMC11 NUMBER 16
409				
410	001776	000000	PACT17: 0	;PASS COUNT FOR DMC11 NUMBER 17
411	002000	000000	ERCT17: 0	;ERROR COUNT FOR DMC11 NUMBER 17
412				


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524 002312 000000          HALT          ;STOP THE SHOW
525 002314 000776          BR          -2          ;DISQUALIFY CONTINUE SWITCH
526 002316 004737 010512 17$: JSR          PC,AUTO.SIZE ;GO DO THE AUTO SIZE
527 002322 105737 001324 16$: TSTB         INIFLG      ;FIRST TIME?
528 002326 001410          BEQ          21$          ;BR IF YES
529 002330 105737 001236  TSTB         STRTSW      ;IF USING SAME PARAMETERS DONT TYPE MAP
530 002334 100431          BMI          1$          ;
531 002336 032737 000006 001236 BIT          #BIT1!BIT2,STRTSW;IS TEST NO. OR LOCK SELECTED
532 002344 001403          BEQ          24$          ;IF NO THEN TYPE STATUS
533 002346 000424          BR          1$          ;IF YES DO NOT TYPE STATUS
534 002350 005137 001324 21$: COM          INIFLG      ;SET FLAG
535 002354 104402 006224 24$: TYPE         ,XHEAD      ;TYPE HEADER
536 002360 012704 001500  MOV          #DM.MAP,R4     ;SET POINTER
537 002364 010437 001246 5$:  MOV          R4,TEMP1     ;SET ADDRESS
538 002370 012437 001250  MOV          (R4)+,TEMP2    ;SET CSR
539 002374 001411          BEQ          1$          ;ALL DONE IF ZERO
540 002376 012437 001252  MOV          (R4)+,TEMP3    ;SET STAT1
541 002402 012437 001254  MOV          (R4)+,TEMP4    ;SET STAT2
542 002406 012437 001256  MOV          (R4)+,TEMP5    ;SET STAT3
543 002412 104410          CONVRT         ;TYPE OUT STATUS MAP
544 002414 007454          XSTATQ          ;
545 002416 000762          BR          5$          ;
546 002420 012700 001500 1$:  MOV          #DM.MAP,RO   ;RO POINTS TO STATUS TABLE

```

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*****
;*AUTO SIZE TEST
;*THIS TEST VERIFYS THAT THE DMC11S AND/OR KMC11S ARE AT THE CORRECT FLOATING
;*ADDRESSES FOR YOUR SYSTEM. IF THIS TEST FAILS, IT IS NOT A HARDWARE ERROR.
;*CHECK THE ADDRESSES OF ALL FLOATING DEVICES (DJ,DH,DA,DU,DUP,LK,DMC,DZ,KMC).
;*IF THERE ARE NO OTHER FLOATING DEVICES BEFORE THE DMC11, THE FIRST
;*DMC11 ADDRESS IS 760070, KMC11 IS 760110. NO DEVICE SHOULD EVER BE AT
;*ADDRESS 760000. THIS TEST MAY REQUIRE 2 OR MORE ATTEMPTS TO GET THE
;*RIGHT ADDRESSES. AFTER YOU HAVE CHANGED THE ADDRESS TO WHAT IT TOLD
;*YOU THE FIRST TIME, IT MAY COME BACK AND TELL YOU A DIFFERENT ADDRESS
;*THE NEXT TIME YOU RUN IT. PLEASE HAVE PATIENCE, THE FINAL ADDRESS
;*WILL BE CORRECT (AS LONG AS ALL DEVICES IN FRONT OF THE DMC'S ARE
;*CORRECT).
*****

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```

563 002424 013746 000004          MOV          @#4,-(SP)      ;SAVE LOC 4
564 002430 013746 000006          MOV          @#6,-(SP)      ;SAVE LOC 6
565 002434 005037 000006          CLR          @#6          ;CLEAR VEC+2
566 002440 005037 001252          CLR          TEMP3        ;CLEAR FLAG
567 002444 005005          CLR          R5          ;R5=0=DMC, R5=-1=KMC
568 002446 011037 001404  AUSTRT: MOV          (RO),DMCSR     ;GET NEXT DMC CSR
569 002452 001564          BEQ          AUDONE       ;BR IF DONE
570 002454 005705          TST          R5          ;DMC OR KMC?
571 002456 001005          BNE          1$          ;BR IF KMC
572 002460 032760 100000 000002 BIT          #BIT15,2(RO)   ;CHECK FOR DMC CSR
573 002466 001061          BNE          SKIP        ;SKIP IF NOT DMC
574 002470 000404          BR          2$          ;ITS A DMC SO CONTINUE
575 002472 032760 100000 000002 1$: BIT          #BIT15,2(RO)   ;CHECK FOR KMC CSR
576 002500 001454          BEQ          SKIP        ;SKIP IF NOT KMC
577 002502 012737 002674 000004 2$: MOV          #NODEV,@#4    ;SET UP FOR TIMEOUT
578 002510 005705          TST          R5          ;DMC OR KMC?
579 002512 001003          BNE          3$          ;BR IF KMC

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580	002514	012703	000006		MOV	#6,R3	;R3 IS COUNT OF DEVICES BEFORE DMC
581	002520	000402			BR	4\$;GO ON
582	002522	012703	000010	3\$:	MOV	#10,R3	;R3 IS COUNT OF DEVICES BEFORE KMC
583	002526	012702	003010	4\$:	MOV	#DEV TAB,R2	;R2 IS DEVICE TABLE POINTER
584	002532	012701	160010		MOV	#160010,R1	;START WITH ADDRESS 160010
585	002536	005711		FLOAT:	TST	(R1)	;CHECK ADDRESS IN R1
586	002540	111204			MOVB	(R2),R4	;IF NO TIMEOUT, GET NEXT ADDRESS
587	002542	060401			ADD	R4,R1	;IN R1
588	002544	005201			INC	R1	
589	002546	040401			BIC	R4,R1	
590	002550	005703			TST	R3	;ANY MORE DEVICES TO CHECK FOR?
591	002552	001371			BNE	FLOAT	;BR IF YES
592	002554	012737	002700	000004	MOV	#ERR,2#4	;OK ONLY DMC'S ARE LEFT, SET UP FOR TIMEOUT
593	002562	010137	003022		MOV	R1,XLOC	;SAVE FIRST DMC/KMC ADDRESS
594	002566	005705		FY:	TST	R5	;DMC OR KMC?
595	002570	001005			BNE	1\$;BR IF KMC
596	002572	032760	100000	000002	BIT	#BIT15,2(R0)	;CHECK FOR DMC CSR
597	002600	001014			BNE	SKIP	;SKIP IF NOT DMC
598	002602	000404			BR	2\$;ITS A DMC SO CONTINUE
599	002604	032760	100000	000002	1\$:	BIT	#BIT15,2(R0)
600	002612	001407			BEQ	SKIP	;CHECK FOR KMC CSR
601	002614	005711			2\$:	TST	(R1)
602	002616	020137	001404		CMP	R1,DMCSR	;CHECK DMC ADDRESS
603	002622	001411			BEQ	OK	;DOES IT MATCH
604	002624	062701	000010		ADD	#10,R1	;BR IF YES
605	002630	000756			BR	FY	;GET NEXT DMC ADDRESS
606	002632	062700	000010		SKIP:	ADD	#10,R0
607	002636	011037	001404		MOV	(R0),DMCSR	;DO IT AGAIN
608	002642	001470			BEQ	AUDONE	;SKIP TO NEXT CSR IN TABLE
609	002644	000750			BR	FY	;GET NEXT CSR
610	002646	062700	000010		OK:	ADD	#10,R0
611	002652	062737	000010	003022		ADD	#10,XLOC
612	002660	011037	001404		MOV	(R0),DMCSR	;BR IF DONE
613	002664	001457			BEQ	AUDONE	;ELSE CONTINUE
614	002666	013701	003022		MOV	XLOC,R1	;SKIP TO NEXT DMC CSR
615	002672	000735			BR	FY	;UPDATE EXPECTED DMC/KMC ADDRESS
616	002674	122243			NODEV:	CMPB	(R2)+,-(R3)
617	002676	000002			RTI		;GET NEXT DMC/KMC CSR
618	002700	005737	001252		ERR:	TST	TEMP3
619	002704	001014				BNE	1\$
620	002706	104402			TYPE		;CHECK FLAG IF = 0 TYPE HEADER
621	002710	007223			CONERR		;SKIP HEADER
622	002712	012737	002700	001276	MOV	#ERR,SAVPC	;TYPEOUT HEADER MESSAGE
623	002720	104411			CONVRT		;CONFIGURATION ERROR!!!!
624	002722	002770			ERRPC		;SAVE PC FOR TYPEOUT
625	002724	104402			TYPE		;TYPE OUT ERROR PC
626	002726	007277			CNERR		;TYPE REST OF HEADER
627	002730	012737	177777	001252	1\$:	MOV	#-1,TEMP3
628	002736	010137	001262			MOV	R1,SAVR1
629	002742	104410			CONVRT		;SET FLAG SO IT ONLY GETS TYPED ONCE
630	002744	002776			CONTAB		;SAVE R1 FOR TYPEOUT
631	002746	005705			TST	R5	;TYPE CSR VALUES
632	002750	001003			BNE	3\$;DMC OR KMC ?
633	002752	104402			TYPE		;BR IF KMC
634	002754	007320			DMCM		
635	002756	000402			BR	4\$;CONTINUE

692	003162	013746	000004		MOV	2#4, -(SP)	:SAVE LOC 4
693	003166	005000			CLR	RO	:START AT 0
694	003170	012737	003234	000004	MOV	2\$, 2#4	:SET UP FOR TIME OUT
695	003176	005037	000006		CLR	2#6	:TO AUTOSIZE MEMORY
696	003202	005720		6\$:	TST	(RO)+	:CHECK ADDRESS IN RO
697	003204	022700	157776		CMP	157776, RO	:IS IT AT LEAST 28K
698	003210	001374			BNE	6\$:BR IF NO
699	003212	162700	007776		SUB	7776, RO	:SAVE 2K FOR MONITORS
700	003216	010037	001304	7\$:	MOV	RO, MEM LIM	:STORE MEMORY LIMIT
701	003222	012637	000004		MOV	(SP)+, 2#4	:RESTORE LOC 4
702	003226	012637	000006		MOV	(SP)+, 2#6	:RESTORE LOC 6
703	003232	000413			BR	10\$:CONTINUE
704	003234	022626		2\$:	CMP	(SP)+, (SP)+	:ADJUST STACK
705	003236	162700	000004		SUB	4, RO	:GET LAST GOOD ADDRESS
706	003242	162700	007776		SUB	7776, RO	:SAVE 2K FOR MONITORS
707	003246	022700	030000		CMP	30000, RO	:IS IT 8K?
708	003252	001361			BNE	7\$:BR IF NO
709	003254	012700	037400		MOV	37400, RO	:IF 8K DON'T SAVE 2K
710	003260	000756			BR	7\$:
711	003262	012737	000340	177776	MOV	340, PS	:LOCK OUT INTERRUPTS
712	003270	032737	000004	001236	BIT	BIT2, STRTSW	:CHECK FOR LOCK ON TEST
713	003276	001411			BEQ	1\$:BR IF NO LOCK DESIRED.
714	003300	104402	006043		TYPE	, MLOCK	:TYPE LOCK SELECTED.
715	003304	012737	000240	003612	MOV	1NOP, TTST	:ADJUST SCOPE ROUTINE.
716	003312	012737	000240	003614	MOV	1NOP, TTST+2	:SET UP TO LOCK
717	003320	000406			BR	3\$:CONTINUE ALONG.
718	003322	013737	003730	003612	MOV	BRW, TTST	:PREPARE NORMAL SCOPE ROUTINE
719	003330	013737	003732	003614	MOV	BRX, TTST+2	:LOCK NOT SELECTED, SET UP FOR NORMAL SCOPE LOOP
720	003336	012737	010060	001214	MOV	1CYCLE, RETURN	:START AT "CYCLE" FIND WHICH DEVICE TO TEST
721	003344	032737	000002	001236	BIT	1SW01, STRTSW	:IS TEST NO. SELECTED?
722	003352	001002			BNE	5\$:BR IF YES
723	003354	104402	005755		TYPE	, MR	:TYPE R
724	003360	000177	175630	5\$:	JMP	2RETURN	:START TESTING

```

725                                     :END OF PASS
726                                     :TYPE NAME OF TEST
727                                     :UPDATE PASS COUNT
728                                     :CHECK FOR EXIT TO ACT-11
729                                     :RESTART TEST
730
731 003364 000005                       .EOP: RESET                       :MAKE THE WORLD CLEAN AGAIN.
732 003366 005037 001234                 CLR          LSTERR                   :CLEAR LAST ERROR PC
733 003372 105037 001325                 CLR          ERRFLG                   :CLEAR ERROR FLAG
734 003376 005237 001230                 INC          PASCNT                   :UPDATE PASS COUNT
735 003402 013777 001230 175570         MOV          PASCNT, @DISPLAY          :DISPLAY PASS COUNT
736 003410 104402 005733                 TYPE        ,MEPASS                  :TYPE END PASS
737 003414 104402 006072                 TYPE        ,MCSRX                    :TYPE CSR
738 003420 104411 003546                 CNVRT       ,XCSR                     :SHOW IT
739 003424 104402 006100                 TYPE        ,MVECX                    :TYPE VECTOR
740 003430 104411 003554                 CNVRT       ,XVEC                     :SHOW IT
741 003434 104402 006106                 TYPE        ,MPASSX                   :TYPE PASSES
742 003440 104411 003562                 CNVRT       ,XPASS                    :SHOW IT
743 003444 104402 006117                 TYPE        ,MERRX                    :TYPE ERRORS
744 003450 104411 003570                 CNVRT       ,XERR                     :SHOW IT
745 003454 013700 001322                 MOV          MILK, RO                  :GET POINTER TO PASS COUNT
746 003460 013720 001230                 MOV          PASCNT, (RO)+             :STORE PASS COUNT FOR THIS DMC11
747 003464 013720 001232                 MOV          ERRCNT, (RO)+            :STORE ERROR COUNT FOR THIS DMC11
748 003470 005337 001314                 DEC          SAVNUM                    :ARE ALL DEVICES TESTED?
749 003474 001017                         BNE          RESTR                     :BR IF NO.
750 003476 112737 000377 001327         MOV          #377, QV.FLG              :SET THE QUICK VERIFY FLAG.
751 003504 013737 001310 001314         MOV          DMNUM, SAVNUM             :RESTORE THE COUNT
752 003512 013701 000042                 MOV          @#42, R1                 :CHECK FOR ACT-11 OR DDP
753 003516 001406                         BEQ          RESTR                     :IF NOT, CONTINUE TESTING
754 003520 000005                       RESET                                  :STOP THE SHOW--CLEAR THE WORLD
755 003522
756 003522 004711                       $ENDAD: JSR          PC, (R1)
757 003524 000240                       NOP
758 003526 000240                       NOP
759 003530 000240                       NOP
760 003532 000240                       NOP
761 003534 012737 010060 001214         RESTR: MOV          #CYCLE, RETURN
762 003542 000137 010060                 JMP          CYCLE
763 003546 000001                       XCSR: 1
764 003550 006 002                       .BYTE 6,2
765 003552 001404                       DMCSR
766 003554 000001                       XVEC: 1
767 003556 004 002                       .BYTE 4,2
768 003560 001374                       DMRVEC
769 003562 000001                       XPASS: 1
770 003564 006 002                       .BYTE 6,2
771 003566 001230                       PASCNT
772 003570 000001                       XERR: 1
773 003572 006 002                       .BYTE 6,2
774 003574 001232                       ERRCNT
775
776                                     :SCOPE LOOP AND INTERATION HANDLER
777                                     :-----
778
779 003576 004737 007606                       .SCOPE: JSR          PC, CKSWR          :CHECK FOR SOFT SWR
780 003602 010016                       MOV          RO, (SP)                 :SAVE RO ON THE STACK
    
```


GENERAL UTILITIES (TYPEOUT, ERROR, SCOPE, ETC)

```

791 003604 032777 040000 175370
792 003612 001407
783 003614 000437
784 003616 005737 003734
785 003622 001434
786 003624 005037 003734
787 003630 000415
788 003632 032777 004000 175342
789 003640 001011
790 003642 105737 001327
791 003646 001406
792 003650 005237 001224
793 003654 023737 001224 001222
794 003662 101414
795 003664 105037 001325
796 003670 005037 001224
797 003674 005037 001220
798 003700 012737 000020 001222
799 003706 013737 001216 001214
800 003714 011600
801 003716 022626
802 003720 013701 001404
803 003724 000177 175264
804 003730 001407
805 003732 000437
806 003734 000000
807
808
809
810
811 003736 004737 007606
812 003742 032777 001000 175232
813 003750 001405
814 003752 005737 001220
815 003756 001402
816 003760 013716 001220
817 003764 000002
818
819
820
821
822 003766 010546
823 003770 017605 000002
824 003774 062766 000002 000002
825 004002 005737 010016
826 004006 001004
827 004010 032777 010000 175164
828 004016 001012
829 004020 105715
830 004022 100002
831 004024 104402 005672
832 004030 105777 175154
833 004034 100375
834 004036 112577 175150
835 004042 001357
836 004044 012605

TTST: BIT #BIT14, QSWR ;"LOOP ON THIS TEST"?
      BEQ 1$ ;BR IF NO. (IF LOCK SW01=1; THIS LOC =240)
      BR 3$ ;GOTO 3$ (IF LOCK SW01=1; THIS LOC =240)
      TST DONE ;WAS TKCSR DONE SET?
      BEQ 3$ ;BR IF NO (LOCKED ON TEST)
      CLR DONE ;YES, CLEAR FLAG
      BR 2$ ;GO TO NEXT TEST
1$: BIT #SW11, QSWR ;DELETE ITERATION? (QUICK PASS)
   BNE 2$ ;BR IF YES
   TSTB QV.FLG ;HAVE PASSES BEECOMPLETED?
   BEQ 2$ ;BR IF QUICK PASS.
   INC LPCNT ;UPDATE ITERATION COUNTER
   CMP LPCNT, ICOUNT ;ARE ALL ITERATIONS DONE??
   BLOS 3$ ;BR IF NOT YET
2$: CLRB ERRFLG ;PREPARE FOR NEW TEST
   CLR LPCNT ;START ICOUNTER AT 0
   CLR LOCK
   MOV #20, ICOUNT ;RESET ITERATIONS
   MOV NEXT, RETURN ;GET NEXT TEST
   MOV (SP), RO ;POP RO OFF OF THE STACK
   POP2SP ;FAKE AN "RTI"
   MOV DMCSR, R1 ;R1 CONTAINS BASE DMC ADDRESS
   JMP QRETURN ;GO DO THE TEST
BRW: 1407
BRX: 437
DONE: 0

;CHECK FOR FREEZE ON CURRENT DATA
-----
.SCOPI: JSR PC, CKSWR ;CHECK FOR SOFT SWR
        BIT #SW09, QSWR ;IS SW09=1(SET)?
        BEQ 1$ ;BR IF NOT SET.
        TST LOCK
        BEQ 1$
        MOV LOCK, (SP) ;GOTO THE ADDRESS IN LOCK.
1$: RTI ;GO BACK.

;TELETYPE OUTPUT ROUTINE
-----
.TYPE: MOV R5, -(SP) ;SAVE R5 ON THE STACK.
        MOV @2(SP), R5 ;GET ADDRESS OF MESSAGE.
        ADD #2, 2(SP) ;POP OVER ADDRESS.
4$: TST SWFLG ;SOFT SWR MESSAGE?
   BNE 1$ ;IF YES TYPE IT OUT REGARDLESS OF SW12
   BIT #SW12, QSWR ;INHIBIT ALL PRINT OUT??
   BNE 3$ ;BR IF NO PRINT OUT WANTED (SW12=1)
   TSTB (R5) ;IS NUMBER MINUS? (MSB=1(BIT7))
   BPL 2$ ;BR IF NUMBER IS PLUS
   TYPE MCRLF ;TYPE A CR/LF!
2$: TSTB QTPCSR ;TTY READY?
   BPL 2$ ;BR IF NO.
   MOVB (R5)+, QTPDBR ;PRINT CURRENT CHAR.
   BNE 4$ ;IF NOT ZERO KEEP PRINTING!
3$: MOV (SP)+, R5 ;END OF OUTPUT. RESTORE R5

```



```

837 004046 000002 RTI ;GO HOME
838 ;-----
839
840 004050 010346 .INSTR: MOV R3,-(SP) ;SAVE R3 ON STACK
841 004052 010446 MOV R4,-(SP) ;SAVE R4 ON STACK
842 004054 017637 000004 004072 MOV 4(SP),MSG
843 004062 062766 000002 000004 ADD #2,4(SP)
844 004070 104402 .INST1: TYPE
845 004072 000000 .MSG: 0
846 004074 012704 007502 MOV #INBUF,R4
847 004100 012703 000007 MOV #7,R3
848 004104 105777 175074 1$: TSTB @TKCSR
849 004110 100375 BPL 1$
850 004112 117714 175070 MOVB @TKDBR,(R4)
851 004116 142714 000200 BICB #200,(R4)
852 004122 122427 000015 CMPB (R4)+,#15
853 004126 001417 BEQ INSTR2
854 004130 105777 175054 2$: TSTB @TPCSR
855 004134 100375 BPL 2$
856 004136 017777 175044 175046 MOV @TKDBR,@TPDBR
857 004144 005303 DEC R3
858 004146 001356 BNE 1$
859 004150 012604 MOV (SP)+,R4
860 004152 012603 MOV (SP)+,R3
861 004154 104402 005666 .INSTE: TYPE MQM
862 004160 010346 MOV R3,-(SP)
863 004162 010446 MOV R4,-(SP)
864 004164 000741 BR .INST1
865 004166 012604 INSTR2: MOV (SP)+,R4 ;RESTORE R4
866 004170 012603 MOV (SP)+,R3 ;RESTORE R3
867 004172 000002 RTI
868
869 ;CONVERT ASCII STRING TO OCTAL
870 ;-----
871
872 004174 010546 .PARAM: MOV R5,-(SP)
873 004176 010446 MOV R4,-(SP)
874 004200 016605 000004 MOV 4(SP),R5
875 004204 012537 004364 MOV (R5)+,LOLIM
876 004210 012537 004366 MOV (R5)+,HILIM
877 004214 012537 004370 MOV (R5)+,DEVADR
878 004220 112537 004372 MOVB (R5)+,LOBITS
879 004224 112537 004373 MOVB (R5)+,ADRCNT
880 004230 010566 000004 MOV R5,4(SP)
881 004234 005005 PARAM1: CLR R5
882 004236 012704 007502 MOV #INBUF,R4
883 004242 122714 000015 CMPB #15,(R4)
884 004246 001420 BEQ PARERR
885 004250 121427 000060 1$: CMPB (R4),#60
886 004254 002415 BLT PARERR
887 004256 121427 000067 CMPB (R4),#67
888 004262 003012 BGT PARERR
889 004264 142714 000060 BICB #60,(R4)
890 004270 152405 BISB (R4)+,R5
891 004272 122714 000015 CMPB #15,(R4)
892 004276 001406 BEQ LIMITS
    
```

893	004300	006305			ASL	R5	
894	004302	006305			ASL	R5	
895	004304	006305			ASL	R5	
896	004306	000760			BR	1\$	
897	004310	104404			PARERR: INSTER		
898	004312	000750			BR	PARAM1	
899							
900							
901							
902							
903	004314	020537	004366		LIMITS: CMP	R5, HILIM	
904	004320	101373			BHI	PARERR	
905	004322	020537	004364		CMP	R5, LOLIM	
906	004326	103770			BLO	PARERR	
907	004330	133705	004372		BITB	LOBITS, R5	
908	004334	001365			BNE	PARERR	
909							
910							
911							
912	004336	013704	004370				
913	004342	010524			1\$: MOV	DEVADR, R4	
914	004344	062705	000002		MOV	R5, (R4)+	
915	004350	105337	004373		ADD	#2, R5	
916	004354	001372			DECB	ADRCNT	
917	004356	012604			BNE	1\$	
918	004360	012605			MOV	(SP)+, R4	
919	004362	000002			MOV	(SP)+, R5	
920	004364	000000			RTI		
921	004366	000000			LOLIM: 0		
922	004370	000000			HILIM: 0		
923	004372	000000			DEVADR: 0		
924		004373			LOBITS: 0		
925					ADRCNT=LOBITS+1		
926							
927							
928							
929	004374	016637	000004	001276	.SAV05: MOV	4(SP), SAVPC	; SAVE R7 (PC)
930							
931							
932							
933	004402	010537	001272		SV05: MOV	R5, SAVR5	; SAVE R5
934	004406	010437	001270		MOV	R4, SAVR4	; SAVE R4
935	004412	010337	001266		MOV	R3, SAVR3	; SAVE R3
936	004416	010237	001264		MOV	R2, SAVR2	; SAVE R2
937	004422	010137	001262		MOV	R1, SAVR1	; SAVE R1
938	004426	010037	001260		MOV	R0, SAVR0	; SAVE R0
939	004432	000002			RTI		; LEAVE.
940							
941							
942							
943	004434	013700	001260		.RES05: MOV	SAVR0, R0	; RESTORE R0
944	004440	013701	001262		MOV	SAVR1, R1	; RESTORE R1
945	004444	013702	001264		MOV	SAVR2, R2	; RESTORE R2
946	004450	013703	001266		MOV	SAVR3, R3	; RESTORE R3
947	004454	013704	001270		MOV	SAVR4, R4	; RESTORE R4
948	004460	013705	001272		MOV	SAVR5, R5	; RESTORE R5

GENERAL UTILITIES (TYPEOUT, ERROR, SCOPE, ETC)

```

949 004464 000002 RTI ;LEAVE
950
951 ;CONVERT OCTAL NUMBER TO ASCII AND OUTPUT TO TELEPRINTER
952 -----
953
954 004466 104402 005672 .CONVR: TYPE MCRLF
955 004472 010046 .CNVRT: MOV R0,-(SP)
956 004474 010146 MOV R1,-(SP)
957 004476 010346 MOV R3,-(SP)
958 004500 010446 MOV R4,-(SP)
959 004502 010546 MOV R5,-(SP)
960 004504 017601 000012 MOV @12(SP),R1
961 004510 062766 000002 000012 ADD #2,12(SP)
962 004516 012137 004710 MOV (R1)+,WRDCNT
963 004522 112137 004712 1$: MOV (R1)+,CHRCNT
964 004526 112137 004713 MOV (R1)+,SPACNT
965 004532 013137 004714 MOV @2(R1)+,BINWRD
966 004536 122737 000003 004712 CMPB #3,CHRCNT
967 004544 001003 BNE 2$
968 004546 042737 177400 004714 BIC #177400,BINWRD
969 004554 013704 004714 2$: MOV BINWRD,R4
970 004560 113705 004712 MOVB CHRCNT,R5
971 004564 012700 001416 MOV #TEMP,R0
972 004570 010403 3$: MOV R4,R3
973 004572 042703 177770 BIC #177770,R3
974 004576 062703 000060 ADD #060,R3
975 004602 110320 MOVB R3,(R0)+
976 004604 000241 CLC
977 004606 006004 ROR R4
978 004610 000241 CLC
979 004612 006004 ROR R4
980 004614 000241 CLC
981 004616 006004 ROR R4
982 004620 005305 DEC R5
983 004622 001362 BNE 3$
984 004624 012703 007544 MOV #MDATA,R3
985 004630 114023 4$: MOVB -(R0),(R3)+
986 004632 105337 004712 DECB CHRCNT
987 004636 001374 BNE 4$
988 004640 105737 004713 TSTB SPACNT
989 004644 001405 BEQ 6$
990 004646 112723 000040 5$: MOVB #040,(R3)+
991 004652 105337 004713 DECB SPACNT
992 004656 001373 BNE 5$
993 004660 105013 6$: CLRB (R3)
994 004662 104402 007544 TYPE #MDATA
995 004666 005337 004710 DEC WRDCNT
996 004672 001313 BNE 1$
997 004674 012605 MOV (SP)+,R5
998 004676 012604 MOV (SP)+,R4
999 004700 012603 MOV (SP)+,R3
1000 004702 012601 MOV (SP)+,R1
1001 004704 012600 MOV (SP)+,R0
1002 004706 000002 RTI
1003 004710 000000 WRDCNT: 0
1004 004712 000000 CHRCNT: 0
    
```



```

1005          004713      SPACNT=CHRCNT+1
1006 004714 000000      BINWRD: 0
1007
1008
1009          ; TRAP DISPATCH SERVICE
1010          ; ARGUMENT OF TRAP IS EXTRACTED
1011          ; AND USED AS OFFSET TO OBTAIN POINTER
1012          ; TO SELECTED SUBROUTINE
1013
1014 004716 011646      .TRPSR: MOV      (SP), -(SP)      ; GET PC OF RETURN
1015 004720 162716 000002      SUB      #2, (SP)      ; =PC OF TRAP
1016 004724 017616 000000      MOV      @2(SP), (SP)      ; GET TRP
1017 004730 006316      TRPOK: ASL      (SP)      ; MULTIPLY TRAP ARG BY 2
1018 004732 042716 177001      BIC      #177001, (SP)      ; CLEAR UNWANTED BITS
1019 004736 062716 001330      ADD      #.TRPTAB, (SP)      ; POINTER TO SUBROUTINE ADDRESS
1020 004742 017616 000000      MOV      @2(SP), (SP)      ; SUBROUTINE ADDRESS
1021 004746 000136      JMP      @2(SP)+      ; GO TO SUBROUTINE
1022
1023          ; ERROR HANDLER
1024          ; -----
1025
1026 004750 004737 007606      .HLT:   JSR      PC, CKSWR      ; CHECK FOR SOFT SWR
1027 004754 032777 010000 174220      BIT      #SW12, @SWR      ; BELL ON ERROR?
1028 004762 001406          BEQ      XBX          ; BR IF NO BELL
1029 004764 105777 174220      TSTB    @TPCSR          ; TTY READY.
1030 004770 100003          BPL      XBX          ; DON'T WAIT IF TTY NOT READY.
1031 004772 112777 000207 174212      MOVB    #207, @TPDBR      ; PUSH A BELL AT THE TTY.
1032 005000 032777 020000 174174      XBX:   BIT      #SW13, @SWR      ; DELETE ERROR PRINT OUT?
1033 005006 001105          BNE      HALTS          ; BR IF NO PRINT OUT WANTED.
1034 005010 021637 001234      CMP      (SP), LSTERR      ; WAS THIS ERROR FOUND LAST TIME?
1035 005014 001404          BEQ      1$          ; BR IF YES
1036 005016 011637 001234      MOV      (SP), LSTERR      ; RECORD BEING HERE
1037 005022 105037 001325      CLRB    ERRFLG          ; PREPARE HEADER
1038 005026 104406      1$:   SAVO5          ; SAVE ALL PROC REGISTERS
1039 005030 011605          MOV      (SP), R5          ; GET THE PC OF ERROR
1040 005032 162705 000002      SUB      #2, R5          ; GET ADDRESS OF TRAP CALL
1041 005036 011504          MOV      (R5), R4          ; GET HLT INSTRUCTION
1042 005040 006304          ASL      R4          ; MULT BY TWO
1043 005042 061504          ADD      (R5), R4          ; DOUBLE IT
1044 005044 006304          ASL      R4          ; MULT AGAIN
1045 005046 042704 177001      BIC      #177001, R4          ; CLEAR JUNK
1046 005052 062704 023414      ADD      #.ERRTAB, R4          ; GET POINTER
1047 005056 012437 005172      MOV      (R4)+, ERMSG          ; GET ERROR MESSAGE
1048 005062 012437 005204      MOV      (R4)+, DATAHD          ; GET DATA HEADRER
1049 005066 011437 005216      MOV      (R4), DATABP          ; GET DATA TABLE
1050 005072 105737 001325      TSTB    ERRFLG          ; TYPE HEADREER
1051 005076 001403          BEQ      TYPMSG          ; BR IF YES
1052 005100 005737 005216      TST      DATABP          ; DOES DATA TABLE EXIST?
1053 005104 001040          BNE      TYPDAT          ; BR IF YES.
1054 005106 104402 005672      TYPMSG: TYPE      , MCRLF
1055 005112 104402 005672          TYPE      , MCRLF
1056 005116 005737 001220          TST      LOCK
1057 005122 001402          BEQ      1$
1058 005124 104402 006142          TYPE      , MASTEK
1059 005130 104402 006130      1$:   TYPE      , MTSTN
1060 005134 104411 005330          CNVRT    , XTSTN          ; SHOW IT
    
```

GENERAL UTILITIES (TYPEOUT, ERROR, SCOPE, ETC)

1061	005140	104402	006217		TYPE	,MERRPC	;TYPE PC.
1062	005144	104411	005322		CNVRT	,ERTABO	;SHOW IT
1063	005150	104402	005672		TYPE	,MCRLF	;GIVE A CR/LF
1064	005154	112737	177777	001325	MOVB	#-1,ERRFLG	;NO MORE HEADER UNLESS NO DATA TABLE.
1065	005162	005737	005172		TST	ERRMSG	;IS THERE AN ERROR MESSAGE?
1066	005166	001402			BEQ	WRKO.FM	;BR IF NO.
1067	005170	104402			TYPE		;TYPE
1068	005172	000000			ERRMSG: 0		;ERROR MESSAGE
1069	005174				WRKO.FM:		
1070	005174	005737	005204		TST	DATAHD	;DATA HEADER?
1071	005200	001402			BEQ	TYPDAT	;BR IF NO
1072	005202	104402			TYPE		;TYPE
1073	005204	000000			DATAHD: 0		;DATA HEADER
1074	005206	005737	005216		TYPDAT: TST	DATABP	;DATA TABLE?
1075	005212	001402			BEQ	RESREG	;BR IF NO.
1076	005214	104410			CONVRT		;SHOW
1077	005216	000000			DATABP: 0		;DATA TABLE
1078	005220	104407			RESREG: RES05		;RESTORE PROC REGISTERS
1079	005222	022737	003522	000042	HALTS: CMP	#\$ENDAD,2#42	;IF ACT-11 AUTOMATIC MODE, HALT!!
1080	005230	001403			BEQ	1\$	
1081	005232	005777	173744		TST	2\$SWR	;HALT ON ERROR?
1082	005236	100005			BPL	EXITER	;BR IF NO HALT ON ERROR
1083	005240	010046			1\$: PUSHRO		;SAVE RO
1084	005242	016600	000002		MOV	2(SP),RO	;SHOW ERROR PC IN DATA LIGHTS
1085	005246	000000			HALT		;HALT
1086	005250	012600			POPPO		;GET RO
1087	005252	005237	001232		EXITER: INC	ERRCNT	;UPDATE ERROR COUNT
1088	005256	032777	000400	173716	BIT	#\$SW08,2\$SWR	;GOTO TOP OF TEST?
1089	005264	001007			BNE	1\$;BR IF YES
1090	005266	032777	002000	173706	BIT	#\$SW10,2\$SWR	;GOTO NEXT TEST?
1091	005274	001411			BEQ	2\$;BR IF NO
1092	005276	013737	001216	001214	MOV	NEXT,RETURN	;SET FOR NEXT TEST
1093	005304	012706	001200		1\$: MOV	#\$STACK,SP	;RESET SP
1094	005310	013701	001404		MOV	DMCSR,R1	;SET UP R1
1095	005314	000177	173674		JMP	2\$RETURN	;GOTO SPECIFIED TEST
1096	005320	000002			2\$: RTI		;RETURN
1097	005322	000001			ERTABO: 1		
1098	005324	006	002		.BYTE	6,2	
1099	005326	001276			SAVPC		
1100	005330	000001			XTSTN: 1		
1101	005332	003	002		.BYTE	3,2	
1102	005334	001226			TSTNO		
1103					;ENTER HERE ON POWER FAILURE		
1104					-----		
1105							
1106							
1107	005336				.PFAIL:		
1108	005336	012737	005350	000024	MOV	#\$RESTART,24	;SET UP FOR POWER UP TRAP
1109	005344	000000			HALT		;HALT ON POWER DOWN NORMAL
1110	005346	000777			BR	.	
1111							
1112							
1113							
1114	005350				RESTAR:		
1115	005350	012737	005336	000024	MOV	#\$PFAIL,24	;SET UP FOR POWER FAILURE
1116	005356	012706	001200		MOV	#\$STACK,SP	;RESET THE STACK POINTER


```

1117 005362 013701 001404      MOV      DMCSR,R1      ;RESTORE R1
1118 005366 005037 001416      CLR      TEMP         ;READY FOR TIMMER
1119 005372 005237 001416      INC      TEMP         ;PLUS ONE TO THE TIMER!
1120 005376 001375                BNE     -4            ;BR IF MORE TO GO
1121 005400 104402 005675      TYPE    ,MPFAIL      ;TYPE THE MESSAGE
1122 005404 104411 005430      CNVRT   PFTAB        ;TELL WHAT TEST TO RETURN TO.
1123 005410 105037 001325      CLRB    ERRFLG       ;START CLEAN
1124 005414 005037 001234      CLR     LSTERR       ;
1125 005420 005011                CLR     (R1)         ;CLEAR MAINT BITS
1126 005422 104412                MSTCLR ;START CLEAN UP OF DEVICE
1127 005424 000177 173564      JMP     @RETURN      ;START DOING THAT TEST AGAIN.
1128 005430 000001                PFTAB: 1
1129 005432 003      002      .BYTE  3,2
1130 005434 001226                TSTNO
1131
1132 005436                .DELAY:
1133 005436 012777 000020 173746      MOV     #20,@DMP04
1134 005444 104414                ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
1135 005446 121111                121111 ;POKE CLOCK DELAY BIT
1136 005450                1$:
1137 005450 104414                ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
1138 005452 121224                121224 ;PORT4+IBUS*11
1139 005454 032777 000020 173730      BIT    #BIT4,@DMP04 ;IS CLOCK BIT SET?
1140 005462 001772                BEQ    1$            ;BR IF NO
1141 005464 000002                RTI
1142
1143 005466                .MSTCLR:
1144 005466 152777 000100 173712      BISB   #BIT6,@DMCSRH ;SET MASTER CLEAR
1145 005474 142777 000300 173704      BICB   #BIT6!BIT7,@DMCSRH ;CLEAR MASTER CLEAR AND RUN
1146 005502 000002                RTI                ;RETURN
1147
1148 005504                .ROMCLK:
1149 005504 152777 000002 173674      BISB   #BIT1,@DMCSRH ;SET ROMI
1150 005512 013677 173676      MOV    @(SP)+,@DMP06 ;LOAD INSTRUCTION IN SEL6
1151 005516 062746 000002                ADD    #2,-(SP)      ;ADJUST STACK
1152 005522 032777 000100 173452      BIT    #SW06,@SWR    ;HALT IF SW06 =1
1153 005530 001401                BEQ    1$            ;BR IF SW06 =0
1154 005532 000000                HALT   ;HALT BEFORE CLOCKING INSTRUCTION
1155 005534 152777 000003 173644      1$:  BISB   #BIT1!BIT0,@DMCSRH ;CLOCK INSTRUCTION
1156 005542 142777 000007 173636      BICB   #BIT2!BIT1!BIT0,@DMCSRH ;CLEAR ROMO, ROMI, STEP
1157 005550 000002                RTI
1158
1159 005552                .DATACLK:
1160 005552 013637 001416      MOV    @(SP)+,TEMP    ;PUT TICK COUNT IN TEMP
1161 005556 062746 000002                ADD    #2,-(SP)      ;ADJUST STACK
1162 005562 152777 000020 173616      1$:  BISB   #BIT4,@DMCSRH ;SET STEP LU
1163 005570 027777 173610 173606      CMP    @DMCSR,@DMCSR ;WASTE TIME
1164 005576 142777 000020 173602      BICB   #BIT4,@DMCSRH ;CLEAR STEP LU
1165 005604 005337 001416      DEC    TEMP          ;DEC TICK COUNT
1166 005610 001364                BNE    1$            ;BR IF NOT DONE
1167 005612 000002                RTI                ;RETURN
1168 005614 000001                3$:  .BLKW 1
1169
1170 005616                .TIMER:
1171 005616 013637 001416      MOV    @(SP)+,TEMP    ;MOVE COUNT TO TEMP
1172 005622 062746 000002                ADD    #2,-(SP)      ;ADJUST STACK

```


GENERAL UTILITIES (TYPEOUT, ERROR, SCOPE, ETC)

```

1173 005626
1174 005626 104414
1175 005630 021364
1176 005632 032777 000002 173552
1177 005640 001772
1178 005642
1179 005642 104414
1180 005644 021364
1181 005646 032777 000002 173536
1182 005654 001372
1183 005656 005337 001416
1184 005662 001361
1185 005664 000002
1186
1187 005666 020040 000077
(2) 005672 005015 000
(2) 005675 377 053520 020122
(2) 005733 377 047105 020104
(2) 005755 377 000122
(2) 005760 047377 020117 042504
(2) 006005 377 047111 052523
(2) 006031 377 042524 052123
(2) 006043 377 047514 045503
(2) 006072 051503 035122 000040
(2) 006100 042526 035103 000040
(2) 006106 040520 051523 051505
(2) 006117 105 051122 051117
(2) 006130 042524 052123 047040
(2) 006142 000052
(2) 006144 051777 052105 051440
(2) 006217 120 035103 000040
(2) 006224 020212 020040 020040
(2) 006263 377 020040 020040
(2) 006322 020212 050040 020103
(2) 006374 026777 026455 026455
(2) 006450 044377 053517 046440
(2) 006510 041777 051123 040440
(2) 006526 053377 041505 047524
(2) 006547 377 051102 050040
(2) 006606 044777 020106 046504
(2) 006704 053777 044510 044103
(2) 007016 051777 044527 041524
(2) 007054 051777 044527 041524
(2) 007114 044777 020123 044124
(2) 007154 047377 020117 042504
(2) 007205 377 051412 051127
(2) 007215 116 053505 020077
(2) 007223 377 042377 041515
(2) 007277 377 054105 042520
(2) 007320 024040 046504 024503
(2) 007330 024040 046513 024503
(2) 007340 042377 041515 030461
(2)
(2) 007454 000005
1188 007456 006 003
1189 007460 001246

```

```

1$: ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
021364 ;PORT4+IBUS* REG11
BIT #2,ADMP04 ;IS PGM CLOCK BIT CLEAR?
BEQ 1$ ;BR IF YES

2$: ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
021364 ;PORT4+IBUS* REG11
BIT #2,ADMP04 ;IS PGM CLOCK BIT SET?
BNE 2$ ;BR IF YES
DEC TEMP ;DEC COUNT
BNE 1$ ;BR IF NOT DONE
RTI ;RETURN

MQM: .ASCIZ / ?/
MCRLF: .ASCIZ <15><12>
MPFAIL: .ASCIZ <377>/PWR FAILED. RESTART AT TEST /
MEPASS: .ASCIZ <377>/END PASS DZDMH /
MR: .ASCIZ <377>/R/
MERR2: .ASCIZ <377>/NO DEVICES PRESENT./
MERR3: .ASCIZ <377>/INSUFFICIENT DATA!/
MTSTPC: .ASCIZ <377>/TEST PC-/
MLOCK: .ASCIZ <377>/LOCK ON SELECTED TEST/
MCSRX: .ASCIZ /CSR: /
MVECX: .ASCIZ /VEC: /
MPASSX: .ASCIZ /PASSES: /
MERRX: .ASCIZ /ERRORS: /
MTSTN: .ASCIZ /TEST NO: /
MASTEK: .ASCIZ /*/
MNEW: .ASCIZ <377>/SET SWITCH REG TO DMC11'S DESIRED ACTIVE./
MERRPC: .ASCIZ /PC: /
XHEAD: .ASCII <212>/ MAP OF DMC11 STATUS/
.ASCII <377>/-----/
.ASCII <212>/ PC CSR STAT1 STAT2 STAT3/
.ASCII <377>/-----/

NUM: .ASCIZ <377>/HOW MANY DMC11'S TO BE TESTED?/
CSR: .ASCIZ <377>/CSR ADDRESS?/
VEC: .ASCIZ <377>/VECTOR ADDRESS?/
PRIO: .ASCIZ <377>/BR PRIORITY LEVEL? (4,5,6,7)?/
CRAM: .ASCIZ <377>/IF DMC HAS CRAM (M8204) TYPE "Y", IF CROM (M8200) TYPE "N"
MODU: .ASCIZ <377>/WHICH LINE UNIT? IF NONE TYPE "N", IF M8201 TYPE "1", IF M
LINE: .ASCIZ <377>/SWITCH PAC#1 (DDCMP LINE #)?/
BM: .ASCIZ <377>/SWITCH PAC#2 (BM873 BOOT ADD)?/
CONN: .ASCIZ <377>/IS THE LOOP BACK CONNECTOR ON?/
NOACT: .ASCIZ <377>/NO DEVICES ARE SELECTED/
SWMES: .ASCIZ <377><12>/SWR= /
SWMES1: .ASCIZ /NEW? /
CONERR: .ASCIZ <377><377>/DMC11 FOUND AT NON-STANDARD ADDRESS PC: /
CNERR: .ASCIZ <377>/EXPECTED FOUND/
DMCM: .ASCIZ / (DMC) /
KMCM: .ASCIZ / (KMC) /
SPEED: .ASCIZ <377>/DMC11-AR(REMOTE,LOW SPEED) OR DMC11-AL(LOCAL,HIGH SPEED) T
.EVEN
XSTATQ: 5
.BYTE 6,3
TEMP1

```

1190	007462	006	003		.BYTE	6,3		
1191	007464	001250			TEMP2			
1192	007466	006	003		.BYTE	6,3		
1193	007470	001252			TEMP3			
1194	007472	006	003		.BYTE	6,3		
1195	007474	001254			TEMP4			
1196	007476	006	002		.BYTE	6,2		
1197	007500	001256			TEMPS			
1198					.EVEN			
1199								
1200					:BUFFERS FOR INPUT-OUTPUT			
1201								
1202	007502	000000			INBUF:	0		
1203		007544			.=. +40			
1204	007544	000000			MDATA:	0		
1205		007606			.=. +40			
1206								
1207								
1208					:ROUTINE USED TO CHANGE SOFTWARE SWITCH			
1209					:REGISTER USING THE CONSOLE TERMINAL			
1210					-----			
1211								
1212	007606	022737	000176	001202	CKSWR:	CMP	#SWREG, SWR	: IS THE SOFT SWR BEING USED?
1213	007614	001077				BNE	CKSWR5	: BR IF NO
1214	007616	105777	171362			TSTB	@TKCSR	: IS DONE SET?
1215	007622	100003				BPL	2\$: GO ON IF NOT SET
1216	007624	012737	177777	003734		MOV	#-1, DONE	: IF DONE SET, SET FLAG
1217	007632	022777	000007	171346	2\$:	CMP	#7, @TKDBR	: WAS CTRL G TYPED? (7 BIT ASCII)
1218	007640	001404				BEQ	1\$: BR IF YES
1219	007642	022777	000207	171336		CMP	#207, @TKDBR	: WAS CTRL G TYPED? (8 BIT ASCII)
1220	007650	001061				BNE	CKSWR5	: BR IF NO
1221	007652	010246			1\$:	MOV	R2, -(SP)	: STORE R2
1222	007654	010346				MOV	R3, -(SP)	: STORE R3
1223	007656	010446				MOV	R4, -(SP)	: STORE R4
1224	007660	012737	177777	010016		MOV	#-1, SWFLG	: SET SOFT TYPE OUT FLAG
1225	007666	005002			CKSWR1:	CLR	R2	: CLEAR NEW SWR CONTENTS
1226	007670	012704	177777			MOV	#-1, R4	: SET FLAG TO ALL ONES
1227	007674	104402	007205			TYPE	, SWMES	: TYPE "SWR="
1228	007700	104411			CKSWR2:	CNVRT		: TYPE OUT PRESENT CONTENTS
1229	007702	010052				SOFTSW		: OF SOFT SWITCH REGISTER
1230	007704	104402	007215		CKSWR3:	TYPE	, SWMES1	: TYPE "NEW?"
1231	007710	004737	010020		CKSWR4:	JSR	PC, INCHAR	: GET RESPONSE
1232	007714	022703	000015			CMP	#15, R3	: WAS IT A CR?
1233	007720	001424				BEQ	5\$: BR IF YES
1234	007722	022703	000012			CMP	#12, R3	: WAS IT A LF?
1235	007726	001416				BEQ	4\$: BR IF YES
1236	007730	022703	000025			CMP	#25, R3	: WAS IT CTRL U?
1237	007734	001754				BEQ	CKSWR1	: BR IF YES (START OVER)
1238	007736	022703	000007			CMP	#7, R3	: IF CNTL G GET NEXT CHAR
1239	007742	001762				BEQ	CKSWR4	
1240	007744	005004				CLR	R4	: IT MUST BE A DIGIT SO CLR FLAG
1241	007746	042703	177770			BIC	#177770, R3	: ONLY 0-7 ARE LEGAL SO MASK OFF BITS
1242	007752	006302				ASL	R2	: SHIFT R2 3 TIMES
1243	007754	006302				ASL	R2	
1244	007756	006302				ASL	R2	
1245	007760	050302				BIS	R3, R2	: ADD LAST DIGIT

1246	007762	000752			BR	CKSWR4	:GET NEXT CHARACTER
1247	007764	012766	002002	000006	4\$: MOV	#.START,6(SP)	:LF WAS TYPED SO GO TO START
1248	007772	005704			5\$: TST	R4	:IS FLAG CLEAR?
1249	007774	001002			BNE	6\$:IF NOT DON'T CHANGE SOFT SWR
1250	007776	010277	171200		MOV	R2,@SWR	:IF YES THEN WRITE NEW CONTENTS TO SOFT SWR
1251	010002	005037	010016		6\$: CLR	SWFLG	:CLEAR TYPEOUT FLAG
1252	010006	012604			MOV	(SP)+,R4	:RESTORE R4
1253	010010	012603			MOV	(SP)+,R3	:RESTORE R3
1254	010012	012602			MOV	(SP)+,R2	:RESTORE R2
1255	010014	000207			CKSWRS: RTS	PC	:RETURN
1256							
1257	010016	000000			SWFLG: 0		
1258							
1259	010020	105777	171160		INCHAR: TSTB	@TKCSR	
1260	010024	100375			BPL	-4	
1261	010026	017703	171154		MOV	@TKDBR,R3	
1262	010032	105777	171152		TSTB	@TPCSR	
1263	010036	100375			BPL	-4	
1264	010040	010377	171146		MOV	R3,@TPDBR	
1265	010044	042703	000200		BIC	#BIT7,R3	
1266	010050	000207			RTS	PC	
1267							
1268	010052	000001			SOFTSW: 1		
1269	010054	006	002		.BYTE	6,2	
1270	010056	000176			SWREG		


```

1271
1272
1273
1274
1275
1276
1277
1278
1279
1280 010060 005737 001306          CYCLE: TST      DMACTV      ;ARE ANY DMC11'S TO BE TESTED?
1281 010064 001004                BNE      1$          ;BR IF OK.
1282 010066 104402 007154          TYPE     ,NOACT     ;NO DMC11'S SELECTED!!
1283 010072 000000                HALT                    ;STOP THE SHOW.
1284 010074 000776                BR                    ;DISQUALIFY CONT. SW.
1285 010076 000241                1$: CLC          ;CLEAR PROC. CARRY BIT.
1286 010100 006137 001316          ROL                    ;UPDATE POINTER
1287 010104 005537 001316          ADC      RUN        ;CATCH CARRY FROM RUN
1288 010110 062737 000004 001322  ADD      #4,MILK     ;UPDATE POINTER
1289 010116 062737 000010 001320  ADD      #10,CREAM   ;UPDATE ADDRESS POINTER.
1290 010124 022737 001700 001320  CMP      #DM.MAP+200,CREAM
1291 010132 001006                BNE      2$          ;KEEP GOING; NOT ALL TESTED FOR.
1292 010134 012737 001500 001320  MOV      #DM.MAP,CREAM ;RESET ADDRESS POINTER.
1293 010142 012737 001702 001322  MOV      #CNT.MAP,MILK ;RESET PASS COUNT POINTER
1294 010150 033737 001316 001306  2$: BIT      RUN,DMACTV ;IS THIS ONE ACTIVE?
1295 010156 001747                BEQ      1$          ;BR IF NO
1296 010160 013700 001320          MOV      CREAM,R0    ;GET ADDRESS POINTER
1297 010164 013702 001322          MOV      MILK,R2     ;GET PASS COUNT POINTER
1298 010170 012037 001404          MOV      (R0)+,DMCSR ;LOAD SYSTEM CTRL. REG
1299 010174 011037 001374          MOV      (R0),DMRVEC ;LOAD VECTOR
1300 010200 042737 177000 001374  BIC      #177000,DMRVEC ;CLEAR UNWANTED BITS
1301 010206 012037 001366          MOV      (R0)+,STAT1 ;LOAD STAT1
1302 010212 012037 001370          MOV      (R0)+,STAT2 ;LOAD STAT2
1303 010216 012037 001372          MOV      (R0)+,STAT3 ;LOAD STAT3
1304 010222 012237 001230          MOV      (R2)+,PASCNT ;LOAD PASS COUNT
1305 010226 012237 001232          MOV      (R2)+,ERRCNT ;LOAD ERROR COUNT
1306 010232 012700 000002          MOV      #2,R0      ;SAVE CORE THIS WAY!
1307 010236 013737 001404 001406  MOV      DMCSR,DMCSRH
1308 010244 005237 001406          INC      DMCSRH
1309 010250 013737 001406 001410  MOV      DMCSRH,DMCTL
1310 010256 005237 001410          INC      DMCTL
1311 010262 013737 001410 001412  MOV      DMCTL,DMP04
1312 010270 060037 001412          ADD      R0,DMP04
1313 010274 013737 001412 001414  MOV      DMP04,DMP06
1314 010302 060037 001414          ADD      R0,DMP06
1315
1316 010306 013737 001374 001376  MOV      DMRVEC,DMRLVL ;PTY LVL
1317 010314 060037 001376          ADD      R0,DMRLVL
1318 010320 013737 001376 001400  MOV      DMRLVL,DMTVEC ;TX VEC
1319 010326 060037 001400          ADD      R0,DMTVEC
1320 010332 013737 001400 001402  MOV      DMTVEC,DMTLVL ;TX LVL
1321 010340 060037 001402          ADD      R0,DMTLVL
1322
1323 010344 032737 000002 001236  BIT      #SW01,STATSW ;IS TEST NO. SELECTED
1324 010352 001450                BEQ                    ;BR IF NO
1325 010354
1326 010354 005737 000042          4$: TST      0#42    ;RUNNING IN AUTO MODE?

```

1327	010360	001045			BNE	7\$;BR IF YES
1328	010362	104402	005672		TYPE	,MCRLF		
1329	010366	104403			INSTR			;GET TEST NO.
1330	010370	006130			MTSTN			
1331	010372	104405			PARAM			
1332	010374	000001			1			
1333	010376	001000			1000			
1334	010400	001226			TSTNO			
1335	010402	000			0			
1336	010403	001			.BYTE			
1337	010404	012700	012320		.BYTE			
1338	010410	022710			5\$:	MOV	#TST1,RO	
1339	010412	012737				CMP	(PC)+,(RO)	;CMP FIRST WORD TO 12737
1340	010414	001020				MOV	(PC)+,2(PC)+	
1341	010416	023760	001226	000002		BNE	6\$;BR IF NOT SAME
1342	010424	001014				CMP	TSTNO,2(RO)	;DOES TSTNO MATCH?
1343	010426	022760	001226	000004		BNE	6\$;BR IF NO
1344	010434	001010				CMP	#TSTNO,4(RO)	;IS LAST WORD OK?
1345	010436	010037	001214			BNE	6\$;BR IF NO
1346	010442	104402	005755			MOV	RO,RETURN	;IT IS A LEGAL TEST SO DO IT
1347	010446	042737	000002	001236		TYPE	,MR	
1348	010454	000412				BIC	#SW01,STRTSW	
1349	010456	005720				BR	8\$	
1350	010460	020027	016224		6\$:	TST	(RO)+	;POP RO
1351	010464	001351				CMP	RO,#TLAST+10	;AT END YET?
1352	010466	104402	005666			BNE	5\$;BR IF NO
1353	010472	000730				TYPE	,MQM	;YES ILLEGAL TEST NO.
1354						BR	4\$;TRY AGAIN
1355	010474	012737	012320	001214	7\$:	MOV	#TST1,RETURN	;PREPARE RETURN ADDRESS
1356	010502	013701	001404		8\$:	MOV	DMCSR,R1	;R1 = BASE DMC11 ADDRESS
1357	010506	000177	170502			JMP	2RETURN	;GO START TESTING.
1358								
1359								
1360								
1361								
1362								
1363								
1364								
1365								
1366								
1367								
1368	010512							
1369	010512	000005						
1370	010514	012702	001500					
1371	010520	005022						
1372	010522	022702	001700					
1373	010526	001374						
1374	010530	005037	001310					
1375	010534	012702	001500					
1376	010540	005037	001306					
1377	010544	032737	000001	001236				
1378	010552	001002						
1379	010554	000137	011252					
1380	010560	012737	000001	001256				
1381	010566	104403						
1382	010570	006450						

;ROUTINE USED TO "AUTO SIZE" THE DMC11
 ;CSR AND VECTOR.
 ;NOTE: THE CSR MAY BE ANY WHERE IN THE FLOATING
 ; ADDRESS RANGE (160000:164000)
 ; AND THE VECTOR MAY BE ANY WHERE IN THE
 ; FLOATING VECTOR RANGE (300:770)
 ;

AUTO.SIZE:
 CSRMAP: RESET ;INSURE A BUS INIT.
 1\$: MOV #DM.MAP,R2 ;LOAD MAP POINTER.
 CLR (R2)+ ;ZERO ENTIRE MAP
 CMP #DM.END,R2 ;ALL DONE?
 BNE 1\$;BR IF NO
 CLR DMNUM ;SET OCTAL NUMBER OF DMC11'S TO 0
 MOV #DM.MAP,R2 ;R2 POINTS TO DMC MAP
 CLR DMACTV ;CLEAR ACTIVE
 BIT #SW00,STRTSW ;QUESTIONS?
 BNE .+6 ;BR IF YES
 JMP 7\$;IF NO SKIP QUESTIONS
 MOV #1,TEMPS ;START WITH 1
 INSTR
 NUM

1383	010572	104405			PARAM		
1384	010574	000001			1		
1385	010576	000020			16.		
1386	010600	001252			TEMP3		
1387	010602	000			.BYTE	0	
1388	010603	001			.BYTE	1	
1389	010604	013737	001252	001310	MOV	TEMP3,DMNUM	;DMNUM = HOW MANY
1390	010612	104402	005672		TYPE	,MCRLF	
1391	010616	104410			CONVRT		;TYPE WHICH DMC IS BEING DONE
1392	010620	012002			WHICH		;TEMPS IS WHICH DMC
1393	010622	005237	001256		INC	TEMPS	
1394	010626	104403			INSTR		
1395	010630	006510			CSR		
1396	010632	104405			PARAM		
1397	010634	160000			160000		
1398	010636	164000			164000		
1399	010640	001254			TEMP4		
1400	010642	000			.BYTE	0	
1401	010643	001			.BYTE	1	
1402	010644	013722	001254		MOV	TEMP4,(R2)+	;STORE CSR IN MAP
1403	010650	104403			INSTR		
1404	010652	006526			VEC		
1405	010654	104405			PARAM		
1406	010656	000000			0		
1407	010660	000776			776		
1408	010662	001254			TEMP4		
1409	010664	000			.BYTE	0	
1410	010665	001			.BYTE	1	
1411	010666	013712	001254		MOV	TEMP4,(R2)	;STORE VECTOR IN MAP
1412	010672	104402			TYPE		
1413	010674	006547			PRIO		;ASK WHAT BR LEVEL
1414	010676	004737	012266		JSR	PC,INTTY	;GET RESPONSE
1415	010702	022703	000024		CMP	#24,R3	
1416	010706	101014			BHI	50\$;BR IF LESS THAN 4
1417	010710	022703	000027		CMP	#27,R3	
1418	010714	103411			BLO	50\$;BR IF GREATER THAN 7
1419	010716	012704	000011		MOV	#11,R4	;R4 = NUMBER OF SHIFTS
1420	010722	006303			ASL	R3	;SHIFT R3 LEFT
1421	010724	005304			DEC	R4	;DEC SHIFT COUNT
1422	010726	001375			BNE	.-4	;BR IF NOT DONE
1423	010730	042703	170777		BIC	#170777,R3	;BIC UNWANTED BITS
1424	010734	050312			BIS	R3,(R2)	;PUT BR LEVEL IN STATUS MAP
1425	010736	000403			BR	8\$;CONTINUE
1426	010740	104402			TYPE		
1427	010742	005666			MQM		;RESPONSE IS OUT OF LIMITS
1428	010744	000752			BR	10\$;TRY AGAIN
1429	010746	104402			TYPE		
1430	010750	006606			CRAM		;DOES DMC HAVE CRAM?
1431	010752	004737	012266		JSR	PC,INTTY	;GET REPLY
1432	010756	022703	000131		CMP	#131,R3	
1433	010762	001427			BEQ	9\$;YES
1434	010764	022703	000116		CMP	#116,R3	;NO
1435	010770	001403			BEQ	40\$;NOT A Y OR N
1436	010772	104402			TYPE		
1437	010774	005666			MQM		;TYPE "??"
1438	010776	000763			BR	8\$;ASK AGAIN

1439	011000	104402		40\$:	TYPE		
1440	011002	007340			SPEED		:DMC11-AR OR DMC11-AL?
1441	011004	004737	012266		JSR	PC,INTTY	:GET RESPONSE
1442	011010	022703	000122		CMP	#122,R3	:IS IT R
1443	011014	001414			BEQ	16\$:BR IF REMOTE
1444	011016	022703	000114		CMP	#114,R3	:IS IT L
1445	011022	001403			BEQ	41\$:BR IF LOCAL
1446	011024	104402			TYPE		
1447	011026	005666			MQM		
1448	011030	000763			BR	40\$:TRY AGAIN
1449	011032	052762	000002 000004	41\$:	BIS	#BIT1,4(R2)	:SET BIT1 IN STAT3
1450	011040	000402			BR	16\$:CONTINUE
1451	011042	052712	100000	9\$:	BIS	#BIT15,(R2)	:SET BIT 15 IF CRAM
1452	011046	104402		16\$:	TYPE		
1453	011050	006704			MODU		:ASK WHICH LINE UNIT
1454	011052	004737	012266		JSR	PC,INTTY	:GET REPLY
1455	011056	022703	000021		CMP	#21,R3	: "1"
1456	011062	001417			BEQ	30\$	
1457	011064	022703	000022		CMP	#22,R3	: "2"
1458	011070	001412			BEQ	31\$	
1459	011072	022703	000116		CMP	#116,R3	: "N"
1460	011076	001403			BEQ	32\$	
1461	011100	104402			TYPE		
1462	011102	005666			MQM		: IF NOT A 1,2 OR N TYPE "?"
1463	011104	000760			BR	16\$:TRY AGIAN
1464	011106	052722	010000	32\$:	BIS	#BIT12,(R2)+	:SET BIT 12 IN STAT2 IF NO LU
1465	011112	022222			CMP	(R2)+,(R2)+	:POP OVER STAT2 AND STAT3
1466	011114	000447			BR	33\$	
1467	011116	052712	020000	31\$:	BIS	#BIT13,(R2)	:SET BIT 13 IN STAT2 IF M8202
1468	011122	104402		30\$:	TYPE		
1469	011124	007114			CONN		:ASK IF LOOP-BACK IS ON
1470	011126	004737	012266		JSR	PC,INTTY	:GET REPLY
1471	011132	022703	000131		CMP	#131,R3	:Y
1472	011136	001406			BEQ	17\$	
1473	011140	022703	000116		CMP	#116,R3	:N
1474	011144	001406			BEQ	18\$	
1475	011146	104402			TYPE		
1476	011150	005666			MQM		: IF NOT Y OR N TYPE "?"
1477	011152	000763			BR	30\$:TRY AGAIN
1478	011154	052722	040000	17\$:	BIS	#BIT14,(R2)+	:TURNAROUND IS CONNECTED
1479	011160	000402			BR	19\$	
1480	011162	042722	040000	18\$:	BIC	#BIT14,(R2)+	:NO TURNAROUND
1481	011166			19\$:			
1482	011166	104403			INSTR		
1483	011170	007016			LINE		
1484	011172	104405			PARAM		
1485	011174	000000			0		
1486	011176	000377			377		
1487	011200	001254			TEMP4		
1488	011202	000			.BYTE	0	
1489	011203	001			.BYTE	1	
1490	011204	113722	001254		MOV8	TEMP4,(R2)+	:STORE SWITCH PAC IN MAP
1491	011210	104403			INSTR		
1492	011212	007054			BM		
1493	011214	104405			PARAM		
1494	011216	000000			0		

1495	011220	000377			377			
1496	011222	001254			TEMP4			
1497	011224	000			.BYTE	0		
1498	011225	001			.BYTE	1		
1499	011226	113722	001254		MOVB	TEMP4,(R2)+	:STORE SWITCH PAC IN MAP	
1500	011232	005722			TST	(R2)+	:POP OVER STAT3	
1501	011234	005337	001252	33\$:	DEC	TEMP3	:DEC DMC COUNT	
1502	011240	001402			BEQ	34\$:BR IF DONE	
1503	011242	000137	010612		JMP	12\$:JUMP IF NOT	
1504	011246	000137	011702	34\$:	JMP	13\$:CONTINUE	
1505	011252	012701	160000	7\$:	MOV	#160000,R1	:SET FOR FIRST ADDRESS TO BE TESTED	
1506	011256	012737	011774	000004	MOV	#6\$,J#4	:SET FOR NON-EXISTANT DEVICE TIME OUT	
1507	011264	005011		2\$:	CLR	(R1)	:CLEAR SEL0	
1508	011266	005711			TST	(R1)	:IF DMC11 DMCSR S/B 0	
1509	011270	001172			BNE	3\$:IF NO DEV ; TRAP TO 4. IF NO BIT 8 THEN NO DMC1	
1510	011272	005061	000006		CLR	6(R1)	:CLEAR SEL6	
1511	011276	005761	000006		TST	6(R1)	:IF DMC11 THEN DMRC S/B =0!	
1512	011302	001165			BNE	3\$:BR IF NOT DMC11	
1513	011304	012711	002000		MOV	#BIT10,(R1)	:SET ROM0	
1514	011310	005061	000004		CLR	4(R1)	:CLEAR SEL4	
1515	011314	012761	125252	000006	MOV	#125252,6(R1)	:WRITE THIS TO SEL6	
1516	011322	052711	020000		BIS	#BIT13,(R1)	:WRITE IT!	
1517	011326	022761	125252	000004	CMP	#125252,4(R1)	:WAS IT WRITTEN?	
1518	011334	001004			BNE	21\$:IF NO IT IS NOT CRAM	
1519	011336	052762	100000	000002	BIS	#BIT15,2(R2)	:SET BIT15 IF CRAM	
1520	011344	000431			BR	22\$		
1521	011346	012711	001000	21\$:	MOV	#BIT9,(R1)	:SET ROM1	
1522	011352	012761	100417	000006	MOV	#100417,6(R1)	:PUT INSTRUCTION IN SEL6	
1523	011360	012711	001400		MOV	#BIT9!BIT8,(R1)	:CLOCK INSTRUCTION (MICRO PROC PC TO 0)	
1524	011364	012711	002000		MOV	#BIT10,(R1)	:SET ROM0	
1525	011370	022761	000626	000006	CMP	#626,6(R1)	:IS IT LOCAL CROM	
1526	011376	001411			BEQ	23\$:BR IF YES	
1527	011400	022761	016520	000006	CMP	#16520,6(R1)	:IS IT REMOTE CROM?	
1528	011406	001410			BEQ	22\$:BR IF YES	
1529	011410	022761	177777	000006	CMP	#-1,6(R1)	:NO CROM?	
1530	011416	001404			BEQ	22\$:BR IF YES	
1531	011420	000516			BR	3\$:NOT A DMC	
1532	011422	052762	000002	000006	23\$:	BIS	#BIT1,6(R2)	:SET BIT 1 IN STAT3
1533					:AT THIS POINT IT IS ASSUMED THAT R1 HOLDS A DMC11 CSR ADDRESS.			
1534	011430	010122		22\$:	MOV	R1,(R2)+	:STORE CSR IN CORE TABLE.	
1535	011432	012711	001000	15\$:	MOV	#BIT9,(R1)	:CLEAR LINE UNIT LOOP	
1536	011436	005061	000004		CLR	4(R1)	:CLEAR PORT4	
1537	011442	012761	122113	000006	MOV	#122113,6(R1)	:LOAD INSTRUCTION (CLR DTR)	
1538	011450	052711	000400		BIS	#BIT8,(R1)	:CLOCK INSTRUCTION	
1539	011454	012761	021264	000006	MOV	#021264,6(R1)	:LOAD INSTRUCTION	
1540	011462	052711	000400		BIS	#BIT8,(R1)	:CLOCK INSTRUCTION	
1541	011466	122761	000377	000004	CMPB	#377,4(R1)	:IS IT ALL ONES?	
1542	011474	001003			BNE	+.10	:BR IF NO	
1543	011476	052712	010000		BIS	#BIT12,(R2)	:IF YES, NO LINE UNIT, SET STATUS BIT	
1544	011502	000436			BR	20\$		
1545	011504	032761	000002	000004	BIT	#BIT1,4(R1)	:IS SWITCH A ONE?	
1546	011512	001403			BEQ	+.10	:BR IF M8201	
1547	011514	052712	060000		BIS	#BIT13!BIT14,(R2)	:M8202 ASSUME CONNECTOR	
1548	011520	000427			BR	20\$:CONNECTOR ON)	
1549	011522	032761	000010	000004	BIT	#BIT3,4(R1)	:IS MRDY SET	
1550	011530	001023			BNE	20\$:BR IF M8201 NO CONNECTOR (ON LINE)	

GENERAL UTILITIES (TYPEOUT, ERROR, SCOPE, ETC)

1551	011532	012761	000100	000004	MOV	#BIT6,4(R1)	;LOAD PORT4
1552	011540	012761	122113	000006	MOV	#122113,6(R1)	;LOAD INSTRUCTION
1553	011546	052711	000400		BIS	#BIT8,(R1)	;CLOCK INSTRUCTION(SET DTR)
1554	011552	012761	021264	000006	MOV	#021264,6(R1)	;LOAD INSTRUCTION
1555	011560	052711	000400		BIS	#BIT8,(R1)	;CLOCK INSTRUCTION(READ MODEM REG)
1556	011564	032761	000010	000004	BIT	#BIT3,4(R1)	;IS MRDY SET NOW?
1557	011572	001402			BEQ	20\$;BR IF NO CONNECTOR
1558	011574	052712	040000		BIS	#BIT14,(R2)	;SET STATUS BIT FOR CONNECTOR
1559	011600	005722			20\$: TST	(R2)+	;POP POINTER
1560	011602	012761	021324	000006	MOV	#021324,6(R1)	;PUT INSTRUCTION IN PORT6
1561	011610	012711	001400		MOV	#BIT9!BIT8,(R1)	;PORT4+LU 15
1562	011614	156122	000004		BISB	4(R1),(R2)+	;STORE DDCMP LINE # IN TABLE
1563	011620	012761	021344	000006	MOV	#021344,6(R1)	;PORT6+INSTRUCTION
1564	011626	012711	001400		MOV	#BIT8!BIT9,(R1)	;CLOCK INSTR.
1565	011632	156122	000004		BISB	4(R1),(R2)+	;STORE BM873 ADD IN TABLE
1566	011636	005722			TST	(R2)+	;POP OVER STAT3
1567	011640	005011			CLR	(R1)	;CLEAR ROMI
1568	011642	005237	001310		INC	DMNUM	;UPDATE DEVICE COUNTER
1569	011646	022737	000020	001310	CMP	#20,DMNUM	;ARE MAX. NO. OF DEV FOUND?
1570	011654	001412			BEQ	13\$;YES DON'T LOOK FOR ANY MORE.
1571	011656	005011			3\$: CLR	(R1)	;CLEAR BIT 10
1572	011660	005061	000006		CLR	6(R1)	;CLEAR SEL 6
1573	011664	062701	000010		14\$: ADD	#10,R1	;UPDATE CSR POINTER ADDRESS
1574	011670	022701	164000		CMP	#164000,R1	
1575	011674	001402			BEQ	13\$;BR IF DONE
1576	011676	000137	011264		JMP	2\$;JUMP IF NOT
1577	011702	005037	001306		13\$: CLR	DMACTV	
1578	011706	005737	001310		TST	DMNUM	;WERE ANY DMC11'S FOUND AT ALL?
1579	011712	001423			BEQ	5\$;ERROR AUTO SIZER FOUND NO DMC11'S IN THIS SYS.
1580	011714	013701	001310		MOV	DMNUM,R1	
1581	011720	010137	001314		MOV	R1,SAVNUM	;SAVE NUMBER OF DEVICES
1582	011724	000241			4\$: CLC		
1583	011726	006137	001306		ROL	DMACTV	;GENERATE ACTIVE REGISTER OF DEVICES.
1584	011732	005237	001306		INC	DMACTV	;SET THE BIT
1585	011736	005301			DEC	R1	
1586	011740	001371			BNE	4\$;BR IF MORE TO GENERATE
1587	011742	012737	000006	000004	MOV	#6,2#4	;RESTORE TRAP VECTOR
1588	011750	013737	001306	001312	MOV	DMACTV,SAVACT	;SAVE ACTIVE REGISTER
1589	011756	000137	012010		JMP	VECMAP	;GO FIND THE VECTOR NOW.
1590	011762	104402	005760		5\$: TYPE	,MERR2	;NOTIFY OPR THAT NO DMC11'S FOUND.
1591	011766	005000			CLR	RO	;MAKE DATA LIGHTS ZERO
1592	011770	000000			HALT		;STOP THE SHOW
1593	011772	000776			BR	.-2	;DISABLE CONT. SW.
1594	011774	012716	011664		6\$: MOV	#14\$,(SP)	;ENTERED BY NON-EXISTANT TIME-OUT.
1595	012000	000002			RTI		;RETURN TO MAINSTREAM
1596							
1597	012002	000001			WHICH: 1		
1598	012004	002	002		.BYTE	2,2	
1599	012006	001256			TEMPS		
1600							
1601	012010	032737	000001	001236	VECMAP: BIT	#SW00,STRTSW	
1602	012016	001114			BNE	5\$	
1603	012020	012737	000340	000022	MOV	#340,2#22	;SET IOT TRAP PRIO TO 7
1604	012026	012737	012202	000020	MOV	#4\$,2#20	;SET IOT TRAP VECTOR
1605	012034	012702	001500		MOV	#DM.MAP,R2	;SET SOFTWARE POINTER
1606	012040	012700	000300		MOV	#300,RO	;FLOATING VECTORS START HERE.

GENERAL UTILITIES (TYPEOUT, ERROR, SCOPE, ETC)

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1607 012044 012701 000302
1608 012050 010120
1609 012052 012721 000004
1610 012056 022021
1611 012060 020127 001000
1612 012064 101771
1613 012066 013737 001306 001246
1614 012074 006037 001246
1615 012100 103063
1616 012102 012704 000012
1617 012106 016437 012252 177776
1618 012114 011201
1619 012116 012761 000200 000004
1620 012124 012711 001000
1621 012130 012761 121111 000006
1622 012136 012711 001400
1623 012142 105200
1624 012144 001376
1625 012146 162704 000002
1626 012152 001404
1627 012154 016437 012252 177776
1628 012162 000767
1629 012164 052762 005300 000002
1630 012172 005011
1631 012174 062702 000010
1632 012200 000735
1633 012202 051662 000002
1634 012206 042762 000007 000002
1635 012214 016405 012254
1636 012220 006305
1637 012222 006305
1638 012224 006305
1639 012226 006305
1640 012230 042705 170777
1641 012234 050562 000002
1642 012240 022626
1643 012242 012716 012172
1644 012246 000002
1645 012250 000207
1646
1647 012252 000000
1648 012254 000000
1649 012256 000200
1650 012260 000240
1651 012262 000300
1652 012264 000340
1653
1654
1655 012266 105777 166712
1656 012272 100375
1657 012274 017703 166706
1658 012300 105777 166704
1659 012304 100375
1660 012306 010377 166700
1661 012312 042703 000240
1662 012316 000207

1$: MOV #302,R1 ;PC OF IOT INSTR
MOV R1,(R0)+ ;START FILLING VECTOR AREA
MOV #4,(R1)+ ;WITH .+2; IOT
CMP (R0)+(R1)+ ;ADD 2 TO R0 +R1
CMP R1,#1000
BLOS 1$ ;BR IF MORE TO FILL
MOV DMACTV,TEMP1 ;STORE TEMPORALLY
ROR TEMP1 ;BRING OUT A BIT
BCC 5$ ;BR IF ALL DONE
MOV #12,R4 ;R4 IS INDEX REGISTER
MOV BRLVL(R4),PS ;SET PS TO 7
MOV (R2),R1
MOV #200,4(R1)
MOV #BIT9,(R1) ;SET ROMI
MOV #121111,6(R1) ;PUT INSTRUCTION IN PORT6
MOV #BIT9!BIT8,(R1) ;FORCE AN INTERRUPT
ROR ;STALL
BNE .-2 ;FOR TIME TO INTERRUPT
SUB #2,R4 ;GET NEXT LOWEST PS LEVEL
BEQ 6$ ;BR IF R4 = 0
MOV BRLVL(R4),PS ;MOVE NEXT LOWER LEVEL IN PS
BR 7$ ;BR TO DELAY
6$: BIS #5300,2(R2) ;NO INTERRUPT ASSUME 300 AT LEVEL 5 AND FIX DMC11
3$: CLR (R1) ;CLEAR ROMI
ADD #10,R2 ;POP SOFTWARE POINTER
BR 2$ ;KEEP GOING
4$: BIS (SP),2(R2) ;GET VECTOR ADDRESS
BIC #7,2(R2) ;CLEAR JUNK
MOV BRLVL+2(R4),R5 ;GET BR LEVEL OF DMC11
ASL R5 ;SHIFT LEVEL 4 PLACES
ASL R5 ;TO THE LEFT FOR THE
ASL R5 ;STATUS TABLE
BIC #170777,R5 ;CLEAR UNWANTED BITS
BIS R5,2(R2) ;PUT BR LEVEL IN STATUS TABLE
CMP (SP)+(SP)+ ;POP IOT JUNK OFF STACK
MOV #3$,(SP) ;SET FOR RETURN
5$: RTI PC ;ALL DONE WITH "AUTO SIZING"

BRLVL: 0 ;LEVEL 0
0 ;LEVEL 0
200 ;LEVEL 4
240 ;LEVEL 5
300 ;LEVEL 6
340 ;LEVEL 7

INTTY: TSTB @TKCSR ;WAIT FOR DONE
BPL .-4
MOV @TKDBR,R3 ;PUT CHAR IN R3
TSTB @TPCSR ;WAIT UNTIL PRINTER IS READY
BPL .-4
MOV R3,@TPDBR ;ECHO CHAR
BIC #BIT7!BITS,R3 ;MASK OFF LOWER CASE
RTS PC ;RETURN
    
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K04

DZDMH MACY11 30(1046) 11-JUL-77 12:32 PAGE 34

DZDMH.P11 16-MAY-77 09:54

GENERAL UTILITIES (TYPEOUT, ERROR, SCOPE, ETC)

PAGE: 0049

1663

1720	012516	012761	021440	000004	MOV	#BASE,4(R1)	;SET UP BASE ADDRESS
1721	012524	005061	000006		CLR	6(R1)	;CLEAR COUNT
1722	012530	142711	000040		BICB	#40,(R1)	;CLEAR RQI
1723	012534	005037	001416		CLR	TEMP	;GET SET TO DELAY
1724	012540	105711		3\$:	TSTB	(R1)	;IS RDI GONE?
1725	012542	100020			BPL	8\$;BR IF YES
1726	012544	005237	001416		INC	TEMP	;INC DELAY
1727	012550	001373			BNE	3\$;BR IF NOT DONE
1728	012552	105761	000002		TSTB	2(R1)	;IS THERE A CNTL 0 ERROR
1729	012556	100011			BPL	18\$;BR IF NO
1730	012560	016137	000004	001252	MOV	4(R1),TEMP3	;SAVE SEL4 FOR TYPEOUT
1731	012566	016137	000006	001254	MOV	6(R1),TEMP4	;SAVE SEL6 FOR TYPEOUT
1732	012574	104016			HLT	16	;CNTL 0 ERROR
1733	012576	000137	013402		JMP	14\$;FATAL ERROR STOP
1734	012602	104014		18\$:	HLT	14	;ERROR RDI STILL SET
1735	012604			8\$:			
1736	012604	152711	000041		BISB	#41,(R1)	;ASK FOR CNTL I
1737	012610	105711		64\$:	TSTB	(R1)	;WAIT FOR RDI
1738	012612	100376			BPL	64\$;BR IF NOT SETY
1739	012614	005061	000006		CLR	6(R1)	;SET FULL DUPLEX
1740	012620	142711	000040		BICB	#40,(R1)	;CLEAR RQI
1741	012624	105711		65\$:	TSTB	(R1)	;RDI UP?
1742	012626	100776			BMI	65\$;BR IF YES
1743	012630	152711	000044		BISB	#44,(R1)	;REC BA/CC
1744	012634	005037	001416		CLR	TEMP	;GET SET TO DELAY
1745	012640	105711		4\$:	TSTB	(R1)	;IS RDI SET?
1746	012642	100404			BMI	.+12	;BR IF YES
1747	012644	005237	001416		INC	TEMP	;INC DELAY
1748	012650	001373			BNE	4\$;BR IF DELAY NOT DONE
1749	012652	104014			HLT	14	;ERROR RDI NOT SET
1750	012654	012761	021372	000004	MOV	#RBUF,4(R1)	;LOAD REC BA
1751	012662	013761	021370	000006	MOV	RCOUNT,6(R1)	;LOAD REC COUNT
1752	012670	142711	000040		BICB	#40,(R1)	;CLEAR RQI
1753	012674	005037	001416		CLR	TEMP	;GET SET TO DELAY
1754	012700	105711		5\$:	TSTB	(R1)	;RDI GONE?
1755	012702	100004			BPL	.+12	;BR IF YES
1756	012704	005237	001416		INC	TEMP	;INC DELAY
1757	012710	001373			BNE	5\$;BR IF NO DONE
1758	012712	104014			HLT	14	;ERROR RDI STILL SET
1759	012714	152711	000040		BISB	#40,(R1)	;XMIT BA/CC
1760	012720	005037	001416		CLR	TEMP	;GET SET TO DELAY
1761	012724	105711		6\$:	TSTB	(R1)	;RDI SET?
1762	012726	100404			BMI	.+12	;BR IF YES
1763	012730	005237	001416		INC	TEMP	;INC DELAY
1764	012734	001373			BNE	6\$;BR IF NOT DONE
1765	012736	104014			HLT	14	;ERROR RDI NOT SET
1766	012740	012761	021324	000004	MOV	#TBUF,4(R1)	;LOAD XMIT BUFFER
1767	012746	013761	021322	000006	MOV	TCOUNT,6(R1)	;LOAD COUNT
1768	012754	142711	000040		BICB	#40,(R1)	;CLEAR RQI
1769	012760	005037	001416		CLR	TEMP	;GET SET TO DELAY
1770	012764	105711		7\$:	TSTB	(R1)	;RDI GONE?
1771	012766	100004			BPL	.+12	;BR IF YES
1772	012770	005237	001416		INC	TEMP	;INC DELAY
1773	012774	001373			BNE	7\$;BR IF NOT DONE DELAY
1774	012776	104014			HLT	14	;ERROR RDI STILL SET
1775	013000	005037	001416	16\$:	CLR	TEMP	;GET SET TO DELAY

1776	013004	012737	000022	001246		MOV	#22,TEMP1	:GET SET FOR LONG DELAY
1777	013012	105761	000002		11\$:	TSTB	2(R1)	:RDO SET?
1778	013016	100407				BMI	17\$:BR IF YES
1779	013020	005237	001416			INC	TEMP	:INC DELAY
1780	013024	001372				BNE	11\$:BR IF DELAY NOT DONE
1781	013026	005337	001246			DEC	TEMP1	:DEC DELAY COUNT
1782	013032	001367				BNE	11\$:BR IF NOT DONE DELAY
1783	013034	104014				HLT	14	:ERROR RDO NOT SET
1784	013036	016137	000002	001250	17\$:	MOV	2(R1),TEMP2	:SAVE SEL2
1785	013044	001001				BNE	+.4	:BR IF OK
1786	013046	104014				HLT	14	:ERROR!!! SEL2 = 0!!!!!!
1787	013050	032761	000004	000002		BIT	#BIT2,2(R1)	:REC OR XMIT?
1788	013056	001032				BNE	13\$:BR IF REC
1789	013060	005737	021316		12\$:	TST	TFLAG	:FIRST TIME HERE?
1790	013064	001401				BEQ	+.4	:BR IF YES
1791	013066	104014				HLT	14	:ERROR MULTIPLE XMIT DONES
1792	013070	012737	177777	021316		MOV	#-1,TFLAG	:SET TFLAG TO -1
1793	013076	132761	000001	000002		BITB	#BIT0,2(R1)	:IS IT CONTROL 0
1794	013104	001401				BEQ	+.4	:BR IF NO
1795	013106	104014				HLT	14	:XMIT ERROR
1796	013110	022761	021324	000004		CMP	#TBUF,4(R1)	:XMIT BA CORRECT?
1797	013116	001401				BEQ	+.4	:BR IF YES
1798	013120	104014				HLT	14	:XMIT BA ERROR
1799	013122	023761	021322	000006		CMP	TCount,6(R1)	:COUNT OK?
1800	013130	001401				BEQ	+.4	:BR IF YES
1801	013132	104014				HLT	14	:XMIT COUNT ERROR
1802	013134	142761	000207	000002		BICB	#207,2(R1)	:CLEAR RDO AND BITS 0-2
1803	013142	000453				BR	15\$:CONTINUE
1804	013144	005737	021320		13\$:	TST	RFLAG	:FIRST TIME HERE?
1805	013150	001401				BEQ	+.4	:BR IF YES
1806	013152	104014				HLT	14	:ERROR MULTIPLE REC DONES
1807	013154	012737	177777	021320		MOV	#-1,RFLAG	:SET RFLAG TO -1
1808	013162	132761	000001	000002		BITB	#BIT0,2(R1)	:IS IT CNTL 0
1809	013170	001401				BEQ	+.4	:BR IF NO
1810	013172	104014				HLT	14	:RECEIVE ERROR
1811	013174	022761	021372	000004		CMP	#RBUF,4(R1)	:REC BA CORRECT?
1812	013202	001401				BEQ	+.4	:BR IF YES
1813	013204	104014				HLT	14	:REC BA ERROR
1814	013206	023761	021370	000006		CMP	RCount,6(R1)	:COUNT OK?
1815	013214	001401				BEQ	+.4	:BR IF YES
1816	013216	104014				HLT	14	:REC COUNT ERROR
1817	013220	013700	021370			MOV	RCount,R0	:GET SET TO CHECK DATA
1818	013224	012702	021324			MOV	#TBUF,R2	:R2 POINTS TO GOOD DATA
1819	013230	012703	021372			MOV	#RBUF,R3	:R3 POINTS TO RECEIVE DATA
1820	013234	010337	001252		9\$:	MOV	R3,TEMP3	:SAVE ADDRESS FOR TYPEOUT
1821	013240	112205				MOVB	(R2)+,R5	:R5 = XMIT DATA
1822	013242	112304				MOVB	(R3)+,R4	:R4 = RECEIVE DATA
1823	013244	120504				CMPB	R5,R4	:CHECK DATA
1824	013246	001401				BEQ	+.4	:BR IF OK
1825	013250	104013				HLT	13	:DATA ERROR
1826	013252	005300				DEC	R0	:DEC COUNT
1827	013254	001367				BNE	9\$:BR IF NOT DONE
1828	013256	005713				TST	(R3)	:THIS SHOULD BE 0, ELSE
1829	013260	001401				BEQ	+.4	:IT RECEIVED TO MUCH!!
1830	013262	104014				HLT	14	:ERROR
1831	013264	142761	000207	000002		BICB	#207,2(R1)	:CLEAR RDO AND BITS 0-2


```

1832 013272 005737 021320      15$:  TST      RFLAG      ;REC DONE?
1833 013276 001640              BEQ      16$          ;BR IF NO
1834 013300 005737 021316      TST      TFLAG      ;XMIT DONE?
1835 013304 001635              BEQ      16$          ;BR IF NO
1836 013306 004737 022502      JSR      PC,SHUTDOWN ;SHUTDOWN DMC
1837 013312 012700 013340      MOV      #25$,R0     ;POINTER TO EXPECTED SOFT COUNTS
1838 013316 012701 021443      21$:  MOV      #BASE+3,R1 ;POINTER TO ACTUAL COUNTS
1839 013322 012702 000010      MOV      #10,R2     ;COUNT
1840 013326 122021              CMPB     (R0)+,(R1)+ ;COMPARE SOFT ERROR COUNTS
1841 013330 001007              BNE      23$          ;IF ERROR BR 23$
1842 013332 005302              DEC      R2          ;DEC COUNT
1843 013334 001374              BNE      22$          ;CONTINUE CHECKING IF NOT DONE
1844 013336 000421              BR       24$          ;ALL COUNTS OK, GET OUT
1845 013340          000      000      000      25$:  .BYTE    0,0,0,0,0,0,0,0 ;EXPECTED ERROR COUNTS
1846 013343          000      000      000
1847 013346          000      000
1848 013350 113737 021443 001250      23$:  MOVB     BASE+3,TEMP2
1849 013356 113737 021445 001252      MOVB     BASE+5,TEMP3
1850 013364 113737 021447 001254      MOVB     BASE+7,TEMP4
1851 013372 113737 021451 001256      MOVB     BASE+11,TEMP5
1852 013400 104017              HLT      17
1853 013402              24$:
1854 013402 104400      14$:  SCOPE          ;SCOPE THIS TEST
1855
1856
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1859
1860
1861
1862
1863
1864

```

```

;***** TEST 2 *****
;*OVERUN TEST
;*IN FREE RUNNING MODE SEND MESSAGE WITH NO RECEIVE
;*BUFFER AVAILABLE, VERIFY THAT AN OVERRUN ERROR OCCURS
;*****

```

; TEST 2

```

1865 013404 012737 000002 001226      TST2:  MOV      #2,TSTNO
1866 013412 012737 013754 001216      MOV      #TST3,NEXT
1867
1868 013420 032737 100000 001366      BIT      #BIT15,STAT1 ;R1 CONTAINS BASE DMC11 ADDRESS
1869 013426 001406              BEQ      .+16         ;IS IT A DMC?
1870 013430 032737 000001 001372      BIT      #BIT0,STAT3 ;BR IF YES
1871 013436 001002              BNE      .+6          ;KMC WITH BIT0 SET?
1872 013440 000137 013736              JMP      10$          ;BR IF YES
1873 013444 032737 010000 001366      BIT      #BIT12,STAT1 ;SKIP TEST
1874 013452 001372              BNE      .-12         ;LU PRESENT?
1875 013454 004737 022040              JSR      PC,BASELD    ;BR IF NO
1876 013460 004537 022450              JSR      R5,XFREL0    ;LOAD DMC BASE ADDRESS
1877 013464 021324              TBUF          ;LOAD XMIT BA/CC
1878 013466 000044              44            ;BA
1879 013470 012700 000010              MOV      #10,R0      ;CC
1880 013474 012703 000015              MOV      #15,R3      ;RO = RETRANSMISSION COUNT
1881 013500 005037 001416              CLR      TEMP        ;DELAY COUNT
1882 013504 105761 000002      1$:  TSTB     2(R1)       ;CLEAR DELAY COUNTER
1883 013510 100407              BMI      .+20         ;IS RDY 0 SET?
1884 013512 005237 001416              INC      TEMP        ;BR IF SET
1885 013516 001372              BNE      1$          ;INC DELAY COUNTER
1886 013520 005303              DEC      R3          ;BR IF NOT DONE DELAY
1887 013522 001370              BNE      1$          ;DEC DELAY COUNT
                          ;BR IF DELAY NOT DONE

```

```

1888 013524 104014 HLT 14 ;ERROR, RDY 0 NOT SET
1889 013526 000503 BR 10$ ;GET OUT
1890 013530 132761 000001 000002 BITB #BIT0,2(R1) ;IS IT CNTL 0?
1891 013536 001002 BNE 11$ ;BR IF YES
1892 013540 104014 HLT 14 ;ERROR, NOT CNTL 0
1893 013542 000475 BR 10$ ;CONTINUE
1894 013544 012705 000004 11$: MOV #BIT2,R5 ;PUT "EXPECTED" IN R5
1895 013550 016104 000006 MOV 6(R1),R4 ;PUT "FOUND" IN R4
1896 013554 020504 CMP R5,R4 ;IS ORUN SET?
1897 013556 001404 BEQ 12$ ;BR IF YES
1898 013560 022704 000001 CMP #1,R4 ;DATA CK ERROR?
1899 013564 001465 BEQ 13$ ;BR IF YES
1900 013566 104015 HLT 15 ;ERROR, ORUN NOT SET
1901 013570 042761 000207 000002 12$: BIC #207,2(R1) ;CLEAR RDO
1902 013576 005037 001416 CLR TEMP ;RESET DELAY
1903 013602 005300 DEC R0 ;DEC RETRANS COUNT
1904 013604 001337 BNE 1$ ;CONTINUE
1905 013606 004737 022502 JSR PC,SHUTDOWN ;SHUTDOWN DMC
1906 013612 032737 020000 001366 BIT #BIT13,STAT1 ;IS IT AN M8201?
1907 013620 001446 BEQ 10$ ;SKIP BASE CHECK IF YES
1908 013622 012700 013664 MOV #25$,R0 ;POINTER TO EXPECTED SOFT COUNTS (LOW SPEED)
1909 013626 032737 000002 001372 BIT #BIT1,STAT3 ;IS IT HIGH OR LOW
1910 013634 001402 BEQ 21$ ;BR IF LOW
1911 013636 012700 013674 MOV #26$,R0 ;POINTER TO EXPECTED SOFT COUNTS (HIGH SPEED)
1912 013642 012701 021443 21$: MOV #BASE+3,R1 ;POINTER TO ACTUAL COUNTS
1913 013646 012702 000010 MOV #10,R2 ;COUNT
1914 013652 122021 22$: CMPB (R0)+,(R1)+ ;COMPARE SOFT ERROR COUNTS
1915 013654 001013 BNE 23$ ;IF ERROR BR 23$
1916 013656 005302 DEC R2 ;DEC COUNT
1917 013660 001374 BNE 22$ ;CONTINUE CHECKING IF NOT DONE
1918 013662 000425 BR 24$ ;ALL COUNTS OK, GET OUT
1919 013664 000 000 000 25$: .BYTE 0,0,0,100,0,0,0,0 ;EXPECTED ERROR COUNTS (LOW SPEED)
1920 013667 100 000 000
1921 013672 000 000
1922 013674 000 000 077 26$: .BYTE 0,0,77,100,0,0,0,0 ;EXPECTED ERROR COUNTS (HIGH SPEED)
1923 013677 100 090 000
1924 013702 000 000
1925 013704 113737 021443 001250 23$: MOVB BASE+3,TEMP2
1926 013712 113737 021445 001252 MOVB BASE+5,TEMP3
1927 013720 113737 021447 001254 MOVB BASE+7,TEMP4
1928 013726 113737 021451 001256 MOVB BASE+11,TEMP5
1929 013734 104017 HLT 17
1930 013736 24$:
1931 013736 104400 10$: SCOPE ;SCOPE THIS TEST
1932 013740 042761 000207 000002 13$: BIC #207,2(R1) ;IGNOR THIS ERROR
1933 013746 005037 001416 CLR TEMP ;RESET DELAY
1934 013752 000654 BR 1$ ;CONTINUE
1935
1936
1937 ;***** TEST 3 *****
1938 ;*LOST DATA TEST
1939 ;*IN FREE RUNNING MODE SEND A MESSAGE LONGER THAN THE RECEIVE
1940 ;*BUFFER, VERIFY THAT A LOST DATA ERROR OCCURS.
1941 ;*****
1942
1943 ; TEST 3

```


Address	Hex	Hex	Hex	Hex	Label	Code	Comments
2000	014234	104400			10\$: SCOPE		;SCOPE THIS TEST
2001							
2002							
2003							***** TEST 4 *****
2004							*TRANSMIT NON-EXISTENT MEMORY TEST
2005							*IN FREE RUNNING MODE, LOAD A TRANSMIT BA THAT WILL TIME OUT
2006							*VERIFY THAT A NON-EXISTENT MEMORY ERROR OCCURS
2007							*****
2008							
2009							
2010							; TEST 4
2011	014236	012737	000004	001226	TST4:	MOV #4,TSTNO	
2012	014244	012737	014510	001216		MOV #TST5,NEXT	
2013							;R1 CONTAINS BASE DMC11 ADDRESS
2014	014252	104412				MSTCLR	;MASTER CLEAR DMC11
2015	014254	032737	100000	001366		BIT #BIT15,STAT1	;IS IT A DMC?
2016	014262	001406				BEQ .+16	;BR IF YES
2017	014264	032737	000001	001372		BIT #BIT0,STAT3	;KMC WITH BIT0 SET?
2018	014272	001002				BNE .+6	;BR IF YES
2019	014274	000137	014506			JMP 10\$;SKIP TEST
2020	014300	032737	010000	001366		BIT #BIT12,STAT1	;LU PRESENT?
2021	014306	001372				BNE .-12	;BR IF NO
2022	014310	004737	022040			JSR PC,BASELD	;LOAD DMC BASE ADDRESS
2023	014314	004537	022450			JSR RS,XFRELD	;LOAD XMIT BA/CC
2024	014320	177320				177320	;BA
2025	014322	140044				140044	;CC
2026	014324	012703	000015			MOV #15,R3	;DELAY COUNT
2027	014330	005037	001416			CLR TEMP	;CLEAR DELAY COUNTER
2028	014334	105761	000002		1\$:	TSTB 2(R1)	;IS RDY 0 SET?
2029	014340	100407				BMI .+20	;BR IF SET
2030	014342	005237	001416			INC TEMP	;INC DELAY COUNTER
2031	014346	001372				BNE 1\$;BR IF NOT DONE DELAY
2032	014350	005303				DEC R3	;DEC DELAY COUNT
2033	014352	001370				BNE 1\$;BR IF DELAY NOT DONE
2034	014354	104014				HLT 14	;ERROR, RDY 0 NOT SET
2035	014356	000453				BR 10\$;GET OUT
2036	014360	132761	000001	000002		BITB #BIT0,2(R1)	;IS IT CNTL 0?
2037	014366	001002				BNE 11\$;BR IF YES
2038	014370	104014				HLT 14	;ERROR, NOT CNTL 0
2039	014372	000445				BR 10\$;CONTINUE
2040	014374	012705	000400		11\$:	MOV #BIT8,R5	;PUT "EXPECTED" IN R5
2041	014400	016104	000006			MOV 6(R1),R4	;PUT "FOUND" IN R4
2042	014404	020504				CMP R5,R4	;IS NON-EX-MEM SET?
2043	014406	001401				BEQ .+4	;BR IF YES
2044	014410	104015				HLT 15	;ERROR NON-EX-MEM NOT SET
2045	014412	004737	022502			JSR PC,SHUTDOWN	;SHUTDOWN DMC
2046	014416	012700	014444			MOV #25\$,R0	;POINTER TO EXPECTED SOFT COUNTS
2047	014422	012701	021443		21\$:	MOV #BASE+3,R1	;POINTER TO ACTUAL COUNTS
2048	014426	012702	000010			MOV #10,R2	;COUNT
2049	014432	122021			22\$:	CMPB (R0)+,(R1)+	;COMPARE SOFT ERROR COUNTS
2050	014434	001007				BNE 23\$;IF ERROR BR 23\$
2051	014436	005302				DEC R2	;DEC COUNT
2052	014440	001374				BNE 22\$;CONTINUE CHECKING IF NOT DONE
2053	014442	000421				BR 24\$;ALL COUNTS OK, GET OUT
2054	014444	000	000	000	25\$:	.BYTE 0,0,0,0,0,0,0,0	;EXPECTED ERROR COUNTS
2055	014447	000	000	000			


```

2056 014452 000 000
2057 014454 113737 021443 001250
2058 014462 113737 021445 001252
2059 014470 113737 021447 001254
2060 014476 113737 021451 001256
2061 014504 104017
2062 014506
2063 014506 104400
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2074 014510 012737 000005 001226
2075 014516 012737 014772 001216
2076
2077 014524 104412
2078 014526 032737 100000 001366
2079 014534 001406
2080 014536 032737 000001 001372
2081 014544 001002
2082 014546 000137 014770
2083 014552 032737 010000 001366
2084 014560 001372
2085 014562 004737 022040
2086 014566 004537 022416
2087 014572 177320
2088 014574 140044
2089 014576 004537 022450
2090 014602 021324
2091 014604 000044
2092 014606 012703 000015
2093 014612 005037 001416
2094 014616 105761 000002
2095 014622 100407
2096 014624 005237 001416
2097 014630 001372
2098 014632 005303
2099 014634 001370
2100 014636 104014
2101 014640 000453
2102 014642 132761 000001 000002
2103 014650 001002
2104 014652 104014
2105 014654 000445
2106 014656 012705 000400
2107 014662 016104 000006
2108 014666 020504
2109 014670 001401
2110 014672 104015
2111 014674 004737 022502

```

```

23$: MOVB BASE+3,TEMP2
      MOVB BASE+5,TEMP3
      MOVB BASE+7,TEMP4
      MOVB BASE+11,TEMP5
      HLT 17
24$:
10$: SCOPE ;SCOPE THIS TEST

```

```

:***** TEST 5 *****
:*RECEIVE NON-EXISTENT MEMORY TEST
:*IN FREE RUNNING MODE, LOAD A RECEIVE BA THAT WILL TIME OUT
:*VERIFY THAT A NON-EXISTENT MEMORY ERROR OCCURS
:*****

```

: TEST 5

```

TST5: MOV #5,TSTNO
      MOV #TST6,NEXT
      MSTCLR ;R1 CONTAINS BASE DMC11 ADDRESS
      BIT #BIT15,STAT1 ;MASTER CLEAR DMC11
      BEQ .+16 ;IS IT A DMC?
      BNE .+6 ;BR IF YES
      JMP 10$ ;KMC WITH BIT0 SET?
      BIT #BIT0,STAT3 ;BR IF YES
      BNE .+6 ;SKIP TEST
      JSR PC,BASELD ;LU PRESENT?
      JSR RS,RFRELD ;BR IF NO
      JSR RS,XFRELD ;LOAD DMC BASE ADDRESS
      TBUF ;LOAD RECEIVE BA/CC
      44 ;BA
      44 ;CC
      MOV #15,R3 ;LOAD XMIT BA/CC
      CLR TEMP ;BA
      TSTB 2(R1) ;CC
      BMI .+20 ;DELAY COUNT
      INC TEMP ;CLEAR DELAY COUNTER
      BNE 1$ ;IS RDY 0 SET?
      DEC R3 ;BR IF SET
      BNE 1$ ;INC DELAY COUNTER
      HLT 14 ;BR IF NOT DONE DELAY
      BR 10$ ;DEC DELAY COUNT
      BITB #BIT0,2(R1) ;BR IF DELAY NOT DONE
      BNE 11$ ;ERROR, RDY 0 NOT SET
      HLT 14 ;GET OUT
      BR 10$ ;IS IT CNTL 0?
      MOV #BIT8,R5 ;BR IF YES
      MOV 6(R1),R4 ;ERROR, NOT CNTL 0
      CMP R5,R4 ;CONTINUE
      BEQ .+4 ;PUT "EXPECTED" IN R5
      HLT 15 ;PUT "FOUND" IN R4
      JSR PC,SHUTDOWN ;IS NON-EX-MEM SET?
      ;BR IF YES
      ;ERROR NON-EX-MEM NOT SET
      ;SHUTDOWN DMC

```

1\$:

11\$:

2112	014700	012700	014726			MOV	#25\$,R0	: POINTER TO EXPECTED SOFT COUNTS
2113	014704	012701	021443		21\$:	MOV	#BASE+3,R1	: POINTER TO ACTUAL COUNTS
2114	014710	012702	000010			MOV	#10,R2	: COUNT
2115	014714	122021			22\$:	CMPB	(R0)+,(R1)+	: COMPARE SOFT ERROR COUNTS
2116	014716	001007				BNE	23\$: IF ERROR BR 23\$
2117	014720	005302				DEC	R2	: DEC COUNT
2118	014722	001374				BNE	22\$: CONTINUE CHECKING IF NOT DONE
2119	014724	000421				BR	24\$: ALL COUNTS OK, GET OUT
2120	014726	000	000	000	25\$:	.BYTE	0,0,0,0,0,0,0,0	: EXPECTED ERROR COUNTS
2121	014731	000	000	000				
2122	014734	000	000					
2123	014736	113737	021443	001250	23\$:	MOVB	BASE+3,TEMP2	
2124	014744	113737	021445	001252		MOVB	BASE+5,TEMP3	
2125	014752	113737	021447	001254		MOVB	BASE+7,TEMP4	
2126	014760	113737	021451	001256		MOVB	BASE+11,TEMP5	
2127	014766	104017				HLT	17	
2128	014770				24\$:			
2129	014770	104400			10\$:	SCOPE		: SCOPE THIS TEST

```

:***** TEST 6 *****
: *PROCESSOR ERROR TEST
: *IN FREE RUNNING MODE, DO A BASE TRANSFER REQUEST AFTER A
: *BASE HAS BEEN SET UP, VERIFY THAT A PROCESSOR ERROR OCCURS.
:*****

```

: TEST 6

2140	014772	012737	000006	001226	TST6:	MOV	#6,TSTNO	
2141	015000	012737	015234	001216		MOV	#TST7,NEXT	
2142								: R1 CONTAINS BASE DMC11 ADDRESS
2143	015006	104412				MSTCLR		: MASTER CLEAR DMC11
2144	015010	032737	100000	001366		BIT	#BIT15,STAT1	: IS IT A DMC?
2145	015016	001406				BEQ	+.16	: BR IF YES
2146	015020	032737	000001	001372		BIT	#BIT0,STAT3	: KMC WITH BIT0 SET?
2147	015026	001002				BNE	+.6	: BR IF YES
2148	015030	000137	015232			JMP	10\$: SKIP TEST
2149	015034	032737	010000	001366		BIT	#BIT12,STAT1	: LU PRESENT?
2150	015042	001372				BNE	-.12	: BR IF NO
2151	015044	004737	022040			JSR	PC,BASELD	: LOAD BASE ADDRESS
2152	015050	152711	000043		12\$:	BISB	#43,(R1)	: 2ND BASE REQUEST
2153	015054	105711				TSTB	(R1)	: RDI SET?
2154	015056	100376				BPL	-.2	: BR IF NO
2155	015060	142711	000040			BICB	#40,(R1)	: CLEAR RQI
2156	015064	005037	001416			CLR	TEMP	: GET SET TO DELAY
2157	015070	105761	000002		13\$:	TSTB	2(R1)	: RDO SET?
2158	015074	100405				BMI	14\$: BR IF YES
2159	015076	005237	001416			INC	TEMP	: INC DELAY
2160	015102	001372				BNE	13\$: BR IF NOT DONE DELAY
2161	015104	104014				HLT	14	: ERROR, RDO NOT SET
2162	015106	000770				BR	13\$: TRY AGAIN
2163	015110	132761	000001	000002	14\$:	BITB	#BIT0,2(R1)	: IS IS CNTL 0?
2164	015116	001002				BNE	11\$: BR IF YES
2165	015120	104014				HLT	14	: ERROR NOT CNTL 0
2166	015122	000443				BR	10\$: CONTINUE
2167	015124	012705	001000		11\$:	MOV	#BIT9,R5	: PUT "EXPECTED" IN R5

2168	015130	016104	000006			MOV	6(R1),R4	;PUT "FOUND" IN R4
2169	015134	020504				CMP	R5,R4	;IS PROC ERROR SET?
2170	015136	001401				BEQ	.+4	;BR IF YES
2171	015140	104015				HLT	15	;ERROR, PROC ERROR NOT SET
2172	015142	012700	015170			MOV	#25\$,R0	;POINTER TO EXPECTED SOFT COUNTS
2173	015146	012701	021443	21\$:		MOV	#BASE+3,R1	;POINTER TO ACTUAL COUNTS
2174	015152	012702	000010			MOV	#10,R2	;COUNT
2175	015156	122021		22\$:		CMPB	(R0)+,(R1)+	;COMPARE SOFT ERROR COUNTS
2176	015160	001007				BNE	23\$;IF ERROR BR 23\$
2177	015162	005302				DEC	R2	;DEC COUNT
2178	015164	001374				BNE	22\$;CONTINUE CHECKING IF NOT DONE
2179	015166	000421				BR	24\$;ALL COUNTS OK, GET OUT
2180	015170	000	000	000	25\$:	.BYTE	0,0,0,0,0,0,0,0	;EXPECTED ERROR COUNTS
2181	015173	000	000	000				
2182	015176	000	000	000				
2183	015200	113737	021443	001250	23\$:	MOVB	BASE+3,TEMP2	
2184	015206	113737	021445	001252		MOVB	BASE+5,TEMP3	
2185	015214	113737	021447	001254		MOVB	BASE+7,TEMP4	
2186	015222	113737	021451	001256		MOVB	BASE+11,TEMP5	
2187	015230	104017				HLT	17	
2188	015232				24\$:			
2189	015232	104400			10\$:	SCOPE		;SCOPE THIS TEST

```

;***** TEST 7 *****
;*PROCESSOR ERROR TEST
;*IN FREE RUNNING MODE DO A RQI WITH AN ILLEGAL IO CODE
;*VERIFY THAT A PROCESSOR ERROR OCCURS
;*****

```

: TEST 7

2200	015234	012737	000007	001226	TST7:	MOV	#7,TSTNO	
2201	015242	012737	015476	001216		MOV	#TSTIO,NEXT	
2202								;R1 CONTAINS BASE DMC11 ADDRESS
2203	015250	104412				MSTCLR		;MASTER CLEAR DMC11
2204	015252	032737	100000	001366		BIT	#BIT15,STAT1	;IS IT A DMC?
2205	015260	001406				.+16		;BR IF YES
2206	015262	032737	000001	001372		BIT	#BIT0,STAT3	;KMC WITH BIT0 SET?
2207	015270	001002				BNE	.+6	;BR IF YES
2208	015272	000137	015474			JMP	10\$;SKIP TEST
2209	015276	032737	010000	001366		BIT	#BIT12,STAT1	;LU PRESENT?
2210	015304	001372				BNE	.-12	;BR IF NO
2211	015306	004737	022040			JSR	PC,BASELD	;LOAD DMC BASE ADDRESS
2212	015312	152711	000046			BISB	#46,(R1)	;RQI AND ILLEGAL CODE
2213	015316	105711				TSTB	(R1)	;WAIT FOR RDI
2214	015320	100376				RPL	.-2	;BR IF NO RDI
2215	015322	142711	000040			BICB	#40,(R1)	;CLEAR RQI
2216	015326	005037	001416			CLR	TEMP	;CLEAR COUNTER
2217	015332	105761	000002		1\$:	TSTB	2(R1)	;RDY 0 SET?
2218	015336	100405				BMI	.+14	;BR IF YES
2219	015340	005237	001416			INC	TEMP	;BUMP COUNTER DELAY
2220	015344	001372				BNE	1\$;BR IF NOT DONE
2221	015346	104014				HLT	14	;ERROR NO RDY 0
2222	015350	000770				BR	1\$;TRY AGAIN
2223	015352	132761	000001	000002		BITB	#BIT0,2(R1)	;IS IT CNTL 0

```

2224 015360 001002          BNE      11$      ;BR IF YES
2225 015362 104014          HLT      14      ;ERROR, NOT CNTL 0
2226 015364 000443          BR       10$     ;CONTINUE
2227 015366 012705 001000    11$:  MOV    #BIT9,R5 ;PUT "EXPECTED" IN R5
2228 015372 016104 000006    MOV    6(R1),R4 ;PUT "FOUND" IN R4
2229 015376 020504          CMP     R5,R4   ;IS PROC ERROR SET?
2230 015400 001401          BEQ     +4      ;BR IF YES
2231 015402 104015          HLT     15      ;ERROR PROC ERROR NOT SET
2232 015404 012700 015432    MOV    #25$,R0 ;POINTER TO EXPECTED SOFT COUNTS
2233 015410 012701 021443    21$:  MOV    #BASE+3,R1 ;POINTER TO ACTUAL COUNTS
2234 015414 012702 000010    MOV    #10,R2  ;COUNT
2235 015420 122021          22$:  CMPB  (R0)+,(R1)+ ;COMPARE SOFT ERROR COUNTS
2236 015422 001007          BNE     23$    ;IF ERROR BR 23$
2237 015424 005302          DEC     R2     ;DEC COUNT
2238 015426 001374          BNE     22$    ;CONTINUE CHECKING IF NOT DONE
2239 015430 000421          BR      24$    ;ALL COUNTS OK, GET OUT
2240 015432          000      000      000    25$:  .BYTE 0,0,0,0,0,0,0,0 ;EXPECTED ERROR COUNTS
2241 015435          000      000      000
2242 015440          000      000
2243 015442 113737 021443 001250    23$:  MOVB  BASE+3,TEMP2
2244 015450 113737 021445 001252    MOVB  BASE+5,TEMP3
2245 015456 113737 021447 001254    MOVB  BASE+7,TEMP4
2246 015464 113737 021451 001256    MOVB  BASE+11,TEMP5
2247 015472 104017          HLT     17
2248 015474          24$:
2249 015474 104400          10$:  SCOPE          ;SCOPE THIS TEST

```

```

:***** TEST 10 *****
: *HALF DUPLEX TEST
: *IN FREE RUNNING MODE, SET HALF DUPLEX AND L U LOOP
: *SEND A MESSAGE AND VERIFY THAT THERE ARE NO DONES
:*****

```

: TEST 10

```

2260 015476 012737 000010 001226    TST10: MOV    #10,TSTNO
2261 015504 012737 015632 001216    MOV    #TST11,NEXT
2262
2263 015512 104412          MSTCLR          ;R1 CONTAINS BASE DMC11 ADDRESS
2264 015514 032737 100000 001366    BIT    #BIT15,STAT1 ;MASTER CLEAR DMC11
2265 015522 001406          BEQ     +16     ;IS IT A DMC?
2266 015524 032737 000001 001372    BIT    #BIT0,STAT3 ;BR IF YES
2267 015532 001002          BNE     +6     ;KMC WITH BIT0 SET?
2268 015534 000137 015624          JMP    10$    ;BR IF YES
2269 015540 032737 010000 001366    BIT    #BIT12,STAT1 ;SKIP TEST
2270 015546 001372          BNE     -12    ;LU PRESENT?
2271 015550 004737 022156          JSR    PC,BASELH ;BR IF NO
2272 015554 004537 022416          JSR    R5,RFRELD ;LOAD BASE AND HALF DUPLEX
2273 015560 021372          RBUF          ;LOAD RECEIVE BUFFER
2274 015562 000044          44          ;BA
2275 015564 004537 022450          JSR    R5,XFRELD ;CC
2276 015570 021324          TBUF          ;LOAD TRANSMIT BUFFER
2277 015572 000044          44          ;BA
2278 015574 012703 000003          MOV    #3,R3   ;CC
2279 015600 005037 001416          CLR    TEMP    ;LOAD DELAY COUNT
                ;CLEAR DELAY

```


2280	015604	105761	000002	4\$:	TSTB	2(R1)	: IS DONE SET?
2281	015610	100406			BMI	5\$: BR IF YES (ERROR)
2282	015612	005237	001416		INC	TEMP	: INC DELAY
2283	015616	001372			BNE	4\$: BR IF DELAY NOT DONE
2284	015620	005303			DEC	R3	: DEC DELAY COUNT
2285	015622	001370			BNE	4\$: BR IF DELAY NOT DONE
2286	015624	104400		10\$:	SCOPE		: SCOPE THIS TEST
2287	015626	104014		5\$:	HLT	14	: ERROR DONE WITH HALF-DUPLEX
2288	015630	000775			BR	10\$: GET OUT

```

***** TEST 11 *****
*RESUME TEST
*THIS TEST SENDS AND RECEIVES A BUFFER AND SHUTS DOWN THE
*DMC. THEN A MASTER CLEAR IS ISSUED AND A BASE WITH RESUME
*BIT SET IS GIVEN, ANOTHER BUFFER IS SENT AND RECEIVED.
*DATA IS CHECKED.
*****

```

; TEST 11

2300								
2301	015632	012737	000011	001226	TST11:	MOV	#11,TSTNO	
2302	015640	012737	016214	001216		MOV	#TST12,NEXT	
2303								: R1 CONTAINS BASE DMC11 ADDRESS
2304	015646	104412				MSTCLR		: MASTER CLEAR DMC11
2305	015650	032737	100000	001366		BIT	#BIT15,STAT1	: IS IT A DMC?
2306	015656	001406				BEQ	.+16	: BR IF YES
2307	015660	032737	000001	001372		BIT	#BIT0,STAT3	: KMC WITH BIT0 SET?
2308	015666	001002				BNE	.+6	: BR IF YES
2309	015670	000137	016212			JMP	10\$: SKIP TEST
2310	015674	032737	010000	001366		BIT	#BIT12,STAT1	: LU PRESENT?
2311	015702	001372				BNE	.-12	: BR IF NO
2312	015704	005037	020060			CLR	RESUME	: CLR RESUME FLAG
2313	015710	005737	020060		1\$:	TST	RESUME	: FIRST OR SECOND PASS?
2314	015714	001003				BNE	2\$: BR IF SECOND
2315	015716	004737	022040			JSR	PC,BASELD	: BASE
2316	015722	000402				BR	3\$: CONTINUE
2317	015724	004737	022276		2\$:	JSR	PC,RESUM	: BASE WITH RESUME BIT
2318	015730	004537	022416		3\$:	JSR	R5,RFRELD	: RECEIVE BUFFER
2319	015734	021372				RBUF		: BA
2320	015736	000044				44		: CC
2321	015740	004537	022450			JSR	R5,XFRELD	: XMIT BUFFER
2322	015744	021324				TBUF		: BA
2323	015746	000044				44		: CC
2324	015750	012703	000030			MOV	#30,R3	: DELAY COUNT
2325	015754	012700	000002			MOV	#2,R0	: NEED TWO DONES
2326	015760	005037	001416			CLR	TEMP	: CLEAR DELAY COUNTER
2327	015764	105761	000002		4\$:	TSTB	2(R1)	: IS RDY 0 SET?
2328	015770	100407				BMI	+.20	: BR IF SET
2329	015772	005237	001416			INC	TEMP	: INC DELAY COUNTER
2330	015776	001372				BNE	4\$: BR IF NOT DONE DELAY
2331	016000	005303				DEC	R3	: DEC DELAY COUNT
2332	016002	001370				BNE	4\$: BR IF DELAY NOT DONE
2333	016004	104014				HLT	14	: ERROR, RDY 0 NOT SET
2334	016006	000501				BR	10\$: GET OUT
2335	016010	042761	000207	000002		BIC	#207,2(R1)	: CLEAR DONE

2336	016016	005300				DEC	R0	: TWO DONES YET?
2337	016020	001361				BNE	4\$: BR IF NOT
2338	016022	012702	021324			MOV	#TBUF,R2	: ADDRESS OF GOOD DATA
2339	016026	012703	021372			MOV	#RBUF,R3	: ADDRESS OF RECEIVED DATA
2340	016032	012700	000044			MOV	#44,R0	: COUNT
2341	016036	112205			6\$:	MOVB	(R2)+,R5	: LOAD GOOD DATA
2342	016040	112304				MOVB	(R3)+,R4	: LOAD FOUND DATA
2343	016042	120504				CMPB	R5,R4	: COMPARE DATA
2344	016044	001401				BEQ	7\$: BR IF OK
2345	016046	104012				HLT	12	: DATA ERROR
2346	016050	005300			7\$:	DEC	R0	: DONE YET?
2347	016052	001371				BNE	6\$: BR IF NOT
2348	016054	004737	022502			JSR	PC,SHUTDOWN	: SHUTDOWN DMC
2349	016060	005737	020060			TST	RESUME	
2350	016064	001004				BNE	8\$: BR IF ALL DONE
2351	016066	012737	177777	020060		MOV	#-1,RESUME	: SET FLAG FOR SECOND PASS
2352	016074	000705				BR	1\$: CONTINUE
2353	016076				8\$:			
2354	016076	012700	016140			MOV	#25\$,R0	: POINTER TO EXPECTED SOFT COUNTS (LOW SPEED)
2355	016102	032737	000002	001372		BIT	#BIT1,STAT3	: IS IT HIGH OR LOW
2356	016110	001402				BEQ	21\$: BR IF LOW
2357	016112	012700	016150			MOV	#26\$,R0	: POINTER TO EXPECTED SOFT COUNTS (HIGH SPEED)
2358	016116	012701	021443		21\$:	MOV	#BASE+3,R1	: POINTER TO ACTUAL COUNTS
2359	016122	012702	000010			MOV	#10,R2	: COUNT
2360	016126	122021			22\$:	CMPB	(R0)+,(R1)+	: COMPARE SOFT ERROR COUNTS
2361	016130	001013				BNE	23\$: IF ERROR BR 23\$
2362	016132	005302				DEC	R2	: DEC COUNT
2363	016134	001374				BNE	22\$: CONTINUE CHECKING IF NOT DONE
2364	016136	000425				BR	24\$: ALL COUNTS OK, GET OUT
2365	016140	000	000	000	25\$:	.BYTE	0,0,0,0,0,0,1,1	: EXPECTED ERROR COUNTS (LOW SPEED)
2366	016143	000	000	000				
2367	016146	001	001					
2368	016150	000	000	000	26\$:	.BYTE	0,0,0,0,0,0,0,0	: EXPECTED ERROR COUNTS (HIGH SPEED)
2369	016153	000	000	000				
2370	016156	000	000					
2371	016160	113737	021443	001250	23\$:	MOVB	BASE+3,TEMP2	
2372	016166	113737	021445	001252		MOVB	BASE+5,TEMP3	
2373	016174	113737	021447	001254		MOVB	BASE+7,TEMP4	
2374	016202	113737	021451	001256		MOVB	BASE+11,TEMP5	
2375	016210	104017				HLT	17	
2376	016212				24\$:			
2377	016212	104400			10\$:	SCOPE		: SCOPE THIS TEST
2378								
2379								
2380								
2381								
2382								
2383								
2384								
2385								
2386								
2387								
2388								
2389								
2390								
2391								

:***** TEST 12 *****
 :*FREE RUNNING DATA TEST (INTERRUPT DRIVEN EXERCISER)
 :*THIS TEST REPEATEDLY QUEUES UP 7 RECEIVE BUFFERS AND
 :*7 TRANSMIT BUFFERS AND CHECKS DATA WHEN ALL 7 BUFFERS
 :*ARE RECEIVED. TRANSMIT COUNTS RANGE FROM 2 TO 104.
 :*DATA IS A BINARY COUNT PATTERN. THE RESUME FUNCTION
 :*IS CHECKED IN THIS TEST. THIS TEST USES THE TURNAROUND CONNECTOR
 :*IF IT IS PRESENT, OTHERWISE LINE UNIT LOOP IS SET.
 :*****
 : TEST 12
 :-----

2392	016214	012737	000012	001226	TST12:	MOV	#12,TSTNO	
2393	016222	012737	003364	001216		MOV	#.EOP,NEXT	
2394								;R1 CONTAINS BASE DMC11 ADDRESS
2395	016230	104412				MSTCLR		;MASTER CLEAR DMC11
2396	016232	032737	100000	001366		BIT	#BIT15,STAT1	;IS IT A DMC?
2397	016240	001406				BEQ	+.16	;BR IF YES
2398	016242	032737	000001	001372		BIT	#BIT0,STAT3	;KMC WITH BIT0 SET?
2399	016250	001002				BNE	+.6	;BR IF YES
2400	016252	000137	017044			JMP	ENDEX1	;SKIP TEST
2401	016256	032737	010000	001366		BIT	#BIT12,STAT1	;LU PRESENT?
2402	016264	001372				BNE	-.12	;BR IF NO
2403	016266	012737	000340	177776		MOV	#340,PS	;LOCK OUT INTERRUPTS
2404	016274	013700	001366			MOV	STAT1,RO	;GET BR LEVEL
2405	016300	006200				ASR	RO	;SHIFT RIGHT 4 TIMES
2406	016302	006200				ASR	RO	
2407	016304	006200				ASR	RO	
2408	016306	006200				ASR	RO	
2409	016310	042700	177437			BIC	#177437,RO	;PUT BR LEVEL IN RO
2410	016314	012777	017132	163052		MOV	#IISR,DMRVEC	;LOAD INPUT VECTOR
2411	016322	010077	163050			MOV	RO,DMRLVL	;LOAD LEVEL
2412	016326	012777	017422	163044		MOV	#OISR,DMTVEC	;LOAD OUTPUT VECTOR
2413	016334	010077	163042			MOV	RO,DMTLVL	;LOAD LEVEL
2414								
2415								;INITIALIZE ALL BUFFER LISTS AND COUNT LISTS
2416								
2417	016340	012737	000104	021316		MOV	#104,TFLAG	;TFLAG CONTAINS COUNT
2418	016346	012700	020064			MOV	#XMITBA+2,RO	;RO POINTS TO BA LIST
2419	016352	012703	020356			MOV	#RBUF,R3	;R3 CONTAINS BUFFER ADDRESS
2420	016356	010320			1\$:	MOV	R3,(RO)+	;LOAD BA LIST WITH REC BA
2421	016360	062703	000104			ADD	#104,R3	;UPDATE BUFFER ADDRESS
2422	016364	022700	020102			CMP	#XMITBA+20,RO	;END OF REC BUFFERS?
2423	016370	001372				BNE	1\$;NO LOAD NEXT ONE
2424	016372	012720	020120		2\$:	MOV	#TBUF,(RO)+	;LOAD BA LIST WITH XMIT BA
2425	016376	022700	020120			CMP	#XMITBA+36,RO	;END OF XMIT BUFFERS?
2426	016402	001373				BNE	2\$;NO LOAD NEXT BUFFER
2427	016404	012700	020232			MOV	#RCNTAB+2,RO	;RO POINTS TO COUNT LIST
2428	016410	013720	021316		3\$:	MOV	TFLAG,(RO)+	;LOAD COUNT OF 104
2429	016414	022700	020250			CMP	#RCNTAB+20,RO	;END OF REC COUNT LIST?
2430	016420	001373				BNE	3\$;BR IF NO
2431	016422	012737	000005	021314		MOV	#5,FLAG ;LOOP COUNT	
2432	016430	012711	040000			MOV	#BIT14,(R1)	;SET MASTER CLEAR
2433	016434	032737	100000	001366		BIT	#BIT15,STAT1	;IOP?
2434	016442	001402				BEQ	+.6	;BR IF NO
2435	016444	012711	100000			MOV	#BIT15,(R1)	;SET RUN ON IOP
2436	016450	012700	177777			MOV	#-1,RO	;RO IS INPUT DONE COUNTER
2437	016454	005037	020060		CLRTAB:	CLR	RESUME	;CLEAR RESUME FLAG
2438	016460	012705	020266			MOV	#RDNTAB,R5	;GET READY TO CLEAR ALL RECEIVE
2439	016464	005025			2\$:	CLR	(R5)+	;BUFFERS
2440	016466	022705	021312			CMP	#RBUFE,R5	;END OF BUFFER?
2441	016472	001374				BNE	2\$;BR IF NO
2442	016474	012704	020250			MOV	#XCNTAB,R4	;R4 POINTS TO XMIT COUNT LIST
2443	016500	013724	021316		4\$:	MOV	TFLAG,(R4)+	;LOAD XMIT CHAR COUNT
2444	016504	022704	020266			CMP	#XCNTAB+16,R4	;DONE?
2445	016510	001373				BNE	4\$;BR IF NO
2446	016512	005002			5\$:	CLR	R2	;R2 IS OUTPUT DONE COUNTER
2447	016514	005004				CLR	R4	;R4 IS USED AS INDEX IN OISR

2448	016516	005711				TST	(R1)	: IS RUN SET?
2449	016520	100376				BPL	.-2	: WAIT FOR RUN
2450	016522	152761	000100	000002		BISB	#BIT6,2(R1)	: SET IE0
2451	016530	032737	040000	001366		BIT	#BIT14,STAT1	: LOOP BACK CONNECTOR?
2452	016536	001002				BNE	.+6	: BR IF YES
2453	016540	052711	004000			BIS	#BIT11,(R1)	: SET LINE UNIT LOOP
2454	016544	022737	000005	021314		CMP	#5,FLAG	: FIRST TIME?
2455	016552	001003				BNE	1\$: BR IF NOT
2456	016554	052711	000143			BIS	#143,(R1)	: SET IEI,RQI,BASE I
2457	016560	000402				BR	3\$: CONTINUE
2458	016562	052711	000144		1\$:	BIS	#144,(R1)	: SET IEI,RQI,REC BA/CC
2459	016566	005037	001416		3\$:	CLR	TEMP	: SET UP FOR DELAY COUNT
2460	016572	012737	000022	001250		MOV	#22,TEMP2	: GET SET FOR DELAY
2461	016600	005037	177776			CLR	PS	: ALLOW INTERRUPTS
2462	016604	022700	000020		SCAN:	CMP	#20,R0	: INPUT DONE?
2463	016610	001402				BEQ	SCAN2	: BR IF YES
2464	016612	000137	017102			JMP	SCAN1	: BR IF NO
2465	016616	022702	000034		SCAN2:	CMP	#34,R2	: XMIT DONE FOR ALL MESSAGES?
2466	016622	001402				BEQ	8\$: BR IF YES
2467	016624	000137	017102			JMP	SCAN1	: BR IF NO
2468	016630	022704	000034		8\$:	CMP	#34,R4	: REC DONE FOR ALL MESSAGES?
2469	016634	001402				BEQ	9\$: BR IF YES
2470	016636	000137	017102			JMP	SCAN1	: BR IF NO
2471	016642				9\$:			
2472	016642	012700	020266			MOV	#RDNTAB,R0	: GET FIRST REC BUFFER
2473	016646	012002			5\$:	MOV	(R0)+,R2	: R2 POINTS TO BUFFER
2474	016650	005005				CLR	R5	: R5=EXPECTED
2475	016652	005003				CLR	R3	: R3 = COUNT
2476	016654	010237	001252		6\$:	MOV	R2,TEMP3	: SAVE ADDRESS FOR TYPEOUT
2477	016660	112204				MOVB	(R2)+,R4	: GET RECEIVE DATA
2478	016662	120504				CMPB	R5,R4	: IS IT CORRECT?
2479	016664	001401				BEQ	.+4	: BR IF YES
2480	016666	104013				HLT	13	: DATA ERROR
2481	016670	005205				INC	R5	: NEXT CHARACTER
2482	016672	005203				INC	R3	: INC COUNT
2483	016674	021003				CMP	(R0),R3	: DONE YET?
2484	016676	001366				BNE	6\$: BR IF NO
2485	016700	062700	000002			ADD	#2,R0	: GET NEXT REC BUFFER
2486	016704	022700	020322			CMP	#RDNTAB+34,R0	: DONE YET?
2487	016710	001356				BNE	5\$: BR IF NO
2488	016712	012700	000001			MOV	#1,R0	: SET R0 TO 1
2489	016716	032737	000001	021314	4\$:	BIT	#BIT0,FLAG	: CHANGE CHAR COUNT FOR NEXT LOOP
2490	016724	001003				BNE	1\$: BR TO SUB 40
2491	016726	005337	021316			DEC	TFLAG	: DEC BY ONE
2492	016732	000403				BR	2\$: CONTINUE
2493	016734	162737	000040	021316	1\$:	SUB	#40,TFLAG	: SUBTRACT 40 FROM XMIT COUNT
2494	016742	005337	021314		2\$:	DEC	FLAG	: DEC LOOP COUNT
2495	016746	001242				BNE	CLRTAB	: GO DO IT AGAIN
2496	016750	152711	000146		ENDEX:	BISB	#146,(R1)	: SHUT DOWN DMC
2497	016754	005737	021314		1\$:	TST	FLAG	: HAS INTERRUPT OCCURED?
2498	016760	001775				BEQ	1\$: BR IF NO
2499	016762	012700	017024			MOV	#10\$,R0	: R0 POINTS TO LO SPEED COUNTS
2500	016766	032737	000002	001372		BIT	#BIT1,STAT3	: IS IT LO SPEED?
2501	016774	001402				BEQ	2\$: BR IF YES
2502	016776	012700	017034			MOV	#11\$,R0	: R0 POINTS TO HI COUNTS
2503	017002	012701	021443		2\$:	MOV	#BASE+3,R1	: POINTER TO ACTUAL COUNTS


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2504 017006 012702 000010          MOV      #10,R2          ;10 COUNTS TO CHECK
2505 017012 122021          3$:     CMPB     (R0)+,(R1)+ ;CHECK COUNT
2506 017014 103414          BLO     ENDEX2          ;BR IF ERROR
2507 017016 005302          DEC     R2              ;DEC COUNT
2508 017020 001374          BNE     3$              ;BR IF NOT DONE
2509 017022 000410          BR      ENDEX1          ;ALL OK GET OUT
2510 017024          000          000          000          10$:     .BYTE    0,0,0,0,0,0,5,5 ;EXPECTED LO SPEED COUNTS
2511 017027          000          000          000
2512 017032          005          005
2513 017034          000          000          005          11$:     .BYTE    0,0,5,0,0,0,5,5 ;EXPECTED HI SPEED COUNTS
2514 017037          000          000          000
2515 017042          005          005
2516 017044 104400          ENDEX1: SCOPE          ;SCOPE THIS TEST
2517 017046 113737 021443 001250          ENDEX2: MOVB     BASE+3,TEMP2 ;SAVE ALL ODD ADDRESSES
2518 017054 113737 021445 001252          MOVB    BASE+5,TEMP3      ;FOR TYPEOUT
2519 017062 113737 021447 001254          MOVB    BASE+7,TEMP4
2520 017070 113737 021451 001256          MOVB    BASE+11,TEMP5
2521 017076 104017          HLT     17              ;NON ZERO ERROR COUNT
2522 017100 000761          BR      ENDEX1          ;GET OUT
2523 017102 005337 001416          SCAN1: DEC     TEMP        ;DECREMENT DELAY COUNTER
2524 017106 001402          BEQ     1$              ;BR IF ZERO
2525 017110 000137 016604          JMP     SCAN            ;BR IF NOT DONE DELAY
2526 017114 005337 001250          1$:     DEC     TEMP2      ;DEC DELAY COUNT
2527 017120 001402          BEQ     2$              ;BR IF DONE DELAY
2528 017122 000137 016604          JMP     SCAN            ;BR IF NOT DONE
2529 017126 104001          2$:     HLT     1          ;ERROR HUNG
2530 017130 000745          BR      ENDEX1          ;GET OUT
2531
2532
2533          ;INPUT INTERRUPT SERVICE ROUTINE
2534 017132 022700 000017          IISR:   CMP      #17,R0    ;PROC. ERROR DONE?
2535 017136 001421          BEQ     12$             ;BR IF YES
2536 017140 005737 020060          TST     RESUME          ;IS THIS A RESUME INTERRUPT
2537 017144 001432          BEQ     8$              ;BR IF NO
2538 017146 032711 000002          BIT     #BIT1,(R1)      ;CNTL OR BASE?
2539 017152 001407          BEQ     13$             ;BR IF CNTL I
2540 017154 012761 021440 000004          MOV     #BASE,4(R1)     ;LOAD BASE ADDRESS
2541 017162 012761 010000 000006          MOV     #BIT12,6(R1)    ;WITH RESUME BIT SET
2542 017170 000404          BR      12$             ;CONTINUE
2543 017172 005061 000006          13$:   CLR     6(R1)       ;SELECT FULL DUPLEX
2544 017176 005037 020060          CLR     RESUME          ;CLEAR RESUME FLAG
2545 017202 142711 000040          12$:   BICB    #40,(R1)    ;CLEAR RQI
2546 017206 105711          TSTB   (R1)             ;IS RDI GONE?
2547 017210 100776          BMI     -2              ;BR IF NO
2548 017212 005737 020060          TST     RESUME          ;BASE OR CNTL I?
2549 017216 001403          BEQ     14$             ;BR IF IT WAS CNTL I
2550 017220 152711 000041          BISB   #41,(R1)        ;ASK FOR CNTL I
2551 017224 000002          RTI
2552 017226 105011          14$:   CLRB   (R1)       ;CLEAR BSEL 0
2553 017230 000002          RTI
2554 017232 005700          8$:     TST     R0         ;FIRST TIME HERE?
2555 017234 100006          BPL     7$              ;LOAD BASE IF MINUS
2556 017236 012761 021440 000004          MOV     #BASE,4(R1)     ;SET UP BASE ADDRESS
2557 017244 005061 000006          CLR     6(R1)          ;CLEAR COUNT
2558 017250 000434          BR      3$              ;CONTINUE
2559 017252 001003          7$:     BNE     1$         ;CNTL I FULL DUPLEX IF 0
    
```

2560	017254	005061	000006		CLR	6(R1)	:SELECT FULL DUPLEX	
2561	017260	000430			BR	3\$:CONTINUE	
2562	017262	032700	000010	1\$:	BIT	#BIT3,RO	:XMIT?	
2563	017266	001013			BNE	2\$:BR IF YES	
2564	017270	000241			CLC		:CLEAR CARRY	
2565	017272	006100			ROL	RO	:MAKE RO EVEN	
2566	017274	016061	020062	000004	MOV	RECBA(RO),4(R1)	:LOAD REC BUFFER	
2567	017302	016061	020230	000006	MOV	RCNTAB(RO),6(R1)	:LOAD COUNT	
2568	017310	000241			CLC		:CLEAR CARRY	
2569	017312	006000			ROR	RO	:GET RO BACK	
2570	017314	000412			BR	3\$:CONTINUE	
2571	017316	000241		2\$:	CLC		:CLEAR CARRY	
2572	017320	006100			ROL	RO	:MAKE IT EVEN	
2573	017322	016061	020062	000004	MOV	XMITBA(RO),4(R1)	:LOAD XMIT BUFFER	
2574	017330	016061	020230	000006	MOV	RCNTAB(RO),6(R1)	:LOAD COUNT	
2575	017336	000241			CLC		:CLEAR CARRY	
2576	017340	006000			ROR	RO	:PUT IT BACK	
2577	017342	142711	000040	3\$:	BICB	#40,(R1)	:CLEAR RQI	
2578	017346	105711			TSTB	(R1)	:WAIT FOR	
2579	017350	100776			BMI	-2	:RDI TO GO AWAY	
2580	017352	005200			INC	RO	:INC COUNT	
2581	017354	001003			BNE	6\$:IF 0 ASK FOR CNTL I	
2582	017356	152711	000041		BISB	#41,(R1)	:ASK FOR CNTL I	
2583	017362	000002			RTI		:RETURN	
2584	017364	022700	000017	6\$:	CMP	#17,RO	:DONE YET?	
2585	017370	001411			BEQ	4\$:BR IF YES	
2586	017372	032700	000010		BIT	#BIT3,RO	:XMIT?	
2587	017376	001003			BNE	5\$:BR IF YES	
2588	017400	152711	000044		BISB	#44,(R1)	:ASK FOR REC BA/CC	
2589	017404	000002			RTI		:RETURN	
2590	017406	152711	000040	5\$:	BISB	#40,(R1)	:ASK FOR XMIT BA/CC	
2591	017412	000002			RTI		:RETURN	
2592	017414	152711	000046	4\$:	BISB	#46,(R1)	:FORCE PROC. ERROR	
2593	017420	000002			RTI		:RETURN	
2594								
2595							:OUTPUT INTERRUPT SERVICE ROUTINE	
2596								
2597	017422	032761	000001	000002	OISR:	BIT	#BIT0,2(R1)	:IS THIS AN ERROR?
2598	017430	001467			BEQ	1\$:BR IF NO	
2599	017432	005737	021314		TST	FLAG	:IS THIS SHUT DOWN INTERRUPT?	
2600	017436	001006			BNE	9\$:BR IF NO	
2601	017440	005237	021314		INC	FLAG	:YES MAKE FLAG NON-ZERO	
2602	017444	022761	001000	000006	CMP	#BIT9,6(R1)	:SHUT DOWN BIT SET?	
2603	017452	001531			BEQ	10\$:YES ALL IS OK	
2604	017454	022700	000017	9\$:	CMP	#17,RO	:RESUME INTERRUPT?	
2605	017460	001041			BNE	11\$:BR IF NO	
2606	017462	022761	001000	000006	CMP	#BIT9,6(R1)	:PROC. ERROR BIT SET?	
2607	017470	001035			BNE	11\$:BR IF NO	
2608	017472	005200			INC	RO	:BUMP COUNTER (TO 20)	
2609	017474	012711	040000		MOV	#BIT14,(R1)	:MASTER CLEAR DEVICE	
2610	017500	032737	100000	001366	BIT	#BIT15,STAT1	:DMC OR KMC?	
2611	017506	001405			BEQ	+14	:BR IF DMC	
2612	017510	012711	100000		MOV	#BIT15,(R1)	:SET RUN ON KMC	
2613	017514	105227	000000		INCB	#0	:DELAY ON KMC	
2614	017520	001375			BNE	-4		
2615	017522	012737	177777	020060	MOV	#-1,RESUME	:SET RESUME FLAG	

2672 020050 142761 000207 000002
 2673 020056 000002
 2674
 2675
 2676
 2677
 2678 020060 000000
 2679 020062
 2680 020062 000017
 2681
 2682 020120
 2683 020120 000 001 002
 2684 020123 003 004 005
 2685 020126 006 007
 2686 020130 010 011 012
 2687 020133 013 014 015
 2688 020136 016 017
 2689 020140 020 021 022
 2690 020143 023 024 025
 2691 020146 026 027
 2692 020150 030 031 032
 2693 020153 033 034 035
 2694 020156 036 037
 2695 020160 040 041 042
 2696 020163 043 044 045
 2697 020166 046 047
 2698 020170 050 051 052
 2699 020173 053 054 055
 2700 020176 056 057
 2701 020200 060 061 062
 2702 020203 063 064 065
 2703 020206 066 067
 2704 020210 070 071 072
 2705 020213 073 074 075
 2706 020215 076 077
 2707 020220 100 101 102
 2708 020223 103 104 105
 2709 020226 106 107
 2710
 2711 020230 000010
 2712 020250 000007
 2713
 2714 020266 000016
 2715 020322 000016
 2716
 2717 020356
 2718 020356 000104
 2719 020462 000104
 2720 020566 000104
 2721 020672 000104
 2722 020776 000104
 2723 021102 000104
 2724 021206 000104
 2725 021312 000000
 2726
 2727

BICB #207,2(R1) ;CLEAR RDO
 RTI ;RETURN

 ;BUFFERS
 RESUME: 0
 RECBA:
 XMITBA: .BLKW 17 ;REC & XMIT BA LIST
 TBUFF: ;TRANSMIT DATA
 .BYTE 0,1,2,3,4,5,6,7

 .BYTE 10,11,12,13,14,15,16,17

 .BYTE 20,21,22,23,24,25,26,27

 .BYTE 30,31,32,33,34,35,36,37

 .BYTE 40,41,42,43,44,45,46,47

 .BYTE 50,51,52,53,54,55,56,57

 .BYTE 60,61,62,63,64,65,66,67

 .BYTE 70,71,72,73,74,75,76,77

 .BYTE 100,101,102,103,104,105,106,107

 RCNTAB: .BLKW 10 ;RECEIVE COUNT TABLE
 XCNTAB: .BLKW 7 ;TRANSMIT COUNT TABLE

 RDNTAB: .BLKW 16 ;RECEIVE DONE TABLE (BA/CC)
 XDNTAB: .BLKW 16 ;XMIT DONE TABLE (BA/CC)
 RBUFF: ;RECEIVER BUFFERS
 RBUFF1: .BLKB 104
 RBUFF2: .BLKB 104
 RBUFF3: .BLKB 104
 RBUFF4: .BLKB 104
 RBUFF5: .BLKB 104
 RBUFF6: .BLKB 104
 RBUFF7: .BLKB 104
 RBUFF8: 0 ;END OF RECEIVER BUFFERS

81800
81900


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2728      82000
2729      82100      ;BUFFER AREA
2730      82200      ;-----
2731      82300
2732      021314 000000      82400      FLAG: 0
2733      021316 000000      82500      TFLAG: 0
2734      021320 000000      82600      RFLAG: 0
2735      021322 000044      82700      TCOUNT: 44
2736      021324 041101 042103 043105      82800      TBUF: .ASCII/ABCDEFGHIJKLMNOPQRSTUVWXYZ0123456789/
2737      021332 044107 045111 046113
2738      021340 047115 050117 051121
2739      021346 052123 053125 054127
2740      021354 055131 030460 031462
2741      021362 032464 033466 034470
2742      82900      .EVEN
2743      021370 000044      83000      RCOUNT: 44
2744      021372 021440      83100      RBUF: .+.46
2745      83200      .EVEN
2746      021440 022040      83300      BASE: .+.256.
2747      00300
2748      00400
2749      00500      ;SUBROUTINES
2750      00600      ;-----
2751      00700
2752      00800
2753      022040      00900      BASELD:
2754      01000      ;THIS SUBROUTINE LOADS THE DMC WITH A BASE ADDRESS
2755      01100      ;AND PUTS DMC INTO FULL-DUPLEX MODE
2756      01200
2757      022040 012711 040000      01300      MOV      #BIT14,(R1)      ;MASTER CLEAR
2758      022044 032737 100000 001366      01400      BIT      #BIT15,STAT1      ;CRAM?
2759      022052 001402      01500      BEQ      .+6      ;BR IF NO
2760      022054 012711 100000      01600      MOV      #BIT15,(R1)      ;IF CRAM SET RUN
2761      022060 105227 000000      01700      INCB     #0      ;DELAY
2762      022064 001375      01800      BNE     .-4      ;BR IF NOT DONE DELAY
2763      022066 005711      01900      1$:    TST     (R1)      ;IS RUN SET?
2764      022070 100376      02000      BPL     1$      ;BR IF NO
2765      022072 052711 004000      02100      BIS     #BIT11,(R1)      ;SET LU LOOP
2766      022076 152711 000043      02200      BISR    #43,(R1)      ;BASE REQUEST
2767      022102 105711      02300      2$:    TSTB   (R1)      ;RDY I SET?
2768      022104 100376      02400      BPL     2$      ;BR IF NO
2769      022106 012761 021440 000004      02500      MOV     #BASE,4(R1)      ;LOAD BASE ADDRESS
2770      022114 005061 000006      02600      CLR     6(R1)      ;CLEAR CC
2771      022120 142711 000040      02700      BICB   #40,(R1)      ;CLEAR RQI
2772      022124 105711      02800      3$:    TSTB   (R1)      ;RDY I CLEAR?
2773      022126 100776      02900      BMI     3$      ;BR IF NO
2774      022130 152711 000041      BMI     #41,(R1)      ;ASK FOR CNTL I
2775      022134 105711      64$:    TSTB   (R1)      ;WAIT FOR RDI
2776      022136 100376      BPL     64$      ;BR IF NOT SETY
2777      022140 005061 000006      CLR     6(R1)      ;SET FULL DUPLEX
2778      022144 142711 000040      BICB   #40,(R1)      ;CLEAR RQI
2779      022150 105711      65$:    TSTB   (R1)      ;RDI UP?
2780      022152 100776      BMI     65$      ;BR IF YES
2781      022154 000207      03100      RTS     PC      ;RETURN
2782      03200
2783      022156      03300      BASELH:

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2784      03400
2785      03500
2786      03600
2787 022156 012711 040000      03700
2788 022162 032737 100000 001366 03800
2789 022170 001402      03900
2790 022172 012711 100000      04000
2791 022176 105227 000000      04100
2792 022202 001375      04200
2793 022204 005711      04300 1$:
2794 022206 100376      04400
2795 022210 052711 004000      04500
2796 022214 152711 000043      04600
2797 022220 105711      04700 2$:
2798 022222 100376      04800
2799 022224 012761 021440 000004 04900
2800 022232 005061 000006      05000
2801 022236 142711 000040      05100
2802 022242 105711      05200 3$:
2803 022244 100776      05300
2804 022246 152711 000041      05400
2805 022252 105711      05500 64$:
2806 022254 100376      05600
2807 022256 012761 002000 000006 05700
2808 022264 142711 000040      05800
2809 022270 105711      05900
2810 022272 100776      06000
2811 022274 000207      06100
2812      05600
2813 022276      05700
2814      05800
2815      05900
2816      06000
2817 022276 012711 040000      06100
2818 022302 032737 100000 001366 06200
2819 022310 001402      06300
2820 022312 012711 100000      06400
2821 022316 105227 000000      06500
2822 022322 001375      06600
2823 022324 005711      06700 1$:
2824 022326 100376      06800
2825 022330 052711 004000      06900
2826 022334 152711 000043      07000
2827 022340 105711      07100 2$:
2828 022342 100376      07200
2829 022344 012761 021440 000004 07300
2830 022352 012761 010000 000006 07400
2831 022360 142711 000040      07500
2832 022364 105711      07600 3$:
2833 022366 100776      07700
2834 022370 152711 000041      07800
2835 022374 105711      07900 64$:
2836 022376 100376      08000
2837 022400 005061 000006      08100
2838 022404 142711 000040      08200
2839 022410 105711      08300
    
```

```

;THIS SUBROUTINE LOADS THE DMC WITH A BASE ADDRESS
;AND PUTS DMC INTO HALF-DUPLEX MODE
    
```

```

MOV     #BIT14,(R1)      ;MASTER CLEAR
BIT     #BIT15,STAT1    ;CRAM?
BEQ     .+6              ;BR IF NO
MOV     #BIT15,(R1)     ;IF CRAM SET RUN
INCB   #0                ;DELAY
BNE     .-4              ;BR IF NOT DONE DELAY
1$:    TST     (R1)      ;IS RUN SET?
BPL     1$               ;BR IF NO
BIS     #BIT11,(R1)     ;SET LU LOOP
BISB   #43,(R1)        ;BASE REQUEST
2$:    TSTB   (R1)      ;RDY I SET?
BPL     2$               ;BR IF NO
MOV     #BASE,4(R1)    ;LOAD BASE ADDRESS
CLR     6(R1)           ;CLEAR CC
BICB   #40,(R1)        ;CLEAR RQI
3$:    TSTB   (R1)      ;RDY I CLEAR?
BMI     3$               ;BR IF NO
BISB   #41,(R1)        ;ASK FOR CNTL I
64$:   TSTB   (R1)      ;WAIT FOR RDI
BPL     64$             ;BR IF NOT SETY
MOV     #BIT10,6(R1)   ;SET HALF DUPLEX
BICB   #40,(R1)        ;CLEAR RQI
65$:   TSTB   (R1)      ;RDI UP?
BMI     65$             ;BR IF YES
RTS     PC              ;RETURN
    
```

RESUM:

```

;THIS SUBROUTINE LOADS THE DMC WITH A BASE ADDRESS
;WITH RESUME BIT SET AND PUTS DMC INTO FULL-DUPLEX MODE
    
```

```

MOV     #BIT14,(R1)      ;MASTER CLEAR
BIT     #BIT15,STAT1    ;CRAM?
BEQ     .+6              ;BR IF NO
MOV     #BIT15,(R1)     ;IF CRAM SET RUN
INCB   #0                ;DELAY
BNE     .-4              ;BR IF NOT DONE DELAY
1$:    TST     (R1)      ;IS RUN SET?
BPL     1$               ;BR IF NO
BIS     #BIT11,(R1)     ;SET LU LOOP
BISB   #43,(R1)        ;BASE REQUEST
2$:    TSTB   (R1)      ;RDY I SET?
BPL     2$               ;BR IF NO
MOV     #BASE,4(R1)    ;LOAD BASE ADDRESS
MOV     #BIT12,6(R1)   ;SET RESUME BIT
BICB   #40,(R1)        ;CLEAR RQI
3$:    TSTB   (R1)      ;RDY I CLEAR?
BMI     3$               ;BR IF NO
BISB   #41,(R1)        ;ASK FOR CNTL I
64$:   TSTB   (R1)      ;WAIT FOR RDI
BPL     64$             ;BR IF NOT SETY
CLR     6(R1)           ;SET FULL DUPLEX
BICB   #40,(R1)        ;CLEAR RQI
65$:   TSTB   (R1)      ;RDI UP?
    
```


2840	022412	100776				BMI	65\$;BR IF YES
2841	022414	000207			07900	RTS	PC		;RETURN
2842					08000				
2843	022416				08100	RFRELD:			
2844					08200				; THIS SUBROUTINE LOADS THE DMC WITH A RECEIVE BA/CC
2845					08300				
2846	022416	152711	000044		08400	1\$:	BISB #44,(R1)		;REC BA/CC REQUEST
2847	022422	105711			08500		TSTB (R1)		;RDY I SET?
2848	022424	100376			08600		BPL 1\$;BR IF NO
2849	022426	012561	000004		08700		MOV (R5)+,4(R1)		;LOAD REC BA
2850	022432	012561	000006		08800		MOV (R5)+,6(R1)		;LOAD REC CC
2851	022436	142711	000040		08900	2\$:	BICB #40,(R1)		;CLEAR RQI
2852	022442	105711			09000		TSTB (R1)		;IS RDY I CLEAR
2853	022444	100776			09100		BMI 2\$;BR IF NO
2854	022446	000205			09200		RTS R5		;RETURN
2855					09300				
2856	022450				09400	XFRELD:			
2857					09500				; THIS SUBROUTINE LOADS THE DMC WITH A TRANSMIT BA/CC
2858					09600				
2859	022450	152711	000040		09700	1\$:	BISB #40,(R1)		;XMIT BA/CC REQUEST
2860	022454	105711			09800		TSTB (R1)		;RDY I SET?
2861	022456	100376			09900		BPL 1\$;BR IF NO
2862	022460	012561	000004		10000		MOV (R5)+,4(R1)		;LOAD XMIT BA
2863	022464	012561	000006		10100		MOV (R5)+,6(R1)		;LOAD XMIT CC
2864	022470	142711	000040		10200	2\$:	BICB #40,(R1)		;CLEAR RQI
2865	022474	105711			10300		TSTB (R1)		;IS RDY I CLEAR
2866	022476	100776			10400		BMI 2\$;BR IF NO
2867	022500	000205			10500		RTS R5		;RETURN
2868					10600				
2869					10700				
2870	022502				10800	SHUTDOWN:			
2871					10900				; THIS SUBROUTINE FORCES THE DMC TO UPDATE THE BASE TABLE
2872					11000				
2873	022502	042761	000207	000002	11100	1\$:	BIC #207,2(R1)		;CLEAR ANY OUTPUT DONES
2874	022510	152711	000046		11200		BISB #46,(R1)		;ASK FOR ILLEGAL REQUEST
2875	022514	105711			11300		TSTB (R1)		;RDI SET?
2876	022516	100376			11400		BPL 1\$;BR IF NO
2877	022520	142711	000040		11500	2\$:	BICB #40,(R1)		;CLEAR RQI
2878	022524	105761	000002		11600		TSTB 2(R1)		;OUTPUT DONE SET?
2879	022530	100375			11700		BPL 2\$;BR IF NOT
2880	022532	000207			11800		RTS PC		;RETURN
2881					11900				
2882					00300				
	022534	052377	040522	051516	00400	EM2:	.ASCIZ <377>/TRANSMIT BA ERROR/		
	022557	377	051124	047101	00500	EM3:	.ASCIZ <377>/TRANSMIT COUNT ERROR/		
	022605	377	042522	042503	00600	EM4:	.ASCIZ <377>/RECEIVE BA ERROR/		
	022627	377	042522	042503	00700	EM5:	.ASCIZ <377>/RECEIVE COUNT ERROR/		
	022654	051377	041505	044505	00800	EM11:	.ASCIZ <377>/RECEIVE DATA ERROR/		
	022700	043377	042522	020105	00900	EM12:	.ASCIZ <377>/FREE RUNNING ERROR/		
	022724	041777	047117	051124	01000	EM13:	.ASCIZ <377>/CONTROL OUT ERROR/		
	022747	377	047111	042524	01100	EM14:	.ASCIZ <377>/INTERNAL DDCMP ERROR COUNTS NON ZERO/		
					01200				
	023015	377	054105	042520	01300	DH1:	.ASCIZ <377>/EXPECTED FOUND ADDRESS/		
	023047	377	054105	042520	01400	DH2:	.ASCIZ <377>/EXPECTED FOUND/		
	023070	020377	042523	032114	01500	DH3:	.ASCIZ <377>/SEL4 SEL6/		
	023111	377	040502	042523	01600	DH4:	.ASCIZ <377>/BASE+3 THRU BASE+12 /		

Address	Offset	Label	Value	Register	Operation	Comment
023137	377	046504	030503	01700	DHS:	.ASCIZ <377>/DMC11 IS HUNG/
				01800	.EVEN	
				01900		
023156	000003			02000	DT1:	3
023160	006	004		02100	.BYTE	6,4
023162	001264			02200	SAVR2	
023164	006	004		02300	.BYTE	6,4
023166	001270			02400	SAVR4	
023170	004	002		02500	.BYTE	4,2
023172	001260			02600	SAVR0	
023174	000003			02700	DT2:	3
023176	006	004		02800	.BYTE	6,4
023200	001272			02900	SAVR5	
023202	006	004		03000	.BYTE	6,4
023204	001270			03100	SAVR4	
023206	004	002		03200	.BYTE	4,2
023210	001264			03300	SAVR2	
023212	000003			03400	DT3:	3
023214	006	004		03500	.BYTE	6,4
023216	001272			03600	SAVR5	
023220	006	004		03700	.BYTE	6,4
023222	001270			03800	SAVR4	
023224	004	002		03900	.BYTE	4,2
023226	001252			04000	TEMP3	
023230	000002			04100	DT4:	2
023232	003	007		04200	.BYTE	3,7
023234	001272			04300	SAVR5	
023236	003	002		04400	.BYTE	3,2
023240	001270			04500	SAVR4	
023242	000002			04600	DT5:	2
023244	006	004		04700	.BYTE	6,4
023246	001272			04800	SAVR5	
023250	006	002		04900	.BYTE	6,2
023252	001270			05000	SAVR4	
023254	000003			05100	DT6:	3
023256	003	010		05200	.BYTE	3,10
023260	001272			05300	SAVR5	
023262	003	004		05400	.BYTE	3,4
023264	001270			05500	SAVR4	
023266	004	002		05600	.BYTE	4,2
023270	021314			05700	FLAG	
023272	000003			05800	DT7:	3
023274	003	010		05900	.BYTE	3,10
023276	001272			06000	SAVR5	
023300	003	004		06100	.BYTE	3,4
023302	001270			06200	SAVR4	
023304	004	002		06300	.BYTE	4,2
023306	001264			06400	SAVR2	
023310	000003			06500	DT10:	3
023312	003	007		06600	.BYTE	3,7
023314	001272			06700	SAVR5	
023316	003	004		06800	.BYTE	3,4
023320	001270			06900	SAVR4	
023322	006	002		07000	.BYTE	6,2
023324	001252			07100	TEMP3	
023326	000002			07200	DT11:	2

023330	006	004	07300	.BYTE	6,4
023332	001252		07400	TEMP3	
023334	006	002	07500	.BYTE	6,2
023336	001254		07600	TEMP4	
023340	000010		07700	DT12:	10
023342	003	002	07800	.BYTE	3,2
023344	001250		07900	TEMP2	
023346	003	002	08000	.BYTE	3,2
023350	021444		08100	BASE+4	
023352	003	002	08200	.BYTE	3,2
023354	001252		08300	TEMP3	
023356	003	002	08400	.BYTE	3,2
023360	021446		08500	BASE+6	
023362	003	002	08600	.BYTE	3,2
023364	001254		08700	TEMP4	
023366	003	002	08800	.BYTE	3,2
023370	021450		08900	BASE+10	
023372	003	002	09000	.BYTE	3,2
023374	001256		09100	TEMP5	
023376	003	002	09200	.BYTE	3,2
023400	021452		09300	BASE+12	
023402	000002		09400	DT13:	2
023404	006	004	09500	.BYTE	6,4
023406	001272		09600	SAVR5	
023410	006	002	09700	.BYTE	6,2
023412	001270		09800	SAVR4	
			09900		
023414			10000	.ERRTAB:	
023414	000000		10100	0	
023416	000000		10200	0	
023420	000000		10300	0	
023422	022700		10400	EM12	
023424	023137		10500	DH5 ;HLT	1
023426	000000		10600	0	
023430	022534		10700	EM2	
023432	023047		10800	DH2 ;HLT	2
023434	023402		10900	DT13	
023436	022557		11000	EM3	
023440	000000		11100	0 ;HLT	3
023442	000000		11200	0	
023444	022605		11300	EM4	
023446	000000		11400	0 ;HLT	4
023450	000000		11500	0	
023452	022627		11600	EM5	
023454	000000		11700	0	
023456	000000		11800	0	
023460	022605		11900	EM4	
023462	023047		12000	DH2 ;HLT	6
023464	023242		12100	DT5	
023466	022627		12200	EM5	
023470	023047		12300	DH2 ;HLT	7
023472	023230		12400	DT4	
023474	000000		12500	0	
023476	023015		12600	DH1 ;HLT	10
023500	023254		12700	DT6	
023502	000000		12800	0	

023504	023015	12900	DH1	;HLT	11
023506	023272	13000	DT7		
023510	000000	13100	0		
023512	023047	13200	DH2	;HLT	12
023514	023230	13300	DT4		
023516	022654	13400	EM11		
023520	023015	13500	DH1	;HLT	13
023522	023310	13600	DT10		
023524	022700	13700	EM12		
023526	000000	13800	0	;HLT	14
023530	000000	13900	0		
023532	022700	14000	EM12		
023534	023047	14100	DH2	;HLT	15
023536	023242	14200	DT5		
023540	022724	14300	EM13		
023542	023070	14400	DH3	;HLT	16
023544	023326	14500	DT11		
023546	022747	14600	EM14		
023550	023111	14700	DH4	;HLT	17
023552	023340	14800	DT12		
		14900			
		15000			
023554		15100	CORMAX:		
	000001	15600	.END		

CROSS REFERENCE TABLE -- USER SYMBOLS

ADRCNT=	004373	879*	915*	924#										
AUDONE	003024	569	608	613	659#									
AUSTRY	002446	568#	663											
AUTO.S	010512	526	1368#											
BASE	021440	1720	1838	1848	1849	1850	1851	1912	1925	1926	1927	1928	1984	1994
		1995	1996	1997	2047	2057	2058	2059	2060	2113	2123	2124	2125	2126
		2173	2183	2184	2185	2186	2233	2243	2244	2245	2246	2358	2371	2372
		2373	2374	2503	2517	2518	2519	2520	2540	2556	2746#	2769	2799	2829
		2882												
BASELD	022040	1875	1956	2022	2085	2151	2211	2315	2753#					
BASELH	022156	2271	2783#											
BINWRD	004714	965*	968*	969	1006#									
BITO =	000001	95#	1155	1156	1687	1793	1808	1870	1890	1951	1973	2017	2036	2080
		2102	2146	2163	2206	2223	2266	2307	2398	2489	2597	2500	2538	
BIT1 =	000002	94#	531	1149	1155	1156	1449	1532	1545	1909	2355	2500	2538	
BIT10 =	002000	85#	1513	1524	2807									
BIT11 =	004000	84#	2453	2621	2765	2795	2825							
BIT12 =	010000	83#	1464	1543	1690	1873	1954	2020	2083	2149	2209	2269	2310	2401
		2541	2830											
BIT13 =	020000	82#	1467	1516	1547	1906								
BIT14 =	040000	81#	781	1478	1480	1547	1558	1700	2432	2451	2609	2619	2757	2787
		2817												
BIT15 =	100000	80#	485	572	575	596	599	1451	1519	1685	1701	1703	1868	1949
		2015	2078	2144	2204	2264	2305	2396	2433	2435	2610	2612	2758	2760
		2788	2790	2818	2820									
BIT2 =	000004	93#	531	712	1156	1787	1894	2629						
BIT3 =	000010	92#	1549	1556	2562	2586								
BIT4 =	000020	91#	1139	1162	1164	1977								
BIT5 =	000040	90#	1661											
BIT6 =	000100	89#	1144	1145	1551	2450	2618							
BIT7 =	000200	88#	1145	1265	1661									
BIT8 =	000400	87#	1523	1538	1540	1553	1555	1561	1564	1622	2040	2106		
BIT9 =	001000	86#	1521	1523	1535	1561	1564	1620	1622	2167	2227	2602	2606	
BM	007054	1187#	1492											
BRLVL	012252	1617	1627	1635	1647#									
BRW	003730	718	804#											
BRX	003732	719	805#											
CHRCNT	004712	963*	966	970	986*	1004#	1005							
CKSWR	007606	512	779	811	1026	1212#								
CKSWR1	007666	1225#	1237											
CKSWR2	007700	1228#												
CKSWR3	007704	1230#												
CKSWR4	007710	1231#	1239	1246										
CKSWR5	010014	1213	1220	1255#										
CLKX	001242	171#												
CLRTAB	016454	2437#	2495											
CNERR	007277	626	1187#											
CNT.MA	001702	196	364#	484	486	488	1293							
CNVRT =	104411	233#	623	738	740	742	744	1060	1062	1122	1228			
CONERR	007223	621	1187#											
CONN	007114	1187#	1469											
CONTAB	002776	630	643#											
CONVRT =	104410	231#	543	629	1076	1391								
CORMAX	023554	2882#												
CRAM	006606	1187#	1430											
CREAM	001320	195#	483*	1289*	1290	1292*	1296							

DZDMH.P11 16-MAY-77 09:54 CROSS REFERENCE TABLE -- USER SYMBOLS

EM3	022557	2882#								
EM4	022605	2882#								
EM5	022627	2882#								
ENDEX	016750	2496#								
ENDEX1	017044	2400	2509	2516#	2522	2530	2628			
ENDEX2	017046	2506	2517#							
ERCT00	001704	366#								
ERCT01	001710	369#								
ERCT02	001714	372#								
ERCT03	001720	375#								
ERCT04	001724	378#								
ERCT05	001730	381#								
ERCT06	001734	384#								
ERCT07	001740	387#								
ERCT10	001744	390#								
ERCT11	001750	393#								
ERCT12	001754	396#								
ERCT13	001760	399#								
ERCT14	001764	402#								
ERCT15	001770	405#								
ERCT16	001774	408#								
ERCT17	002000	411#								
ERR	002700	592	618#	622						
ERRCNT	001232	163#	747	774	1087*	1305*				
ERRFLG	001325	202#	481*	733*	795*	1037*	1050	1064*	1123*	
ERRMSG	005172	1047*	1065	1068#						
ERRPC	002770	624	640#							
ERTAB0	005322	1062	1097#							
EXIT =	000205	96#								
EXITER	005252	1082	1087#							
FLAG	021314	2431*	2454	2489	2494*	2497	2599	2601*	2732#	2882
FLOAT	002536	585#	591							
FY	002566	594#	605	609	615					
HALTS	005222	1033	1079#							
HILIM	004366	876*	903	921#						
ICOUNT	001222	159#	793	798*						
IISR	017132	2410	2534#							
INBUF	007502	846	882	1202#						
INCHAR	010020	1231	1259#							
INIFLG	001324	201#	507	527	534*					
INSTER=	104404	223#	897							
INSTR =	104403	221#	1329	1381	1394	1403	1482	1491		
INSTR2	004166	853	865#							
INTTY	012266	1414	1431	1441	1454	1470	1655#			
KMCM	007330	637	1187#							
LIMITS	004314	892	903#							
LINE	007016	1187#	1483							
LOBITS	004372	878*	907	923#	924					
LOCK	001220	158#	797*	814	816	1056				
LOKFLG	001326	203#								
LOLIM	004364	875*	905	920#						
LPCNT	001224	160#	792*	793	796*					
LSTERR	001234	164#	490*	732*	1034	1036*	1124*			
MASKX	001244	172#								
MASTEK	006142	1058	1187#							
MCRLF	005672	831	954	1054	1055	1063	1187#	1328	1390	

DMEND	1#	725													
DMFRNT	1#														
HLT	75#	1711	1719	1732	1734	1749	1758	1765	1774	1783	1786	1791	1795	1798	1801
	1806	1810	1813	1816	1825	1830	1852	1888	1892	1900	1929	1971	1975	1981	1998
	2034	2038	2044	2061	2100	2104	2110	2127	2161	2165	2171	2187	2221	2225	2231
	2247	2297	2333	2345	2375	2480	2521	2529	2626	2637	2644	2660	2667		
\$AUTO	1#	547													
\$BASEC	1#	1837	1908	1983	2046	2112	2172	2232	2353						
\$BUFFE	1#	1199													
\$BYTE	1#	2683	2686	2689	2692	2695	2698	2701	2704	2707					
\$CKDAT	1#	2472													
\$COMP	1#														
\$CYCLE	1#	1271													
\$DATAF	1#	1664													
\$EOP	1#	725													
\$EXER	1#	2378													
\$FD	1#	1736	2774	2834											
\$FINI	1#	2747													
\$GETPA	1#														
\$HALF	1#	2250													
\$HD	1#	2804													
\$HEADE	1#														
\$LSTDA	1#	1935													
\$MARHI	1#														
\$MOCK	1#														
\$MSG	1#	1187													
\$NONEX	1#	2001	2064												
\$ORUN	1#	1855													
\$PFAIL	1#	1103													
\$PROC	1#	2130													
\$PROC1	1#	2190													
\$QUEST	1#	1381	1394	1403	1482	1491									
\$RAMCL	1#	1131													
\$RCLK	1#	1134	1137	1174	1179										
\$RESUM	1#	2289													
\$SCOPE	1#	775													
\$SETUP	1#	1868	1949	2015	2078										
\$SIMBC	1#														
\$SKIPT	1#	1685	1868	1949	2015	2078	2144	2204	2264	2305	2396				
\$SOFTC	1#	1207													
\$TRPDE	1#	215	217	219	221	223	225	227	229	231	233	235	237	239	241
	243														
\$TSTN	1#	1680	1863	1943	2009	2072	2138	2198	2258	2299	2390				
\$VARIA	1#	134													
\$XZ	1#	1664	1678	1855	1861	1935	1941	2001	2007	2064	2070	2130	2136	2190	2196
	2250	2256	2289	2297	2378	2388									

. ABS. 023554 000

ERRORS DETECTED: 0

DZDMH, DZDMH/SOL/CRF+IPLUTL, DZDMH
RUN-TIME: 8 12 1 SECONDS
RUN-TIME RATIO: 199/21=9.1

H07

DZDMH MACY11 30(1046) 11-JUL-77 12:32 PAGE 72
DZDMH.P11 16-MAY-77 09:54 CROSS REFERENCE TABLE -- MACRO NAMES

PAGE: 0085

CORE USED: 24K (47 PAGES)