

RK11/05F/J

RK11 BASIC LOGIC TEST 2
MD-11-DZRKK-E

EP-DZRKK-E-DL-A
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The main body of the document consists of a 10x10 grid of small, square diagrams. Each diagram appears to be a test pattern or a logic diagram, but the text within them is too small and faded to be legible. The diagrams are arranged in a regular grid across the page.

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IDENTIFICATION

PRODUCT CODE:	MAINDEC-11-DZRKK-E-D
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QUICK LOOK-UP OPERATING INSTRUCTIONS
FOR A QUICK REFERENCE, LOOK UP THE FOLLOWING SECTIONS:
1.0 ABSTRACT
2.0 REQUIREMENTS
4.1 LOADING AND OPERATOR ACTION
7.0 SWITCH OPTIONS
FOR A MORE COMPLETE EXPLANATION REFER TO THE TABLE OF
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1.0 ABSTRACT

THE RK11 LOGIC TESTS CONSIST OF A SERIES OF TESTS AIMED AT CHECKING THE BASIC LOGIC OF THE RK11 CONTROLLER. THIS PROGRAM IS THE SECOND PART OF THE TWO-PART RK11 LOGIC TESTS. IT SHOULD BE NOTED THAT LOGIC TEST I AND LOGIC TEST II TOGETHER CONSTITUTE A COMPLETE PROGRAM AND BOTH OF THEM SHOULD BE RUN.

WHEN USED IN CONJUNCTION WITH A DRIVE IT IS CAPABLE OF DETECTING FAULTS IN THE DRIVE ALSO.

USED CORRECTLY THIS PROGRAM CAN BE AN EFFECTIVE ANALYTIC AND DIAGNOSTIC TOOL.

2.0 REQUIREMENTS

2.1 EQUIPMENT

- A. PDP11 WITH CONSOLE TELETYPE.
- B. 8K OF MEMORY
- C. RK11 OR RKV11 CONTROLLER
- D. 1-8 RK05 OR RK05F DRIVES OR THE RK05 SIMULATOR (DRIVE TYPES MAY BE MIXED)

2.2 PRELIMINARY PROGRAMS

RK11 BASIC LOGIC TEST I (MD-11-DZRKJ)

2.3 EXECUTION TIME

ERROR FREE FIRST PASS ON PDP11/20 WITH CORE MEMORY TAKES APPROXIMATELY TWO MINUTES. CONSIDERABLY LESS FOR FASTER MACHINES OR MEMORIES.

3.0 STARTING ADDRESS

200 FOR ANY MODE OF OPERATION. NORMAL START UP WITH ALL SWITCHES DOWN.

4.0 PROGRAM CONTROL MODES & OPERATOR ACTION

PAPER TAPE LOADING
RKDP DUMP MODE
RKDP CHAIN MODE
ACT11

- 4.1 PAPER TAPE LOADING
- 4.1.1 LOAD PROGRAM INTO MEMORY USING STANDARD PROCEDURE FOR .ABS TAPES.
- 4.1.2 MAKE SURE THAT THE DRIVES TO BE CHECKED ARE LOADED WITH DISKS AND ARE IN 'RUN'. 'WRT ENABLE' THEM. CHECK THAT 'WRT PROT' LIGHT ON THESE DRIVES IS OFF. PUT DRIVES THAT ARE NOT TO BE TESTED ON 'LOAD'.
- 4.1.3 LOAD ADDRESS 200
- 4.1.4 SET SWITCHES IF DESIRED (SEE SEC 7.0) IF TESTING ON SIMULATOR PUT SW<10> UP.
- PRESS START.
- 4.1.5 THE PROGRAM IDENTIFIES ITSELF (NAME, MAINDEC NO). THEN THE FOLLOWING QUESTION IS ASKED:

DRIVES TO BE TESTED?

THE USER SHOULD TYPE IN THE DRIVE NUMBERS THAT ARE IN 'RUN' AND TO BE TESTED. CARRIAGE RETURN SHOULD TERMINATE THE STRING. IF AN RK-05F IS TO BE TESTED, TYPE THE SUFFIX 'F' WITH THE FIRST DRIVE OF THE PAIR. FOR EXAMPLE, IF DRIVES 2 AND 3 ARE ON AN RK-05F, TYPE ONLY 2F.

EXMP: DRIVES TO BE TESTED? 0,1,2<CR>

THE DRIVES DO NOT HAVE TO BE IN LOGICAL ORDER.

EXMP: DRIVES TO BE TESTED? 2,4<CR>

IF ANY ONE DRIVE IS TO BE TESTED, TYPE IN THAT NUMBER. IT DOES NOT HAVE TO BE DRIVE 0.

THUS A NORMAL SEQUENCE WITH DRIVES 0,1 WOULD BE:

RK11 BASIC LOGIC TEST 2
MAINDEC-11-DZRKK-E
DRIVES TO BE TESTED? 0,1<CR>

- 4.1.6 THERE IS A "RUBOUT" FEATURE WHICH ALLOWS RUBBING OUT ANY NUMBER OF CHARACTERS THAT WERE TYPED IN WRONG. THE RUBBED OUT CHARACTERS ARE ECHOED BACK WITHIN SLASHES.

"↑J" DELETES THE ENTIRE LINE

- 4.1.7 IF REPLY TO ANY OF THE ABOVE QUESTION IS IN A WRONG FORMAT (EX: 012<CR>:0.8<CR>: 0.A<CR>: M<CR> ETC) IT IS AUTOMATICALLY REJECTED, A "???" IS PRINTED OUT;

THE CORRECT ANSWER CAN NOW BE RETYPED AGAIN.

- 4.1.8 THE DRIVE NUMBER BEING TESTED OUT IS PRINTED:

DRIVE N :N=0,1...7
IF THE DRIVE IS AN RK-05F, AN F IS APPENDED

AT THE END OF A PASS THE FOLLOWING TYPE-OUT OCCURS

END PASS # X

WHERE X= PASS NUMBER (1,2,3---), CONTROL IS PASSED TO THE BEGINNING OF THE PROGRAM AND RE-EXECUTION BEGINS. NO QUESTIONS ARE TO BE ANSWERED AGAIN.

- 4.1.9 ERROR FREE PASSES OF THE PROGRAM APPEAR AS SHOWN BELOW.

```

RK11 BASIC LOGIC TEST 2
MAINDEC-11-DZRKK-E
DRIVES TO BE TESTED*
0,1<CR>
DRIVE 0
DRIVE 1
END PASS # 1
0
DRIVE 1
END PASS # 2
...
...

```

- 4.2 RKDP DUMP MODE

- 4.2.1 THE PROGRAM IS LOADED INTO THE MEMORY BY THE RKDP MONITOR

- 4.2.2 START AS NORMALLY USING SA 200

- 4.2.3 THE PROGRAM IDENTIFIES ITSELF (NAME,MAINDEC NO.). ON FINDING OUT THAT THE LOADING WAS BY RKDP (DUMP MODE), THE FOLLOWING MESSAGE APPEARS:

'TO TEST DRIVE 'N' HALT PROGRAM, REMOVE RKDP PACK AND REPLACE IT WITH A WORK PACK, CLEAR LOCATION 40, AND RESTART PROGRAM'

IF DRIVE 'N' IS TO BE TESTED, THE RKDP PACK ON THAT

DRIVE SHOULD BE REPLACED BY ANOTHER PACK, THE DRIVE SHOULD BE PUT ON 'WRT ENABL' (BECAUSE RKDP WRITE PROTECTS THE DRIVE).

IF DRIVE 'N' IS NOT TO BE CHECKED, THEN THE MESSAGE SHOULD BE IGNORED.

AFTER THIS, THE SEQUENCE OF QUESTIONING IS AS EXPLAINED IN SEC 4.1.5.

4.3 RKDP CHAIN MODE

THE PROGRAM IS CHAIN-LOADED FROM THE RKDP PACK ON DRIVE 'N'. AFTER THE PROGRAM IDENTIFIES ITSELF THE FOLLOWING PRINTOUT OCCURS.

'DRIVE 'N' NOT TESTED'

THERE IS NO OPERATOR INTERVENTION REQUIRED. THE PROGRAM FINDS OUT THE NUMBER OF DRIVES PRESENT.

4.4 ACT11 MODE

THE PROGRAM IS LOADED BY THE ACT11 MONITOR. ON STARTING, IDENTIFIES ITSELF, ASCERTAINS THE NUMBER OF DRIVES AND PROCEEDS WITH THE EXECUTION OF THE TESTS AS BEFORE.

5.0 DRIVE SELECTION

IF ANY PARTICULAR DRIVE IS TO BE SELECTED FOR TESTING, PUT THAT DRIVE ON 'RUN', 'WRITE ENABLE'; PUT REST OF THE DRIVES ON 'LOAD', 'WRITE LOCK' AND IN REPLY TO THE QUESTIONS 'TO BE TESTED?' TYPE IN THE DRIVE NUMBER FOLLOWED BY CR. SEE SEC 4.1.5.

6.0 DRIVE-LESS TEST

USE RK11 BASIC LOGIC TEST I, WHICH IS ACTUALLY THE FIRST PART OF THE TWO-PART RK11 BASIC LOGIC TESTS. SEE SEC 1.0, 2.2.

7.0 SWITCH OPTIONS

IF THE PROGRAM IS BEING RUN ON A SWITCHLESS PROCESSOR (I.E. AN 11/34) THE PROGRAM WILL DETERMINE THAT THE HARDWARE SWITCH REGISTER IS NOT PRESENT AND WILL USE A 'SOFTWARE' SWITCH REGISTER. THE 'SOFTWARE' SWITCH REGISTER IS LOCATED AT LOCATION 176 (8). THE SETTINGS OF THE 'SOFTWARE' SWITCHES ARE CONTROLLED THROUGH A KEYBOARD ROUTINE WHICH IS CALLED BY TYPING A 'CONTROL G'. THE PROGRAM WILL RECOGNIZE THE 'CONTROL G' WHENEVER THE PROGRAM ENTERS

THE SCOPE ROUTINE OR BEGINS A NEW TEST. THE 'SOFTWARE' SWITCH VALUES ARE ENTERED AS AN OCTAL NUMBER IN RESPONSE TO THE PROMPT FROM THE SWITCH ENTRY ROUTINE:

'SWR = NNNNNN NEW ='

EACH TIME SWITCH SETTING ARE ENTERED, THE ENTIRE SWITCH REGISTER IMAGE MUST BE ENTERED. LEADING ZEROS ARE NOT REQUIRED. 'RUBOUT' AND 'CONTROL U' FUNCTIONS MAY BE USED TO CORRECT TYPING ERRORS DURING SWITCH ENTRY.

ON PROCESSORS WITH HARDWARE SWITCH REGISTERS, THE 'SOFTWARE' SWITCH REGISTER MAY BE USED. IF THE PROGRAM FINDS ALL 16 SWITCHES IN THE 'UP' POSITION, ALL SWITCH REGISTER REFERENCES WILL BE TO THE 'SOFTWARE' REGISTER AND THE PROCEDURES DESCRIBED ABOVE MUST BE FOLLOWED.

SW<15>=1	HALT ON ERROR
SW<14>=1	LOOP ON TEST
SW<13>=1	INHIBIT ERROR PRINTOUTS
SW<12>=1	CYCLE ON ERROR TO THE PREVIOUS 'SCOPE' STATEMENT
SW<11>=1	INHIBIT ITERATIONS
SW<10>=1	TESTING ON SIMULATOR
SW<09>=1	LOOP ON SPECIFIC ERROR
SW<08>=1	LOOP ON TEST AS PER SW<07:00>
SW<06>=1	DROP THE DRIVE AFTER MAXIMUM ALLOWABLE NUMBER OF ERRORS OCCUR

7.1 SW<15>

THE PROGRAM HALTS ON ENCOUNTERING AN ERROR, AFTER TYPING OUT THE ERROR MESSAGE AND PERTINENT INFORMATION. PRESSING "CONTINUE" RESTORES NORMAL OPERATION OF THE PROGRAM.

7.2 SW<14>

THE PROGRAM LOOPS ON THE SUBTEST THAT IS BEING EXECUTED WHEN THE SWITCH IS PUT ON. THIS SWITCH IS USED NORMALLY ALONG SW 15. SEE SEC 8.0.

7.3 SW <13>

THIS SWITCH INHIBITS ALL ERROR MESSAGES. NORMALLY USED WHEN LOOPING ON TEST (SW 14) OR LOOPING ON ERROR (SW 9).

7.4 SW <12>

THIS SWITCH ALLOWS THE PROGRAM TO CYCLE FROM THE POINT OF ERROR TO THE PREVIOUS SCOPE STATEMENT. NOTE THAT IN DOING SO ANY INITIALIZATION BEING DONE AT THE BEGINNING OF THE SUBTEST WILL BE DONE AGAIN AND AGAIN. SEE SEC 8.0 FOR DIFFERENT SCOPE LOOPS

AVAILABLE.

7.5 SW <11>

EACH SUBTEST WILL BE EXECUTED ONLY ONCE. NORMALLY
AFTER THE FIRST PASS, EACH SUBTEST IS ITERATED A
NUMBER OF TIMES (USUALLY 50, 5 IN SOME CASES).
SETTING THIS SWITCH INHIBITS ITERATIONS, SO THAT
QUICK PASSES CAN BE MADE.

7.6 SW <10>

THIS SWITCH WHEN SET INDICATES THAT TESTING IS BEING
DONE ON A SIMULATOR. THE SWITCH SHOULD BE PUT UP
BEFORE STARTING THE PROGRAM. NOTE THAT RK11C IS
NOT COMPATIBLE WITH THE SIMULATOR.

7.7 SW <09>

THIS SWITCH PROVIDES THE TIGHTEST POSSIBLE SCOPE
LOOP. NOTE THAT THE SW12 THE INITIALIZATION OF
PARAMETERS AT THE BEGINNING OF THE SUBTEST MAY NOT
BE DONE IN THIS CASE. THIS SWITCH IS HELPFUL WHEN A
PARTICULAR PART OF A SUBTEST IS BEING REPEATED USING
DIFFERENT PARAMETERS AND YOU WANT TO SCOPE ON THE
PARAMETER IN ERROR. (EXAMPLE: RKDA IS BEING WRITTEN
AND READ BACK WITH COUNT PATTERNS FROM 1 TO 177777.
PATTERN 561 IS GIVING ERROR, YOU MIGHT NOT WANT TO
GO THROUGH THE 560 PATTERNS BEFORE HITTING ERROR ON
THE 561TH PATTERN. IN THIS CASE SW 9 WILL GIVE YOU
A SCOPE LOOP ON THE 561TH PATTERN ONLY

7.8 SW <08>

THIS SWITCH IS USED TO SELECT A PARTICULAR TEST (AS
PER SW<00-07>) FOR EXECUTION AND SUBSEQUENT LOOPING.
THUS IF TEST 15 IS TO BE SELECTED THE SWITCH SETTING
WOULD BE 000415. IT SHOULD BE NOTED THAT BEFORE
SELECTING TEST 15, ALL THE PREVIOUS TESTS (1-14)
WILL BE EXECUTED.

7.9 SW<06>

THIS SWITCH ALLOWS THE PROGRAM TO DROP A DRIVE FROM
THE SELECTION LIST AND TESTING AFTER MAXIMUM
ALLOWABLE ERROR COUNT (TOTAL NUMBER OF ERRORS) ON
THAT DRIVE IS EXCEEDED. THE MAXIMUM ALLOWABLE ERROR
COUNT IS 5, AFTER 5 ERRORS HAVE OCCURED DRIVE
IS DROPPED AND A MESSAGE (DRIVE # XXX DROPPED) IS
PRINTED.

8.0 SCOPE LOOPS

THERE ARE THREE KINDS OF SCOPE LOOPS AVAILABLE

1. SW14: LOOPING IS DONE FOR THE ENTIRE SUB-TEST
2. SW12: LOOPING IS DONE FROM THE POINT OF ERROR BACK TO THE PREVIOUS 'SCOPE' STATEMENT.
3. SW09: PROVIDE THE TIGHTEST POSSIBLE SCOPE LOOP SEE SEC. 7.7

EXAMPLE:

TST1: SCOPE
:

INITIALIZATION

```

:
ERROR 1
:
ERROR 2
:
ERROR 3
:
ERROR 4
:
:
TST2: SCOPE

```

THE SEQUENCE OF LOOPING FOR DIFFERENT CASES IS EXPLAINED BELOW. NOTE THAT 'TST1' AND 'TST2' ARE TAGS WHICH DEFINE THE BOUNDARY OF A TEST, (IN THIS CASE TEST 1). TEST 1 STARTS AT 'TST1' AND ENDS JUST BEFORE 'TST2'.

IN THE ILLUSTRATION BELOW --> INDICATES THE POINT FROM WHERE RETURN IS MADE AND LOOPING IS DONE.

1. ERROR 2 OCCURS, SW 14 SET.

TST1..ERROR 2..TST2-->TST1..ERROR 2..TST2-->TST1...

2. ERROR 2 OCCURS, SW 12 SET.

TST1...ERROR 2-->TST1...ERROR2-->TST1...

3. ERROR 2,3; SW 14 SET.

TST1..ERROR 2..ERROR 3..TST2-->TST1..ERROR 2..ERROR 3..TST2-->TST1...

4. ERROR 2,3; SW 12 SET.

TST1...ERROR 2-->TST1...ERROR 2-->TST1....

WHICH DRIVES ARE TO BE TESTED, ETC.

9.2 DRIVE DEPENDENT CONTROLLER TESTS

THIS SECTION FORMS A MAJOR PART OF THE PROGRAM WHEREIN MOST OF THE CONTROLLER IS CHECKED.

JUST BEFORE ENTERING THIS SECTION THE PROGRAM FINDS OUT WHICH DRIVE IS TO BE CHECKED. IF IN RKDP CHAIN MODE, DRIVE 'N' IF PRESENT, IS SKIPPED AND THE NEXT AVAILABLE DRIVE IS SELECTED.

THE DRIVE NUMBER BEING TESTED IS PRINTED OUT:

DRIVE N ;N=0,1,2...7

THE TESTING IS DONE IN A LOGICAL HIERCHY, SIMPLER THINGS FIRST, THEN MORE COMPLEX AND SO ON.

IN ONE OF THE TESTS THE ENTIRE DISK PACK IS FORMATTED, CHECKS ARE MADE FOR ERROR CONDITIONS. THE FIRST WORD OF EVERY SECTOR IS WRITTEN AS A PSUEDO-HEADER, REFLECTING THE ABSOLUTE ADDRESS OF THAT SECTOR (DRIVE #, CYLINDER #, SURFACE #, SECTOR #). EXAMPLE: THE PSUEDO-HEADER FOR SECTOR 5, SURFACE 0, CYLINDER 20, DRIVE 0 WOULD BE 001005.

IN THE NEXT TEST THE HEADERS FROM THE ENTIRE PACK ARE READ AND CHECKED FOR CORRECTNESS. IN A SUBSEQUENT TEST ALL THE PSUEDO-HEADERS ARE READ AND VERIFIED.

ALL THE FUNCTIONS ARE CHECKED OUT. 'SEEK' IS CHECKED IN THE THREE DIFFERENT VELOCITY MODES (HIGH, MEDIUM, LOW). VARIOUS ERRORS LIKE 'NXD', 'NXC', ETC. ARE SIMULATED AND CHECKED.

HARDWARE POGIC IS CHECKED USING ALL THE DRIVES THAT HAVE BEEN INDICATED.

AT THE END OF THIS SECTION, A CHECK IS MADE IF ALL INDICATED DRIVES HAVE BEEN TESTED. IF NOT, CONTROL IS TRANSFERRED TO THE BEGINNING OF THIS SECTION.

THUS ONE PASS OF THE PROGRAM INVOLVES DOING

1. SUBTEST #1 ONCE
2. DRIVE-DEPENDENT TESTS FOR ALL THE SELECTED DRIVES.

10.0 ERROR REPORTING

THE ERROR TABLE STARTING AT \$ERRTB CONTAINS INFORMATION PERTAINING TO EVERY ERROR THAT CAN OCCUR. EACH ITEM IN THE TABLE CONSISTS OF FOUR

ENTRIES.

- A. EM - THIS IS A POINTER TO THE ERROR MESSAGE TO BE TYPED OUT WHEN THE ERROR OCCURS.
- B. DH - THIS IS A POINTER TO THE DATA HEADER TO BE TYPED OUT.
- C. DT - THIS IS A POINTER TO THE DATA WHICH IS TO BE TYPED TYPED OUT UNDER THE HEADERS.
- D. D - THIS IS A TERMINATOR SIGNIFYING THE END OF THE ITEM.

THE ERROR CALL IS AN EMT INSTRUCTION WITH ITS LOWER BYTE ENCODED TO INDICATE THE ERROR NUMBER. THUS "OR 1" WOULD BE (EMT+1) IE 104001.

EVERY ERROR CORRESPONDS TO AN ITEM IN THE ERROR TABLE. THUS "ERROR 14" WOULD CORRESPOND TO ITEM 14. AS FAR AS POSSIBLE, THE ERROR MESSAGES HAVE BEEN KEPT SHORT, BUT CLARITY IS NOT SACRIFICED FOR BREVITY. INSPITE OF THIS, IF THE USER FINDS A NEED, HE CAN LOOK UP THE ENTIRE ERROR MESSAGE IN THE ERROR ITEMS TABLE FOUND IN THE BEGINNING OF THE LISTINGS. THUS FOR "ERROR 14", "ITEM 14" IN THE ITEM TABLE CAN BE LOOKED UP. WHEN THE ERROR INSTRUCTION IS EXECUTED A TRAP OCCURS TO THE ERROR HA LOCATED AT \$ERROR WHICH PROCESSES THE ERROR CALL. SEE SEC 12.3

11.0 ERROR INTERPRETATION

WHENEVER AN ERROR MESSAGE IS PRINTED OUT, ALL REGISTERS AND OTHER DATA PERTAINING TO THE ERROR ARE ALSO GIVEN. RKDS, RKER...RKBA INDICATE THE CONTENTS OF THE CORRESPONDING REGISTERS AT THE TIME OF ERROR.

EVERY ERROR MESSAGE CONTAINS A PC. THIS PC INDICATES THE POSITION IN PROGRAM WHERE THE ERROR CALL IS LOCATED. THE ERROR MESSAGE, BECAUSE OF PRACTICAL CONSIDERATIONS IS MADE SHORT AND MEANINGFUL. THE USER IS ADVTD TO LOOK UP THE PC IN THE PROGRAM LISTING, WHERE HE WILL FIND MORE INFORMATION ABOUT THE ERROR. IN MANY INSTANCES, A SINGLE FAULT WILL GIVE RISE TO MORE THAN ONE ERROR REPORT. A LITTLE DELIBERATION AND CAREFUL EXAMINATION OF THE DATA GIVEN WILL BE CERTAINLY VERY HELPFUL IN PINPOINTING THE FAULT. A BRIEF EXPLANATION OF WHAT IS BEING CHECKED IN THE SUBTEST IS GIVEN AT THE BEGINNING OF EVERY SUBTEST. ALL THE NUMBERS GIVEN WITH ERROR MESSAGES ARE IN OCTAL.

12.0 HANDLES AND COMMON ROUTINES

THE COMPOSED ROUTINES USED IN THE PROGRAM ARE CALLED IN TWO WAYS.

- A. AS A SUBROUTINE THROUGH 'JSR' CALL
- B. THROUGH A 'TRAP' HANDLER

12.1 TRAP HANDLER

MANY COMMONLY USED ROUTINES IN THE PROGRAM ARE CALLED USING THE TRAP INSTRUCTION AND THE 'TRAP' HANDLER. THE LOWER BYTE OF THE TRAP INSTRUCTION IS ENCODED DIFFERENTLY FOR DIFFERENT ROUTINES. THE TRAP HANDLER IS LOCATED AT '\$TRAP'. WHEN A CALL FOR A ROUTINE IS EXECUTED, A TRAP OCCURS TO THE HANDLER AT '\$TRAP'. THE HANDLER PICKS UP THE LOWER BYTE OF THE "CALL INSTRUCTION" AND USES IT TO FORM THE STARTING ADDRESS OF THE ROUTINE TO GO TO FOR SERVICE.

12.2 SCOPE HANDLER

THE 'IOT' TRAP IS USED BY THE 'SCOPE' STATEMENT. WHEN 'SCOPE' IS EXECUTED, AN IOT TRAP OCCURS TO MEMORY LOCATION '\$SCOPE'. THE SCOPE HANDLER STARTS AT '\$SCOPE'. DEPENDING ON THE SWITCH SETTINGS THE HANDLER DECIDES TO LOOP ON TEXT, INHIBIT ITERATIONS ETC. THERE ARE CERTAIN POINTERS AND FLAGS WHICH ARE ADJUSTED. THUS, IT IS NOT ADVISABLE START THE PROGRAM AT ANY GIVEN LOCATION SINCE THE VARIOUS POINTERS AND FLAGS MAY NOT BE CORRECTLY ADJUSTED.

12.3 ERROR HANDLER

AN EMT TRAP INSTRUCTION IS USED BY THE ERROR CALL. THE LOWER BYTE IS ENCODED TO GIVE DIFFERENT ERROR CALLS. (EX: ERROR 1 = 104000+1; ERROR 16 = 104000+16). WHEN THE ERROR STATEMENT IS EXECUTED, A TRAP OCCURS TO MEMORY LOCATION '\$ERROR'. THE ERROR HANDLER IS LOCATED AT '\$ERROR'. THE HANDLER FORMS THE POINTER TO ERROR TABLE, WHICH IS USED IF AN ERROR MESSAGE IS TO BE TYPED DEPENDING ON THE SWITCH SETTINGS, A DECISION ABOUT HALTING ON ERROR, INHIBITING TYPEOUT, LOOPING ON ERROR ETC. IS MADE. IF AN ERROR MESSAGE IS TO BE TYPED OUT AN EXIT IS MADE TO THE ERROR MESSAGE TYPEOUT ROUTINE LOCATED AT '\$ERRTYP'.

12.4 CONTROL RESET ROUTINE

THE CALL FOR THIS ROUTINE IS "CNT.RESET" AND IS AN ENCODED 'TRAP' INSTRUCTION. WHEN "CNT.RESET" IS EXECUTED THE CONTROL RESET ROUTINE STARTING AT

"CN.RST" IS ENTERED. A CONTROL RESET IS ISSUED THE PROGRAM WAITS TILL THE CONTROL READY SETS, ON WHICH THE ROUTINE IS EXITED. IF CONTROL READY DOES NOT SET WITHIN A CERTAIN TIME AN ERROR IS REPORTED. THE PC TYPED OUT IS THE LOCATION WHERE THE "CNT.RESET" CALL IS LOCATED. THE WAITING TIME IS 2.8 MS FOR 11/20 AND 560 US FOR 11/45 WITH BIPOLAR MEMORY.

12.5 CONTROL READY ROUTINE

THIS ROUTINE IS CALLED BY "CNT.RDY" (AN ENCODED 'TRAP' INSTRUCTION) AND IS LOCATED AT "CN.RDY". THE ROUTINE WAITS FOR THE CONTROL READY TO SET AND WHEN IT DOES, EXITS. IF CONTROL READY DOES NOT SET WITHIN A SPECIFIED TIME AN ERROR MESSAGE IS GIVEN

CNTRL RDY DIDN'T SET
PC = XXXXXX RKCS = YYYYYY

THE PC IS THE LOCATION AT WHICH THE "CNT.RDY" CALL IS LOCATED. THE WAITING TIME IS 949 MS FOR 11-20 AND 189 MS FOR 11/45 WITH BIPOLAR MEMORY.

12.6 DRIVE RESET ROUTINE

THE DRIVE - RESET ROUTINE IS LOCATED AT "DRESET" AND IS CALLED BY A "JSR". IT ISSUES A DRIVE RESET AND WAITS FOR THE R/W/S RDY TO SET, ON WHICH THE ROUTINE IS EXITED. THE WAITING TIME IS 4959 MS FOR 11/20 AND 991 MS FOR 11/45 WITH BIPOLAR MEMORY.

12.7 TIME DELAY ROUTINE

THIS ROUTINE PROVIDES A VARIABLE TIME DELAY. THE CALL IS DELAY ,N WHERE N=1 TO 177777 (OCTAL) TIME DELAY PROVIDED= 7.5 TIMES(X) N MICRO SECS FOR 11/20, 1.5N US FOR 11/45 (N CONVERTED TO DECIMAL BEFORE COMPUTING DELAY) IF THE USER WANTS TO CHANGE THE DELAY AT ANY POINT IT CAN BE DONE BY SIMPLY CHANGING VARIABLE 'N'.

12.8 WAIT FOR INTERRUPT ROUTINE

THIS ROUTINE PROVIDES A VARIABLE TIME LIMIT DURING WHICH RK11 INTERRUPT MAY OCCUR. THE IS
WAT.INT ,N N=1 TO 1777777 (OCTAL)
WAITING TIME=7.5 TIMES(X) N US FOR 11/20, 1.5N US

FOR 11/45 UPON ENTERING THE ROUTINE CPU PRIORITY IS DROPPED SO THAT RK11 CAN INTERRUPT.

12.9 OTHER ROUTINES

THERE ARE OTHER COMMONLY USED ROUTINES AS LISTED BELOW.

\$TYPE:
 TYPE ROUTINE FOR TYPING OUT ASCII STRINGS.
 LOCATED AT "\$TYPE"
 CALLED BY "TYPE"

\$TYPOC:
 ROUTINE FOR TYPING OUT OCTAL NUMBERS.
 LOCATED AT "\$TYPOC"
 CALLED BY "TYPOC"

\$TYPDS:
 ROUTINE FOR TYPING OUT DECIMAL NUMBERS.
 LOCATED AT "\$TYPDS"
 CALLED BY "TYPDS"

\$RDLIN:
 ROUTINE FOR INPUTTING ASCII STRINGS FROM TTY.
 LOCATED AT "\$RDLIN"
 CALLED BY "RDLIN"

\$ERRTYP:
 ROUTINE FOR TYPING OUT ERROR MESSAGES.
 LOCATED AT \$ERRTYP
 CALLED BY "JSR \$ERRTYP"

\$PWRDN:
 ROUTINE FOR HANDLING POWER FAILURE.
 LOCATED AT \$PWRDN
 CALLED WHEN THERE IS A POWER FAILURE.

\$PWRUP:
 ROUTINE FOR HANDLING POWER UP AFTER A POWER FAIL.
 LOCATED AT \$PWRUP
 CALLED WHEN POWER RETURNS AFTER HAVING GONE DOWN.

13.0 UNEXPECTED TIMEOUTS AND RK11 INTERRUPTS

WHEN AN UNEXPECTED TIMEOUT OCCURS, THE PC AT WHICH TIME OUT OCCURED IS TYPED OUT AND THE PROGRAM HALTS. IF IT IS INTACT, IT CAN BE RESTARTED BY PRESSING CONTINUE.

IF AN UNEXPECTED RK11 INTERRUPT OCCURS THE PROGRAM TYPES OUT THE PC AT WHICH THE INTERRUPT CAME IN AND THEN HALTS. PRESSING CONTINUE WOULD RESTART THE PROGRAM FROM BEGINING. SW 9- LOOPING CARRY IS PROVIDED AS A TROUBLE SHOOTING AID.

14.0 QUICK VERIFYING MODE

THE FIRST PASS OF THE PROGRAM IS A QUICK VERIFYING MODE. ALL THE TESTS ARE DONE ONLY ONCE, ON SUBSEQUENT PASSES THE TESTS ARE ITERATED (NORMALLY 50 TIMES, 5 IN SOME CASES). THUS THE FIRST PASS TAKES A SHORTER TIME TO COMPLETE, WHEREAS SUBSEQUENT PASSES TAKE MORE TIME.

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.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY JIM KAPADIA
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
.*
.*PROGRAM REVISED BY TOM SAWYER, MARCH, 1976
.*REVISED BY CHUCK HESS, AUGUST, 1976
.SBTTL OPERATIONAL SWITCH SETTINGS
.*
.*      SWITCH          USE
.*      -----          -
.*      15             HALT ON ERROR
.*      14             LOOP ON TEST
.*      13             INHIBIT ERROR TYPEOUTS
.*      12             CYCLE ON ERROR TO PREVIOUS 'SCOPE' STATEMENT
.*      11             INHIBIT ITERATIONS
.*      10             TESTING ON SIMULATOR
.*      9              LOOP ON ERROR
.*      8              LOOP ON TEST IN SWR(7:0)
.*      6              DROP THE DRIVE IF MORE THAN 5 ERRORS

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;YOU ARE ADVISED TO READ THE DOCUMENT BEFORE USING THIS PROGRAM.
;ON GETTING AN ERROR REFER TO THE LISTINGS AT THE PC POINTED

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JC1100

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000040
000100
000140
000200
000240
000300
000340

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040000
020000
010000
004000
002000
001000
000400
000200
000100

:OUT IN THE ERROR MESSAGE. ADJACENT ERROR MESSAGES IF FOLLOWED
:CAREFULLY COULD LEAD TO AN EASY PINPOINTING OF THE FAULT

:*****
:SBTTL BASIC DEFINITIONS

:*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***

STACK= 1100
.EQUIV EMT,ERROR ;:BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;:BASIC DEFINITION OF SCOPE CALL

:*MISCELLANEOUS DEFINITIONS

HT= 11 ;:CODE FOR HORIZONTAL TAB
LF= 12 ;:CODE FOR LINE FEED
CR= 15 ;:CODE FOR CARRIAGE RETURN
CRLF= 200 ;:CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;:PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774 ;:STACK LIMIT REGISTER
PIRQ= 177772 ;:PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;:HARDWARE SWITCH REGISTER
DDISP= 177570 ;:HARDWARE DISPLAY REGISTER

:*GENERAL PURPOSE REGISTER DEFINITIONS

R0= %0 ;:GENERAL REGISTER
R1= %1 ;:GENERAL REGISTER
R2= %2 ;:GENERAL REGISTER
R3= %3 ;:GENERAL REGISTER
R4= %4 ;:GENERAL REGISTER
R5= %5 ;:GENERAL REGISTER
R6= %6 ;:GENERAL REGISTER
R7= %7 ;:GENERAL REGISTER
SP= %6 ;:STACK POINTER
PC= %7 ;:PROGRAM COUNTER

:*PRIORITY LEVEL DEFINITIONS

PR0= 0 ;:PRIORITY LEVEL 0
PR1= 40 ;:PRIORITY LEVEL 1
PR2= 100 ;:PRIORITY LEVEL 2
PR3= 140 ;:PRIORITY LEVEL 3
PR4= 200 ;:PRIORITY LEVEL 4
PR5= 240 ;:PRIORITY LEVEL 5
PR6= 300 ;:PRIORITY LEVEL 6
PR7= 340 ;:PRIORITY LEVEL 7

:*"SWITCH REGISTER" SWITCH DEFINITIONS

SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100

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950      000040      SW05= 40
951      000020      SW04= 20
952      000010      SW03= 10
953      000004      SW02= 4
954      000002      SW01= 2
955      000001      SW00= 1
956      .EQUIV      SW09,SW9
957      .EQUIV      SW08,SW8
958      .EQUIV      SW07,SW7
959      .EQUIV      SW06,SW6
960      .EQUIV      SW05,SW5
961      .EQUIV      SW04,SW4
962      .EQUIV      SW03,SW3
963      .EQUIV      SW02,SW2
964      .EQUIV      SW01,SW1
965      .EQUIV      SW00,SW0
966
967      .*DATA BIT DEFINITIONS (BIT00 TO BIT15,
968      100000      BIT15= 100000
969      040000      BIT14= 40000
970      020000      BIT13= 20000
971      010000      BIT12= 10000
972      004000      BIT11= 4000
973      002000      BIT10= 2000
974      001000      BIT09= 1000
975      000400      BIT08= 400
976      000200      BIT07= 200
977      000100      BIT06= 100
978      000040      BIT05= 40
979      000020      BIT04= 20
980      000010      BIT03= 10
981      000004      BIT02= 4
982      000002      BIT01= 2
983      000001      BIT00= 1
984      .EQUIV      BIT09,BIT9
985      .EQUIV      BIT08,BIT8
986      .EQUIV      BIT07,BIT7
987      .EQUIV      BIT06,BIT6
988      .EQUIV      BIT05,BIT5
989      .EQUIV      BIT04,BIT4
990      .EQUIV      BIT03,BIT3
991      .EQUIV      BIT02,BIT2
992      .EQUIV      BIT01,BIT1
993      .EQUIV      BIT00,BIT0
994
995      .*BASIC "CPU" TRAP VECTOR ADDRESSES
996      000004      ERRVEC= 4      ;; TIME OUT AND OTHER ERRORS
997      000010      RESVEC= 10     ;; RESERVED AND ILLEGAL INSTRUCTIONS
998      000014      TBITVEC=14     ;; "T" BIT
999      000014      TRTVEC= 14     ;; TRACE TRAP
1000     000014      BPTVEC= 14     ;; BREAKPOINT TRAP (BPT)
1001     000020      IOTVEC= 20     ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
1002     000024      PWRVEC= 24     ;; POWER FAIL
1003     000030      EMTVEC= 30     ;; EMULATOR TRAP (EMT) **ERROR**
1004     000034      TRAPVEC=34     ;; "TRAP" TRAP
1005     000060      TKVEC= 60      ;; TTY KEYBOARD VECTOR

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1006      000064      TPVEC= 64          ;;TTY PRINTER VECTOR
1007      000240      PIRQVEC=240       ;;PROGRAM INTERRUPT REQUEST VECTOR
1008
1009
1010      000000      .SBTTL TRAP CATCHER
1011
1012      .=0
1013      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
1014      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
1015      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
1016      000174      000174      .=174
1017      000176      000000      DISPREG: .WORD 0          ;;SOFTWARE DISPLAY REGISTER
1018      000200      000137      002636  SWREG: .WORD 0          ;;SOFTWARE SWITCH REGISTER
1019      .SBTTL STARTING ADDRESS(ES)
1020      JMP @#START ;;JUMP TO STARTING ADDRESS OF PROGRAM
1021      .SBTTL ACT11 HOOKS
1022      ;;*****
1023      :HOOKS REQUIRED BY ACT11
1024      $SVPC=.          ;SAVE PC
1025      .=46
1026      $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SECF
1027      .=52
1028      .WORD 0          ;;2)SET LOC.52 TO ZERO
          .=$SVPC          ;; RESTORE PC

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1085 001224 020105 051120 051505
 1086 001232 052116 000
 1087 001236 001236
 1088 001236 005015 047516 042516
 1089 001244 000
 1090
 1091 001245 015 041412 052116
 1092 001252 051040 054504 042040
 1093 001260 042111 023516 020124
 1094 001266 042523 000124
 1095
 1096 001272 005015 051104 053111
 1097 001300 020105 000
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 1099 001303 015 040412 046114
 1100 001310 042040 053122 123
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 1102 001315 040 051104 050117
 1103 001322 006504 000012
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 1111
 1112 001326 177400
 1113 001330 177402
 1114 001332 177404
 1115 001334 177406
 1116 001336 177410
 1117 001340 177412
 1118 001342 177416
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 1125 001344 000000
 1126 001346 000000
 1127 001350 000000
 1128 001352 000000
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 1131 001354 000000
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 1133 001356 000000
 1134 001360 000000
 1135 001362 000000
 1136 001364 000000
 1137 001366 000000
 1138 001370 000000
 1139
 1140

MSG2: .EVEN
 .ASCIZ <15><12>/NONE/
 MSG3: .ASCIZ <15><12>/CNT RDY DIDN'T SET/
 MSG4: .ASCIZ <15><12>/DRIVE /
 MSG5: .ASCII <15><12>/ALL DRVS/
 MSG6: .ASCIZ / DROPD/<15><12>
 .EVEN

;RK11 REGISTERS
 ;IF FOR ANY REASON THE REGISTER ADDRESSES ARE DIFFERENT FROM THESE
 ; (GIVEN BELOW), THE CONTENTS OF THE APPROPRIATE POINTERS SHOULD BE
 ; MODIFIED SO THAT THE CORRECT ADDRESS IS USED.

.EVEN
 RKDS: 177400
 RKER: 177402
 RKCS: 177404
 RKWC: 177406
 RKBA: 177410
 RKDA: 177412
 RKDB: 177416

;TAGS AND GENERAL DATA AREA

SIMUL: 0 ;FLAG TO BE SET TO 1 WHEN ON SIMULATOR
 FTITLE: 0 ;FLAG FOR PRINTING PROGRAM TITLE
 DRIVAD: 0 ;CONTAINS ADDRESS OF THE DRIVE UNDER TEST
 DRVCON: 0 ;CONTAINS THE NUMBER OF DRIVES CHECKED.
 ;IT IS INCREMENTED EACH TIME THE TESTS FOR
 ;A DRIVE IS COMPLETED.
 DRVPTR: 0 ;CONTAINS THE POINTER TO THE DRIVE FLAG (DRIVEC
 ;-DRIVE?) OF THE DRIVE TO BE CHECKED NEXT.
 INDX1: 0 ;GENERAL INDEX FOR KEEPING COUNT
 INDX2: 0 ;GENERAL INDEX
 COUNT: 0 ;GENERAL COUNT REGISTER
 COUNT1: 0 ;COUNT REGISTER USED FOR 'DRESET' SUBROUTINE
 TIMER: 0 ;TIMER REGISTER
 EFLG1: 0 ;SET TO INDICATE A PARTICULAR
 ;ERROR CONDITION

1141	001372	000100	SEEK0:	100	: CONTAINS ADDRESS OF CYLINDER 2
1142	001374	001000	SEEK1:	1000	: CONTAINS ADDRESS OF CYLINDER 20
1143	001376	014500	SEEK2:	14500	: CONTAINS ADDRESS OF CYLINDER 312
1144	001400	000200	RKPRI:	200	: CONTAINS THE CPU LEVEL AT WHICH
1145					: RK11 NORMALLY INTERRUPTS. THIS WORD
1146					: SHOULD BE CHANGED IF RK11 IS DESINGATED
1147					: 0 OR LEVEL OTHER THAN 5. E.G. IF IT IS CHANGED
1148					: TO 5, THIS WORD SHOULD BE CHANGED TO 240.
1149	001402	000220	RKVEC:	220	: CONTAINS THE NORMAL VECTOR ADDRESS TO WHICH
1150					: RK11 INTERRUPTS. IF THIS IS NOT SO, CHANGE
1151					: THIS WORD TO CONTAIN MODIFIED VECTOR ADDRESS.
1152	001404	000000	FFLAG:	0	
1153	001406	000000	ODDEVN:	0	: USED TO DETERMINE WHICH OF RK-DSF DRIVES ACTIVE
1154					: 0 IF EVEN DRIVE
1155					: -1 IF ODD DRIVE
1156	001410	000000	DDPCH:	0	: IF PROGRAM LOADED FROM RKOS, CONTAINS
1157					: ADDRESS OF DRIVE WITH RKDP PACK
1158	001412	000000	DRIVS:	0	: CONTAINS THE NUMBER OF DRIVES PRESENT
1159					
1160					
1161					
1162					
1163					: THE FLAGS BELOW (BIT 0) ARE SET TO 1 TO INDICATE THAT A PARTICULAR DRIVE
1164					: IS PRESENT AND IS TO BE TESTED. BIT 12, IF SET, INDICATES THAT THE DRIVE
1165					: WAS DROPPED AFTER MAXIMUM ALLOWABLE NUMBER OF ERRORS OCCURED ON THAT
1166					: DRIVE (SW 6 SET).
1167					: IF MORE THAN 5 ERRORS OCCUR IN THE HARDWARE POLLING TEST (LAST)
1168					: THEN ALL DRIVES ARE DROPPED. BUT BIT 12 IS NOT SET.
1169					
1170	001414	000000	DRIVO:	0	: FLAG SET TO 1 WHEN DRIVE 0 PRESENT
1171	001416	000000	DRIV1:	0	: FOR DRIVE 1
1172	001420	000000	DRIV2:	0	: FOR DRIVE 2
1173	001422	000000	DRIV3:	0	: FOR DRIVE 3
1174	001424	000000	DRIV4:	0	: FOR DRIVE 4
1175	001426	000000	DRIV5:	0	: FOR DRIVE 5
1176	001430	000000	DRIV6:	0	: FOR DRIVE 6
1177	001432	000000	DRIV7:	0	: FOR DRIVE 7
1178					
1179	001434	000000	T56FLG:	0	
1180	001436	000000	PHYDRV:	0	
1181	001440	000000	SIZYET:	0	

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001442

.SBTTL ERROR POINTER TABLE
; *THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
; *THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
; *LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
; *NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
; *NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

; * EM ;: POINTS TO THE ERROR MESSAGE
; * DH ;: POINTS TO THE DATA HEADER
; * DT ;: POINTS TO THE DATA
; * DF ;: POINTS TO THE DATA FORMAT

\$ERRTB:

; THE ERROR ITEMS TABLE CONSISTS OF ALL THE POSSIBLE ERROR MESSAGES
; USED IN THIS PROGRAM. AN ERROR CALL IN THE PROGRAM CORRESPONDS TO
; THE ITEM NUMBER IN THE ERROR TABLE. THUS 'ERROR 1' IN THE
; PROGRAM CORRESPONDS TO 'ITEM 1' IN THE ERROR TABLE.
; 'EM###' IS THE POINTER TO THE ERROR MESSAGE WHICH WILL BE TYPED
; OUT IN CASE THAT ERROR WERE TO OCCUR. THUS FOR 'ERROR 1' THE ERROR
; MESSAGE TYPE OUT WILL BE 'TIME OUT ON RK11 REG'.
; 'DH###' IS THE POINTER TO THE HEADER BLOCK WHICH WILL BE TYPED OUT
; IMMEDIATELY AFTER THE ERROR MESSAGE.
; 'DT###' SERVES AS A POINTER TO THE MEMORY LOCATIONS WHERE
; THE INFORMATION RELEVANT TO THE ERROR TYPE OUTS (LIKE PC, CONTENTS
; OF RKCS ETC.) WILL BE PICKED UP FROM.
; THE LAST ROW CONTAINING '0' SERVES AS A TERMINATOR.
; EXAMPLE:
; IF ON RUNNING THIS PROGRAM A TIMEOUT WERE TO OCCUR ON ADDRESSING RKDS
; (177400), BECAUSE OF SOME FAULT, THE FOLOWING TYPEOUT WOULD
; OCCUR ON THE TELETYPE.

```
TIME OUT ON RK11 REG
PC      REG
***** 177400
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; NOTE THAT ***** WOULD BE THE ACTUAL PC WHERE 'ERROR 1' IS LOCATED.

; THE ERROR HANDLER IS LOCATED AT '\$ERROR'. THE ERROR CALL IS AN 'EM'
; INSTRUCTION WITH ITS LOWER BYTE ENCODED TO PROVIDE INDEXING TO THE
; ITEMS IN THE ERROR TABLE.
; THUS 'ERROR 1' IS 104001
; 'ERROR 103' IS 104126 ETC.

; ERROR ITEMS TABLE

1294			:ITEM	11	
1295					
1296	001542	025545		EM34	;'SOK' DID NOT SET
1297	001544	032207		DH34	;'PC RKDS
1298	001546	031720		DT1	;'SERRPC \$REGO
1299	001550	000000		0	
1300					
1301			:ITEM	12	
1302					
1303	001552	025564		EM35	;'SEC COUNTR' DIDN'T COUNT TO 0
1304	001554	032225		DH35	;'PC SEC-CNTR
1305	001556	031720		DT1	;'SERRPC \$REGO
1306	001560	000000		0	
1307					
1308			:ITEM	13	
1309					
1310	001562	025617		EM36	;'SEC COUNTR' DIDN'T INCREMENT
1311	001564	032245		DH36	;'PC PRSNT-COUNT NXT-COUNT
1312	001566	031726		DT2	;'SERRPC \$REGO \$REG1
1313	001570	000000		0	
1314					
1315			:ITEM	14	
1316					
1317	001572	025647		EM37	;'SECTOR COUNTER' INCREMENTED WRONG
1318	001574	032043		DH4	;'PC EXPCTD RECVD
1319	001576	031726		DT2	;'SERRPC \$REGO \$REG1
1320	001600	000000		0	
1321					
1322			:ITEM	15	
1323					
1324	001602	025703		EM40	;'DIDN'T GET SC=SA FOR THIS SECTOR
1325	001604	032275		DH40	;'PC SECTOR RKDS
1326	001606	031726		DT2	;'SERRPC \$REGO \$REG1
1327	001610	000000		0	
1328					
1329			:ITEM	16	
1330					
1331	001612	025743		EM41	;'ERROR-'R/W/S RDY' SHOLLD BE SET.
1332	001614	032207		DH34	;'PC RKDS
1333	001616	031720		DT1	;'SERRPC \$REGO
1334	001620	000000		0	
1335					
1336			:ITEM	17	
1337					
1338	001622	025411		EM13	;'RKBA ERROR
1339	001624	032043		DH4	;'PC EXPCT RECVD
1340	001626	031726		DT2	;'SERRPC \$REGO \$REG1
1341	001630	000000		0	
1342					
1343			:ITEM	20	
1344					
1345	001632	026000		EM43	;'UNEXPECTED RK11 INTERRUPT
1346	001634	032144		DH21	;'PC
1347	001636	031752		DT21	;'SERRPC
1348	001640	000000		0	
1349					

1350			:ITEM	21		
1351					EM44	: 'CNTRL RDY' DIDN'T SET AFTER SEEK OR DRIVE RESET
1352	001642	026032			DH44	: PC RKCS RKER RKDS RKDA
1353	001644	032323			DT20	: SERRPC \$REG0 \$REG1 \$REG2 \$REG3.
1354	001646	031736			0	
1355	001650	000000				
1356						
1357			:ITEM	22		
1358					EM45	: 'ERR' OR 'HE' SET ON SEEK OR DRIVE RESET
1359	001652	026106			DH44	: PC RKCS RKER RKDS RKDA
1360	001654	032323			DT20	: SERRPC \$REG0 \$REG1 \$REG2 \$REG3
1361	001656	031736			0	
1362	001660	000000				
1363						
1364			:ITEM	23		
1365					EM46	: RKER BIT, ON SEEK OR DRIVE RESET
1366	001662	026154			DH30	: PC RKCS RKER RKDS
1367	001664	032151			DT26	: SERRPC \$REG0 \$REG1 \$REG2
1368	001666	031756			0	
1369	001670	000000				
1370						
1371			:ITEM	24		
1372					EM47	: RKCS CHANGED AFTER FUNCTION WAS DONE
1373	001672	026212			DH4	: PC EXPCT RECVD
1374	001674	032043			DT2	: SERRPC \$REG0 \$REG1
1375	001676	031726			0	
1376	001700	000000				
1377						
1378			:ITEM	25		
1379					EM50	: 'R/W/S RDY' DID NOT CLEAR
1380	001702	026254			DH30	: PC RKCS RKER RKDS
1381	001704	032151			DT26	: SERRPC \$REG0 \$REG1 \$REG2
1382	001706	031756			0	
1383	001710	000000				
1384						
1385			:ITEM	26		
1386					EM51	: 'R/W/S RDY' DIDN'T SET AFTER SEEK OR DRIVE RESET
1387	001712	026303			DH44	: PC RKCS RKER RKDS RKDA
1388	001714	032323			DT20	: SERRPC \$REG0 \$REG1 \$REG2 \$REG3
1389	001716	031736			0	
1390	001720	000000				
1391						
1392			:ITEM	27		
1393					EM52	: RKDA CHANGED AFTER SEEK
1394	001722	026356			DH4	: PC EXPCTD REGVD
1395	001724	032043			DT2	: SERRPC \$REG0 \$REG1
1396	001726	031726			0	
1397	001730	000000				
1398						
1399			:ITEM	30		
1400					EM53	: 'CNTRL RDY' DIDN'T CLEAR AS GO WAS SET
1401	001732	026403			DH30	: PC RKCS RKER RKDS
1402	001734	032151			DT26	: SERRPC \$REG0 \$REG1 \$REG2
1403	001736	031756			0	
1404	001740	000000				
1405						

1406			:ITEM 31	
1407				
1408	001742	026446	EMS4	: 'CNTRL RDY' DIDN'T SET ON DOING WRITE/FMT STARTING
1409				: FROM (DSK-ADRES)
1410	001744	032370	DMS4	: PC RKCS RKER RKDS RKDA
1411				: DRV# CYL (DSK-ADRES) SUR SECTR
1412	001746	031770	DT54	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1413				: \$REG4 \$REG5 \$REG6 \$REG7
1414	001750	000000	0	
1415				
1416			:ITEM 32	
1417				
1418	001752	026540	EMS5	: 'HE' OR 'ERR' ON WRITE/FMT STARTING FROM
1419				: (DSK-ADRES)
1420	001754	032370	DMS4	: PC RKCS RKER RKDS RKDA
1421				: DRV# CYL (DSK-ADRES) SUR SECTR
1422	001756	031770	DT54	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1423				: \$REG4 \$REG5 \$REG6 \$REG7
1424	001760	000000	0	
1425				
1426			:ITEM 33	
1427				
1428	001762	026617	EMS6	: RKDA INCREMENTED WRONG ON WRITE OR WRITE FORMAT
1429	001764	032477	DMS6	: PC EXPCT: DRV# CYL SUR SECTR
1430				: RECVD: DRV# CYL SUR SECTR
1431	001766	031770	DT54	: \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1432				: \$REG4 \$REG5 \$REG6 \$REG7
1433	001770	000000	0	
1434				
1435			:ITEM 34	
1436				
1437	001772	026656	EMS7	: RKWC DIDN'T OVERFLOW ON WRITE OR WRITE FORMAT
1438	001774	032071	DMS	: PC RECVD
1439	001776	031720	DT1	: \$ERRPC \$REG0
1440	002000	000000	0	
1441				
1442			:ITEM 35	
1443				
1444	002002	026714	EMS0	: RKBA INCREMENTED WRONG ON WRITE OR WRITE FORMAT
1445	002004	032043	DMS	: PC EXPCT RECVD
1446	002006	031726	DT2	: \$ERRPC \$REG0 \$REG1
1447	002010	000000	0	
1448				
1449			:ITEM 36	
1450				
1451	002012	026753	EMS1	: RKER SET ON WRITE/READ/FORMAT
1452	002014	032151	DMS0	: PC RKCS RKER RKDS
1453	002016	031756	DT26	: \$ERRPC \$REG0 \$REG1 \$REG2
1454	002020	000000	0	
1455				
1456			:ITEM 37	
1457				
1458	002022	027010	EMS2	: RKDB ERROR
1459	002024	032043	DMS	: PC EXPCT RECVD
1460	002026	031726	DT2	: \$ERRPC \$REG0 \$REG1
1461	002030	000000	0	

1518	002120	000000	0	
1519				
1520			: ITEM	47
1521				
1522	002122	027416	EM72	: WRONG DRIVE ID IN RKDS AFTER SEEK
1523	002124	032043	DH4	: PC EXPCT RECVD
1524	002126	031726	DT2	: \$ERRPC \$REGO \$REG1
1525	002130	000000	0	
1526				
1527			: ITEM	50
1528				
1529	002132	027460	EM73	: HARDWARE POLL, DRIVE ID BITS (13-15) SHOULD BE CLEAR
1530	002134	032207	DH34	: PC RKDS
1531	002136	031726	DT2	: \$ERRPC \$REGO
1532	002140	000000	0	
1533				
1534			: ITEM	51
1535				
1536	002142	027532	EM74	: HARDWARE POLL, INTERRUPTING DRIVE # NOT PRESENT
1537	002144	032726	DH74	: PC DRIVE #
1538	002146	031720	DT1	: \$ERRPC \$REGO
1539	002150	000000	0	
1540				
1541			: ITEM	52
1542				
1543	002152	027602	EM75	: 'DRIVE #' DID NOT INTERRUPT DURING HARDWARE POLL
1544	002154	032726	DH74	: PC DRIVE #
1545	002156	031720	DT1	: \$ERRPC \$REGO
1546	002160	000000	0	
1547				
1548			: ITEM	53
1549				
1550	002162	027652	EM76	: SCP DID NOT SET AFTER WAS DONE
1551	002164	033102	DH117	: PC RKCS
1552	002166	031720	DT1	: \$ERRPC \$REGO
1553	002170	000000	0	
1554				
1555			: ITEM	54
1556				
1557	002172	027715	EM77	: RKDA CHANGED AFTER 'DRIVE RESET'
1558	002174	032043	DH4	: PC EXPCT RECVD
1559	002176	031726	DT2	: \$ERRPC \$REGO \$REG1
1560	002200	000000	0	
1561				
1562			: ITEM	55
1563				
1564	002202	027752	EM100	: DATA ERROR AT WORD#
1565	002204	032747	DH100	: PC WORD# EXPCT RECVD
1566	002206	031756	DT26	: \$ERRPC \$REGO \$REG1 \$REG2
1567	002210	000000	0	
1568				
1569			: ITEM	56
1570				
1571	002212	027775	EM101	: CNTL RDY DID NOT SET AFTER READ CHECK
1572	002214	032323	DH44	: PC RKCS RKER RKDS RKDA
1573	002216	031736	DT20	: \$ERRPC \$REGO \$REG1 \$REG2 \$REG3

1574	002220	000000	0	
1575				
1576			; ITEM	57
1577				
1578	002222	030037	EM102	; 'ERR' OF 'HE' SET ON READ CHECK
1579	002224	032151	DH30	; PC RKCS RKER RKDS
1580	002226	031756	DT26	; \$ERRPC \$REG0 \$REG1 \$REG2
1581	002230	000000	0	
1582				
1583			; ITEM	60
1584				
1585	002232	030063	EM103	; 'CSE' ON READ CHECK
1586	002234	033004	DH103	; PC RKER
1587	002236	031720	DT1	; \$ERRPC \$REG0
1588	002240	000000	0	
1589				
1590			; ITEM	61
1591				
1592	002242	030101	EM104	; RKWC DID NOT OVERFLOW ON READ CHECK OR WRITE CHECK
1593	002244	033020	DH104	; PC RECVD RKCS
1594	002246	031726	DT2	; \$ERRPC \$REG0 \$REG1
1595	002250	000000	0	
1596				
1597			; ITEM	62
1598				
1599	002252	030152	EM105	; RKDA INCREMENTED WRONG ON READ CHECK
1600	002254	032043	DH4	; PC EXPCT RECVD
1601	002256	031726	DT2	; \$ERRPC \$REG0 \$REG1
1602	002260	000000	0	
1603				
1604			; ITEM	63
1605				
1606	002262	030210	EM106	; RKBA CHANGED AFTER READ CHECK
1607	002264	032043	DH4	; PC EXPCT RECVD
1608	002266	031726	DT2	; \$ERRPC \$REG0 \$REG1
1609	002270	000000	0	
1610				
1611			; ITEM	64
1612				
1613	002272	030241	EM107	; MEMORY WORD CHANGED AFTER READ CHECK
1614	002274	033044	DH107	; PC LOC EXPCT RECVD
1615	002276	031756	DT26	; \$ERRPC \$REG0 \$REG1 \$REG2
1616	002300	000000	0	
1617				
1618			; ITEM	65
1619				
1620	002302	030302	EM110	; CNTRL RDY DID NOT SET AFTER WRITE CHECK
1621	002304	032323	DH44	; PC RKCS RKER RKDS RKDA
1622	002306	031736	DT20	; \$ERRPC \$REG0 \$REG1 \$REG2 \$REG3
1623	002310	000000	0	
1624				
1625			; ITEM	66
1626				
1627	002312	030345	EM111	; HE OR ERR BIT SET AFTER DOING WRITE CHECK
1628	002314	032151	DH30	; PC RKCS RKER RKDS
1629	002316	031756	DT26	; \$ERRPC \$REG0 \$REG1 \$REG2

1630	002320	000000	0					
1631								
1632				; ITEM	67			
1633								
1634	002322	030372	EM112			; WRITE CHECK ERROR		
1635	002324	032151	DH30			; PC RKCS RKER RKDS		
1636	002326	031756	DT26			; \$ERRPC \$REGO \$REG1 \$REG2		
1637	002330	000000	0					
1638								
1639				; ITEM	70			
1640								
1641	002332	030413	EM113			; RKDA INCREMENTED WRONG ON WRITE CHECK		
1642	002334	032043	DH4			; PC EXPCT RECVD		
1643	002336	031726	DT2			; \$ERRPC \$REGO \$REG1		
1644	002340	000000	0					
1645								
1646				; ITEM	71			
1647								
1648	002342	030452	EM114			; RKBA INCREMENTED WRONG ON WRITE CHECK		
1649	002344	032043	DH4			; PC EXPCT RECVD		
1650	002346	031726	DT2			; \$ERRPC \$REGO \$REG1		
1651	002350	000000	0					
1652								
1653				; ITEM	72			
1654								
1655	002352	030511	EM115			; RKBA INCREMENTED WITH IBA SET		
1656	002354	032043	DH4			; PC EXPCT RECVD		
1657	002356	031726	DT2			; \$ERRPC \$REGO \$REG1		
1658	002360	000000	0					
1659								
1660				; ITEM	73			
1661								
1662	002362	030545	EM116			; WRONG MEMORY LOCATION CHANGED WITH IBA SET		
1663	002364	032747	DH100			; PC WORD# EXPCT RECVD		
1664	002366	031756	DT26			; \$ERRPC \$REGO \$REG1 \$REG2		
1665	002370	000000	0					
1666								
1667				; ITEM	74			
1668								
1669	002372	030620	EM117			; RK11 DID NOT INTERRUPT WHEN IDE WAS SET		
1670	002374	033102	DH117			; PC RKCS		
1671	002376	031720	DT1			; \$ERRPC \$REGO		
1672	002400	000000	0					
1673								
1674				; ITEM	75			
1675								
1676	002402	030665	EM120			; RK11 DID NOT INTERRUPT AFTER SEEK WAS INITIATED		
1677	002404	033102	DH117			; PC RKCS		
1678	002406	031720	DT1			; \$ERRPC \$REGO		
1679	002410	000000	0					
1680								
1681				; ITEM	76			
1682								
1683	002412	030740	EM121			; SCP SET BEFORE SEEK COMPLETED		
1684	002414	033102	DH117			; PC RKCS		
1685	002416	031720	DT1			; \$ERRPC \$REGO		

1686	002420	000000	0	
1687				
1688			: ITEM	77
1689				
1690	002422	030776	EM122	:RK11 DID NOT INTERRUPT AFTER SEEK COMPLETED
1691	002424	032151	DH30	:PC RKCS RKER RKDS
1692	002426	031756	DT26	:SERRPC \$REG0 \$REG1 \$REG2
1693	002430	000000	0	
1694				
1695			: ITEM	100
1696				
1697	002432	031045	EM123	:CNTRL RESET DID NOT CLEAR 'SCP' BIT
1698	002434	033102	DH117	:PC RKCS
1699	002436	031720	DT1	:SERRPC \$REG0
1700	002440	000000	0	
1701				
1702			: ITEM	101
1703				
1704	002442	031104	EM124	:RK11 DID NOT INTERRUPT AFTER READ WAS DONE
1705	002444	033102	DH117	:PC RKCS
1706	002446	031720	DT1	:SERRPC \$REG0
1707	002450	000000	0	
1708				
1709			: ITEM	102
1710				
1711	002452	031146	EM125	:CNTRL RESET DID NOT CLEAR REGISTER
1712	002454	032014	DH2	:PC REGADD RECVD
1713	002456	031726	DT2	:SERRPC \$REG0 \$REG1
1714	002460	000000	0	
1715				
1716			: ITEM	103
1717				
1718	002462	031205	EM126	:RK11 DID NOT INTERRUPT AT CPU LEVEL
1719	002464	033116	DH126	:PC LEVEL RKCS
1720	002466	031726	DT2	:SERRPC \$REG0 \$REG1
1721	002470	000000	0	
1722				
1723			: ITEM	104
1724				
1725	002472	031246	EM127	:RK11 INTERRUPTED AT WRONG CPU LEVEL
1726	002474	033116	DH126	:PC LEVEL RKCS
1727	002476	031726	DT2	:SERRPC \$REG0 \$REG1
1728	002500	000000	0	
1729				
1730			: ITEM	105
1731				
1732	002502	031310	EM130	: 'ERR BIT' DID NOT SET IN RKER
1733	002504	033144	DH130	:PC RKCS RKER ERR BIT
1734	002506	031756	DT26	:SERRPC \$REG0 \$REG1 \$REG2
1735	002510	000000	0	
1736				
1737				
1738			: ITEM	106
1739				
1740	002512	031345	EM131	:HE OR ERR DID NOT SET
1741	002514	033203	DH131	:PC RKCS RKER

1798	002614	032726	DH74	:PC	DRIVE #	
1799	002616	031720	DT1	;\$ERRPC	\$REGO	
1800	002620	000000	0			
1801						
1802			:ITEM	117		
1803						
1804	002622	025364	EM11	;\$RWC	ERROR	
1805	002624	032043	DH4	:PC	EXPCT	RECVD
1806	002626	031726	DT2	;\$ERRPC	\$REGO	\$REGI
1807	002630	000000	0			
1808			:ITEM	120		
1809	002632	031656	EM142			
1810	002634	000000	0			
1811						
1812						
1813						

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1814 002636 000005 START: RESET ;CLEAR THE BUS
1815 ;;GIVE DRIVES TIME TO LOAD HEADS ;IN CASE OF AN APT START.
1816 002640 023737 000042 000046 CMP #42,#46 ;ARE WE IN ACT11 AUTOMATIC MODE?
1817 002646 001016 BNE STARTA ;NO, SKIP DELAY
1818 002650 005077 176464 CLR #RKDA ;SELECT UNIT 0
1819 002654 012700 000250 MOV #250,R0 ;WAIT FOR..
1820 002660 032777 000200 176440 20$: BIT #200,#RKDS ;DRIVE READY..
1821 002666 001006 BNE STARTA ;IN CASE..
1822 002670 005001 CLR R1 ;OF APT..
1823 002672 005301 DEC R1 ;START, BUT..
1824 002674 001376 BNE #-2 ;DON'T WAIT..
1825 002676 005300 DEC R0 ;FOREVER.
1826 002700 001367 BNE 20$
1827 002702 000000 HALT ;RKDS BIT 7 (DRIVE READY) NEVER SET
1828 002704
1829
1830 STARTA: ;SBTTL INITIALIZE THE COMMON TAGS
;:CLEAR THE COMMON TAGS ($CMTAG) AREA
1831 002704 012706 001100 MOV #CMTAG,R6 ;:FIRST LOCATION TO BE CLEARED
1832 002710 005026 CLR (R6)+ ;:CLEAR MEMORY LOCATION
1833 002712 022706 001140 CMP #SWR,R6 ;:DONE?
1834 002716 001374 BNE #-6 ;:LOOP BACK IF NO
1835 002720 012706 001100 MOV #STACK,SP ;:SETUP THE STACK POINTER
1836 ;:INITIALIZE A FEW VECTORS
1837 002724 012737 022134 000020 MOV #SCOPE,#IOTVEC ;:IOT VECTOR FOR SCOPE ROUTINE
1838 002732 012737 000340 000022 MOV #340,#IOTVEC+2 ;:LEVEL 7
1839 002740 012737 022406 000030 MOV #ERROR,#EMTVEC ;:EMT VECTOR FOR ERROR ROUTINE
1840 002746 012737 000340 000032 MOV #340,#EMTVEC+2 ;:LEVEL 7
1841 002754 012737 024672 000034 MOV #STRAP,#TRAPVEC ;:TRAP VECTOR FOR TRAP CALLS
1842 002762 012737 000340 000036 MOV #340,#TRAPVEC+2 ;:LEVEL 7
1843 002770 012737 024772 000024 MOV #SPWRDN,#PWRVEC ;:POWER FAILURE VECTOR
1844 002776 012737 000340 000026 MOV #340,#PWRVEC+2 ;:LEVEL 7
1845 003004 005037 001206 CLR $TIMES ;:INITIALIZE NUMBER OF ITERATIONS
1846 003010 005037 001210 CLR $ESCAPE ;:CLEAR THE ESCAPE ON ERROR ADDRESS
1847 003014 112737 000001 001115 MOVB #1,$ERMAX ;:ALLOW ONE ERROR PER TEST
1848 003022 012737 003022 001106 MOV #,$LPADR ;:INITIALIZE THE LOOP ADDRESS FOR SCOPE
1849 003030 012737 003030 001110 MOV #,$LPERR ;:SETUP THE ERROR LOOP ADDRESS
1850 ;:SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
1851 ;:EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
1852 003036 013746 000004 MOV #ERRVEC,-(SP) ;:SAVE ERROR VECTOR
1853 003042 012737 003076 000004 MOV #64$,#ERRVEC ;:SET UP ERROR VECTOR
1854 003050 012737 177570 001140 MOV #DSWR,SWR ;:SETUP FOR A HARDWARE SWICH REGISTER
1855 003056 012737 177570 001142 MOV #DISP,DISPLAY ;:AND A HARDWARE DISPLAY REGISTER
1856 003064 022777 177777 176046 CMP #-1,#SWR ;:TRY TO REFERENCE HARDWARE SWR
1857 003072 001012 BNE 66$ ;:BRANCH IF NO TIMEOUT TRAP OCCURED
1858 ;:AND THE HARDWARE SWR IS NOT = -1
1859 003074 000403 BR 65$ ;:BRANCH IF NO TIMEOUT
1860 003076 012716 003104 64$: MOV #65$,(SP) ;:SET UP FOR TRAP RETURN
1861 003102 000002 RTI
1862 003104 012737 000176 001140 65$: MOV #SWREG,SWR ;:POINT TO SOFTWARE SWR
1863 003112 012737 000174 001142 MOV #DISPREG,DISPLAY
1864 003120 012637 000004 66$: MOV (SP)+,#ERRVEC ;:RESTORE ERROR VECTOR
1865
1866 003124 023737 000042 000046 CMP #42,#46 ;ARE WE IN ACT11 AUTOMATIC MODE?
1867 003132 001416 BEQ 69$ ;YES, SKIP TITLE
1868 ;SBTTL TYPE PROGRAM NAME
1869 ;:TYPE THE NAME OF THE PROGRAM IF FIRST PASS

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1870 003134 005227 177777      INC      #-1      ;:FIRST TIME?
1871 003140 001043      BNE      67$      ;:BRANCH IF NO
1872 003142 104401 003200      TYPE     68$      ;:TYPE ASCIZ STRING
1873      .SBTTL  GET VALUE FOR SOFTWARE SWITCH REGISTER
1874 003146 005737 000042      TST     2#42     ;:ARE WE RUNNING UNDER XXDP/ACT?
1875 003152 001006      BNE      69$      ;:BRANCH IF YES
1876 003154 023727 001140 000176      CMP     SWR,#SWREG ;:SOFTWARE SWITCH REG SELECTED?
1877 003162 001005      BNE      70$      ;:BRANCH IF NO
1878 003164 104406      GTSWR                    ;:GET SOFT-SWR SETTINGS
1879 003166 000403      BR      70$
1880 003170 112737 000001 001134 69$:  MOVB   #1,$AUTOB ;:SET AUTO-MODE INDICATOR
1881 003176 70$:
1882 003176 000424      BR      67$      ;:GET OVER THE ASCIZ
1883      ;:68$: .ASCIZ <CRLF>/RK11 LOGIC TEST 2/<15><12>/MAINDEC-11-DZRKK-E/<CRLF>
1884      67$:
1885 003250      MOV     #DDPCH,RO
1886 003254 012701 001410      MOV     #-13,R1
1887 003260 005020      1$:   CLR     (R0)+
1888 003262 005201      INC     R1
1889 003264 001375      BNE     1$
1890 003266 005227 177777      INC     #-1      ;:FIRST START ?
1891 003272 001020      BNE     START1    ;:BR IF NOT
1892 003274 013746 000004      MOV     ERRVEC,-(SP) ;:SAVE ERROR VECTOR ADDRESS
1893 003300 012737 003314 000004      MOV     #2$,ERRVEC ;:NEW VECTOR ADDRESS
1894 003306 005737 177776      TST     PS        ;:SEE IF PROGRAM CAN REFERENCE THE
1895      ;:PROCESSOR STATUS WORD
1896 003312 000406      BR      3$        ;:BR IF REFERENCE DIDN'T CAUSE TRAP
1897 003314 012737 000140 001400 2$:  MOV     #140,RKPRI ;:SETUP INTERRUPTING PRIORITY TO VALUE
1898      ;:WHICH WILL ALLOW INTERRUPT ON AN LSI-1!
1899 003322 012716 003330      MOV     #3$, (SP) ;:SETUP RETURN ADDRESS
1900 003326 000002      RTI
1901 003330 012637 000004      3$:   MOV     (SP)+,ERRVEC ;:RESTORE THE ERROR VECTOR
1902
1903      ;:FIND OUT IF ACT11, 'XXDP' CHAIN OR DUMP MODE
1904
1905 003334 012700 001410      START1: MOV    #DDPCH,RO
1906 003340 012701 177766      MOV    #-12,R1    ;:CLEAR OUT DRIVE TABLE AREA
1907 003344 005020      1$:   CLR    (R0)+
1908 003346 005201      INC    R1
1909 003350 001375      BNE    1$
1910 003352 122737 000002 000041      CMPB   #2,41     ;:LOADED FROM AN RK05 ?
1911 003360 001166      BNE    ST2
1912 003362 013737 000040 001410      MOV    40,DDPCH  ;:GET DEVICE INDICATOR AND DRIVE ADDRESS JF
1913      ;:LOADING RK05
1914 003370 122737 000010 001410      CMPB   #10,DDPCH ;:VALID DRIVE NUMBER IN BYTE 40 ?
1915 003376 101002      BHI    2$
1916 003400 105037 001410      CLRB   DDPCH     ;:MUST BE DRIVE ZERO WHICH LOADED
1917      ;:THIS PROGRAM
1918 003404 005737 000042      2$:   TST    42
1919 003410 001432      BEQ    4$
1920 003412 005737 001410      TST    DDPCH
1921 003416 001002      BNE    3$
1922 003420 000137 004262      JMP    ST3       ;:RUNNING FROM AN RK05 ?
1923      ;:BR IF YES
1924 003424 104401 003432      3$:   JMP    ST3       ;:FIND OUT NUMBER OF DRIVES
1925 003430 000413      TYPE   65$      ;:TYPE ASCIZ STRING
      BP    64$      ;:GET OVER THE ASCIZ

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1926				::65\$: .ASCIZ <15><12>/NOT TESTING DRIVE /
1927	003460			64\$: CLR -(SP) ;CLEAR WORD ON STACK
1928	003460	005046		MOV B DDPCH,(SP) ;GET DRIVE ADDRESS
1929	003462	113716	001410	TYPOS ;TYPE THE ADDRESS
1930	003466	104403		.BYTE 1 ;ONLY 1 CHARACTER
1931	003470	001		.BYTE 0 ;SUPPRESS LEADING ZEROS
1932	003471	000		JMP ST3 ;GET NUMBER OF DRIVES
1933	003472	000137	004262	INC #-1 ;FIRST TIME THROUGH HERE ?
1934	003476	005227	177777	BNE ST2 ;BR IF NOT
1935	003502	001115		TYPE 67\$;TYPE ASCIZ STRING
1936	003504	104401	003512	BR 66\$;GET OVER THE ASCIZ
1937	003510	000411		::67\$: .ASCIZ <15><12>/TO TEST DRIVE /
1938				66\$: CLR -(SP) ;CLEAR WORD ON THE STACK
1939	003534			MOV B DDPCH,(SP) ;GET DRIVE ADDRESS
1940	003534	005046		TYPOS ;TYPE THE DRIVE ADDRESS
1941	003536	113716	001410	.BYTE 1 ;ONLY 1 CHARACTER
1942	003542	104403		.BYTE 0 ;SUPPRESS LEADING ZEROS
1943	003544	001		TYPE 69\$;TYPE ASCIZ STRING
1944	003545	000		BR 68\$;GET OVER THE ASCIZ
1945	003546	104401	003554	::69\$: .ASCIZ / HALT PROGRAM, REMOVE RKDP PACK AND REPLACE IT<<15><12>
1946	003552	000431		68\$: TYPE 71\$;TYPE ASCIZ STRING
1947				BR 70\$;GET OVER THE ASCIZ
1948	003636			::71\$: .ASCIZ /WITH A WORK PACK, CLEAR LOCATION 40, AND RESTART PROGRAM/
1949	003636	104401	003644	70\$:
1950	003642	000435		
1951				
1952	003736			
1953				
1954				;FIND OUT FROM USER WHICH DRIVES (LOGICAL ADDRESSES) ARE TO BE
1955				;TESTED (DRIVES TO BE TESTED ?). IN REPLY THE USER SHOULD TYPE IN THE
1956				;LOGICAL ADDRESSES SEPERATED BY COMMAS. THUS IF 2 DRIVES 0,1 ARE PRESENT:
1957				; 'DRIVS TO B TSTD?'
1958				; '0,1<CR>' A CAR. RET. SHOULD BE TYPED TO TERMINATE THE LIST.
1959	003736	012700	001412	ST2: MOV #DRIVS,R0
1960	003742	012701	177767	MOV #-11,R1
1961	003746	005020		13\$: CLR (R0)+
1962	003750	005201		INC R1
1963	003752	001375		BNE 13\$
1964	003754	104401	003762	TYPE 65\$;TYPE ASCIZ STRING
1965	003760	000415		BR 64\$;GET OVER THE ASCIZ
1966				::65\$: .ASCIZ <15><12>/DRIVES TO BE TESTED ?/<15><12>
1967	004014			64\$: RDLIN
1968	004014	104411		MOV (SP)+,R0 ;GET STARTING ADRES OF ASCII STRING
1969	004016	012600		MOV #-10,R1 ;SET UP COUNT
1970	004020	012701	177770	MOV (R0)+,R2 ;GET ASCII CHARACTER
1971	004024	112002		1\$: MOV B (R0)+,R2 ;MASK UNWANTED BITS
1972	004026	042702	177400	BIC #177400,R2
1973	004032	012703	001414	MOV #DRIVO,R3
1974	004036	012704	177770	MOV #-10,R4
1975	004042	012705	000060	MOV #60,R5
1976	004046	020502		2\$: CMP R5,R2 ;WAS THE TYPED IN CHARACTER
1977				;A NUMBER BETWEEN 0-7?
1978	004050	001414		BEG 3\$;YES, BRANCH
1979	004052	005205		INC R5 ;NO, INCREMENT
1980	004054	005723		TST (R3)+ ;INCREMENT POINTER TO DRV FLAG
1981	004056	005204		INC R4 ;CHARACTER THAT WAS INPUT


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2038 ;CHECK NUMBER OF DRIVES
2039 004262 012737 177777 001440 ST3: MOV #1,SIZYET ;CHECK FOR RKDSF LATER
2040 004270 012737 004442 000004 MOV #55,2#4 ;SET UP ADRES FOR TIME-OUT VECTOR
2041 004276 005777 175024 TST 2RKDS ;REFERENCE RKDS
2042 004302 005777 175032 TST 2RKDA ;REFERENCE RKDA
2043 004306 012737 004534 000004 MOV #BADTMO,2#4
2044 004314 104401 TYPE
2045 004316 001216 MSG1
2046 004320 012700 177770 MOV #10,R0 ;INITIALIZE COUNT FOR THE 8 DRIVES
2047 004324 005037 001412 CLR DRVS ;INITIALIZE # OF DRIVES PRESENT TO 0
2048 004330 005001 CLR R1 ;INITIALIZE ADDRESS TO DRIVE 0
2049 004332 005004 CLR R4
2050 004334 012702 001414 MOV #DRIVO,R2
2051 004340 010177 174774 15: MOV R1,2RKDA ;ADDRESS THE DRIVE
2052 004344 020177 174770 CMP R1,2RKDA ;CHECK, WAS IT ADDRESSED?
2053 004350 001405 BEQ 3$ ;YES
2054 004352 012703 004356 MOV #25,R3
2055 004356 004737 021022 25: JSR PC,↑YERM ;WHILE CHECKING NUMBER OF DRIVE
2056 ;UNDER NON-MANUAL MODE :-
2057 ;RKDA HAD TO BE ADRESED BUT
2058 ;IT WAS FOUND THAT THE DRIVE NO
2059 ;THAT WAS WRITTEN COULD NOT BE READ BACK
2060 ;CORRECTLY.
2061
2062 004362 000413 BR 4$
2063 004364 032777 000200 174734 35: BIT #200,2RKDS ;CHECK IF 'DRY' BIT IS SET. IF SET DRIVE IS
2064 ;PRESENT
2065 004372 001407 BEQ 4$
2066 004374 104401 TYPE
2067 004376 001213 $CRLF
2068 004400 005237 001412 INC DRVS ;IF PRESENT, INCREMENT # OF DRIVES
2069 004404 005212 INC (R2) ;SET UP FLAG INDICATING THIS DRIVE PRESENT
2070 004406 010446 MOV R4,-(SP)
2071 004410 104402 TYP0C
2072 004412 005722 45: TST (R2)+ ;SHIFT POINTER TO NXT DRIVE INDICATOR
2073 004414 062701 020000 ADD #20000,R1 ;SET UP ADDRESS FOR THE NEXT DRIVE
2074 004420 005204 INC R4 ;HAVE U CHECKED FOR ALL 8 DRIVES
2075 004422 005200 INC R0
2076 004424 001345 BNE 1$
2077 004426 005737 001412 TST DRVS
2078 004432 001011 BNE ST4
2079 004434 104401 TYPE
2080 004436 001236 MSG2
2081 004440 000406 BR ST4 ;GO CHECK THE DRIVE INDEPENDENT
2082 ;CONTROLLER LOGIC
2083 004442 011603 55: MOV (SP),R3 ;GET PC WHERE TIMEOUT OCCURED
2084 004444 022626 CMP (SP)↓,(SP)+ ;RESTORE STACK
2085 004446 062703 177776 ADD #-2,R3
2086 004452 004737 021022 JSR PC,↑YERM ;GO TYPE ERROR MESSAGE
2087 ;WHILE CHECKING FOR THE NUMBER OF
2088 ;DRIVES IN NON-MANUAL MODE:-
2089 ;RKDS AND RKDA HAD TO BE REFERENCED, TIMEOUT
2090 ;OCCURED ON REFERENCING.PC IN THE ERROR
2091 ;MESSAGE INDICATES WHERE THE TIMEOUT OCCURED:
2092
2093

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2094
2095
2096 004456 005037 001434      ST4:  CLR      T56FLG
2097 004462 005737 001412      TST      DRIVS
2098 004466 001004              BNE      1$
2099 004470 004737 021736      JSR      PC,WATIME
2100 004474 000137 020646      JMP      $EOP
2101 004500 012737 001414 001354 1$:  MOV      #DRIVO,DRVPTA
2102 004506 005037 001352      CLR      DRVDON      ;INITIALIZE THE NO. OF DRIVES
2103                          ;THAT HAVE BEEN CHECKED
2104 004512 005037 001350      CLR      DRIVAD      ;INITIALIZE DRIVE ADDRESS TO
2105                          ;THE FIRST DRIVE
2106 004516 012737 004534 000004  MOV      #BADTMO,2#4  ;SET TIME OUT VECTOR FOR UNEXPECTED
2107                          ;TIME OUTS
2108 004524 012777 004600 174650  MOV      #BADINT,2#KVEC ;SET UP RK11 INTERRUPT VECTOR FOR
2109                          ;UNEXPECTED INTERRUPTS FROM RK11
2110 004532 000465              BR       TST1        ;GO TO TEST 1
2111
2112
2113
2114
2115                          ;THIS ROUTINE HANDLES UNEXPECTED TIME OUTS
2116
2117 004534 011600      BADTMO: MOV      (SP),RO ;SAVE PC WHERE TIME OUT OCCURED
2118 004536 005740      TST      -(RO)
2119 004540 022626      CMP      (SP)+,(SP)+ ;RESTORE STACK POINTER
2120 004542 104401 004550      TYPE    65$        ;:TYPE ASCIZ STRING
2121 004546 000407      BR       64$        ;:GET OVER THE ASCIZ
2122      ;:65$: .ASCIZ <15><12>/TIMOUT,PC=/
2123      64$:
2124 004566              MOV      RO,-(SP)    ;SET UP FOR TYPING OUT PC
2125 004570 104402      TYPOC   ;GO TYPE OUT OCTAL PC
2126 004572 000000      HALT
2127 004574 000137 002636      JMP      2#START
2128
2129
2130
2131                          ;THIS ROUTINE HANDLES UNEXPECTED INTERRUPTS FROM RK11
2132                          ;SW 9 AND 10 FOR LOOPING ON ERROR
2133                          ;AND LOOPING ON TEST IN WHICH TIMEOUT
2134                          ;OCCURRED, ARE PROVIDED.
2135
2136 004600 011600      BADINT: MOV      (SP),RC ;SAVE PC WHERE INTERRUPT OCCURED
2137 004602 005740      TST      -(RO)
2138 004604 032777 020000 174326  BIT      #20000,2#SWR ;INHIBIT ERROR TYPEOUT"
2139 004612 001014              BNE      1$        ;YES, DON'T TYPE OUT
2140 004614 104401              TYPE
2141 004616 001213      $CRLF
2142 004620 104401              TYPE
2143 004622 026000      EM43              ;TYPE 'UNEXPEXTED RK11 INTERRUPT'
2144                          ;TYPE ' AT PC='
2145 004624 104401 004632      TYPE    65$        ;:TYPE ASCIZ STRING
2146 004630 000403      BR       64$        ;:GET OVER THE ASCIZ
2147      ;:65$: .ASCIZ /,PC=/
2148      64$:
2149 004640              MOV      RO,-(SP)    ;SET UP FOR TYPING OUT PC

```

```

2150 004642 104402          TYP0C          ;GO TYPE OCTAL PC WHERE BAD
2151                          ;INTERUPT OCCURED
2152 004644 032777 001000 174266 1$: BIT #1000,DSWR ;LOOP ON ERROR?
2153 004652 001403          BEQ 2$          ;NO BRANCH
2154 004654 022626          CMP (SP)+,(SP)+ ;YES REPOSITION STACK
2155 004656 000177 174224          JMP DS$LPADR    ;GO TO THE STARTING ADDRESS OF
2156                          ;THE TEST THAT GAVE UNEXPECTED INTERRUPT
2157 004662 032777 040000 174250 2$: BIT #40000,DSWR ;LOOP ON TEST?
2158 004670 001401          BEQ 3$          ;NO BRANCH
2159 004672 000002          RTI             ;YES, LOOP. GO BACK WHER U INTERRUPTED FROM.
2160 004674 000000          3$: HALT        ;UNEXPECTED INTERRUPT OCCURED AS
2161                          ;INDICATED IN THE TYPE OUT.U CAN LOOP
2162                          ;ON ERROR, TEST,OR INHIBIT TYPEOUT BY
2163                          ;SETTING APPROPRIATE SWITCHES.
2164 004676 000137 002636          JMP DS$START    ;GO BACK TO THE START OF THE
2165                          ;PROGRAM. THUS PRESSING CONTINUE
2166                          ;AFTER THE ABOVE HALT WILL
2167                          ;RESTART THE PROGRAM

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2171 ;RESTART AFTER POWER FAIL
2172 ;THE PROGRAM WOULD RESTART HERE IF POWER CAME BACK AFTER A FALIURE.

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2174 004702 004737 021736          PFSTRT: JSR PC,WATIME ;KILL TIME

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2178 ;*****
2179 ;*TEST 1 CHECK THAT THE DRIVES THAT ARE NOT SPECIFIED ARE NOT FOUND TO BE PRESENT
2180 ;*THIS TEST CHECKS THAT THE DRIVES THAT ARE NOT SPECIFIED
2181 ;*(IN RESPONSE TO "DRIVS TO BE YSTD") ARE NOT FOUND TO BE PRESENT.
2182 ;*EVERY DRIVE FROM 0 TO 7 IS ADDRESSED. IF A PARTICULAR DRIVE
2183 ;*GIVES 'DRY' (IN RKDS), IT IS CHECKED THAT THIS DRIVE
2184 ;*WAS SPECIFIED BY THE USER, IF IT WAS NOT AN ERROR IS
2185 ;*REPORTED, GIVING THE DRIVE NUMBER. IT IS LIKELY THAT THE USER
2186 ;*MAY HAVE FORGOTTEN TO PUT THE DRIVE (THAT IS NOT SPECIFIED) ON
2187 ;*'LOAD'. IF THIS IS THE CASE THEN PUT THIS DRIVE ON 'LOAD'.
2188 ;*IF THIS IS NOT THE CASE, THERE IS A GENUINE ERROR. (TWO DIFFERENT
2189 ;*DRIVE ADDRESSES MAY BE RESULTING IN THE SELECTION OF THE SAME
2190 ;*PHYSICAL DRIVE.)
2191 ;*****

```

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2192 004706 000004          †ST1: SCOPE
2193
2194 004710 012700 001414          MOV #DRIVO,R0 ;INITIALIZE POINTER
2195 004714 005001          CLR R1        ;INITIALIZE DRIVE ADRES 0
2196 004716 005002          CLR R2        ;INITIALIZE DRIVE # 0
2197 004720 005737 001410          1$: TST DDPCH ;LOADED FROM AN RK05 ?
2198 004724 001403          BEQ 2$        ;B IF NOT
2199 004726 120237 001410          CMPB R2,DDPCH ;LOADED FROM THIS DRIVE ?
2200 004732 001435          BEQ 4$        ;BR IF YES
2201 004734 010177 174400          2$: MOV R1,DRKDA ;ADRES THE DRIVE
2202 004740 105777 174362          TSTB DRKDS    ;DRIVE READY?
2203 004744 100005          BPL 3$        ;NO, THIS DRIVE NOT PRESENT
2204                          ;YES, THIS DRIVE SELECTED
2205 004746 005710          TST DR0       ;WAS THIS DRIVE SPECIFIED BY

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2206 ; THE USER?
2207 004750 001026 BNE 4$ ; YES, OK
2208 ; NO, THIS DRIVE # WAS NOT SPECIFIED
2209 ; BY THE USER, BUT STILL IS GIVING
2210 ; 'DRY' WHEN ADRESED. REPORT EROR.
2211 004752 010237 001162 MOV R2,$REGO ; GET DRIVE #
2212 004756 104116 ERROR 116 ; THIS DRIVE # WAS NOT SPECIFIED BY
2213 ; THE USER, BUT WHEN ADRESED GAVE
2214 ; 'DRY'. CHECK THAT THIS DRIVE # IF
2215 ; PHYSICALLY PRESENT IS ON 'LOAD'. IF
2216 ; THIS IS NOT THE CASE, THEN ONE DRIVE
2217 ; MAY BE GETTING SELECTED BY TWO DIFFERENT
2218 ; LOGICAL ADDRESSES.
2219 004760 005710 3$: TST 2R0 ; CHECK THAT THIS DRIVE WAS NOT INDICATED
2220 004762 001421 BEQ 4$ ; IF IT WAS, & IT IS NOT FOUND TO BE
2221 ; PRESENT (DRY CLEAR), REPORT ERROR.
2222 004764 004737 020770 JSR PC,GT4RG ; GET RKCS, ER, DS, DA
2223 004770 104010 ERROR 10 ; DRIVE # (AS IN RKDA) WAS INDICATED BY
2224 ; THE USER, BUT WAS NOT FOUND TO BE PRESENT.
2225 ; CHECK THAT THE ROTARY DRIVE SELECTION
2226 ; SWITCH ON THE MODULE IS SET TO THE RIGHT
2227 ; DRIVE #.
2228
2229 004772 005010 CLR 2R0 ; THIS DRIVE IS NOT FOUND TO BE PRESENT
2230 ; HENCE DROP IT FROM THE SELECTION TABLE.
2231 004774 010003 MOV R0,R3 ; DRIVE ADDR
2232 004776 162703 001414 SUB #DRIVO,R3 ; MINUS OFFSET FOR TABLE
2233 005002 042703 000003 BIC #3,R3 ; EVEN DRIVE OF PAIR
2234 005006 062703 001414 ADD #DRIVO,R3 ; POINT TO EVEN OF PAIR IF RK05 F
2235 005012 042723 100000 BIC #100000,(R3)+ ; NOT SPECIFIED AS F MODEL
2236 005016 042713 100000 BIC #100000,(R3) ; SAME
2237 005022 005337 001412 DEC DRIVS ; DECREMENT DRIVE COUNT
2238 005026 005202 4$: INC R2 ; INCRMNT DRIVE #
2239 005030 005720 TST (R0)+ ; INCRMNT POINTER
2240 005032 062701 020000 ADD #20000,R1 ; INCRMNT ADRES TO NXT DRIVE
2241 005036 001330 BNE 1$ ; LUP BAK IF NOT DONE
2242
2243
2244 ; THIS PART OF THE PROGRAM IS GOING TO BE REPEATED FOR
2245 ; EACH DRIVE PRESENT
2246
2247 ; 'DRIVAD' CONTAINS IN BITS 15,14,13 THE ADDRESS OF THE
2248 ; DRIVE BEING CURRENTLY CHECKED.
2249
2250 005040 NUDRV:
2251
2252
2253 ; *****
2254 ; *TEST 2 FIND OUT NEXT DRIVE TO BE CHECKED
2255 ; THIS CODE FINDS OUT THE NEXT DRIVE THAT IS PRESENT AND THEN SETS UP
2256 ; THE ADDRESS IN DRIVAD (BITS 13,14,15). THUS THROUGHOUT THE FOLLOWING TESTS
2257 ; THE DRIVE TESTED IS THE DRIVE WHOOSE ADDRESS IS IN 'DRIVAD'.
2258 ; *****
2259 005040 000004 †ST2: SCOPE
2260 005042 012737 000001 001206 MOV #1,$TIMES ; DO 1 ITERATION
2261 005050 012737 000002 001102 MOV #2,$STNM ; RESET POINTER TO THIS TEST

```



```

2318                                     ;THIS IS A VERY BASIC ERR& IF IT
2319                                     ;OCCURS GO BACK TO TEST 10
2320 005242 013700 001326                MOV    RKDS,RO
2321 005246 013777 001350 174064        MOV    DRIVAD,DRKDA
2322 005254 005710                       TST    DRD
2323 005256 001003                       BNE    IS
2324 005260 011037 001162                MOV    DRD,$REGD
2325 005264 104004                       ERROR  4
2326                                     ;RKDS ERROR! RKDS IF ADDRESSED
2327 005266 012777 000015 174036 1S:    MOV    #15,DRKCS
2328                                     ;CORRECTLY SHOULD BE NON-ZERO
2329 005274 005001                       CLR    R1
2330 005276 032710 010000 2S:           BIT    #10000,DRD
2331 005302 001003                       BNE    3S
2332 005304 005201                       INC    R1
2333 005306 001373                       BNE    2S
2334 005310 000403                       BR     4S-2
2335 005312 004737 020776 3S:         JSR    PC,GT3RG
2336 005316 104005                       ERROR  5
2337
2338
2339 005320 005001                       CLR    R1
2340 005322 032710 000100 4S:         BIT    #100,DRD
2341 005326 001010                       BNE    TST4
2342 005330 104417 000011               DELAY  11
2343 005334 005201                       INC    R1
2344 005336 001371                       BNE    4S
2345 005340 017737 173762 001162        MOV    DRKDS,$REGD
2346 005346 104016                       ERROR  16
2347
2348                                     ;R/W/S RDY DID NOT SET AFTER
2349                                     ;DRIVE RESET. DRIVE RESET WAS DONE
2350                                     ;TO CHECK 'DPL' BIT. THIS TEST
2351                                     ;IS NOT FOR CHECKING DRIVE RESET.
2352                                     ;YOU MIGHT WANT TO USE THE TEST PROVIDED
2353                                     ;FOR CHECKING DRIVE RESET.
2354
2355
2356 005350 000004
2357 005352 104413
2358
2359
2360
2361
2362
2363
2364
2365
2366
2367 005354 013777 001350 173756        MOV    DRIVAD,DRKDA
2368 005362 017700 173740                MOV    DRKDS,R1
2369 005366 032700 002000                BIT    #2000,RC
2370 005372 001403                       BEG    IS
2371 005374 004737 020776                JSR    PC,GT3RG
2372 005400 104006                       ERROR  6
2373

```

```

*****
*TEST 4 CHECK THAT 'DRIVE UNSAFE' IS CLEAR, 'HDEN' IS SET, 'WPS' IS CLEAR
*****

```

```

*ST4: SCPE
      CNT.RESET
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;SET DRIVE ADDRESS
;GET RKDS
;IS 'DRU' BIT OF RKDS SET?
;NO
;GO, GET RKCS, ER, DS
;DPL BIT OF RKDS IS SET, CHECK
;DRIV BY PUTTING RUN-LOAD SW TO LOAD

```

```

2374                                     ; THEN BACK TO RUN
2375 005402 032700 004000 15: BIT #4000,R0 ; IS 'MDEN' BIT SET?
2376 005406 001004 BNE 25 ; YES, BRANCH
2377 005410 017737 173712 001162 MOV #RKDS,$REGD ; GET RKDS
2378 005416 104007 ERROR 7 ; ERROR, 'RKDS' BIT IS NOT SET
2379
2380 005420 032777 000040 173700 25: BIT #40,RKDS ; IS 'WPS' CLEAR?
2381 005426 001403 BEQ *T5 ; YES, EXIT
2382 005430 004737 020770 JSR PC,GT4RG ; GET RKCS, ER, DS, DA
2383 005434 104114 ERROR 114 ; 'WPS'-WRITE PROTECT STATUS- BIT OF
2384 ; OF RKDS SHOULD BE CLEAR, IF THIS DRIVE
2385 ; IS WRITE ENABLED. CHECK & SEE IF THIS
2386 ; DRIVE IS WRITE ENABLED, IF IT IS NOT,
2387 ; WRITE ENABLE IT.
2388
2389
2390
2391
2392
2393
2394
2395
2396
2397
2398
2399
2400
2401
2402
2403
2404 005442 013777 001350 173670 MOV DRIVAD,ARKDA ; ADDR THE DRIVE
2405 005450 105777 173652 TSTB ARKDS ; IS 'DRY' SET?
2406 005454 100403 BMI T56 ; YES, OK
2407 005456 004737 020770 JSR PC,GT4RG ; GO, GET RKCS, ER, DS, DA
2408 005462 104010 ERROR 10 ; 'DRY' NOT SET
2409
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```

```

*****
;*TEST 5 CHECK THAT 'DRIVE READY' IS SET IN RKDS
*****

```

```

T5T5: SCOPE ; GO, DO CONTROL RESET
CNT.RESET ; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR & IF IT
; OCCURS GO BACK TO TEST 10
; ADDR THE DRIVE
; IS 'DRY' SET?
; YES, OK
; GO, GET RKCS, ER, DS, DA
; 'DRY' NOT SET

```

```

*****
;*TEST 6 CHECK THAT 'SOK' BIT CAN SET
; * THIS TEST CHECKS THAT WITHIN A CERTAIN TIME
; * 'SOK' BIT CAN SET, IF IT DOES NOT AN ERROR IS REPORTED
*****

```

```

T5T6: SCOPE ; ADDR THE DRIVE
MOV DRIVAD,ARKDA ; INITIALIZE COUNT FOR TIMING WAIT LOOP
CLR R1 ; IS SOK SET?
15: BIT #400,ARKDS ; EXIT
BNE T57 ; NO, WAIT
INC R1 ; WAITED LONG?
BNE 15 ; GET RKDS
MOV ARKDS,$REGD ; WAITED LONG BUT 'SEC OK' BIT DID NOT
ERROR 11 ; SET
;

```

```

*****
;*TEST 7 CHECK THAT 'SECTOR COUNTER' CAN COUNT FROM 0-13
*****

```

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2485

005522 000004
005524 104413

005526 013777 001350 173604
005534 013700 001326
005540 005037 001356
005544 005005
005546 012704 177764
005552 012703 000001
005556 005037 001360
005562 005237 001356
005566 001440
005570 005237 001360
005574 001441
005576 011001
005600 032701 000400
005604 001771
005606 021001
005610 001362
005612 042701 177760
005616 001357
005620 005204
005622 001447
005624 005205
005626 001431
005630 011002
005632 032702 000400

: * THIS TEST CHECKS THAT THE SECTOR COUNTER CAN COUNT FROM
: * 0-13
: * 1) FIRST, FOR INITIALIZING PURPOSES THERE IS A TIMED LOOP
: * DURING WHICH SECTOR COUNTER SHOULD COUNT DOWN TO 0. IF THIS
: * IS NOT DONE AN ERROR IS REPORTED
: * 2) AFTER A COUNT OF 0 IS REACHED, THE PROGRAM WAITS
: * FOR A CERTAIN TIME, DURING WHICH THE SEC COUNTER
: * IS SAMPLED. IF THE COUNTER DOES NOT CHANGE WITHIN THIS
: * TIME PERIOD AN ERROR IS REPORTED.
: * 3) UPON FINDING THAT THE COUNTER HAS CHANGED, IT IS CHECKED
: * IF IT INCREMENTED CORRECTLY. IF IT DID NOT AN ERROR IS REPORTED
: * 4) IF IT INCREMENTED CORRECTLY, THE PROGRAM AGAIN WAITS IN A
: * LOOP TILL THE COUNTER CHANGES. (STEPS 2,3,4 ARE REPEATED
: * TILL THE COUNTER COUNTS UP TO 13)

TST7: SCOPE
CNT.RESET

:GO, DO CONTROL RESET
:THIS IS A CALL FOR THE 'CNTRL-
:RESET' ROUTINE. A CONTROL RESET IS
:ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET
:AN ERROR IS REPORTED. NOTE THAT
:THE PC IN ERROR MESSAGE IS THE
:PC WHERE 'CNT.RESET' IS LOCATED.
:THIS IS A VERY BASIC ERR & IF IT
:OCCURS GO BACK TO TEST 10

:INITIALIZE
:'COUNT' - TO TIME 'ERROR 35'
:INITIALIZE 'COUNT' - TO TIME
:'ERROR 36' (WAIT LOOP)
:INITIALIZE 'COUNT' - FOR THE 12 SECTORS.
:R3 CONTAINS THE 'NEXT' COUNT OF SEC-CNTR
:R1 CONTAINS THE 'PREVIOUS' COUNT OF SEC-CNTR
:R2 CONTAINS THE 'PRESENT' COUNT OF SEC-CNTR
:INITIALIZE 'COUNT' - TO TIME
:(WAIT LOOP) 'ERROR 34'
:KEEP TIMING FOR 'ERROR 35'
:BRANCH & REPORT ERROR IF WAITED LONG'
:KEEP TIMING FOR 'ERROR 34'
:BRANCH & REPORT ERROR IF WAITED LONG'

:GET RKDS
:IS 'SOK' SET?
:NO, WAIT FOR IT TO SET
:MAKE SURE THAT 2 CONSECUTIVE
:READINGS OF SEC-CNTR ARE SAME
:YES, MASK OUT NON-SEC CNTR BITS
:IS IT SECTOR 0, IF NOT LOOP BACK &
:WAIT FOR SECTOR 0
:KEEP TRACK OF SECTORS CHECKED
:EXIT IF ALL SECTORS CHKD
:KEEP TIMING FOR 'ERROR 36'
:BR & REPORT ERROR IF WAITED LONG
:GET RKDS
:IS SOK SET'

1\$:
2\$:
3\$:
4\$:

MOV DRIVAD, DRKDA
MOV RKDS, R0
CLR INDX1
CLR R5
MOV #14, R4
MOV #1, R3
CLR INDX2
INC INDX1
BEQ 6\$
INC INDX2
BEQ 7\$
MOV @R0, R1
BIT #400, R1
BEQ 2\$
CMP @R0, R1
BNE 1\$
BIC #177760, R1
BNE !\$
INC R4
BEQ TST10
INC R5
BEQ 8\$
MOV @R0, R2
BIT #400, R2

```

2486 005636 001772 BEQ 4$ ;NO WAIT FOR SOK
2487 005640 021002 CMP JRO,R2 ;MAKE SURE THAT 2 CONSECUTIVE
2488 005642 001370 BNE 4$ ;READINGS OF SEC-CNTR ARE SAME
2489 005644 042702 177760 BIC #177760,R2 ;MASK NON-SEC-CNTR BITS
2490 005650 020201 CMP R2,R1 ;HAS SEC CNTR INCREMENTED?
2491 005652 001764 BEQ 4$ ;NO, WAIT FOR IT TO CHANGE
2492 005654 020203 CMP R2,R3 ;YES, DID IT INCREMENT CORRECTLY?
2493 005656 001023 BNE 9$ ;NO - REPORT ERROR
2494
2495 005660 005203 5$: INC R3 ;INCREMENT "NEXT COUNT"
2496 005662 005201 INC R1 ;INCREMENT "PREVIOUS COUNT"
2497 005664 005005 CLR R5 ;INITIALIZE AGAIN FOR TIMING 'ERROR 36'
2498 005666 000754 BR 3$ ;GO & CHECK THE NEXT SECTOR COUNT
2499
2500 005670 010137 001162 6$: MOV R1,$REG0 ;GET 'SEC CNTR'
2501 005674 104012 ERROR 12 ;WAITED LONG, BUT SECTOR COUNTER
2502 ;DID NOT COUNT TO 0
2503 005676 000421 BR TST10 ;EXIT
2504
2505 005700 017737 173422 001162 7$: MOV JAKDS,$REG0 ;GET RKDS
2506 005706 104011 ERROR 11 ;WAITED LONG, BUT 'SOK' BIT DID
2507 ;NOT SET
2508 005710 000414 BR TST10 ;EXIT
2509
2510 005712 010237 001162 8$: MOV R2,$REG0 ;GET SEC CNTR (PRESENT COUNT)
2511 005716 010337 001164 MOV R3,$REG1 ;GET "NEXT COUNT"
2512 005722 104013 ERROR 13 ;WAITED LONG, BUT THE SECTOR
2513 ;COUNTER DID NOT INCREMENT FROM
2514 ;THE PRESENT COUNT TO THE NEXT COUNT
2515 005724 000406 BR TST10 ;EXIT
2516
2517 005726 010337 001162 9$: MOV R3,$REG0 ;GET 'NEXT COUNT' (SEC CNTR SHOULD BE THIS)
2518 005732 010237 001164 MOV R2,$REG1 ;GET PRESENT COUNT (WHAT SEC CNTR WAS)
2519 005736 104014 ERROR 14 ;SEC CNTR INCREMENTED WRONG, DID
2520 ;NOT INCREMENT FROM PRESENT COUNT
2521 ;TO NEXT COUNT
2522 005740 000747 BR 5$
2523 ;
2524
2525 ;*****
2526 ;*TEST 10 CHECK THAT SC=SA CAN BE GENERATED
2527 ;* THIS TEST CHECKS THAT SC=SA CAN BE GENERATED FOR
2528 ;* EVERY SECTOR
2529 ;*****
2530 005742 000004 †TST10: SCOPE
2531 005744 104413 CNT.RESET ;GO, DO CONTROL RESET
2532 ;THIS IS A CALL FOR THE 'CNTRL-
2533 ;RESET' ROUTINE. A CONTROL RESET IS
2534 ;ISSUED AND AFTER A CERTAIN TIME
2535 ;IF THE 'CNTRL RDY' DOES NOT SET
2536 ;AN ERROR IS REPORTED. NOTE THAT
2537 ;THE PC IN ERROR MESSAGE IS THE
2538 ;PC WHERE 'CNT.RESET' IS LOCATED.
2539 ;THIS IS A VERY BASIC ERR & IF IT
2540 ;OCCURS GO BACK TO TEST 10
2541 005746 013704 001350 MOV DRIVAD,R4

```



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2599
2600
2601
2602
2603
2604
2605
2606 006110 013700 001332      MOV      RKCS,R0
2607 006114 005004              CLR      R4
2608 006116 013777 00135C 173214  MOV      DRIVAD,DRKDA
2609 006124 012710 000015      MOV      #15,DR0
2610 006130 104412      CHKCRDY
2611
2612 006132 104021      ERROR   21
2613
2614
2615
2616 006134 012705 177776 25:      MOV      #-2,R5
2617 006140 032777 000100 173160 65:      BIT      #100,DRKDS
2618 006146 001402              BEQ
2619 006150 000137 006172              JMP      3$
2620 006154 005204              INC      R4
2621 006156 001370              BNE     6$
2622 006160 005205              INC      R5
2623 006162 001366              BNE     6$
2624 006154 004737 020770      JSR      PC,GT4RG
2625 006170 104026      ERROR   26
2626
2627
2628 006172 032777 001000 173126 3$:      BIT      #1000,DRKDS
2629 006200 001403              BEQ      5$
2630 006202 004737 020770      JSR      PC,GT4RG
2631 006206 104001      ERROR   1
2632
2633 006210 032710 140000 5$:      BIT      #140000,DR0
2634 006214 001403              BEQ      4$
2635 006216 004737 020770      JSR      PC,GT4RG
2636 006222 104022      ERROR   22
2637
2638 006224 022710 000214 4$:      CMP      #214,DR0
2639
2640 006230 001406              BEQ      TST13
2641 006232 012737 000214 001162      MOV      #214,$REG0
2642 006240 011037 001164      MOV      DR0,$REG1
2643 006244 104024      ERROR   24
2644
2645
2646
2647
2648
2649
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2651
2652
2653 00624E 000004      TST13:  SCOPE

```

```

;RESET ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;INITIALIZ COUNT - TO TIME ERROR
;ADDRESS THE DRIVE
;'DRIVE RESET' GO
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;CNTRL RDY DID NOT SET AFTER
;SENDING CYL ADDR TO THE DRIV.
;'ADD ACK' SHOULD HAVE COME BACK
;FROM DRIVE. THEREUPON SETTING 'CN RDY'
;SET UP DELAY COUNTER
;CHECK FOR R/W/S READY
;GO, GET RKCS, ER, DS, DA
;R/W/S RDY DID NOT SET AFTER
;DRIVE RESET
;DID SIN SET?
;NO, BRANCH
;GO, GET RKCS, ER, DS, DA
;SIN SET, AFTER A
;DRIVE RESET.
;WAS 'ERR' BIT OR 'HE' BIT SET?
;NO
;GO, GET RKCS, ER, DS, DA
;'ERR' OR 'HE' BIT SET WHILE DOING
;DRIVE RESET
;DOES RKCS STILL CONTAIN THE
;'DRIV RES' BITS
;YES, EXIT
;GET EXPCTD RKCS
;GET RKCS, RECVD
;NO - RKCS SHOULD CONTAIN THE 'DRIV RES'
;FUNCTION, ERROR IF DIFFERENT.

```

```

*****
;TEST 13 CHECK 'SEEK' TO CYLINDER 0
; * THIS TEST CHECKS THE SEEK LOGIC DOING SEEK TO CYLINDER 0.
; * NOTE THAT SINCE THE HEADS ARE ALREADY ON CYLINDER 0, NO
; * HEAD MOVEMENT IS INVOLVED AND THE STRESS IS ON THE BASIC SEEK
; * LOGIC.
*****

```

```

2654 006250 104413          CNT.RESET          ;GO, DO CONTROL RESET
2655                                     ;THIS IS A CALL FOR THE 'CNTRL-
2656                                     ;RESET' ROUTINE. A CONTROL RESET IS
2657                                     ;ISSUED AND AFTER A CERTAIN TIME
2658                                     ;IF THE 'CNTRL RDY' DOES NOT SET
2659                                     ;AN ERROR IS REPORTED. NOTE THAT
2660                                     ;THE PC IN ERROR MESSAGE IS THE
2661                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
2662                                     ;THIS IS A VERY BASIC ERR & IF IT
2663                                     ;OCCURS GO BACK TO TEST 10
2664 006252 104421          TST.SIN          ;GO CHECK IF SIN SET. IF SET
2665                                     ;A DO DRIVE RESET TO CLEAR IT
2666 006254 013700 001332    MOV          RKCS,RO
2667 006260 013777 001350 173052 MOV          DRIVAD,ARKDA ;ADDRESS THE DRIVE
2668                                     ;
2669 006266 012710 000011    MOV          #11,ARD
2670 006272 104412          CHKCRDY
2671                                     ;'SEEK' GO
2672 006274 104021          ERROR          21 ;GO CHECK IF CONTROL RDY IS SET
2673                                     ;IF SO, SKIP THE EROR MESSAGE.
2674                                     ;'CNTRL RDY' DID NOT SET AFTER SENDING
2675                                     ;CYL ADDR TO THE DRIVE, 'ADD ACK'
2676                                     ;SHOULD HAVE COME BACK FROM THE
2677                                     ;DRIVE, THEREUPON SETTING 'CNTRL RDY'
2676 006276 005005          CLR          RS
2677 006300 032777 000100 173020 BIT          #100,ARKDS ;DID R/W/S RDY BIT SET?
2678 006306 001005          BNE          3$ ;YES, BRANCH
2679 006310 005205          INC          RS ;WAITED LONG ENOUGH?
2680 006312 001372          BNE          2$+2 ;IF NOT, LUP BAK & WAIT
2681 006314 004737 020770 JSR          PC,GT4RG ;GO, GET RKCS, ER, DS, DA
2682 006320 104026          ERROR          26 ;R/W/S RDY DID NOT SET AFTER SEEK
2683 006322 032777 001000 172776 BIT          #1000,ARKDS ;DID SIN SET?
2684 006330 001403          BEQ          6$ ;NO, BRANCH
2685 006332 004737 020770 JSR          PC,GT4RG ;GO, GET RKCS, ER, DS, DA
2686 006336 104001          ERROR          1 ;SIN SET ON DOING SEEK
2687                                     ;TO CYL 0 NOTE THIS IS THE
2688                                     ;FIRST TIME THE HEADS HAVE
2689                                     ;BEEN MOVED
2690
2691 006340 032710 140000    BIT          #140000,ARD ;WAS 'ERR' OR 'HE' BIT SET?
2692 006344 001403          BEQ          4$
2693
2694 006346 004737 020770 JSR          PC,GT4RG ;GO, GET RKCS, ER, DS, DA
2695 006352 104022          ERROR          22 ;'ERR' OR 'HE' BIT SET WHILE DOING 'SEEK'
2696
2697 006354 005777 172750    TST          ARKER ;WAS ANY BIT IN RKER SET?
2698 006360 001403          BEQ          5$ ;NO
2699 006362 004737 020776 JSR          PC,GT3RG ;GO, GET RKCS, ER, DS
2700 006366 104023          ERROR          23 ;RKER SHOWS AN ERROR BIT, CHECK
2701
2702 006370 022710 000210    CMP          #210,ARD ;DOES RKCS STILL CONTAIN 'SEEK' FUNCTION
2703 006374 001406          BEQ          TST14 ;: YES, EXIT
2704 006376 012737 000210 001162 MOV          #210,$REG0 ;GET EXPCTD RKCS
2705 006404 011037 001164 MOV          ARD,$REG1 ;GET RKCS RECVD
2706 006410 104024          ERROR          24 ;NO, RKCS SHOULD BE STILL CONTAINING
2707                                     ;'SEEK' FUNCTION ERROR - IF IT CHANGED
2708
2709 ;:*****

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2719 006412 000004
2720 006414 104413
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2729
2730 006416 104421
2731
2732 006420 004737 021500
2733 006424 104026
2734
2735 006426 005005
2736 006430 013777 001350 172702
2737 006436 052777 000100 172674
2738 006444 013701 001326
2739 006450 012777 000011 172654
2740 006456 032711 000100
2741 006462 001405
2742 006464 005205
2743 006466 100373
2744 006470 004737 020776
2745 006474 104025
2746
2747
2748 006476 004737 021432
2749 006502 104016
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;*TEST 14 CHECK R/W/S RDY IS CLEAR WHEN HEADS ARE IN MOTION
;*THIS TEST CHECKS THAT R/W/S DOES GET CLEARED
;*WHEN THE HEADS ARE IN MOTION. SINCE 'MOVE L' ON
;*M7700 (RK05) GENERATES THIS SIGNAL, ABSENCE OF
;*R/W/S RDY-CLEAR COULD MEAN A FAULT ON M7702
;*WHERE 'MOVE L' IS GENERATED.
;*NOTE THIS IS THE FIRST TIME HEADS ARE MADE 'TO MOVE BY SEEKING
;*TO CYLINDER 2.

::*****

TST14: SCOPE
CNT.RESET

;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET DO DRV-RESET TO CLR IT
;MAKE SURE HEADS R ON CYL 0
;R/W/S RDY DIDN'T SET
;AFTER THE ABOVE DRV RESET

TST.SIN

JSR PC,DRESET
ERROR 26
CLR R5
MOV DRIVAD,DRKDA
BIS #100,DRKDA
MOV RKDS,R1
MOV #11,DRKCS
1\$: BIT #100,DR1
BEQ 2\$
INC R5
BPL 1\$
JSR PC,GT3RG
ERROR 25

;SEEK CYLINDER 2
;SEEK, GO
;DID R/W/S RDY CLR?
;YES, BRANCH

1\$:

;R/W/S RDY WAS NOT CLEAR WHEN HEADS
;WERE SEEKING TO CYLINDER 2

2\$:

JSR PC,TSTRWS
ERROR 16

;GO, WAIT FOR R/W/S RDY TO SET
;R/W/S RDY DID NOT SET AFTER SEEK
;WAS TRIED TO CYLINDER 2 (ABOVE).
;NOTE THIS WAS THE FIRST TIME A SEEK
;WAS TRIED TO A CYLINDER OTHER THAN
;0.

::*****

;*TEST 15 CHECK 'WRITE' FORMAT FUNCTION-CYLINDER 0, SECTOR 0
;*THIS TEST CHECKS THE LOGIC INVOLVED IN THE WRITE FMT
;*FUNCTION. ON ISSUING A WRT FMT, THE FOLLOWING IS CHECKED
;*1) CNTRL RDY WAS CLEARED AS GO WAS SET.
;*2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION OF FUNCTION
;*3) IF 'HE' OR 'ERR' BIT SET?
;*4) IF RKDA INCREMENTED CORRECTLY FROM 0 TO 1'


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2773 006504 000004
2774 006506 104413
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2784 006510 104421
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2786 006512 012703 033336
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2800 006516 012700 000001
2801
2802 006522 010023
2803 006524 010013
2804 006526 005423
2805 006530 005200
2806 006532 022700 000200
2807 006536 001371
2808 006540 005023
2809 006542 012713 125252
2810
2811 006546 012703 033336
2812 006552 013701 001332
2813 006556 013702 001336
2814 006562 010312
2815 006564 012777 177400 172542
2816 006572 013777 001350 172540
2817 006600 012711 002003
2818
2819 006604 105711
2820 006606 100003
2821 006610 004737 020776

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```

;*5) IF RKWC OVERFLOWED CORRECTLY TO 0?
;*6) IF RKBA INCREMENTED CORRECTLY BY 2?
;*7) IF ANY BIT IN RKER SET?
;*8) IF THE 'WRT FMT' FUNCTION BITS ARE STILL IN THE RKCS?
;*NOTE THAT ONE WORD '125252' WAS WRITTEN ON SECTOR
;*0 & IT WILL BE CHECKED IN THE NEXT TESTS.
*****
TST15: SCOPE
CNT.RESET
GO, DO CONTROL RESET
THIS IS A CALL FOR THE 'CNTRL-
RESET' ROUTINE. A CONTROL RESET IS
ISSUED AND AFTER A CERTAIN TIME
IF THE 'CNTRL RDY' DOES NOT SET
AN ERROR IS REPORTED. NOTE THAT
THE PC IN ERROR MESSAGE IS THE
PC WHERE 'CNT.RESET' IS LOCATED.
THIS IS A VERY BASIC ERR & IF IT
OCCURS GO BACK TO TEST 10
GO CHECK IF SIN IS SET
IF SET, DO DRIVE RESET TO CLR IT

TST.SIN
MOV #OUTBUF,R3
THIS CODE SETS UP A 256 WORD BUFFER
WHICH WILL BE USED TO WRITE 1 SECTOR
ON THE DISK
1ST WORD 000001
2ND WORD 177777 2'S COMPLEMENT
3RD WORD 000002 OF ABOVE
4TH WORD 177776
253RD WORD 000177
254TH WORD 177601
255TH WORD 000000
256TH WORD 125252

MOV #1,R0 ;SET COUNT
9$: MOV R0,(R3)+ ;SET UP DATA WORDS
MOV R0,(R3)
NEG (R3)+
INC R0
CMP #200,R0 ;DONE?
BNE 9$
CLR (R3)+ ;SET 255TH WORD TO 0
MOV #125252,R3 ;SET 256TH WORD

MOV #OUTBUF,R3 ;RESET POINTER TO OUTBUF
MOV RKCS,R1
MOV RKBA,R2
MOV R3,R2
MOV #-400,RKWC ;FROM HERE-SET UP CURRENT ADDRESS
MOV DRIVAD,RKDA ;SET UP WORD COUNT 400 WORDS
MOV #2003,R1 ;SET UP DISK ADDR, SECTOR 0, CYLINDER 0
WRITE FORMAT, GO

1$: TSTB R1 ;WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
BPL 2$ ;YES, BRANCH
JSR PC,GT3RG ;GO, GET RKCS, ER, DS

```

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2822 006614 104030          ERROR 30          : 'CNTRL RDY' DIDN'T CLEAR AS GO
2823                                     : WAS SET TO 'WRITE FORMAT'
2824 006616 005000          2$: CLR          RO          : WAS 'CNTRL RDY' SET ON COMPLETION OF WRITE?
2825 006620 105711          TSTB         JR1         : YES, BRANCH
2826 006622 100411          BMI          3$         : NO, HAVE U WAITED LONG ENOUGH?
2827 006624 005200          INC          RO          : IF NOT, LOOP BACK & WAIT
2828 006626 001374          BNE         2$+2        : IF YES, REPORT ERROR
2829                                     : GO, GET RKCS, ER, DS, DA
2830 006630 004737 020770    JSR          PC,GT4RG
2831 006634 013737 001350 001202 MOV          DRIVAD,$REGIO
2832 006642 104416          BRKDA4
2833                                     : GO TO 'BD44' & BREAK CONTENTS OF
2834 006644 104031          ERROR 31          : $REGIO INTO DR #, CYL, SUR, SEC BITS
2835                                     : 'CNTRL RDY' DIDN'T SET ON COMPLETION
2836                                     : OF WRITE FORMAT
2837                                     : WRT FMT WAS DONE STARTING AT <DSK-ADRES>
2838 006646 004737 021230    3$: JSR          PC,CHKHE : INDICATED IN EROR MSGE.
2839                                     : GO CHECK IF 'HE' OR 'ERR' BIT SET,
2840                                     : IF YES, SAVE RKCS, ER, DS, DA.
2841 006652 104032          ERROR 32          : RETURN HERE IF ERROR.
2842                                     : 'HE' OR 'ERR' BIT SET WHILE DOING
2843                                     : A WRITE FORMAT
2844                                     : WRT FMT WAS DONE STARTING AT <DSK-ADRES>
2845 006654 004737 021256    4$: JSR          PC,CHKDA : INDICATED IN EROR MSGE.
2846                                     : GO CHECK IF RKDA INCREMENTED CORRECTLY
2847 006660 104033          ERROR 33          : IF NOT, RETURN HERE.
2848                                     : RKDA SHOULD HAVE INCREMENTED BY
2849 006662 004737 021312    5$: JSR          PC,CHKWC : 1 SECTOR, IT DID NOT
2850                                     : CHECK IF WORD COUNT OVERFLOWED. IF
2851 006666 104034          ERROR 34          : NOT RETURN HERE.
2852                                     : RKWC DID NOT OVERFLOW TO 0, AFTER
2853 006670 022712 034336    6$: CMP          #OUTBUF+1000, JR2 : XFER ON WRITE FORMAT
2854 006674 001406          BEQ          7$         : DID RKBA INCREMENT CORRECTLY?
2855 006676 012737 034336 001162 MOV          #OUTBUF+1000, $REGO : YES, BRANCH
2856 006704 011237 001164    MOV          JR2, $REGI : GET EXPCTD RKBA
2857 006710 104035          ERROR 35          : GET ACTUAL RKBA
2858                                     : RKBA DIDN'T INCREMENT BY 1000 AFTER
2859 006712 004737 021336    7$: JSR          PC,CHKER : WRITE FORMAT OF 400 WORDS
2860                                     : CHECK IOF ANY BIT IN RKER SET.
2861 006716 104036          ERROR 36          : IF YES RETURN HERE.
2862                                     : RKER BIT SET ON DOING 1 WORD
2863 006720 022711 002202    8$: CMP          #2202, JR1 : WRITE FORMAT
2864 006724 001406          BEQ          TST16      : DOES RKCS STILL HAVE 'WRT FMT' BITS?
2865 006726 012737 002202 001162 MOV          #2202, $REGO : YES, EXIT
2866 006734 011137 001164    MOV          JR1, $REGI : GET EXPCTD RKCS
2867 006740 104024          ERROR 24          : GET ACTUAL RKCS
2868                                     : RKCS DIDN'T CONTAIN 'WRT FMT' BITS
2869                                     : AFTER THE FUNCTION WAS COMPLETED

```

```

2870 ; *****
2871 ; *EST 16 CHECK 'READ FORMAT' FUNCTION-CYLINDER 0, SECTOR 0
2872 ; *THIS TEST CHECKS THE LOGIC INVOLVED IN THE WRITE FMT
2873 ; *FUNCTION. ON ISSUING A WRT FMT, THE FOLLOWING IS CHECKED
2874 ; *1) CNTRL RDY WAS CLEARED AS GO WAS SET.
2875 ; *2) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION OF FUNCTION
2876 ; *3) IF 'HE' OR 'ERR' BIT SET?
2877 ; *4) IF RKDA INCREMENTED CORRECTLY FROM 0 TO 1?

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2891 006742 000004
2892 006744 005000
2893 006746 104413
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2903 006750 104421
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2905 006752 013701 001332
2906 006756 013702 001336
2907 006762 012703 033336
2908 006766 010312
2909
2910 006770 012777 177777 172336
2911 006776 013777 001350 172334
2912 007004 012711 002005
2913
2914 007010 105711
2915 007012 100003
2916 007014 004737 020776
2917 007020 104030
2918
2919 007022 005000
2920 007024 105711
2921
2922 007026 100411
2923 007030 005200
2924 007032 001374
2925
2926 007034 004737 020770
2927 007040 013737 001350 001202
2928 007046 104416
2929
2930 007050 104045
2931
2932
2933

```

```

;*5) IF RKWC OVERFLOWED CORRECTLY TO 0?
;*6) IF RKBA INCREMENTED CORRECTLY BY 2?
;*7) IF ANY BIT IN RKER SET?
;*8) IF THE CORRECT HEADER WAS RECEIVED?
;*9) FOR RK11C, AFTER RD FMT RKDB CONTAINS THE CHECKSUM
*FOR THAT SECTOR. (125252 IN THIS CASE, BECAUSE THE
*FIRST WORD IN SEC 0 WAS WRITTEN AS 125252 IN
*THE PREVIOUS TEST)
;*10) FOR RK11D, AFTER RD FMT RKDB SHOULD CONTAIN
*A ZERO
;*11) IF THE RD FMT FUNCTION BITS ARE STILL IN
*THE RKCS?

```

```

↑TST16: SCOPE
CLR RO
CNT.RESET

```

```

;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET, DO DRIVE RESET TO CLR IT

```

TST.SIN

```

MOV RKCS,R1
MOV RKBA,R2
MOV #OUTBUF,R3
MOV R3,R2
MOV #-1,DRKWC
MOV DRIVAD,DRKDA
MOV #2005,DR1

```

```

;SETUP ADRS WHERE HEADER WORD IS TO BE
;X-FERRED
;SET UP WORD COUNT
;SET UP DISK ADRS. SECTOR 0, CYLINDER 0
;READ FORMAT, GO

```

```

15: TST DR1
BPL 25
JSR PC,GT3RG
ERROR 30

```

```

;WAS 'CNTRL RDY' CLEARED AS GO WAS SET?
;YES, BRANCH
;GO, GET RKCS, RKER
;CNTRL RDY DIDN'T CLEAR AS GO WAS
;SET TO 'READ FORMAT'

```

```

25: CLR RO
TSTB DR1

```

```

;WAS 'CNTRL RDY' SET ON COMPLETION OF
;TRANSFER
;YES, BRANCH
;NO, HAVE U WAITED LONG ENOUGH?
;IF NOT, LOOP BACK & WAIT
;IF YES, REPORT ERROR
;GO, GET RKCS, ER, DS,DA

```

```

BMI 35
INC RO
BNE 25+2
JSR PC,GT4RG
MOV DRIVAD,$REGIO
BRKDA4

```

```

;GO TO 'BD44' & BREAK CONTENTS OF
;$REGIO INTO DR #,CYL,SUR,SEC BITS
;'CNTRL RDY' DIDN'T SET ON COMPLETION
;OF READ FORMAT
;READ FMT WAS DONE STARTING AT (DSK-ADRES)
;INDICATED IN EROR MESGE

```

ERROR 45


```

2990 ;*READ FROM SECTOR 0
2991 ;*9) IF THE 'READ' FUNCTION BITS ARE STILL IN RKCS
2992 ;*****
2993 007170 000004 TST17: SCOPE
2994 007172 104413 CNT.RESET
2995 ;GO, DO CONTROL RESET
2996 ;THIS IS A CALL FOR THE 'CNTRL-
2997 ;RESET' ROUTINE. A CONTROL RESET IS
2998 ;ISSUED AND AFTER A CERTAIN TIME
2999 ;IF THE 'CNTRL RDY' DOES NOT SET
3000 ;AN ERROR IS REPORTED. NOTE THAT
3001 ;THE PC IN ERROR MESSAGE IS THE
3002 ;PC WHERE 'CNT.RESET' IS LOCATED.
3003 ;THIS IS A VERY BASIC ERR & IF IT
3004 ;OCCURS GO BACK TO TEST 10
3004 007174 104421 TST.SIN ;GO CHECK IF SIN IS SET
3005 ;IF SET, DO DRIVE RESET TO CLR IT
3006 007176 013701 001332 MOV RKCS,R1
3007 007202 005000 CLR R0
3008 007204 013702 001336 MOV RKBA,R2
3009 007210 012703 033336 MOV #OUTBUF,R3
3010 007214 010312 MOV R3,R2 ;SET UP ADDRS WHERE DATA WORD IS
3011 ;TO BE X-FERRED
3012 007216 012777 177400 172110 MOV #-400,DRKWC ;SET UP WORD COUNT
3013 007224 013777 001350 172106 MOV DRIVAD,DRKDA ;SET UP DISK ADRS, SECTOR 0, CYLINDER 0
3014 007232 012711 000005 MOV #5,R1 ;READ, GO
3015
3016 007236 105711 15: TSTB R1 ;WAS 'CNTRL RDY' CLEARED AS GC WAS SET?
3017 007240 100003 BPL 25 ;YES, BRANCH
3018 007242 004737 020776 JSR PC,GT3RG ;GO, GET RKCS, ER
3019 007246 104030 ERROR 30 ;CNTRL RDY DID NOT CLEAR AS GO
3020 ;WAS SET TO 'READ'
3021 007250 005000 25: CLR R0
3022 007252 105711 TSTB R1 ;WAS CNTRL RDY SET ON COMPLETION
3023 ;OF TRANSFER?
3024 007254 100411 BMI 35 ;YES, BRANCH
3025 007256 005200 INC R0 ;NO, HAVE U WAITED LONG ENOUGH?
3026 007260 001374 BNE 25+2 ;IF NOT, LOOP BACK & WAIT
3027 ;IF YES, REPORT ERROR
3028 007262 004737 020770 JSR PC,GT4RG ;GO, GET RKCS, ER, DS,DA
3029 007266 013737 001350 001202 MOV DRIVAD,$REG10
3030 007274 104416 BRKDA4 ;GO TO 'BDAY' & BREAK CONTENTS OF
3031 ;$REG10 INTO DR #,CYL,SUR,SEC BITS
3032 007276 104045 ERROR 45 ;CNTRL RDY DID NOT SET ON
3033 ;COMPLETION OF READ
3034 ;READ WAS DONE STARTING AT DSK-ADRES
3035 ;INDICATED IN EROR MESGE
3036
3037 007300 004737 021230 35: JSR PC,CHKHE ;CHECK IF 'ERR' OR 'HE' BIT IS SET
3038 ;IF YES, RETURN HERE.
3039 007304 104046 ERROR 46 ;'HE' OR 'ERR' BIT SET WHILE
3040 ;DOING A READ.
3041 ;READ WAS DONE STARTING AT 'DSK-ADRES'
3042 ;INDICATED IN EROR MESGE
3043 007306 004737 021256 45: JSR PC,CHKDA ;CHECK IF RKDA INCREMENTED CORRECTLY.
3044 ;IF NOT RETURN HERE.
3045 007312 104040 ERROR 40 ;RKDA DID NOT INCREMENT

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007502 013703 001332
007506 012702 177764
007512 013704 001340
007516 013701 001350
007522 010105
007524 005205
007526 012737 007534 001110
007534 104413
007536 104421
007540 005000
007542 010137 033336
007546 012777 033336 171562
007554 012777 177777 171552
007562 010114
007564 012713 002003
007570 105777 171536
007574 100410
007576 005200
007600 001373
007602 004737 020770
007606 010137 001202

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*****
: *TEST 20 CHECK 'WRITE FORMAT' -CYLINDER 0, SECTOR 0-13
: *THIS TEST GOES ONE STEP FURTHER & PERFORMS A WRT
: *FMT ON CYLINDER 0 & CHECKS THE FOLLOWING
: *1) IF CNTRL RDY SET WITHIN A CERTAIN TIME ON COMPLETION
: *OF THE FUNCTION
: *2) IF 'HE' OR 'ERR' BIT SET?
: *3) IF THE RKDA INCREMENTS CORRECTLY?
: *4) IF THE RKDB IS CLEAR?
: *WRT FMT IS DONE ONE SECTOR AT A TIME
: *THE FIRST WORD OF EVERY SECTOR IS WRITTEN AS A
: *PSUEDO-HEADER CONSISTING OF DRIVE #, CYLINDER #, SURFACE
: *# SECTOR #. THIS WILL BE READ & CHECKED IN THE FOLLOWING TEST.
*****
TST20: SCOPE
MOV RKCS,R3
MOV #-14,R2 ;SET UP COUNT FOR 12 SECTORS
MOV RKDA,R4
MOV DRIVAD,R1 ;GET DRIVE ADDRESS
MOV R1,R5 ;STORE IT
INC R5
MOV #15,$_LPERP ;SET RETURN ADRES FOR LUPING
;ON ERROR (SW 9)
;GO DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET, DO DRIVE RESET TO CLR IT
TST.SIN
CLR R0
MOV R1,OUTBUF ;THIS WORD TO BE X-FERRED. FIRST
;WORD OF EACH SECTOR WILL BE THE
;ACTUAL DRIVE-ADDRS CONSISTING OF
;DRIVE NO, CYL ADDR, SURFACE
;SECTOR NO.
MOV #OUTBUF,ARKBA ;ADRS FROM WHICH DATA WORD IS TO
;X-FERRED
MOV #-1,ARKWC ;SET UP WORD COUNT
MOV R1,AR4 ;ADRS THE DRIVE, CYL 0, & CORRECT SECTOR
MOV #2003,AR3 ;WRITE FORMAT, GO
2$: TSTB ARKCS ;DID 'CNTRL RDY' SET?
BMI 3$ ;YES, BRANCH
INC RC ;NO, HAVE U WAITED LONG?
BNE 2$ ;IF NOT, LOOP BACK & WAIT
;IF YES, REPORT ERROR
JSP PC,GT4RG ;GO GET RKCS, ER, DS,DA
MOV R1,$REGIO ;GET DISK ADRS (UNIT,CYL,SUR,SEC) TO WHICH
;WRITE FORMAT WAS DONE
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3214
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3217 007676 000004
3218 007700 005005
3219 007702 104413
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3229 007704 104421
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3231 007706 013700 001332
3232 007712 012700 177764
3233 007716 013702 001340
3234 007722 013712 001350
3235 007726 012704 033336
3236 007732 010477 171400
3237 007736 012777 177764 171370
3238 007744 012777 002005 171360
3239
3240 007752 105777 171354 1$:
3241 007756 100411
3242 007760 005205
3243 007762 001373
3244
3245 007764 004737 020770
3246 007770 013737 001350 001202
3247 007776 104416
3248
3249 010000 104045
3250
3251
3252
3253
3254 010002 004737 021230 2$:
3255
3256 010006 104046
3257
3258
3259
3260 010010 013705 001350 3$:
3261 010014 062705 000020
3262
3263 010020 004737 021264
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3265 010024 104040
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; *IN QUESTION. USUALLY A TRANSIENT ERROR
; *DISAPPEARS ON RETRIES, WHEREAS A LOGIC ERROR DOES NOT.
; *****
TST21: SCOPE
        CLR     R5
        CNT.RESET
; GO, DO CONTROL RESET
; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR & IF IT
; OCCURS GO BACK TO TEST 10
; GO CHECK IF SIN IS SET
; IS SET, DO DRIVE RESET TO CLR IT
; SET UP COUNT FOR 12 SECTORS
; ADDRESS THE DRIVE
; ADRS TO WHICH X-FER DATA FROM DSK
; SET UP WORD COUNT FOR 12 HEADERS TO BREAC
; READ FORMAT. GO
; DID CNTRL RDY SET ON COMPLETION?
; YES, BRANCH
; NO, WAIT FOR IT TO SET
; IF WAITED LONG ENOUGH REPORT
; ERROR. OTHERWISE LOOP BACK & WAIT
; GO, GET RKCS, ER, DS,DA
; GO TO 'BDAY' & BREAK CONTENTS OF
; $REGIO INTO DR#, CYL, SUR, SEC BITS
; CNTRL RDY DID NOT SET ON COMPLETION
; OF READ FORMAT-OF CYLINDER 0,
; SECTORS 0-13
; READ FMT WAS DONE STARTING AT (DSK-ADRES
; INDICATED IN EROR MESGE
; CHECK IF 'ERR' OR 'HE' BIT IS SET.
; IF YES RETURN HERE.
; 'ERR' OR 'HE' BIT SET ON DOING
; READ FMT-OF CYLINDER 0, SEC 0-13
; READ FMT WAS DONE STARTING AT (DSK-ADRES
; INDICATED IN EROR MESGE
; RKDA SHOULD HAVE INCREMENTD TO (R2)
; CHECK IF RKDA INCREMENTED CORRECTLY.
; IF NOT, RETURN HERE.
; RKDA DID NOT INCREMENT BY 12
; AFTER A 'RD FMT' OF 12 HEADERS OF
; CYLINDER 0, SECTORS 0-13
; RKBA SHOULD INCREMENT BY 24 BYTES
; AT THE END OF X-FER

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3270 010026 022777 033366 171302 4$: CMP #OUTBUF+30, @RKBA ; DID RKBA INCREMENT CORRECTLY?
3271 010034 001407 5$: BEQ 5$ ; YES, BRANCH
3272 010036 012737 033366 001162 MOV #OUTBUF+30, $REG0 ; GET EXPCTD RKBA
3273 010044 017737 171266 001164 MOV @RKBA, $REG1 ; GET ACTUAL RKBA
3274 010052 104042 42: ERROR 42 ; RKBA DID NOT INCREMENT CORRECTLY
3275 ; AFTER READ FORMAT OF 12 HEADERS
3276 010054 004737 021312 5$: JSR PC, CHKWC ; GO CHECK IF RKWC OVERFLOWED TO 0
3277 ; IF NOT RETURN HERE.
3278 010060 104041 41: ERROR 41 ; RKWC DID NOT OVERFLOW TO 0
3279 ; AFTER 'RD FMT' OF 12 HEADERS
3280 ; OF CYLINDER 0
3281 010062 005724 6$: TST (R4)+ ; WAS THE CORRECT HEADER RECIEVED?
3282 010064 001413 7$: BEQ 7$ ; YES, BRANCH
3283 010066 010037 001162 MOV RD, $REG0 ; GET SECTOR FOR WHICH THE HEADER
3284 010072 062737 000014 001162 ADD #14, $REG0 ; COULD NOT BE READ CORRECT
3285 010100 005037 001164 CLR $REG1 ; EXPCTD HEADER-0, FOR CYL 0
3286 010104 014437 001166 MOV -(R4), $REG2 ; GET WRONG HEADER RECVD
3287 010110 104043 43: ERROR 43 ; HEADER WAS NOT READ RIGHT FOR
3288 ; SECTOR (AS IN ER MSGE), & CYL 0
3289 010112 005724 7$: TST (R4)+ ; WAS THE CORRECT HEADER RECVD?
3290 010114 005200 INC RD ; YES, HAVE U CHECKED FOR ALL 12 SECTORS?
3291 010116 001361 6$: BNE 6$ ; IF NOT, LOOP BACK & CHK HDR FRM NXT SECTR
3292
3293 010120 004737 021336 JSR PC, CHKER ; CHECK IF ANY BIT IN RKER IS SET.
3294 ; IF YES, RETURN HERE.
3295 010124 104036 36: ERROR 36 ; RKER BIT SET ON DOING RD FMT
3296 ; OF CYL 0, SECTORS 0-13
3297 010126 022711 002204 8$: CMP #2204, @R1 ; DOES RKCS STILL CONTAIN FUNCTION BITS?
3298 010132 001406 TST22 ; YES, EXIT
3299 010134 012737 002204 001162 MOV #2204, $REG0 ; GET EXPCTD RKCS
3300 010142 011137 001164 MOV @R1, $REG1 ; GET ACTUAL RKCS
3301 010146 104024 24: ERROR 24 ; RKCS DID NOT CONTAIN 'RD FMT'
3302 ; FUNCTION BITS ON COMPETION OF
3303 ; THE FUNCTION
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*****
*TEST 22 CHECK 'READ', CYLINDER 0, SECTORS 0 TO 13
; *THIS TEST PERFORMS A READ OF ALL THE SECTORS OF CYLINDER 0
; *8 CHECKS THE FOLLOWING
; *1) CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
; *OF THE FUNCTION
; *2) IF 'HE' OR 'ERR' BIT SET?
; *3) IF THE CORRECT PSUEDO-HEADER (FIRST WORD OF EVERY)
; *SECTOR, WRITTEN IN A PREVIOUS TEST) WAS RECEIVED.
; *4) IF RKDS CONTAINS THE CORRECT WORD.
; *4) IF RKDA INCREMENTED CORRECTLY.
; *5) IF REST OF THE (377) WORDS IN EACH SECTOR ARE '0' . NOTE
; *PREVIOUSLY ONE WORD WAS WRITTEN PER SECTOR.
; *6) IF RKCS STILL CONTAINS THE 'READ' FUNCTION BITS
; *7) IF CONTROL RESET CLEARS RKDB.
; * IF TESTING IS BEING DONE ON A SIMULATOR ONLY LAST SECTOR(13)
; *IS READ BECAUSE THE SIMULATOR CAN STORE ONLY 1 SECTOR (256 WORDS).
; *HENCE ONLY THE DATA WRITTEN LAST CAN BE READ BACK.
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3326 010150 000004 TST22: SCOPE
3327 010152 012737 010224 001110 MOV #15, $LPERR ;SET RETURN ADRES FOR LUPING
3328 ;ON ERROR (SW 9)
3329 010160 013703 001332 MOV RKCS, R3
3330 010164 013701 001350 MOV DRIVAD, R1
3331 010170 010105 MOV R1, R5
3332 010172 012704 033336 MOV #0UTBUF, R4
3333 010176 005737 001344 TST SIMUL ;TESTING ON SIMULATOR?
3334 010202 001405 BEQ 9$ ;NO, BRANCH
3335 ;IF TESTING ON SIMULATOR READ
3336 ;SECTOR 13 ONLY
3337 010204 052701 000013 BIS #13, R1 ;SET BITS FOR SEC 13
3338 010210 052705 000020 BIS #20, R5 ;RKDA SHOULD INCRMNT TO THIS AFTER READ
3339 010214 000403 BR 1$
3340 010216 012702 177764 9$: MOV #-14, R2 ;SET COUNT FOR 12 SECTORS
3341 010222 005205 INC R5 ;RKDA SHOULD INCREMENT TO
3342 ;THIS AFTER 1 SECTOR READ
3343 010224 104413 1$: CNT.RESET ;GO, DO CONTROL RESET
3344 ;THIS IS A CALL FOR THE 'CNTRL-
3345 ;RESET' ROUTINE. A CONTROL RESET IS
3346 ;ISSUED AND AFTER A CERTAIN TIME
3347 ;IF THE 'CNTRL RDY' DOES NOT SET
3348 ;AN ERROR IS REPORTED. NOTE THAT
3349 ;THE PC IN ERROR MESSAGE IS THE
3350 ;PC WHEPE 'CNT.RESET' IS LOCATED.
3351 ;THIS IS A VERY BASIC ERR & IF IT
3352 ;OCCURS GO BACK TO TEST 10
3353 010226 104421 TST.SIN ;GO CHECK IF SIN IS SET
3354 ;IF SET, DO DRIVE RESET TO CLR IT
3355 010230 010177 171104 MOV R1, DRKDA ;ADDRESS THE DRIVE
3356 010234 010477 171076 MOV R4, DRKBA ;ADRS TO WHICH X-FER DATA FROM DISK
3357 010240 012777 177400 171066 MOV #-400, DRKWC ;SETUP WORD COUNT
3358 010246 012713 000005 MOV #5, DR3 ;READ, GO
3359
3360 010252 005000 CLR R0
3361 010254 105713 2$: TSTB DR3 ;DID CNTRL RDY SET ON COMPETION?
3362 010256 100410 BMI 3$ ;YES, BRANCH
3363 010260 005200 INC R0 ;NO, WAIT FOR IT TO SET
3364 010262 001374 BNE 2$ ;IF WAITED LONG ENOUGH, REPORT
3365 ;ERROR, OTHERWISE LOOP BAK & WAIT
3366 010264 004737 020770 JSR PC, GT4RG ;GO, GET RKCS, ER, DS, DA
3367 010270 010137 001202 MOV R1, $REG10 ;GET SECTOR ADRES WHERE ERROR OCCURED
3368 010274 104416 BRKDAH ;GO TO 'BDAY' & BREAK CONTENTS OF
3369 ;$REG10 INTO DR # CYL, SUR, SEC BITS
3370 010276 104045 ERROR 45 ;CNTRL RDY DID NOT SET ON COMPLETION
3371 ;OF READ OF CYLINDER 0, SECTOR
3372 ;AS SHOWN IN <DSK-ADRES>
3373 ;READ WAS DONE STARTING AT <DSK-ADRES>
3374 ;INDICATED IN EROR MESGE
3375 010300 004737 021222 3$: JSR PC, CHKHE1 ;CHECK IF 'ERR' OR 'HE' BIT IS SET.
3376 ;IF YES RETURN HERE.
3377 010304 104046 ERROR 46 ;HE OR ERR BIT SET
3378 ;ON 'READ' OF CYLINDER 0, SECTOR
3379 ;AS SHOWN IN <DSK-ADRES>
3380 ;READ WAS DONE STARTING AT <DSK-ADRES>
3381 ;INDICATED IN EROR MESGE

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 DZRKKE.P11 07-JUL-77 09:37 T22 CHECK 'READ', CYLINDER 0, SECTORS 0 TO 13

3382	010306	020114		4\$:	CMP	R1,(R4)		: WAS THE DATA WORD RECVD, CORRECT?
3383								: THE FIRST DATA WORD OF EACH SECTOR
3384								: IS AN ADRES WORD COMPRISING OF DRIVE NO.
3385								: CYLINDER ADRES, SUR, SECTOR ADRES
3386	010310	001407			BEQ	5\$		
3387	010312	010137	001162		MOV	R1,\$REG0		: GET EXPCTD DATA WORD FROM DISK
3388	010316	011437	001164		MOV	(R4),\$REG1		: GET THE DATA WORD RECVD
3389	010322	010137	001166		MOV	R1,\$REG2		: GET DISK ADRES
3390	010326	104044			ERROR	44		: DID NOT RECIEVE CORRECT DATA WORD ON
3391								: READ, OF CYLINDER 0, SECTOR AS SHOWN IN 'DSK
3392								: ADRES' OF EXPCTD DATA WORD
3393	010330	004737	021264	5\$:	JSR	PC,CHKDA1		: CHECK IF RKDA INCREMENTED CORRECTLY,
3394								: IF NOT RETURN HERE.
3395	010334	104040			ERROR	40		: RKDA DID NOT INCREMENT CORRECTLY
3396								: AFTER READ OF 1 WORD, FROM CYL 0
3397								: SEC IN ERROR IS 1 LESS THAN THAT
3398								: SHOWN IN EXPCTD RKDA
3399								
3400								: AS A RESULT OF 'WRT FMT' IN A PREVIOUS TEST
3401								: FIRST WORD OF EVERY SECTOR IS NON-
3402								: ZERO (PSUEDO-HDR), REST 377 WORDS
3403								: ARE ALL 0'S.
3404								: CHECK IF THE REST OF THE 377
3405								: WORDS ARE ALL 0'S
3406	010336	012737	177775	001370	MOV	#-3,EFLG1		: ALLOW ONLY 3 ERRORS
3407	010344	012700	033340		MOV	#OUTBUF+2,RO		: INITIALIZE PTR TO 2ND WRD IN BUFR
3408	010350	012737	177401	001362	MOV	#-377,COUNT		: CHECK 377 WORDS IN THE BUFFER
3409	010356	005710			TST	RO	11\$:	: IS THIS WRD 0?
3410	010360	001005			BNE	12\$: NO, ERROR
3411	010362	005720			TST	(RO)+		: INCRMNT PTR TO NXT WRD
3412	010364	005237	001362		INC	COUNT		: CHKD ALL 377 WRDS?
3413	010370	001372			BNE	11\$		
3414	010372	000412			BR	7\$: YES, BRANCH
3415	010374	005037	001162	12\$:	CLR	\$REG0		: GET EXPCTD WORD
3416	010400	012037	001164		MOV	(RO)+,\$REG1		: GET WORD RECVD
3417	010404	010137	001166		MOV	R1,\$REG2		: GET DISK ADRES, ERROR IN THIS
3418								: SECTOR
3419	010410	104044			ERROR	44		: DATA ERROR, THE LAST 377 WORDS
3420								: READ FROM EACH SECTOR SHOULD BE 0
3421								: IN A PREVIOUS TEST, FIRST WORD OF
3422								: EVERY SEC (CYL 0) WAS WRITTEN AS A
3423								: PSUEDO-HDR, REST OF THE WORDS IN THE
3424								: SECTR ARE AUTO ATICALLY WRITTEN AS
3425								: 0'S. THIS ERROR MAY MEAN THAT IT
3426								: DIDN'T HAPPEN SO
3427	010412	005237	001370		INC	EFLG1		: ALLOW ONLY 3 DATA ERORS OF THIS KIND
3428	010416	001357			BNE	11\$		
3429								
3430								
3431	010420	005737	001344	7\$:	TST	SIMUL		: TESTING ON SIMULATOR?
3432	010424	001011			BNE	10\$: YES BRANCH
3433								: IF NOT TESTING ON SIMULATOR GO AHEAD
3434								: & READ ALL 12 SECTORS ON CYL 0
3435	010426	005201			INC	R1		: INCREMENT DRIV-ADRES TO NXT SECTOR
3436	010430	005205			INC	R5		: INCREMENT 'EXPCTD DRIV-ADRES'
3437	010432	122705	000014		CMPB	#14,R5		: R U GOING TO READ THE LAST SECTOR?


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3494
3495 010520 000004
3496 010522 012737 000001 001206
3497 010530 012737 010560 001110
3498
3499 010536 005003
3500
3501 010540 012704 177465
3502 010544 012702 177764
3503 010550 013701 001350
3504 010554 010105
3505 010556 005205
3506 010560 104413
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3515
3516 010562 104421
3517
3518 010564 005037 001362
3519 010570 010137 033336
3520
3521
3522
3523 010574 012777 033336 170534
3524 010602 012777 177777 170524
3525 010610 010177 170524
3526
3527 010614 012777 002003 170510
3528
3529 010622 105777 170504
3530 010626 100411
3531 010630 005237 001362
3532 010634 001372
3533
3534 010636 004737 020770
3535 010642 010137 001202
3536 010646 104416
3537
3538 010650 104031
3539
3540
3541
3542
3543 010652 032777 001000 170446
3544 010660 001405
3545 010662 004737 020776
3546 010666 010137 001170
3547 010672 104001
3548
3549

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*****
↑ST23: SCOPE
MOV #1,$TIMES ;DO 1 ITERATION
MOV #1,$SLPERR ;SET RETURN ADRES FOR LUPING
;ON ERROR (SW 9)
CLR R3 ;(R3)=0, SURFACE 0 BEING WRITTEN
;(R3)-1, SURFACE 1 BEING WRITTEN
MOV #-313,R4 ;SET UP COUNT FOR 203 CYLINDERS
MOV #-14,R2 ;SET UP COUNT FOR 12 SECTORS
MOV DRIVAD,R1 ;GET DRIVE ADRES
MOV R1,R5 ;STORE IT
INC R5
1$: CNT.RESET ;GO DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK IF SIN IS SET
;IF SET, DO DRIVE RESET TO CLR IT
7$: CLR COUNT
MOV R1,OUTBUF ;THIS WORD TO BE WRITTEN. THE FIRST
;WORD OF EACH SECTOR WILL BE THE ACTUAL
;DISK-ADRES, CONSISTING OF THE DRIVE NO.
;CYL ADRES, SURFACE BIT SECTOR ADRES
MOV #OUTBUF,ARKBA ;ADRES FROM WHICH WORD IS TO B X-FERRED
MOV #-1,ARKWC ;SET UP WORD COUNT
MOV R1,ARKDA ;ADRES THE DRIVE, WITH CORRECT CYL
; & SECTOR ADRES
MOV #2003,ARKCS ;WRITE FORMAT, GO
2$: TSTB ARKCS ;DID CNTRL RDY SET
BMI 3$ ;YES, BRANCH
INC COUNT ;NO, HAVE U WAITED LONG ENOUGH?
BNE 2$ ;IF NOT, LOOP BACK & WAIT
;IF YES, REPORT ERROR
JSR PC,GT4RG ;GO GET RKCS, ER, DS DA
MOV R1,$REG10 ;GET DISK ADRES, WHERE ERROR OCCURED
BRKDA4 ;GO TO 'BDAY' & BREAK CONTENTS OF
;$REG10 INTO DR # CYL SUR. SEC BITS
ERROR 31 ;CNTRL RDY DID NOT SET ON COMPLETION
;OF 'WRITE FORMAT' ON SECTOR AS
;SHOWN IN <DSK-ADRES>
;WRT FMT WAS DONE STARTING AT <DSK-ADRES>
;INDICATED IN EROR MSGE.
3$: BIT #1000,ARKDS ;DID SIN BIT SET?
BEQ 4$ ;NO, BRANCH
JSR PC,GT3RG ;GO GET RKCS, ER, DS
MOV R1,$REG3 ;GET, DISK-ADRES WHERE ERROR OCCURED
ERROR 1 ;SIN SET WHILE DOING WRT FMT
;TO DISK-ADRES (AS IN $REG3)

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3606
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3612 011000 000004
3613 011002 012737 000001 001206
3614 011010 012737 011074 001110
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3616 011015 005037 001356
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3618 011022 013701 001350
3619 011026 010102
3620 011030 005737 001344
3621 011034 001410
3622 011036 052701 014533
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3625 011042 052702 014540
3626
3627 011046 012737 177777 001370
3628
3629 011054 000407
3630 011056 012705 177465
3631 011062 012737 177764 001370
3632
3633 011070 062702 000020
3634
3635 011074 104413
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3646 011076 104421
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3648
3649 011100 012703 033336
3650 011104 005037 001360
3651 011110 010377 170222
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3653 011114 013777 001370 170212
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3656 011122 010177 170212
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3658 011126 012777 002005 170176
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3660 011134 105777 170172
3661 011140 100411

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; *OCCURS A DRIVE RESET IS DONE BEFORE READING THE NEXT
; *SECTOR. READING IS DONE IN THIS ORDER CYL 0-SUR 0;
; *CYL 0-SUR 1; CYL 1-SUR 0; CYL 1-SUR 1; CYL 2-SUR 0;
; *CYL 2-SUR 1; -----CYL 312-SUR 1. IF TESTING ON SIMULATOR, ONLY
; *THE LAST CYLINDER (312), LAST SECTOR (13), SURFACE 1 IS REAC.
; *****
†ST24: SCOPE
MOV #1, $TIMES ; DO 1 ITERATION
MOV #1$, $LPERR ; SET RETURN ADRES FOR LUPING
CLR INDX1 ; ON ERROR (SW 9)
; INDX1=0, SURFACE 0 BEING READ
; INDX1=1, SURFACE 1 BEING READ
MOV DRIVAD, R1 ; GET DRIVE ADRES
MOV R1, R2
TST SIMUL ; TESTING ON SIMULATOR?
BEQ 12$ ; NO, BRANCH
BIS #14533, R1 ; SET BITS FOR CYL 312, SEC 13, SUR 1
; ON SIMULATOR, CHECK ONLY CYL 312.
; SECTOR 13, SURFACE 1
; RKDA SHOULD INCRMNT TO THIS AFTR
; RD FMT OF 1 SECTOR
; SET COUNT FOR READING HDR
; FROM 1 SECTOR ONLY
BR 1$
MOV #-313, R5 ; SET UP COUNT FOR 203 CYLINDERS
MOV #-14, $EFLG1 ; SET COUNT FOR 12 HDRS TO BE
; READ FROM EACH CYLINDER
; THIS IS WHAT RKDA SHOULD INCREMENT
; BY, AFTER 'RD FMT' OF EACH CYLINDER
; GO, DO CONTROL RESET
; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR & IF IT
; OCCURS GO BACK TO TEST 10
; CNT.RESET
; CHECK IF SIN IS SET
; IF SET DO DRV-RESET TO CLR IT
TST SIN
; STORE ADRES OF BUFFER
MOV #OUTBUF, R3
CLR INDX2
MOV R3, $RKBA
; ADRES TO WHICH DATA IS TO BE X-FERRED
; FROM THE DISK
MOV EFLG1, $RKWC ; SET UP WORD COUNT FOR 12 HEADERS
; TO BE READ OFF EACH CYLINDER
; (ONLY 1 FOR SIMULATOR)
MOV R1, $RKDA ; ADRES THE DRIVE WITH CORRECT
; CYLINDER & SECTOR ADRES
MOV #2005, $RKCS ; READ FORMAT, GO
; DID CNTRL RDY SET?
; YES, BRANCH
TSTB $RKCS
BMI 3$

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3718	011304	005723		8\$:	TST	(R3)+		; INCREMENT POINTER TO THE NXT WORD
3719								; IN MEMORY WHERE THE RECVD HDR IS STORED
3720	011306	005200			INC	R0		; HAVE U CHECKED ALL 12 HEADERS?
3721	011310	001361			BNE	7\$; IF NOT, LOOP BACK & CHK THE NXT.
3722								; YES, ALL HEADERS FOR THIS CYLINDER
3723								; CHECKED.
3724	011312	004737	021312		JSR	PC.CHKWC		; CHECK IF RKWC OVERFLOWED TO 0, IF
3725								; NOT RETURN HERE.
3726	011316	104041			ERROR	41		; RKWC DID NOT OVERFLOW AFTER DOING
3727								; RDFMT OF 12 SECTORS ON THE CYLINDER
3728								; NOTE THAT 'RKDA' IS THE INCREMENTED
3729								; RKDA AFTER THE RDFMT
3730	011320	005737	001344	9\$:	TST	SIMUL		; TSTING ON SIMULATOR?
3731	011324	001031			BNE	TST25		; IF YES, EXIT
3732								; NO
3733	011326	005737	001356		TST	INDX1		; DOING SURFACE 1
3734	011332	001011			BNE	10\$; YES, BRANCH
3735	011334	005237	001356		INC	INDX1		; NO
3736	011340	062701	000020		ADD	#20,R1		; INCREMENT DRIV ADRES TO THE NXT SURFACE
3737	011344	010102			MOV	R1,R2		
3738	011346	062702	000020		ADD	#20,R2		; THIS IS WHAT RKDA SHOULD INCREMENT
3739								; TO. AFTER READ FMT OF THE CYLINDER
3740	011352	000137	011074		JMP	1\$; GO RD FMT THE NXT SURFACE
3741	011356	005037	001356	10\$:	CLR	INDX1		
3742	011362	042701	000037		BIC	#37,R1		; CLR SEC, SURFACE BITS
3743	011366	062701	000040		ADD	#40,R1		; INCREMENT TO NXT CYL
3744	011372	010102			MOV	R1,R2		; THIS IS WHAT RKDA SHOULD BE
3745	011374	062702	000020		ADD	#20,R2		; AFTER RD FMT OF CYLINDER
3746	011400	005205			INC	R5		; HAVE U DONE ALL CYLINDERS?
3747	011402	001402			BEQ	TST25		; EXIT
3748	011404	000137	011074		JMP	1\$; IF NOT, LOOP BACK & READ FMT FROM
3749								; THE NXT CYLINDER

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:*****
:*TEST 25      CHECK 'READ' OF THE ENTIRE DISK
;*READ OF THE ENTIRE DISK (ONE WORD PER SECTOR) IS DONE
;*IN THIS TEST.  IN A PREVIOUS TEST THE FIRST WORD OF
;*EVERY SECTOR WAS WRITTEN LIKE A PSUEDO-HEADER (DRIVE #,
;*CYLINDER #, SURFACE & SECTOR #).  THESE PSUEDO HEADERS
;*WILL BE READ & CHECKED IN THIS TEST, PROVING THAT ANY
;*SECTOR CAN BE ACCESSED AND READ.
;*THE FOLLOWING CHECKING IS DONE
;*1. CNTRL RDY SETS WITHIN A CERTAIN TIME ON COMPLETION
;*OF FUNCTION.
;*2. IF 'SIN' OCCURRED?
;*3. IF 'HE' OR 'ERR' OCCURRED?
;*4. THE CORRECT FIRST WORD FROM EVERY SECTOR
;*WAS RECEIVED.  THIS WORD REFLECTS THE ABSOLUTE
;*DISK ADDRESS (DRV #, CYL #, SUR, SEC#) OF THAT SECTOR.
;*5. IF RKDB CONTAINED THE CORRECT WORD.
;*IF 'SIN' OCCURS DRIVE RESET IS DONE BEFORE READING
;*THE NEXT SECTOR.  READ IS DONE IN THIS ORDER SEC 0-11
;*CYL 0 SUR 0 -> SEC 0-11 CYL 0 SUR 1 -> SEC 0-11 CYL 1.....
;*IF TESTING ON SIMULATOR ONLY LAST CYLINDER (312).  LAST
;*SECTOR (13), SURFACE 1 IS READ.
    
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3774
3775 011410 000004
3776 011412 012737 000001 001206
3777 011420 012737 011464 001110
3778
3779 011426 012703 033336
3780 011432 005004
3781
3782 011434 013701 001350
3783 011440 005737 001344
3784 011444 001403
3785 011446 052701 014533
3786 011452 000404
3787 011454 012700 177764
3788 011460 012705 177465
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3790 011464 104413
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3800 011466 104421
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3802 011470 005037 001356
3803 011474 010377 167636
3804
3805 011500 012777 177777 167626
3806 011506 010177 167626
3807
3808 011512 012777 000005 167612
3809
3810 011520 105777 167606
3811 011524 100411
3812 011526 005237 001356
3813 011532 001372
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3815 011534 004737 020770
3816 011540 010137 001202
3817 011544 104416
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3819 011546 104045
3820
3821
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3825 011550 032777 001000 167550
3826 011556 001405
3827 011560 004737 020776
3828 011564 010137 001170
3829 011570 104001

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↑ST25: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
MOV #1$,SLPERR ;SET RETURN ADRES FOR
;LOOPING ON ERROR (SW9)
MOV #OUTBUF,R3
CLR R4 ;FLAG, CLEAR WHEN READING SURFACE C
;SET WHEN READING SURFACE 1
MOV DRIVAD,R1 ;GET DRIVE ADDRESS
TST SIMUL ;TSTING ON SIMULATOR?
BEQ 10$ ;IF NOT BRANCH
BIS #14533,R1 ;SET ADRES BITS FOR LAST CYL (312)
BR 1$ ;LAST SECTOR (13), SURFACE 1
10$: MOV #-14,R0 ;SET COUNT FOR 12 SECTORS
MOV #-313,R5 ;SET UP COUNT FOR 203 CYLINDERS
1$: CNT.RESET ;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
;GO CHECK SIN, IF SET DC
;DRIVE RESET TO CLR IT
TST.SIN
8$: CLR INDX1
MOV R3,ARKBA ;ADRES TO WHICH DATA IS TO B X-FERRED
;FROM THE DISK
MOV #-1,ARKWC ;SET UP WORD COUNT
MOV R1,ARKDA ;ADRES THE DRIVE WITH CORRECT
;CYLINDER & SECTOR ADRES
;READ, GO
2$: TSTB ARKCS ;DID CNTRL RDY SET?
BMI 3$ ;YES, BRANCH
INC INDX1 ;NO, HAVE U WAITED LONG ENOUGH
BNE 2$ ;IF NOT, LOOP BACK & WAIT FOR IT
;IF YES, REPORT ERROR
JSR PC,GT4RG ;GO, GET RKCS, ER, DS,DA
MOV R1,$REG10 ;GET DISK-ADRES WHERE ERROR OCCURED
BRKDAY ;GO TO 'BDAY' & BREAK CONTENTS OF
;$REG10 INTO DR #,CYL,SUR,SEC BITS
ERROR 4$ ;CNTRL RDY DID NOT SET AFTER DOING
;A 1 WORD READ FROM ADRES AS
;INDICATED IN <DISK-ADRES>
;'RKDA' IN EROR MSGE GIVES THE
;CONTENTS OF RKDA AT THE TIME OF ERROR
3$: BIT #1000,ARKDS ;DID 'SIN' SET?
BEQ 4$ ;NO, BRANCH
JSR PC,GT3RG ;GO, GET RKCS, ER, DS
MOV R1,$REG3 ;GET DISK-ADRES WHERE SIN OCCURED
ERROR 1 ;'SIN' ERROR ON DOING REAC FROM

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011716 000004
011720 012737 000005 001206
011726 012703 001372
011732 005037 001356
011736 013700 001332
011742 013701 001326
011746 013702 001330
011752 012737 011760 001110
011760 000240
011762 104413
011764 104421
011766 013704 001350
011772 051304
011774 010477 167340
012000 012710 000011
012004 104412
012006 104021
012010 005005
012012 032711 000100
012016 001005
012020 005205
012022 001373
012024 004737 020770
012030 104026
012032 032711 001000
012036 001403
012040 004737 020770
012044 104001
012046 032710 140000

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*****
TST26: SCOPE
MOV #5,$TIMES ;DO 5 ITERATIONS
MOV #SEEK0,R3 ;INITIALIZE POINTER TO THE FIRST
;SEEK ADDRESS
CLR INDX1 ;INDX1, WHEN 0 INDICATES SEEK IN FWD DIRECTION
; WHEN 1 INICATES SEEK IN REV DIRECTION
MOV RKCS,R0
MOV RKDS,R1
MOV RKER,R2
MOV #1,$LPERR ;SET RETURN ADRES FOR LUPING ON
;EROR (SW 9)
NCF
CNT.RESET ;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR & IF IT
;OCCURS GO BACK TO TEST 10
TST.SIN ;GO, CHECK IF SIN IS SET, IF SET
;DO DRV-RESET TO CLEAR IT
MOV DRIVAD,R4 ;GET DRIV-ADRES
BIS (R3),R4 ;SET CYLINDER BITS
MOV R4,DRKDA ;ADDRS THE DRIVE
MOV #11,DR0 ;SET 'SEEK', 'GO'
CHKCRDY ;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
ERROR 21 ;'CNTRL RDY' DID NOT SET AFTER
;SENDING CYL ADD TO THE DRIV, 'ADD ACK'
;FROM DRIVE SHLD HAVE COME BACK
;THEREUPON SETTING 'CNTRL RDY'
CLR R5
BIT #100,DR1 ;DID R/W/S RDY SET?
BNE 6$ ;YES, BRANCH
INC R5 ;NO, WAIT
BNE 5$ ;WAITED LONG?
JSR PC,GT4RG ;GO, GET RKCS, ER, DS, DA
ERROR 26 ;R/W/S RDY DID NOT SET ON
;COMPLETION OF SEEK
BIT #1000,DR1 ;DID SIN SET?
BEQ 7$ ;NO, BRANCH
JSR PC,GT4RG ;GO, GET RKCS, ER, DS, DA
ERROR 1 ;SIN SET ON DOING SEEK
BIT #140000,DR0 ;DID 'HE' OR 'ERR' SET?
```

*** THIS TEST CHECKS SEEK IN DIFFERENT VELOCITY MODES (DIFF '3'
3 < DIFF < 31, DIFF > 31). FOR THESE 3 BASIC VELOCITIES SEEK IS DONE 30*-
IN FWD AND REV DIRECTION TO CHECK THE ADDER & DIFFERENCE LOGIC. IF
WHILE DOING A SEEK 'SIN' OCCURS, A DRIVE RESET IS DONE TO INITIALIZE
THE POSITIONING LOGIC

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3942 012052 001403          BEQ      8$          ;YES
3943 012054 004737 020770  JSR      PC,GT4RG   ;GO GET RKCS, ER, DS, DA
3944 012060 104022          ERROR    22         ;EAR OF 'HE' BIT SET WHEN
3945                                     ;SEEKING TO CYL AS INDICATED
3946                                     ;IN RKDA
3947
3948 012062 022710 000210  9$:     CMP      #210,DR0   ;DOES RKCS STILL CONTAIN THE 'SEEK' FNCTION
3949 012066 001406          BEQ      9$          ;YES - EXIT
3950 012070 011037 001164  MOV      DR0,$REG1   ;NO, GET RKCS RECVD
3951 012074 012737 000210 001162  MOV      #210,$REG0  ;GET EXPCTD RKCS
3952 012102 104024          ERROR    24         ;RKCS SHOULD CONTAIN THE 'SEEK' BITS
3953                                     ;IF NOT, ERROR
3954
3955 012104 020477 167230  9$:     CMP      R4,DRKDA   ;DID RKDA CHANGE?
3956 012110 001406          BEQ      10$         ;NO
3957 012112 010437 001162  MOV      R4,$REG0   ;YES, GET EXPCTD?
3958 012116 017737 167216 001164  MOV      DRKDA,$REG1 ;GET RKDA
3959 012124 104027          ERROR    27         ;RKDA CHANGED AFTER DOING SEEK
3960
3961 012126 010477 167206 10$:     MOV      R4,DRKDA   ;ADRES THE DRIVE, SEC 0
3962 012132 012777 033336 167176  MOV      #OUTBUF,DRKBA ;READ ONE HEADER INTO THIS
3963 012140 012777 177777 167166  MOV      #-1,DRKWC   ;BUS ADRES
3964 012146 012710 002005  MOV      #2005,DR0   ;GO READ FORMAT
3965 012152 104414          CNT.RDY   ;WAIT FOR CNTRL P'Y
3966 012154 021337 033336  CMP      (R3),OUTBUF  ;WAS THE CORRECT READER READ (FROM
3967 012160 001410          BEQ      11$         ;CYLINDER TO WHICH SEEK WAS DONE BEFORE,
3968 012162 005037 001162  CLR      $REG0       ;STORE SEC # FROME WHERE HDR WAS RC .0)
3969 012166 011337 001164  MOV      (R3),$REG1  ;GET EXPCTD HEADER
3970 012172 013737 033336 001166  MOV      OUTBUF,$REG2 ;GET HDR RECVD
3971 012200 104043          ERROR    43         ;WRONG HDR WAS RECVD FROM CYLINDER (ADRES
3972                                     ;IN ER MSGE), NOTE THAT A PURE SEEK WAS
3973                                     ;DONE TO THIS CYL BEFORE READING HDR
3974                                     ;USING READ FORMAT
3975 012202 005737 001356 11$:     TST      INDX1     ;SEEK IN REVRSE DIRECTION?
3976 012206 001007          BNE      12$         ;YES, BRANCH
3977 012210 005723          TST      (R3)+       ;NO, INCREMENT PTR TO NXT SEEK ADRES
3978 012212 022703 001400  CMP      #SEEK2+2,R3 ;DONE WITH ALL SKS IN FWD DIR?
3979 012216 001260          BNE      1$          ;NO, GO & DO NXT ONE
3980 012220 005237 001356  INC      INDX1       ;SET FLAG INDICATING SK IN REVRSE
3981 012224 005743          TST      -(R3)
3982 012226 005743 12$:     TST      -(R3)     ;POSITION PTR TO NXT SK IN REV
3983 012230 022703 001370  CMP      #SEEK0-2,R3 ;DONE WITH ALL?
3984 012234 001251          BNE      1$          ;IF NOT, DO NXT ONE
3985
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3995
3996 012236 000004          ;:*****
3997 012240 012737 000005 001206  †ST27: SCOPE      ;*TEST 27 CHECK DRIVE RESET FROM LAST CYLINDER
                                     ;*THE HEADS ARE POSITIONED ON THE LAST CYLINDER (DOING
                                     ;*AN IMPLIED SEEK-READ), THEN A DRIVE RESET IS ISSUED.
                                     ;*IT'S CHECKED IF THE HEADS WERE BROUGHT BACK TO 0 BY
                                     ;*DOING A 1 WORD READ & CHECKING THAT THE CORRECT WORD
                                     ;*WAS RECEIVED. IF TESTING ON SIMULATOR THIS TEST IS SKIPPED.
                                     ;:*****
MOV      #5,$TIMES          ;:DO 5 ITERATIONS

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3998	012246	005737	001344		IST	SIMUL		: R U ON A SIMULATOR?
3999	012252	001124			BNE	YST3D		: YES, EXIT
4000	012254	013701	001332		MOV	RKCS,R1		
4001	012260	104413			CNT.RESET			: GO DO CONTROL RESET
4002								: THIS IS A CALL FOR THE 'CNTRL-
4003								: RESET' ROUTINE. A CONTROL RESET IS
4004								: ISSUED AND AFTER A CERTAIN TIME
4005								: IF THE 'CNTRL RDY' DOES NOT SET
4006								: AN ERROR IS REPORTED. NOTE THAT
4007								: THE PC IN ERROR MESSAGE IS THE
4008								: PC WHERE 'CNT.RESET' IS LOCATED.
4009								: THIS IS A VERY BASIC ERR & IF IT
4010								: OCCURS GO BACK TO TEST 10
4011	012262	005000			CLR	RO		
4012	012264	012703	033336		MOV	#OUTBUF,R3		: ADRES WHERE DATA WILL BE READ INTO
4013	012270	013704	001350		MOV	DRIVAD,R4		
4014	012274	010405			MOV	R4,R5		
4015	012276	052705	014500		BIS	#14500,R5		: SET CYL ADRES=312 (OCTAL)
4016	012302	010577	167032		MOV	R5,DRKDA		: ADRES THE DRIVE, LAST CYLINDER
4017	012306	012777	177777	167020	MOV	#-1,DRKWC		: READ 1 WORD
4018	012314	010377	167016		MOV	R3,DRVBA		: INTO THIS MEMORY ADRES
4019								
4020	012320	012711	000005		MOV	#5,DR1		: READ, GO
4021								
4022	012324	005000			CLR	RO		
4023	012326	104414		1\$:	CNT.RDY			: THIS IS A CALL FOR CN.RDY ROUTINE
4024								: WHICH WAITS FOR CNTRL RDY TO SET.
4025								: A RETURN IS MADE AFTER CNTRL RDY
4026								: SETS. IF WITHIN A CERTAIN TIME
4027								: CNTRL RDY DOESN'T SET AN ERROR
4028								: MESSAGE IS GIVEN. WAITING TIME
4029								: 883 MS FOR 11/20, 175 MS FOR 11/45
4030	012330	020513		2\$:	CMP	R5,DR3		: WAS THE CORRECT WORD READ?
4031	012332	001407			BEQ	3\$: YES, SEEK TO 312 WAS DONE CORRECTLY.S
4032	012334	010537	001162		MOV	R5,\$REG0		: GET EXPCTD WORD
4033	012340	011337	001164		MOV	DR3,\$REG1		: GET WORD RECVD
4034	012344	010537	001166		MOV	R5,\$REG2		: GET DSK-ADRES FROM WHERE WORD WAS READ
4035	012350	104044			ERROR	44		: DID NOT READ BACK CORRECT WORD FROM
4036								: LAST CYL, SEC 0. IF TEST 45 & 46
4037								: WERE SUCCESSFULLY DONE THIS
4038								: ERROR MEANS THAT IMPLIED SEEK
4039								: TO CYL 312 COULD NOT B DONE
4040	012352	012711	000015	3\$:	MOV	#15,DR1		: DRIVE RESET, GO
4041	012356	104414			CNT.RDY			: THIS IS A CALL FOR CN.RDY ROUTINE
4042								: WHICH WAITS FOR CNTRL RDY TO SET.
4043								: A RETURN IS MADE AFTER CNTRL RDY
4044								: SETS. IF WITHIN A CERTAIN TIME
4045								: CNTRL RDY DOESN'T SET AN ERROR
4046								: MESSAGE IS GIVEN. WAITING TIME
4047								: 883 MS FOR 11/20, 175 MS FOR 11/45
4048	012360	005000			CLR	RO		
4049	012362	032777	000100	166736	4\$:	BIT	#100,DRKDS	: DID R/W/S RDY SET?
4050	012370	001011			BNE	5\$: YES, BRANCH
4051	012372	012702	177763		MOV	#-15,R2		: IF U R ON A SLOWER MACHINE
4052	012376	005202			INC	R2		: & DO NOT NEED SUCH A LARGE MACHINE
4053	012400	001376			BNE	-2		: TIME LOOP, CHANGE THESE 3

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4054                                     : INSTRUCTIONS TO 'NOP' THE
4055                                     : LOOP TIME WILL BE REDUCED
4056                                     : TO 1100 MS
4057
4058                                     : THE TOTAL TIME FOR THE ABOVE
4059                                     : LOOPS (W/O PUTTING 'NOP'S) IS
4060                                     : 5304 MS FOR 11/20 AND
4061                                     : 1061 MS FOR 11/45 WITH MOS
4062                                     : OR BIPOLAR MEMORY
4063 012402 005200          INC      R0
4064 012404 001366          BNE      45
4065                                     : WAITED LONG?
4066 012406 004737 020770   JSR      PC,GT4RG
4067 012412 104026          ERROR    26
4068                                     : IF NOT, LUP BAK & WAIT
4069 012414 032711 140000   5$:     BIT      #140000,AR1
4070 012420 001403          BEQ      65
4071                                     : IF YES, ERROR
4072 012422 004737 020770   JSR      PC,GT4RG
4073 012426 104022          ERROR    22
4074                                     : GET RKCS, ER, DS, DA FOR ERROR MESSAGE
4075 012430 005205          6$:     INC      R5
4076 012432 020577 166702   CMP      R5,ARKDA
4077 012436 001406          BEQ      75
4078 012440 010537 001162   MOV      R5,$REG0
4079 012444 017737 166670 001164   MOV      ARKDA,$REG1
4080 012452 104054          ERROR    54
4081                                     : DID THE CYL ADRES BITS IN RKDA GET CHANGED?
4082                                     : NO, BRANCH
4083 012454 012777 177777 166652 7$:   MOV      #-1,ARKWC
4084 012462 010377 166650   MOV      R3,ARKBA
4085 012466 010477 166646   MOV      R4,ARKDA
4086                                     : GET EXPCTD RKDA
4087 012472 012711 000005   MOV      #5,AR1
4088                                     : GET RKDA RECVD
4089                                     : CYLINDER ADRES BITS IN RKDA
4090 012476 005000          8$:     CLR      R0
4091 012500 104414          CNT.RDY
4092                                     : GOT CHANGED AFTER
4093                                     : DRIVE RESET, FROM LAST CYLINDER
4094                                     : READ 1 WORD
4095                                     : INTO THIS ADRES
4096                                     : FROM THIS DSK ADRES-CYL 0, SEC 0
4097 012502 020413          9$:     CMP      R4,AR3
4098 012504 001407          BEQ      TST30
4099 012506 010437 001162   MOV      R4,$REG0
4100 012512 011337 001164   MOV      AR3,$REG1
4101 012516 010437 001166   MOV      R4,$REG2
4102 012522 104044          ERROR    44
4103                                     : WAS THE CORRECT WORD READ?
4104                                     : YES, EXIT
4105                                     : GET EXPCTD WORD
4106                                     : GET WORD RECVD
4107                                     : GET DISK ADRES WHERE ERROR OCCURED
4108                                     : DID NOT READ CORRECT WORD FROM
4109                                     : CYL 0, SEC 0. IF TEST 45 & 46
4110                                     : WERE SUCCESSFULLY DONE THIS
4111                                     : ERROR COULD MEAN THAT DRIVE-RESET
4112                                     : DID NOT BRING HEADS BACK TO 0.
;:*****

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4117 012524 000004
4118 012526 104413
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4128 012530 104421
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4130 012532 013704 001332
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4145 012536 012700 033336
4146 012542 012701 177401
4147 012546 012702 177400
4148 012552 012703 177400
4149
4150 012556 010320
4151 012560 005202
4152 012562 060103
4153 012564 010320
4154 012566 005202
4155 012570 001374
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4157 012572 012777 177400 166534
4158 012600 012777 033336 166530
4159 012606 013777 001350 166524
4160
4161 012614 012714 000003
4162
4163 012620 105714
4164 012622 100003
4165 012624 004737 020776

```

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;*TEST 30 'WRITE' - 256 WORD BLOCK ON SECTOR 0, CYLINDER 0
;THE TEST BELOW SHOULD BE CONSIDERED AS A SET UP PHASE FOR
;THE FOLLOWING TEST. IT WRITES A BLOCK OF 256 WORDS IN
;SECTOR 0, CYLINDER 0 WITH A SPECIFIC PATTERN AND THIS WRITTEN
;BLOCK WILL BE MADE USE OF IN THE NEXT TEST TO CHECK
;OUT 'WRITE-CHECK' AND 'READ CHECK' FUNCTIONS.
;*****
†ST30: SCOPE
CNT.RESET
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;CHECK IF SIN IS SET, IF SET
;DO DRIVE RESET TO CLEAR IT

TST.SIN
MOV RKCS,R4
;THE FOLLOWING CODE IS FOR SETTING
;UP THE I/O BUFFER IN MEMORY (STARTING AT
;OUTBUF), WITH A PARTICULAR 256 WORD PATTERN.
;STARTING FROM THE FIRST WORD IN THE BUFFER
;THE LO BYTE WILL BE A COUNT PATTERN
;FROM 0 TO 255 (DECIMAL), WHEREAS THE
;HI-BYTE WILL BE THE COMPLEMENT OF LO BYTE.
;A DECREASING COUNT PATTERN FROM 255 TO 0.
;I.E.THE BUFFER WILL LOOK LIKE:
;OUTBUF (1 111 111 1 00 000 000)
;OUTBUF+2 (1 111 111 0 00 000 001)
;LAST WORD (0 000 000 0 11 111 111)

MOV #OUTBUF,R0
MOV #177401,R1 ;PATTERN GENERATING NUMBER
MOV #-400,R2 ;SET UP COUNT FOR 256 WORDS
MOV #177400,R3 ;SET UP THE FIRST PATTERN TO B WRITTEN

MOV R3,(R0)+ ;SET UP FIRST WORD IN I/O BUFFER
INC R2 ;INCREMENT COUNT
1$: ADD R1,R3 ;SET UP NEXT WORD PATTERN
MOV R3,(R0)+ ;WRITE IT IN NXT I/O BUFFER WORD
INC R2 ;HAVE U WRITTEN ALL 256 WORDS
BNE 1$ ;IF NOT GO & WRITE NEXT PATTERN

MOV #-400,ARKWC ;WRITE 256 WORDS
MOV #OUTBUF,ARKBA ;STARTING FROM THIS BUS ADRES
MOV DRIVAD,ARKDA ;TO THIS DISK ADRES, CYL 0, SEC 0

MOV #3,AR4 ;WRITE, GO

2$: TSTB AR4 ;WAS CNTRL RDY CLEARED AS GO WAS SET?
BPL 3$-2 ;YES. BRANCH
JSR PC,GT3RG ;GET RKCS, ER, DS

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4166 012630 104030          ERROR 30          ;CNTRL RDY DID NOT CLEAR AS GO WAS SET
4167                                     ;TO 'WRITE'
4168
4169 012632 005002          CLR      R2          ;DID CNTRL RDY SET?
4170 012634 105777 166472 3$:  TSTB   @RKCS        ;YES, BRANCH
4171 012640 100411          BMI     4$          ;WAITED LONG ENOUGH?
4172 012642 005202          INC     R2          ;IF NOT, LUP BAK & WAIT
4173 012644 001373          BNE     3$          ;IF YES, ERROR
4174                                     ;GO, GET RKCD, ER, DS, DA
4175 012646 004737 020770  JSR     PC,GT4RG    ;GET THE STARTING ADRES
4176 012652 013737 001350 001202 MOV     DRIVAD,$REGIO ;BREAK CONTENTS OF $REGIO INTO
4177 012660 104416          BRKDA4                ;DRV #, CYL, SUR, SEC #
4178                                     ;CNTRL RDY DID NOT SET ON COMPLETION
4179 012662 104031          ERROR 31          ;OF WRITE OF 256 WORDS ON CYL 0, SEC 0
4180                                     ;'RKDA' IN EROR MSGE GIVES THE
4181                                     ;CONTENTS OF RKDA AT THE TIME OF EROR
4182                                     ;WRITE WAS DONE STARTING AT <DSK-ADRES>
4183                                     ;INDICATED IN EROR MSGE
4184                                     ;CHECK IF 'ERR' OR 'HE' BIT IS SET,
4185 012664 004737 021230 4$:  JSR     PC,CHKHE    ;IF YES, RETURN HERE
4186                                     ;HE OR ERR BIT SET ON DOING WRITE OF
4187 012670 104032          ERROR 32          ;256 WORDS ON CYL 0, SEC 0
4188                                     ;WRITE WAS DONE STARTING AT <DSK-ADRES>
4189                                     ;INDICATED IN EROR MSGE
4190                                     ;'RKDA' IN EROR MSGE GIVES THE
4191                                     ;CONTENTS OF RKDA AT THE TIME OF EROR
4192                                     ;DID RKBA INCREMENT CORRECTLY?
4193 012672 020077 166440 5$:  CMP     R0,@RKBA    ;YES, BRANCH
4194 012676 001406          BEQ     6$          ;GET EXPCTD RKBA
4195 012700 010037 001162  MOV     R0,$REGO    ;GET RKBA RECVD
4196 012704 017737 166426 001164 MOV     @RKBA,$REG1 ;RKBA DID NOT INCREMENT CORRECTLY
4197 012712 104035          ERROR 35          ;(BY 1000 OCTAL BYTES) AFTER WRITE
4198                                     ;OF 400 (OCTAL) WORDS ON SEC 0, CYL 0
4199                                     ;CHECK IF RKWC OVERFLOWED TO 0,
4200 012714 004737 021312 6$:  JSR     PC,CHKWC    ;IF NOT RETURN HERE.
4201                                     ;RKWC DID NOT OVERFLOW, AFTER A
4202 012720 104034          ERROR 34          ;WRITE OF 256 WORDS ON CYL 0, SEC 0
4203                                     ;CHECK IF RKDA INCREMENTED CORRECTLY.
4204 012722 004737 021256 7$:  JSR     PC,CHKDA    ;IF NOT RETURN HERE
4205                                     ;RKDA DID NOT INCREMENT BY 1 AFTER
4206 012726 104033          ERROR 33          ;A WRITE OF 256 WORDS IN CYL 0, SEC 0
4207                                     ;CHECK IF ANY BIT RKER IS SET
4208 012730 004737 021336 8$:  JSR     PC,CHKER    ;IF YES RETURN HERE.
4209                                     ;RKER BIT SET ON DOING WRITE ON
4210 012734 104036          ERROR 36          ;CYLINDER 0, SECTOR 0
4211                                     ;DOES RKCS STILL CONTAIN THE WRITE BITS?
4212 012736 022714 000202 9$:  CMP     #202,@R4    ;YES, EXIT
4213 012742 001406          BEQ     TST31        ;GET EXPECTED RKCS
4214 012744 012737 000202 001162 MOV     #202,$REGO  ;GET RKCS RECVD
4215 012752 011437 001164  MOV     @R4,$REG1  ;RKCS DID NOT CONTAIN THE 'WRITE'
4216 012756 104024          ERROR 24          ;BITS AFTER THE FUNCTION WAS DONE.
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;*****
; *TEST 31          CHECK THAT WRITE WAS DONE CORRECTLY

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4231 012760 000004
4232 012762 104413
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4242 012764 104421
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4244 012766 012700 177400
4245 012772 012701 033336
4246 012776 005021
4247 013000 005200
4248 013002 001375
4249 013004 005000
4250 013006 012777 177400 166320
4251 013014 012777 033336 166314
4252 013022 013777 001350 166310
4253
4254 013030 012777 000005 166274
4255
4256 013036 105777 166270
4257 013042 100411
4258 013044 005200
4259 013046 001373
4260
4261 013050 004737 020770
4262 013054 013737 001350 001202
4263 013062 104416
4264
4265 013064 104045
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4270
4271 013066 032777 001000 166232
4272 013074 001033
4273 013076 012701 177400
4274 013102 012702 177777
4275 013106 012703 033336
4276 013112 012705 177773
4277 013116 062702 177401

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; *THIS TEST CHECKS IF THE 'WRITE' OF 256 WORDS DONE IN PREVIOUS
; *TEST IS GOOD. THE SEQUENCE OF OPERATIONS IS AS FOLLOWING:
; *1) DO A READ OF 256 WORDS FROM SECTOR 0, CYLINDER 0
; * INTO A BUFFER STARTING AT 'OUTBUF'.
; *2) COMPARE & CHECK THE DATA THAT IS READ (STARTING AT 'OUTBUF',
; * WITH THE DATA THAT WAS GENERATED PREVIOUSLY
; *3) REPORT AN ERROR IF THE DATA READ BACK FROM DISK DOES
; * NOT COMPARE WITH DATA THAT WAS SUPPOSE TO HAVE BEEN WRITTEN
; *****

```

```

†T31: SCOPE
CNT.RESET

```

```

; GO, DO CONTROL RESET
; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR& IF IT
; OCCURS GO BACK TO TEST 10
; CHECK IF SIN IS SET, IF SET
; DO DRIVE RESET TO CLEAR IT
; SET COUNT FOR 400 WORDS
; TO BE CLEARED IN THE BUFFER
; CLR THE 400 WORD BUFFER
; STARTING AT 'OUTBUF'

```

```

TST.SIN
MOV #400,R0
MOV #OUTBUF,R1
8$: CLR (R1)+
INC R0
BNE 8$
CLR R0
MOV #400,DRKWC
MOV #OUTBUF,DRKBA
MOV DRIVAD,DRKDA
MOV #5,DRKCS
1$: TSTB DRKCS
BMI 2$
INC R0
BNE 1$
JSR PC,GT4RG
MOV DRIVAD,$REG10
BRKDA4
ERROR 45
2$: BIT #1000,DRKDS
BNE TST32
5$: MOV #-400,R1
MOV #177777,R2
MOV #OUTBUF,R3
MOV #-5,R5
6$: ADD #177401,R2

```

```

; READ 256 WORDS
; INTO THIS ADRES
; STARTING FROM THIS DISK ADRES
; READ, GO
; DID CNTRL RDY SET?
; YES, BRANCH
; WAITED LONG ENOUGH?
; IF NOT, LUP BAK & WAIT
; ERROR, IF YES
; GO, GET RKCD, ER, DS, DA
; GET THE STARTING ADRES
; GO TO 'BDAY' & BREAK CONTENTS OF
; $REG10 INTO DRV #, CYL, SUR, SEC BITS
; CNTRL RDY DID NOT SET AFTER READ
; OF 400 WORDS FROM CYL 0, SEC 0
; 'RKDA' IN EROR MSGE GIVES THE
; CONTENTS OF RKDA AT THE TIME OF EROR
; READ WAS DONE STARTING AT <DSK-ADRES>
; INDICATED IN EROR MESGE
; IS SIN SET?
; IF YES, EXIT

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4278 013122 020213
4279
4280 013124 001414
4281
4282 013126 010137 001162
4283 013132 062737 000401 001162
4284 013140 010237 001164
4285
4286 013144 011337 001166
4287 013150 104055
4288
4289
4290
4291 013152 005205
4292 013154 001403
4293 013156 005723
4294
4295 013160 005201
4296 013162 001355
4297
4298
4299
4300
4301
4302
4303
4304
4305 013164 000004
4306 013166 104413
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4316 013170 104421
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4318 013172 013701 001332
4319 013176 013702 001334
4320 013202 013703 001340
4321 013206 013704 001336
4322 013212 012737 052525 033336
4323 013220 012712 177400
4324 013224 013713 001350
4325 013230 012714 033336
4326 013234 012711 000013
4327
4328 013240 105711 15:
4329 013242 100003
4330 013244 004737 020776
4331 013250 104030
4332 013252 104412 25:
4333

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CMP      R2,(R3) ;WAS THE READ WORD SAME AS THE WORD
          ; THAT WAS SUPPOSE TO BE WRITTEN
BEQ      7$      ; YES, BRANCH
          ; NO ERROR
MOV      R1,$REG0 ; GET THE # OF WORD
ADD      #401,$REG0 ; THAT IS IN ERROR (EXAMPLE=1,2--376,377,400,
MOV      R2,$REG1 ; GET EXPCD WORD (THAT WAS SUPPOSED TO
          ; BE WRITTEN)
MOV      (R3),$REG2 ; GET WORD RECVD (THAT WAS READ BAK)
ERROR    55      ; DID NOT READ BACK WORD THAT WAS SUPPOSED
          ; TO HAVE BEEN WRITTEN PREVIOUSLY. POSITION
          ; OF WORD IN ERROR IS AS INDICATED BY
          ; WORD # ($REG0), SEC 0, CYL 0

          ;:EXIT
7$:      BLQ     TST32
          TST    (R3)+ ; INCREMENT POINTER TO NXT WORD (THAT
          ; WAS READ BACK)
          INC    R1    ; HAVE U CHKD ALL 256 WORDS?
          BNE   6$    ; IF NOT, LUP BAK & CHK THE NXT WORD
          ; IF YES, EXIT

;:*****
;:TEST 32 CHECK 'READ CHECK' FUNCTION - CYLINDER 0, SECTOR 0
;:THIS TEST CHECKS OUT THE BASIC 'READ CHECK' LOGIC, USING THE DATA BLOCK
;:('CYLINDER, SECTOR 0) WRITTEN IN A PREVIOUS TEST. HENCE THE TEST WHICH
;:WRITES THE DATA BLOCK SHOULD BE DONE PRIOR TO THIS TEST.
;:*****
TST32:   SCOPE
          CNT.RESET ; GO, DO CONTROL RESET
          ; THIS IS A CALL FOR THE 'CNTRL-
          ; RESET' ROUTINE. A CONTROL RESET IS
          ; ISSUED AND AFTER A CERTAIN TIME
          ; IF THE 'CNTRL RDY' DOES NOT SET
          ; AN ERROR IS REPORTED. NOTE THAT
          ; THE PC IN ERROR MESSAGE IS THE
          ; PC WHERE 'CNT.RESET' IS LOCATED.
          ; THIS IS A VERY BASIC ERR& IF IT
          ; OCCURS GO BACK TO TEST 10
          ; CHECK IF SIN IS SET, IF SET
          ; DO DRIVE RESET TO CLEAR IT

          TST.SIN

          MOV    RKCS,R1
          MOV    RKWC,R2
          MOV    RKDA,R3
          MOV    RKBA,R4
          MOV    #52525,OUTBUF
          MOV    #-400,R2 ; READ CHECK 256 WORDS
          MOV    DRIVAD,R3 ; STARTING FROM CYL 0, SECTOR 0
          MOV    #OUTBUF,R4
          MOV    #13,R1 ; READ CHECK, GO

          TSTB   R1    ; DID CNTRL RDY GET CLEARED AS GO WAS SET?
          BPL    2$    ; YES, BRANCH
          JSR    PC,GT3RG ; GET RKCS, ER, DS
          ERROR  30    ; CNTRL RDY DID NOT CLEAR AS GO
          ; GO CHECK IF CONTROL RDY IS SET
          ; IF SO, SKIP THE EROR MESSAGE.
          CHKCRDY

```

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4334
4335 013254 104056          ERROR 56          : WAS SET TO 'READ CHECK'
4336
4337 013256 032711 140000    3$: BIT      #140000,AR1  : CNTRL RDY DID NOT SET ON DOING
4338 013262 001403          BEQ      4$          : 'READ CHECK' FROM CYL 0, SEC 0
4339 013264 004737 020776    JSR      PC,GT3RG    : DID 'ERR' OR 'HE' BIT SET?
4340 013270 104057          ERROR 57          : NO, BRANCH
4341
4342 013272 032777 000002 166030 4$: BIT      #2,ARKER   : GO, GET RKCS, ER DS FOR ERROR MESSAGE
4343 013300 001404          BEQ      5$          : 'ERR' OR 'HE' BIT SET ON DOING
4344 013302 017737 166022 001162 MOV      ARKER,$REG0 : 'READ CHECK' ON CYLINDER 0, SEC 0
4345 013310 104060          ERROR 60          : DID 'CSE' BIT SET IN ARKER?
4346
4347
4348 013312 005712          5$: TST      AR2       : NO, BRANCH
4349 013314 001405          BEQ      6$          : GET RKER
4350 013316 011237 001162    MOV      AR2,$REG0   : SOFT ERROR - CSE - ON DOING 'READ
4351 013322 011137 001164    MOV      AR1,$REG1   : CHECK' ON CYLINDER 0, SECTOR 0
4352 013326 104061          ERROR 61          : U SHOULD HAVE GOT ERROR 102 ALSO
4353
4354 013330 013702 001350    6$: MOV      DRIVAD,R2  : DID WORD COUNT OVERFLOW TO 0?
4355 013334 005202          INC      R2          : YES, BRANCH
4356 013336 020213          CMP      R2,AR3     : GET RKWC
4357 013340 001405          BEQ      7$          : GET RKCS
4358 013342 010237 001162    MOV      R2,$REG0   : WORD COUNT DID NOT OVERFLOW
4359 013346 011337 001164    MOV      AR3,$REG1   : ON DOING 'READ CHK' ON CYL 0, SEC 0
4360 013352 104062          ERROR 62          : RKDA SHOULD INCREMENT
4361
4362
4363 013354 022714 033336    7$: CMP      #OUTBUF,AR4 : TO THIS AFTER 'RD CHK' IS DONE
4364 013360 001406          BEQ      8$          : DID RKDA INCREMENT CORRECTLY?
4365 013362 012737 033336 001162 MOV      #OUTBUF,$REG0 : GET EXPCTD RKDA
4366 013370 011437 001164    MOV      AR4,$REG1   : GET RKDA RECVD
4367 013374 104063          ERROR 63          : RKDA DID NOT INCREMENT CORRECTLY
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4369
4370 013376 022737 052525 033336 8$: CMP      #52525,OUTBUF : (BY 1) ON DOING 'READ CHK' ON
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4373
4374 013404 001412          BEQ      TST33      : CYL 0, SEC 0
4375
4376 013406 012737 033336 001162 MOV      #OUTBUF,$REG0 : DID RKBA GET CHANGED?
4377 013414 012737 052525 001164 MOV      #52525,$REG1 : NO, BRANCH (RKBA WON'T CHANGE, NO NPR'S)
4378 013422 013737 033336 001166 MOV      OUTBUF,$REG2 : GET EXPCTD RKBA
4379 013430 104064          ERROR 64          : GET RKBA RECVD
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:*****
:*TEST 33 CHECK THE 'WRITE CHECK' FUNCTION - ON CYLINDER 0, SECTOR 0
:*THIS TEST CHECKS OUT THE BASIC 'WRITE CHECK' LOGIC, USING THE 256
:*WORD DATA BLOCK (SECTOR 0, CYLINDER 0) WRITTEN IN A PREVIOUS
:*TEST. THE BUFFER IN MEMORY, USED FOR COMPARISON OF DATA, IS THE

```

: *ONE STARTING AT 'OUTBUF'. HENCE THE TEST WHICH WRITES THE
: *256 WORD BLOCK ON THE DISK (AS WELL AS CREATING THE 256
: *256 WORD MEMORY BUFFER) SHOULD BE DONE BEFORE THIS TEST.

†T33: SCOPE
CNT.RESET

: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF I
: OCCURS GO BACK TO TEST 10
: CHECK IF SIN IS SET, IF SET
: DO DRIVE RESET TO CLEAR IT

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013432 000004
013434 104413

013436 104421

013440 013701 001332
013444 012700 177400
013450 012702 033336
013454 012703 177777
013460 062703 177401
013464 010322
013466 005200
013470 001373
013472 012777 177400 165634
013500 012777 033336 165630
013506 013777 001350 165624
013514 012711 000007

TST.SIN

1\$:

MOV RKCS,R1
MOV #-400,R0
MOV #OUTBUF,R2
MOV #177777,R3
ADD #177401,R3
MOV R3,(R2)+
INC R0
BNE 1\$
MOV #-400,DRKWC
MOV #OUTBUF,DRKBA
MOV DRIVAD,DRKDA
MOV #7,DR1

: WRITE CHECK 256 WORDS
: STARTING AT THIS BUS PDRES
: WITH THIS DISK DATA BLOCK (CYL 0, SEC 0)
: WRITE CHECK, GO

2\$:

CLR R0
TSTB DR1
BPL 3\$
JSR PC,GT3RG
ERROR 30

: GIVE SOME TIME
: DID CNTRL RDY CLEAR AS GO WAS SET?
: YES BRANCH
: GET RKCS, ER, DS
: CNTRL RDY DID NOT CLEAR AS GO WAS
: SET TO DO WRITE CHECK

3\$:

CHKRDY

: GO CHECK IF CONTROL RDY IS SET
: IF SO, SKIP THE EROR MESSAGE.

ERROR 65

: CNTRL RDY DID NOT SET AFTER
: COMPLETING WRITE CHECK ON
: CYLINDER 0, SECTOR 0

4\$:

BIT #140000,DR1
BEQ 5\$
JSR PC,GT3RG
ERROR 66

: DID HE OR ERR BIT SET
: NO. BRANCH
: GO GET RKCS, ER, DS FOR ERROR MESSAGE
: HE OR ERR BIT SET ON DOING WRITE

5\$:

BIT #1,DRKER
BEQ 6\$
JSR PC,GT3PG
ERROR 67

: DID WCE SET IN RKER?
: NO. BRANCH
: YES GET RKCS, ER, DS
: WCE ON WRITE CHECK OF CYL 0, SEC 0
: NOTE THAT IF A PREVIOUS TEST
: & THEN COMPARED WITH MEMORY BUFFER
: TO SEE IF IT WAS WRITTEN CORRECT WAS
: DONE RIGHT BEFORE, THIS ERROR SHOULD NOT
: HAPPEN UNLESS THERE IS A FAULT IN THE
: COMPARING LOGIC OF 'WRT CHK'

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4446 013572 005777 165536 6$: TST 2RKWC ;DID RKWC OVERFLOW?
4447 013576 001406 7$: BEQ 7$ ;YES, BRANCH
4448 013600 017737 165530 001162 MOV 2RKWC,$REG0 ;NO, GET RKWC
4449 013606 011137 001164 MOV 2R1,$REG1 ;GET RKCS
4450 013612 104061 ERROR 61 ;RKWC DID NOT OVERFLOW AFTER
4451 ;WRITE CHECK ON CYL 0, SEC 0
4452 013614 013704 001350 7$: MOV DRIVAD, R4 ;RKDA SHOULD INCREMENT
4453 013620 005204 INC R4 ;TO THIS AFTER WRT CHK
4454 013622 020477 165512 CMP R4,2RKDA ;DID RKDA INCREMENT CORRECTLY?
4455 013626 001406 BEQ 8$ ;YES, BRANCH
4456 013630 010437 001162 MOV R4,$REG0 ;NO, GET EXPCTD RKDA
4457 013634 017737 165500 001164 MOV 2RKDA,$REG1 ;GET RKDA RECVD
4458 013642 104070 ERROR 70 ;RKDA DID NOT INCREMENT CORRECTLY
4459 ;(BY 1 SECTOR) AFTER WRT CHK ON SEC 0, CYL 0
4460 013644 022777 034336 165464 8$: CMP #OUTBUF+1000,2RKBA ;DID RKBA INCREMENT CORRECTLY?
4461 013652 001407 BEQ 9$ ;YES, EXIT
4462 013654 012737 034336 001162 MOV #OUTBUF+1000,$REG0 ;GET EPCTD RKBA
4463 013662 017737 165450 001164 MOV 2RKBA,$REG1 ;GET RKBA RECVD
4464 013670 104071 ERROR 71 ;RKBA DID NOT INCREMENT CORRECTLY
4465 ;(BY 1000 BYTES) AFTER A WRT CHK
4466 ;OF 256 WORDS ON CYL 0, SEC 0
4467 013672 022711 000206 9$: CMP #206,2R1 ;DOES RKCS STILL CONTAIN THE WRT CHK BITS?
4468 013676 001406 BEQ TST34 ;YES, BRANCH
4469 013700 012737 000206 001162 MOV #206,$REG0 ;NO, GET EXPCTD RKCS
4470 013706 011137 001164 MOV 2R1,$REG1 ;GET RKCS RECVD
4471 013712 104024 ERROR 24 ;RKCS BITS CHANGED AFTER WRT CHK
4472 ;WAS DONE
4473 ;*****
4474 ;*TEST 34 CHECK THAT IBA INHIBITS INCREMENTING OF RKBA
4475 ;*THIS TEST CHECKS THAT THE BUS ADDRESS DOES NOT INCREMENT WHEN
4476 ;*THE IBA BIT IS SET. SEQUENCE OF OPERATIONS:
4477 ;*1) CLEAR OUT 256 WORD BUFFER IN MEMORY (OUTBUF,
4478 ;*2) READ FROM SECTOR 0, CYLINDER 0 THE 256 WORD BLOCK THAT WAS
4479 ;*WRITTEN IN A PREVIOUS TEST (NOTE: THAT TEST SHOULD HAVE BEEN
4480 ;*DONE BEFORE THIS). IBA BIT IS SET DURING READ BACK.
4481 ;*3) CHECK THAT RKBA DID NOT INCREMENT
4482 ;*4) CHECK THAT THE ENTIRE BLOCK WAS READ INTO THE SAME MEMORY
4483 ;*WORD (OUTBUF) & THE REST OF THE WORDS IN THAT BUFFER ARE C
4484 ;*AS PREVIOUSLY CLEARED OUT.
4485 ;*****
4486 013714 000004 TST34: SCOPE
4487 013716 104413 CNT.RESET ;GO, DO CONTROL RESET
4488 ;THIS IS A CALL FOR THE 'CNTRL-
4489 ;RESET' ROUTINE. A CONTROL RESET IS
4490 ;ISSUED AND AFTER A CERTAIN TIME
4491 ;IF THE 'CNTRL RDY' DOES NOT SET
4492 ;AN ERROR IS REPORTED. NOTE THAT
4493 ;THE PC IN ERROR MESSAGE IS THE
4494 ;PC WHERE 'CNT.RESET' IS LOCATED.
4495 ;THIS IS A VERY BASIC ERRS IF IT
4496 ;OCCURS GO BACK TO TEST 10
4497 013720 104421 TST.SIN ;CHECK IF SIN IS SET, IF SET
4498 ;DO DRIVE RESET TO CLEAR IT
4499 013722 013701 001332 MOV RKCS,R1
4500 013726 012700 177400 MOV #-400,R0 ;SET UP COUNT FOR 256 WORDS
4501 013732 012702 033336 MOV #OUTBUF,R2

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4502	013736	010203			MOV	R2,R3	
4503							
4504	013740	005023			1\$: CLR	(R3)+	:CLEAR OUT THE 256
4505	013742	005200			INC	RO	:WORD MEMORY BUFFER STARTING
4506	013744	001375			BNE	1\$:AT 'OUTBUF'
4507	013746	012777	177400	165360	MOV	#-400,ARKWC	:READ BACK 256 WORDS
4508	013754	010277	165356		MOV	R2,ARKBA	:INTO THIS BUS ADRES (IBA WILL B SET,
4509	013760	013777	001350	165352	MOV	DRIVAD,ARKDA	:FROM THIS DSK ADRES (SEC 0, CYL 0,
4510							:NOTE: SEC 0 HAS BEEN WRITTEN IN A
4511							:PREVIOUS TEST WITH A UNIQUE PATTERN
4512	013766	012711	004005		MOV	#4005,ARI	:READ, GO, IBA SET
4513							
4514	013772	005037	001362		CLR	COUNT	
4515	013776	105711			2\$: TSTB	ARI	:DID CNTRL RDY SET?
4516	014000	100412			BMI	3\$:YES, BRANCH
4517	014002	005237	001362		INC	COUNT	:WAITED LONG ENOUGH?
4518	014006	001373			BNE	2\$:IF NOT, LUP BAK & WAIT
4519	014010	004737	020770		JSR	PC,GT4RG	:GO GET RKCS, ER, DS, DA
4520	014014	013737	001350	001202	MOV	DRIVAD,\$REG10	:GET THE STARTING ADRES
4521	014022	104416			BRKDAY		:BREAK CONTENTS OF \$REG10
4522							:INTO DR #, CYL, SUR, SEC
4523	014024	104045			ERROR	45	:CNTRL RDY DID NOT SET AFTER DOING
4524							:READ
4525	014026	004737	021230		3\$: JSR	PC,CHKHE	:CHECK IF 'ERR' OR 'HE' BIT IS SET.
4526							:IF YES, RETURN HERE.
4527	014032	104046			ERROR	46	:ERR BIT SET ON DOING READ FROM SEC 0.
4528							:CYL 0 (INDICATED IN <DSK-ADRES >
4529							: 'RKDA' IN EROR MSGE GIVES THE
4530							: CONTENTS OF RKDA AT THE TIME OF EROR
4531							
4532	014034	020277	165276		4\$: CMP	R2,ARKBA	:DID RKBA INCREMENT?
4533	014040	001406			BEQ	5\$:OK IF NOT, BRANCH
4534	014042	010237	001162		MOV	R2,\$REG0	:GET EXPCTD RKBA
4535	014046	017737	165264	001164	MOV	ARKBA,\$REG1	:GET RKBA RECVD
4536	014054	104072			ERROR	72	:RKBA INCREMNTED WHEN IBA BIT WAS
4537							:SET, SHOULD NOT HAVE
4538	014056	032777	001000	165242	5\$: BIT	#1000,ARKDS	:IS SIN SET?
4539	014064	001042			BNE	TST35	::IF YES, EXIT
4540	014066	012700	177400		MOV	#-400,RO	
4541	014072	022712	000377		CMP	#377,AR2	:CHECK THAT THE FIRST WORD IN
4542							: 'OUTBUF' IS 377 (LAST WORD OF SEC 0,
4543							: CYL 0). NOTE THAT READ WAS DONE
4544	014076	001411			BEQ	6\$:INTO THIS SAME WRD WITH IBA SET
4545	014100	012737	000377	001162	MOV	#377,\$REG0	:GET EXPCTD WORD (LAST WORD OF THE BUFFER
4546	014106	011237	001164		MOV	(R2),\$REG1	:GET WORD RECVD (LAST WRD FROM SEC 0)
4547	014112	013737	001350	001166	MOV	DRIVAD,\$REG2	:DISK ADRES WHERE ERROR OCCURED
4548							: (SEC 0, CYL 0 LAST WORD)
4549							: DATA ERROR
4550	014120	104044			ERROR	44	:THE FIRST WORD IN MEM BUFFER (OUTBUF)
4551							: SHOULD BE NON-ZERO & SHOULD CONTAIN
4552							: THE LAST WORD READ BACK FROM SEC 0
4553							: CYL 0, THIS DID NOT HAPPEN IF THE ERROR OCCURS
4554	014122	005722			6\$: TST	(R2)+	:INCREMENT POINTER TO THE NXT WORD
4555	014124	012705	177773		MOV	#-5,RS	:ALLOW ONLY 5 MESSAGES FOR ERR 116
4556	014130	005200			7\$: INC	RO	:CHKD ALL 256 WORDS IN THE BUFFER
4557	014132	001417			BEQ	TST35	::YES, EXIT


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4558 014134 005722      TST      (R2)+      ;IS THIS WORD 0?
4559 014136 001774      BEQ      7$         ;YES, LUP BAK & CHK THE NXT WORD?
4560 014140 005037 001164  CLR      $REG1     ;ERROR. GET EXPCTED WORD - 0
4561 014144 014237 001166  MOV      -(R2), $REG2 ;GET WORD THAT WAS FOUND IN THE BUFFER
4562 014150 010004      MOV      R0, R4
4563 014152 062704 000401  ADD      #401, R4
4564 014156 010437 001162  MOV      R4, $REG0  ;THIS 'WORD #' IN MEMORY BUFFER
4565                                     ;SHOULD HAVE BEEN ZERO
4566 014162 104113      ERROR 73          ;THE 256 WORD BUFR (STARTING AT
4567                                     ;OUTBUF) WAS CLEARED BEFORE READING
4568                                     ;BAK SEC 0 INTO IT. SINCE THE IBA
4569                                     ;BIT WAS SET DURING THE READ, ONLY
4570                                     ;THE FIRST WORD OF (OUTBUF) SHOULD
4571                                     ;HAVE CHANGED. THE REST OF THE WORDS
4572                                     ;SHOULD BE STILL 0. IF THIS ERROR
4573                                     ;OCCURS, 'WORD #' (OF THE BUFFER) AS
4574                                     ;INDICATED IN THE EROR MESSAGE) GOT
4575                                     ;CHANGED WHEN READ WAS DONE FROM
4576                                     ;THE DISK, INDICATING THAT WITH IBA
4577                                     ;SET X-FER WAS NOT DONE INTO THE
4578                                     ;SAME MEMORY LOCATION. 'WORD #'
4579                                     ;IS OCTAL & SPECIFIES THE POSITION
4580                                     ;IN THE BUFFER (FIRST WORD IS 'WORD #' 1)
4581 014164 005205      INC      R5
4582 014166 001401      BEQ      TST35     ;EXIT
4583 014170 000757      BR       7$
4584
4585 ;*****
4586 ;*TEST 35 CHECK THAT RK11 INTERRUPTS WHEN IDE IS SET
4587 ;*THIS TEST CHECKS IF RK11 INTERRUPTS TO ITS DESIGNATED VECTOR
4588 ;*ADDRESS WHEN IDE BIT IS SET, WITH CONTROL READY SET & GO CLEAR.
4589 ;* IT IS NORMALLY 220, UNLESS IT HAS BEEN CHANGED. IF IT HAS BEEN
4590 ;*CHANGED RK11 WILL INTERRUPT TO 'RKVEC'. NOTE 'RKVEC' HAS
4591 ;*TO BE SET UP BY THE USER.
4592 ;*****
4593 014172 000004      TST35: SCOPE
4594 014174 104413      CNT.RESET        ;GO, DO CONTROL RESET
4595                                     ;THIS IS A CALL FOR THE 'CNTRL-
4596                                     ;RESET' ROUTINE. A CONTROL RESET IS
4597                                     ;ISSUED AND AFTER A CERTAIN TIME
4598                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4599                                     ;AN ERROR IS REPORTED. NOTE THAT
4600                                     ;THE PC IN ERROR MESSAGE IS THE
4601                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4602                                     ;THIS IS A VERY BASIC ERR& IF IT
4603                                     ;OCCURS GO BACK TO TEST 10
4604 014176 104421      TST.SIN        ;CHECK IF SIN IS SET, IF SET
4605                                     ;DO DRIVE RESET TO CLEAR IT
4606 014200 012746 000340  MOV      #340, -(SP)
4607 014204 012746 014212  MOV      #64$, -(SP)
4608 014210 000002      RTI
4609
4610 014212 013701 001332      64$: MOV      RKCS, R1
4611 014216 013700 001402  MOV      RKVEC, R0  ;GET POINTER TO RK VECTOR ADRES
4612 014222 012720 014256  MOV      #1$, (R0)+ ;SET UP INTERRUPT VECTOR FOR RK11
4613 014226 012710 000340  MOV      #340, (R0) ;SET PSW ON INTERRUPT

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Address	Op1	Op2	Op3	Op4	Label	Instruction	Comments
4670	014336	013700	001332			MOV RKCS,RO	
4671	014342	013777	001356	164770		MOV DRIVAD,ARKDA	;ADRES THE DRIVE
4672	014350	004737	021500			JSR PC,DRESET	;GO, DO DRIVE RESET
4673	014354	104026				ERROR 26	;R/W/S ROY DIDN'T SET AFTER DOING
4674							;ABOVE DRIVE RESET
4675	014356	013701	001402		2\$:	MOV RKVEC,R1	
4676	014362	012721	014426			MOV #3\$, (R1)+	;SET UP VECTOR ADRES FOR RK11 INTERUPT
4677	014366	012711	000340			MOV #340, (R1)	;SET UP PSW ON INTERRUPT
4678	014372	052777	000040	164740		BIS #40,ARKDA	;ADRES CYLINDER #1
4679	014400	012710	000111			MOV #111,ARO	;SEEK, GO WITH IDE SET
4680	014404	104420	000300			WAT.INT ,300	;WAIT FOR THE DRIVE TO
4681							;INTERRUPT AFTER ADRES WAS RECVD
4682							;WAITING TIME= 1.4 MS FOR 11/20
4683							;280 US FOR 11/45
4684							;ERROR, IF INTERUPT DID NOT OCCUR
4685							;BY NOW
4686	014410	012777	004600	164764		MOV #BADINT,ARKVEC	;RESTORE UNEXPECTED RK11 INTERRUPT
4687	014416	011037	001162			MOV ARO,\$REGO	;GET RKCS
4688	014422	104075				ERROR 75	;INTERRUPT DID NOT OCCUR AFTER
4689							;SEEK WAS INITIATED WITH IDE SET
4690	014424	000402				BR 3\$+4	
4691	014426	022626			3\$:	CMP (SP)+,(SP)+	;OK, IF RK11 INTERRUPTED TO THIS
4692							;RESTORE STACK POINTER (FROM RK11 INTERRUPT)
4693	014430	022626				CMP (SP)+,(SP)+	;RESTORE STACK POINTER (FROM
4694							;WAT.INT)
4695	014432	012777	014476	164742		MOV #5\$,ARKVEC	;SET UP NEW VECTOR ADRES FOR RK11
4696	014440	032710	020000			BIT #20000,ARO	;IS SCP CLEAR
4697	014444	001403				BEQ 4\$;YES, BRANCH
4698	014446	011037	001162			MOV ARO,\$REGO	;GET RKCS
4699	014452	104076				ERROR 76	;SCP SET BEFORE SEEK TO LAST
4700							;CYLINDER WAS DONE
4701	014454	104420	056700		4\$:	WAT.INT ,56700	;WAIT FOR DRIVE TO INTERRUPT
4702							;AFTER SEEK WAS COMPLETED
4703							;WAITING TIME=180 MS FOR 11/20
4704							;36 MS FOR 11/45
4705	014460	012777	004600	164714		MOV #BADINT,ARKVEC	;IT'S AN ERROR IF BY THIS TIME
4706							;INTERRUPT HAS NOT OCCURED
4707	014466	004737	020776			JSR PC,GT3RG	;GO GET RKCS, ER_DS
4708	014472	104077				ERROR 77	;RK11 DID NOT INTERRUPT AFTER SEEK (TO
4709							;LAST CYLINDER) WAS DONE WITH IDE SET
4710	014474	000401				BR 5\$+2	
4711	014476	022626			5\$:	CMP (SP)+,(SP)+	;OK, IF RK11 INTERRUPTED TO THIS AFTER
4712							;SEEK WAS COMPLETED. RESTORE
4713							;STACK POINTER (FROM RK11 INTERRUPT)
4714	014500	022626				CMP (SP)+,(SP)+	;RESTORE STACK POINTER (FROM
4715							;WAT.INT)
4716	014502	012777	004600	164672		MOV #BADINT,ARKVEC	;RESTORE RK11 INTERRUPT VECTOR ADRES
4717							;FOR UNEXPECTED INTERUPTS
4718	014510	032710	020000			BIT #20000,ARO	;DID SCP BIT SET?
4719	014514	001003				BNE 6\$;YES, BRANCH
4720	014516	011037	001162			MOV ARO,\$REGO	;GET RKCS
4721	014522	104053				ERROR 53	;SCP DID NOT SET AFTER RK11 INTERRUPTED
4722							;INDICATING SEEK WAS DONE
4723	014524	017701	164576		6\$:	MOV ARKDS,R1	;GET RKDS
4724	014530	042701	017777			BIC #17777,R1	;MASK NON-ID BITS IN RKDS
4725	014534	020137	001350			CMP R1,DRIVAD	;CORRECT ID BITS IN RKDS?

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4726 014540 001414      BEQ      7$          ;YES, BRANCH
4727
4728 014542 013746 001350      MOV      DRIVAD, -(SP) ;PUSH DRV ADRES ON THE STACK
4729 014546 004737 021174      JSR      PC, SHFRT    ;GO, SHIFT RIGHT DRV #
4730 014552 012637 001162      MOV      (SP)+, $REGO ;GET EXPCD DRV #
4731 014556 010146      MOV      R1, -(SP)   ;PUSH ID BITS ON THE STACK
4732 014560 004737 021174      JSR      PC, SHFRT    ;GO SHIFT THEM RIGHT
4733 014564 012637 001164      MOV      (SP)+, $REG1 ;POP THE RECVD ID BITS
4734 014570 104047      ERROR    47          ;WRONG ID BITS WERE RECVD IN
4735                                     ;RKDS AFTER SEEK WAS DONE (INTRUPT
4736                                     ;MODE). 'EXPCD' INDICATES THE DRIVE
4737                                     ;# THAT SHOULD HAVE BEEN IN THE
4738                                     ;ID BITS, 'RECVD' INDICATES THE
4739                                     ;DRIVE # THAT WAS RECVD IN THE ID BITS
4740
4741 014572      7$:
4742 014572 012746 000340      MOV      #340, -(SP)
4743 014576 012746 014604      MOV      #64$, -(SP)
4744 014602 000002      RTI
4745 014604      64$:
4746 014604 104413      CNT.RESET ;GO DO CONTROL RESET
4747 014606 013777 001350 164524      MOV      DRIVAD, @RKDA ;ADRES THE DRIVE
4748 014614 032777 160000 164504      BIT      #160000, @RKDS ;DID CNTRL RESET CLEAR DRIVE ID BITS?
4749 014622 001404      BEQ      8$          ;YES, BRANCH
4750 014624 017737 164476 001162      MOV      @RKDS, $REGO ;GET RKDS
4751 014632 104050      ERROR    50          ;CONTROL RESET DIDN'T CLEAR THE
4752                                     ;DRIVE ID BITS (13-15) IN RKDS
4753
4754
4755 014634 022710 000200      8$:  CMP      #200, @R0   ;WAS SCP BIT CLEARED BY CNTRL RESET?
4756 014640 001403      BEQ      TST37       ;YES, EXIT
4757 014642 011037 001162      MOV      @R0, $REGO  ;GET RKCS
4758 014646 104100      ERROR    100        ;CNTRL RESET DID NOT CLEAR SCP BIT
4759
4760 ;:*****
4761 ;*TEST 37      CHECK THAT WITH IDE SET RK11 INTERRUPTS WHEN READ IS DONE
4762 ;*THIS TEST CHECKS THAT WHEN A DATA TRANSFER FUNCTION IS DONE
4763 ;*WITH IDE BIT SET, RK11 INTERRUPTS WHEN THE FUNCTION IS COMPLETED
4764 ;*FUNCTION USED IN THIS TEST IS READ.
4765 ;:*****
4766 014650 000004      †TST37: SCOPE
4767 014652 104413      CNT.RESET ;GO, DO CONTROL RESET
4768                                     ;THIS IS A CALL FOR THE 'CNTRL-
4769                                     ;RESET' ROUTINE. A CONTROL RESET IS
4770                                     ;ISSUED AND AFTER A CERTAIN TIME
4771                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4772                                     ;AN ERROR IS REPORTED. NOTE THAT
4773                                     ;THE PC IN ERROR MESSAGE IS THE
4774                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4775                                     ;THIS IS A VERY BASIC ERR& IF IT
4776                                     ;OCCURS GO BACK TO TEST 10
4777 014654 104421      TST.SIN ;CHECK IF SIN IS SET, IF SET
4778                                     ;DO DRIVE RESET TO CLEAR IT
4779
4780 014656 013700 001332      MOV      RKCS, R0
4781 014662 013702 001340      MOV      RKDA, R2

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4782 014666 013704 001336      MOV      RKBA,R4
4783 014672 013701 001350      MOV      DRIVAD,R1
4784 014676 052701 000013      BIS      #13,R1          ;SET BITS FOR SEC 13
4785 014702 012777 177600 164424      MOV      #-200,DRKWC    ;READ 200 (OCTAL WORDS)
4786 014710 010112                MOV      R1,DR2        ;FROM THIS DISK ADRES (CYL 0, SEC 13)
4787 014712 012714 033336      MOV      #OUTBUF,DR4   ;INTO THIS BUS ADRES
4788 014716 013705 001402      MOV      RKVEC,R5
4789 014722 012725 014760      MOV      #15,(R5)+     ;SET UP VECTOR ADRES FOR RK11 TO INTRUPT
4790 014726 012715 000340      MOV      #340,(R5)     ;SET PSW ON INTERUPT
4791 014732 012710 000105      MOV      #105,DR0      ;READ, GO, IDE SET
4792 014736 104420 127710      WAT.INT ,127710       ;WAIT FOR RK11 TO INTERRUPT ON
4793                                ;COMPLETION OF READ
4794                                ;WAITING TIME= 337 MS FOR 11/20
4795                                ;67 MS FOR 11/45
4796 014742 012777 004600 164432      MOV      #BADINT,DRKVEC ;RESTORE UNEXPTED INTERRUPT VECTOR ADRES
4797 014750 011037 001162      MOV      DR0,$REG0     ;GET RKCS
4798 014754 104101                ERROR    101          ;RK11 DID NOT INTERRUPT AFTER READ
4799                                ;WAS DONE, IDE BIT SET.
4800 014756 000404                BR      15+10
4801 014760 022626                15:    CMP      (SP)+,(SP)+  ;OK, IF RK11 INTERRUPTED TO THIS
4802                                ;RESTORE STACK POINTER (FROM RK11 INTERRUPT)
4803 014762 022626                CMP      (SP)+,(SP)+  ;RESTORE STACK POINTER (FROM WAT.INT)
4804 014764 012777 004600 164410      MOV      #BADINT,DRKVEC ;RESTORE UNEXPECTED RK11 INTERRUPT
4805                                ;VECTOR ADRES
4806 014772 004737 021336      JSR      PC,CHKR       ;CHECK IF ANY BIT IN RKER IS SET.
4807                                ;IF YES, RETURN HERE.
4808 014776 104036                ERROR    36          ;RKER SET ON DOING READ FROM SEC 0.
4809                                ;CYL 13 IN INTERRUPT MODE
4810 015000 062701 000005                45:    ADD      #5,R1        ;RKDA SHOULD HAVE INCREMENTED TO THIS
4811 015004 020112                CMP      R1,DR2        ;DID RKDA INCREMENT CORRECTLY?
4812 015006 001405                BEQ     25             ;YES BRANCH
4813 015010 010137 001162      MCV     R1,$REG0       ;GET EXPCTD RTDA
4814 015014 011237 001164      MOV     DR2,$REG1     ;GET RKDA RECVD
4815 015020 104040                ERROR    40          ;RKDA INCREMENTED WRONG ON DOING
4816                                ;A READ ON CYL 0, SEC 13
4817 015022 004737 021312                25:    JSR      PC,CHKWC     ;CHECK THAT RKWC OVERFLOWED TO 0.
4818                                ;IF NOT RETURN HERE.
4819 015026 104041                ERROR    41          ;RKWC DIDN'T OUFLO AFTER
4820                                ;A READ OF 200 WORDS
4821
4822 015030                35:
4823 015030 012746 000340      MOV     #340,-(SP)
4824 015034 012746 015042      MOV     #645,-(SP)
4825 015040 000002                RTI
4826 015042                645:
4827 015042 022714 033736      CMP     #OUTBUF+400,DR4 ;DID RKBA INCREMENT CORRECTLY?
4828 015046 001406                BEQ     15T40         ;:YES, EXIT
4829 015050 012737 033736 001162      MOV     #OUTBUF+400,$REG0 ;GET EXPCT RKBA
4830 015056 011437 001164      MOV     DR4,$REG1     ;GET RKBA RECVD
4831 015062 104042                ERROR    42          ;RKBA DID NOT INCREMENT CORRECTLY
4832                                ;AFTER A READ OF 200 WORDS
4833
4834 ;*****
4835 ;*TEST 40 CHECK THAT RK11 INTERRUPTS AT BR5 ONLY
4836 ;*THIS TEST CHECKS THAT RK11 CAN ITERRUPT AT BR5 ONLY. IF IT
4837 ;*INTERRUPTS AT A LEVEL HIGHER THAN BR5 AN ERROR IS INDICATED.

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4838
4839
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4844 015064 000004
4845 015066 104413
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4851
4852
4853
4854
4855 015070 104421
4856
4857 015072 012737 015126 001110
4858
4859 015100 013700 001332
4860 015104 013777 001350 164226
4861 015112 012701 000007
4862 015116 012702 000340
4863 015122 013703 001400
4864
4865
4866
4867 015126 013704 001402
4868 015132 012724 015240
4869 015136 012714 000340
4870 015142 010246
4871 015144 012746 015152
4872 015150 000002
4873 015152
4874 015152 012710 000100
4875 015156 012705 177760
4876 015162 005205
4877 015164 001376
4878 015166 020203
4879 015170 003005
4880
4881
4882 015172 010137 001162
4883 015176 011037 001164
4884 015202 104103
4885
4886
4887 015204 005010
4888 015206 062702 177740
4889
4890 015212 005301
4891 015214 001344
4892
4893 015216 012777 004600 164156

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; *IF IT DOES NOT INTERRUPT AT BR5 OR LOWER THEN ALSO AN
; *ERROR IS INDICATED. IF FOR SOME REASON THE INTERRUPT
; *LEVEL IS CHANGED FROM BR5, THEN CONTENTS OF RKPRI WILL
; *HAVE TO BE CHANGED ACCORDINGLY AND STILL TEXT WILL
; *CHECK FOR THIS BR LEVEL.
;*****

```

```

†ST40: SCOPE
CNT.RESET

```

```

; GO, DO CONTROL RESET
; THIS IS A CALL FOR THE 'CNTRL-
; RESET' ROUTINE. A CONTROL RESET IS
; ISSUED AND AFTER A CERTAIN TIME
; IF THE 'CNTRL RDY' DOES NOT SET
; AN ERROR IS REPORTED. NOTE THAT
; THE PC IN ERROR MESSAGE IS THE
; PC WHERE 'CNT.RESET' IS LOCATED.
; THIS IS A VERY BASIC ERR& IF IT
; OCCURS GO BACK TO TEST 10
; CHECK IF SIN IS SET, IF SET
; DO DRIVE RESET TO CLEAR IT
; SET RETURN ADRES FOR LUPING
; ON ERROR (SW 9)

```

```

TST.SIN

```

```

MOV #15,$LPERR
MOV RKCS,R0
MOV DRIVAD,DRKDA
MOV #7,R1
MOV #340,R2
MOV RKPRI,R3

```

```

; PRIORITY LEVEL 7
; BR LEVEL 7 FOR PSW
; NOTE, IF RK11 INTERRUPT LEVEL IS
; CHANGED FROM 5 TO ANY OTHER LEVEL
; THEN CHANGE CONTENTS OF 'RKPRI'
; ACCORDINGLY

```

```

1$: MOV RKVEC,R4
MOV #3$,(R4)+
MOV #340,(R4)
MOV R2,-(SP)
MOV #4$,-(SP)
RTI

```

```

; SET UP ADRES FOR RK11 TO INTERUPT
; SET UP PSW ON INTERUPT
; SET PROCESSOR PRIORITY LEVEL AS

```

```

4$: MOV #100,DR0
MOV #-20,R5
INC R5
BNE #-2
CMP R2,R3
BGT 2$

```

```

; INDICATED BY R2
; SET THE IDE BIT
; WAIT FOR THE RK11 INTERRUPT
; WAITING TIME=78 US FOR 11/20
; 13 US FOR 11/45
; WAS THE CPU PRIORITY LEVEL LESS THAN
; THE RK11 LEVEL? IF YES, RK11
; SHOULD HAVE INTERRUPTED. ERROR,
; IF IT DID NOT

```

```

MOV R1,$REG0
MOV DR0,$REG1
ERROR 103

```

```

; GET CPU BR LEVEL
; GET RKCS
; THOUGH CPU LEVEL WAS LESS THAN
; THE RK11 LEVEL (5), RK11 DID NOT
; INTERRUPT

```

```

2$: CLR DR0
ADD #-40,R2

```

```

; CLEAR RKCS
; DECREASE THE PRIORITY LEVEL (FOR
; CPU) BY 1

```

```

DEC R1
BNE 1$

```

```

; CPU WILL B AT THIS LEVEL
; LUP BAK & CHK FOR THIS BR LEVEL.
; DONE WITH CHKING FOR ALL LEVELS.
; RESTORE UNEXPECTED RK11 INTERRUPT

```

```

4894                                     ;VECTOR
4895 015224 012746 000340             MOV    #340,-(SP)
4896 015230 012746 015236             MOV    #64$,-(SP)
4897 015234 000002                     RTI
4898 015236                                     64$:
4899 015236 000414                     BR     TST41                ;;EXIT,TO NXT TST
4900
4901 015240 022626                     3$:  CMP    (SF)+,(SF)+        ;RESTORE STACK POINTER
4902 015242 012777 004600 164132      MOV    #BADINT,ARKVEC      ;RESTORE UNEXPECTED RK11 INTERRUPT
4903                                     ;VECTOR
4904 015250 020203                     CMP    R2,R3              ;IF THIS INTERRUPT OCCURED WHEN
4905 015252 003754                     BLE    2$                 ;CPU LEVEL WAS LESS THAN THE
4906                                     ;RK11 PRIORITY LEVEL (5) THEN IT IS
4907                                     ;OK. IF NOT SO, ERROR
4908 015254 010137 001162             MOV    R1,$REGO           ;GET CPU BR LEVEL
4909 015260 011037 001164             MOV    AR0,$REG1         ;GET RKCS
4910 015264 104104                     ERROR 104                ;RK11 INTERRUPTED WHEN THE CPU
4911                                     ;LEVEL (AS POINTED BY R1) WAS
4912                                     ;HIGHER OR SAME AS THE RK11
4913                                     ;LEVEL (5)
4914 015266 000746                     BR     2$                 ;GO BACK & CHK THE NXT LEVEL
4915
4916                                     ;*****
4917                                     ;*TEST 41 SIMULATE & CHECK 'OVR' ERROR
4918                                     ;*THIS TEST SIMULATES OVERRUN ERROR AND CHECKS IF THE OVR
4919                                     ;*BIT IN RKER GETS SET. THEN IT IS CLEARED USING CNTRL RESET
4920                                     ;*& CHECKED THAT IT WAS CLEARED. OVR CONDITION IS SIMULATED
4921                                     ;*BY TRYING TO READ 401(OCTAL) WORDS FROM LAST CYLINDER(312).
4922                                     ;*LAST SECTOR (13), SURFACE 1.
4923                                     ;*****
4924 015270 000004             TST41: SCOPE
4925 015272 104413             CNT.RESET                ;GO, DO CONTROL RESET
4926                                     ;THIS IS A CALL FOR THE 'CNTRL-
4927                                     ;RESET' ROUTINE. A CONTROL RESET IS
4928                                     ;ISSUED AND AFTER A CERTAIN TIME
4929                                     ;IF THE 'CNTRL RDY' DOES NOT SET
4930                                     ;AN ERROR IS REPORTED. NOTE THAT
4931                                     ;THE PC IN ERROR MESSAGE IS THE
4932                                     ;PC WHERE 'CNT.RESET' IS LOCATED.
4933                                     ;THIS IS A VERY BASIC ERR& IF IT
4934                                     ;OCCURS GO BACK TO TEST 10
4935 015274 104421             TST.SIN                ;CHECK IF SIN IS SET. IF
4936                                     ;SET, DO DRIVE RESET TO CLR IT
4937 015276 013701 001350             MOV    DRIVAD,R1         ;GET ADRES OF DRIVE
4938 015302 052701 014533             BIS    #14533,R1        ;SET BITS FOR LAST CYLINDER (312).
4939                                     ;SUR 1, LAST SECTOR (13)
4940 015306 012777 177377 164020      MOV    #-401,ARKWC       ;READ 401 WORDS
4941 015314 012777 033336 164014      MOV    #OUTBUF,ARKBA    ;INTO THIS MEMORY BUFFER
4942 015322 010177 164012             MOV    R1,ARKDA         ;FROM THIS DSK ADRES. LAST CYL.
4943                                     ;LAST SEC, SURFACE 1
4944 015326 012777 000005 163776      MOV    #5,ARKCS         ;READ. GO
4945
4946 015334 005002                     1$:  CLR    R2
4947 015336 105777 163770             TSTB  ARKCS              ;DID CNTRL RDY SET?
4948 015342 100410             BMI    2$                ;YES, BRANCH
4949 015344 005202             INC    R2                ;NO, WAIT FOR IT

```



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5012 015456 104421          TST.SIN
5013
5014 015460 013701 001330    MOV    RKER,R1
5015 015464 013777 001350 163646  MOV    DRIVAD,DRKDA
5016
5017 015472 012777 002011 163632  MOV    #2011,DRKCS
5018
5019 015500 104414          CNT.RDY
5020
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5023
5024
5025 015502 032711 004000    BIT    #4000,DR1
5026 015506 001006          BNE    1$
5027 015510 012737 004000 001166  MOV    #4000,$REG2
5028 015516 004737 021004    JSR    PC,GT2RG
5029 015522 104105          ERROR  10$
5030
5031
5032
5033 015524 022777 142210 163600 1$:    CMP    #142210,DRKCS
5034 015532 001403          BEQ    2$
5035 015534 004737 021004    JSR    PC,GT2RG
5036 015540 104106          ERROR  10$
5037
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5039 015542 104413          2$:    CNT.RESET
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5049 015544 004737 021352    JSR    PC,CHKECLR
5050
5051 015550 104102          ERROR  102
5052
5053 015552 004737 021376    3$:    JSR    PC,CHKCLR
5054
5055 015556 104102          ERROR  102
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: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10
: GO CHECK IF SIN IS SET, IF
: SET DO DRIVE RESET TO CLR IT
:
: ADRES THE DRIVE, CYLINDER 0
:
: SEEK, GO WITH FMT SET
: THIS IS A PGE SIMULATION
: THIS IS A CALL FOR 'CN.RDY'
: ROUTINE WHICH WAITS FOR CNT
: RDY TO SET. IF CNTRL RDY DOES
: NOT SET WITHIN 883 MS/ 11-20
: (176 MS FOR 11-45 WITH BIPOLAR),
: AN ERROR IS REPORTED
: DID PGE BIT IN RKER SET?
: YES, BRANCH
: THIS BIT IN RKER (PGE) DID NOT SET
: GO GET RKCS, ER FOR MESSAGE
: PGE BIT DID NOT SET IN RKER
: ON SIMULATION OF PGE CONDITION
: $REG2 CONTAINS THE RKER BIT .PGE.
: THAT SHOULD HAVE SET.
: DID HE & ERR BITS SET?
: YES, BRANCH
: GO, GET RKCS, ER
: HE OR ERR BIT DID NOT SET WHEN
: PGE SET IN RKER.
: CLEAR PGE, HE, ERR BITS
: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10
: CHECK IF 'PGE' BIT GOT CLEARED BY
: CONTROL RESET, IF NOT RETURN HERE.
: CNTRL RESET DID NOT CLEAR
: PGE BIT IN RKER
: CHECK IF 'ERR' BITGOT CLEARED BY
: CON.RESET, IF NOT RETURN HERE.
: RKCS BITS HE OR ERR DID NOT
: GET CLEARED BY CNTRL RESET

```

```

: *****
: *TEST 43 SIMULATE & CHECK NXM ERROR
: *THIS TEST SIMULATES A NON-EXISTENT MEMORY ERROR (NXM) AND
: *CHECKS IF IT IS DETECTED BY NXM BIT OR RKER.LOCATION 760000

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015560 000004
015562 104413

015564 104421

015566 005002
015570 013700 001332
015574 012777 177777 163532
015602 012777 160000 163526
015610 013777 001350 163522
015616 012710 000067
015622 105777 163504
015626 100410
015630 005202
015632 001373
015634 004737 021004
015640 017737 163470 001166
015646 104113

015650 032777 002000 163452
015656 001006
015660 004737 021004
015664 012737 002000 001166
015672 104105

015674 022710 140266
015700 001403
015702 004737 021004
015706 104106

015710 104413

: *IS REFERENCED & IT HAPPENS TO BE A NON EXISTENT LOCATION
: *(FOR DIAGNOSTIC PURPOSES LIKE THIS). IT IS ALSO CHECKED
: *IF HE & ERR BITS ALSO SET AND ALL 3 BITS CAN BE CLEARED
: * BY CONTROL RESET.
:*****
TST43: SCOPE
CNT.RESET

TST.SIN
CLR R2
MOV RKCS, R0
MOV #-1, R1
MOV #160000, R2
MOV DRIVAD, R3
MOV #67, R4
1\$: TSTB R1, R2
BMI 2\$
INC R2
BNE 1\$
JSR PC, GT2RG
MOV R1, R2
ERROR 113

2\$: BIT #2000, R1
BNE 3\$
JSR PC, GT2RG
MOV #2000, R2
ERROR 105

3\$: CMP #140266, R1
BEQ 4\$
JSR PC, GT2RG
ERROR 106

4\$: CNT.RESET

:GO, DO CONTROL RESET
:THIS IS A CALL FOR THE 'CNTRL-
:RESET' ROUTINE. A CONTROL RESET IS
:ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET
:AN ERROR IS REPORTED. NOTE THAT
:THE PC IN ERROR MESSAGE IS THE
:PC WHERE 'CNT.RESET' IS LOCATED.
:THIS IS A VERY BASIC ERR& IF IT
:OCCURS GO BACK TO TEST 10
:GO CHECK IF SIN IS SET
:IF SET DO DRIVE RESET TO CLR IT

:WRITE CHECK 1 WORD
:AT THIS BUS ADRES
:WITH THIS DISK ADRES (CYL 0, SEC 0)
:WRT CHK, GO, MEX BITS SET
:DID CNTRL RDY SET AS A RESULT OF HE?
:YES, BRANCH
:WAITED LONG ENOUGH?
:IF NOT LUP BAK & WAIT
:GET RKCS, ER
:GET RKWC
:CNTRL RDY DID NOT SET ON DOING
:A WRT CHK WITH A NXM LOCATION.
:THIS HE SHOULD HAVE SET THE
:CNTRL RDY BIT IN RKCS
:DID NXM BIT IN RKER SET?
:YES, BRANCH
:GO GET RKCS, RKER
:THIS BIT (NXM) DID NOT SET IN RKER
:NXM BIT DID NOT SET IN RKER ON
:SIMULATING NXM CONDITION.
:DID HE & ERR BIT SET?
:YES, BRANCH
:GO, GET RKCS, RKER
:HE OR ERR BIT DID NOT SET WHEN
:NXM ERROR WAS SIMULATED
:CLEAR NXM, HE, ERR BITS
:GO, DO CONTROL RESET
:THIS IS A CALL FOR THE 'CNTRL-
:RESET' ROUTINE. A CONTROL RESET IS
:ISSUED AND AFTER A CERTAIN TIME
:IF THE 'CNTRL RDY' DOES NOT SET
:AN ERROR IS REPORTED. NOTE THAT
:THE PC IN ERROR MESSAGE IS THE
:PC WHERE 'CNT.RESET' IS LOCATED.
:THIS IS A VERY BASIC ERR& IF I
:OCCURS GO BACK TO TEST 10

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S118 015712 004737 021352 JSR PC,CHKECLR ;CHECK IF 'NXM' BIT GOT CLEARED BY
S119 ;CON.RESET, IF NOT RETURN HERE.
S120 015716 104102 ERROR 102 ;CNTRL RESET DID NOT CLEAR
S121 ;NXM BIT IN RKER
S122 015720 004737 021376 5$: JSR PC,CHKCCLR ;CHECK IF 'HE' & 'ERR' BITS GOT CLEARED
S123 ;BY CON.RESET, IF NOT RETURN HERE.
S124 015724 104102 ERROR 102 ;CNTRL RESET DID NOT CLEAR
S125 ;HE OR ERR BIT IN RKCS.
S126 015726 004737 021432 6$: JSR PC,TSTRWS ;GO CHECK IF R/W/S RDY IS SET &
S127 ;WAIT FOR IT. SKIP ERROR IF IT IS SET
S128 015732 104016 ERROR 16 ;R/W/S RDY IS NOT SET
S129
S130 ;*****
S131 ;*TEST 44 SIMULATE & CHECK NXD ERROR
S132 ;*THIS TEST SIMULATES NON-EXISTENT DISK ERROR & CHECKS IF
S133 ;*IT IS DETECTED BY NXD BIT OF RKER. IF ALL EIGHT ARE PRESENT
S134 ;*THEN THIS TEST IS ABORTED FOR SIMULATION CANNOT BE DONE.
S135 ;*****
S136 015734 000004 †TST44: SCOPE
S137 015736 104413 CNT.RESET ;GO, DO CONTROL RESET
S138 ;THIS IS A CALL FOR THE 'CNTRL-
S139 ;RESET' ROUTINE. A CONTROL RESET IS
S140 ;ISSUED AND AFTER A CERTAIN TIME
S141 ;IF THE 'CNTRL RDY' DOES NOT SET
S142 ;AN ERROR IS REPORTED. NOTE THAT
S143 ;THE PC IN ERROR MESSAGE IS THE
S144 ;PC WHERE 'CNT.RESET' IS LOCATED.
S145 ;THIS IS A VERY BASIC ERR3 IF IT
S146 ;OCCURS GO BACK TO TEST 10
S147 015740 104421 TST.SIN ;CHECK IF SIN IS SET, IF SET
S148 ;DO DRV RESET TO CLR IT
S149 015742 013700 001332 MOV RKCS,R0
S150 015746 012702 160000 MOV #160000,R2 ;ADRES DRIVE 7 TO FIND
S151 ;IF IT IS PRESENT
S152 015752 010277 163362 1$: MOV R2,DRKDA ;ADRES DRIVE # POINTED TO BY R2
S153 015756 104417 000001 DELAY ,1 ;TIME DELAY, 7.5 US ON 11-20.
S154 ;1.5 US ON 11/45
S155 015762 105777 163340 TSTB DRKDS ;IS IT PRESENT?
S156 015766 100004 BPL 2$ ;NO, BRANCH
S157 015770 062702 160000 ADD #-20000,R2 ;ADRES THE NXT DRIVE IN THE
S158 ;REVERSE ORDER. I.E. 7,6...
S159 015774 001366 BNE 1$ ;LUP BAK & TRY TO FIND A DRIVE
S160 ;THAT'S NOT PRESENT
S161 015776 000435 BR TST45 ;EXIT TO THE NXT TST
S162
S163 016000 012710 000015 2$: MOV #15,DR0 ;DRIVE RESET, ON A NX DRIVE
S164 016004 104417 000106 DELAY .106 ;TIME DELAY, 525 US ON 11-20
S165 ;105 US ON 11/45
S166 016010 105777 163314 TSTB DRKER ;DID NXD BIT IN RKER SET?
S167 016014 001006 BNE 3$ ;YES, BRANCH
S168 016016 004737 021004 JSR PC,GT2RG ;GET RKCS, RKER
S169 016022 012737 000200 001166 MOV #200,$REG2 ;THIS BIT (NXD) IN RKER DID NOT SET
S170 016030 104105 ERROR 105 ;NXD BIT DID NOT SET ON TRYING
S171 ;TO PERFORM A FUNCTION ON A
S172 ;NON-EXISTENT DRIVE
S173 ;CHECK THAT THE JUMPER CARD CONTAINING

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016032 022710 140214
016036 001403
016040 004737 021004
016044 104106
016046 104413
016050 004737 021352
016054 104102
016056 004737 021376
016062 104102
016064 004737 021432
016070 104016
016072 000004
016074 013700 001332
016100 012737 177773 001362
016106 013702 001350
016112 052702 014540
016116 012737 016124 001110
016124 104413

3\$: CMP #140214,3R0
BEQ 4\$
JSR PC,GT2RG
ERROR 106
4\$: CNT.RESET
5\$: JSR PC,CHKCCLR
ERROR 102
JSR PC,TSTRWS
ERROR 16
*TEST 45
*ST45: SCOPE
MOV RKCS,R0
MOV #-5,COUNT 2\$:
MOV DRIVAD,R2
BIS #14540,R2
MOV #3\$,SLPERR
3\$: CNT.RESET

: JUMPERS FOR DRIVES PRESENT IS PROPERLY
: CONNECTED
: NOTE THAT ON RK11C IF A DRIVE
: IS OFFLINE BUT PHYSICALLY PRESENT
: (IE. DRY IS CLR FOR THAT DRIVE)
: & A FUNCTION IS INITIATED ON THAT
: DRIVE NXD WON'T SET, BUT U WILL
: GET ONLY A DRE, HE & ERR.
: DID HE & ERR SET WHEN NXD SET?
: YES BRANCH
: HE OR ERR BIT DID NOT SET
: WHEN NXD WAS SIMULATED
: CLEAR NXD, HE, ERR BITS
: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT
: THE PC IN ERROR MESSAGE IS THE
: PC WHERE 'CNT.RESET' IS LOCATED.
: THIS IS A VERY BASIC ERR& IF IT
: OCCURS GO BACK TO TEST 10
: CHECK IF 'NXD' BIT WAS CLEARED BY
: CON.RESET. IF NOT, RETURN HERE.
: CNTRL RESET DID NOT CLEAR
: NXD BIT IN RKER
: CHECK IF 'HE' & 'ERR' BITS WERE CLEARED
: BY CON.RESET. IF NOT RETURN HERE.
: CNTRL RESET DID NOT CLEAR
: HE OR ERR BIT IN RKCS
: GO CHECK & WAIT FOR R/W/S RDY
: TO SET. IF SET SKIP ERROR
: R/W/S SHOULD BE SET. IT'S
: NOT
: *****
: *THIS TEST SIMULATES THE NON-EXISTENT CYLINDER ERROR & CHECKS
: *IF IT IS DETECTED BY THE NXD BIT OF RKER, HE & ERR BITS
: *OF RKCS. IT IS CHECKED IF THEY CAN BE CLEARED BY CONTROL
: *RESET
: *****
: ALLOW 'ERROR 133' ONLY 5 TIMES
: GET ADRES OF DRIVE
: SET BITS FOR CYL 313
: SET RETURN ADRES FOR
: LUPING ON EROR (SW9)
: GO, DO CONTROL RESET
: THIS IS A CALL FOR THE 'CNTRL-
: RESET' ROUTINE. A CONTROL RESET IS
: ISSUED AND AFTER A CERTAIN TIME
: IF THE 'CNTRL RDY' DOES NOT SET
: AN ERROR IS REPORTED. NOTE THAT


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5286 016256 104102          ERROR 102          :CNTRL RESET DID NOT CLEAR
5287                                     :NXS BIT IN RKER
5288 016260 032710 140000 7$: BIT #140000,AR0    :DID HE & ERR BITS GET CLEARED?
5289 016264 001405          BEQ TST46          :YES, EXIT
5290 016266 010037 001162    MOV RC,$REG0       :GET ADRES OF RKCS
5291 016272 011037 001164    MOV AR0,$REG1      :GET RKCS CONTENTS
5292 016276 104102          ERROR 102          :CNTRL RESET DID NOT CLEAR
5293                                     :HE OR ERR BIT IN RKCS
5294
5295 :*****
5296 :*TEST 46 SIMULATE & CHECK NXS ERROR
5297 :*THIS TEST SIMULATES NON-EXISTENT SECTOR ERROR & CHECKS THAT
5298 :*IT IS DETECTED BY NXS BIT OF RKER. IT IS CHECKED THAT
5299 :*WHEN NXS SETS HE & ERR OF RKER ALSO SETS, AND ALL THREE
5300 :*CAN BE CLEARED BY CONTROL RESET.
5301 :*****
5302 016300 000004          TST46: SCOPE
5303 016302 104413          CNT.RESET          :GO, DO CONTROL RESET
5304                                     :THIS IS A CALL FOR THE 'CNTRL-
5305 :RESET' ROUTINE. A CONTROL RESET IS
5306 :ISSUED AND AFTER A CERTAIN TIME
5307 :IF THE 'CNTRL RDY' DOES NOT SET
5308 :AN ERROR IS REPORTED. NOTE THAT
5309 :THE PC IN ERROR MESSAGE IS THE
5310 :PC WHERE 'CNT.RESET' IS LOCATED.
5311 :THIS IS A VERY BASIC ERR& IF IT
5312 :OCCURS GO BACK TO TEST 10
5313 016304 013700 001332    MOV RKCS,R0
5314 016310 013777 001350 163022  MOV DRIVAD,ARKDA
5315 016316 052777 000014 163014  BIS #14,ARKDA
5316 016324 012777 177777 163002  MOV #-1,ARKWC
5317 016332 012777 033336 162775  MOV #OUTBUF,ARKBA
5318 016340 012710 000005  MOV #5,AR0
5319 016344 104414          CNT.RDY
5320                                     :GET ADRES OF DRIVE
5321                                     :SET BITS FOR SECTOR 12 (DECIMAL
5322                                     :READ 1 WORD
5323                                     :INTO THIS BUS ADRES
5324                                     :READ, GO (FROM NX SECTOR)
5325                                     :THIS IS A CALL FOR 'CN.RDY'
5326 :ROUTINE WHICH WAITS FOR CNT
5327 :RDY TO SET. IF CNTRL RDY DOES
5328 :NOT SET WITHIN 883 MS/ 11-20
5329 : (176 MS FOR 11-45 WITH BIPOLAR)
5330 :AN ERROR IS REPORTED
5331 :NXS ERROR SHOULD OCCUR NOW
5332 016346 017702 162756    MOV ARKER,R2
5333 016352 032702 000040    BIT #40,R2
5334 016356 001006          BNE 1$
5335 016360 004737 021004    JSR PC,GT2RG
5336 016364 012737 000040 001166  MC: #40,$REG2
5337 016372 104105          ERROR 105
5338                                     :DID NXS BIT SET IN RKER?
5339                                     :YES, BRANCH
5340                                     :GO GET RKCS, RKER
5341                                     :THIS BIT (NXS) IN RKER DID NOT SET
5342 :NXS BIT DID NOT SET ON SIMULATING
5343 :NXS ERROR
5344 016374 042702 000040 1$: BIC #40,R2
5345 016400 001407          BEQ 2$
5346                                     :MASK NXS BIT
5347                                     :CHECK IF ANY OTHER
5348 :RKER BIT SET
5349 016402 012737 000040 001162  MOV #40,$REG0
5350 016410 017737 162714 001164  MOV ARKER,$REG1
5351 016416 104107          ERROR 107
5352                                     :GET EXPCTD RKER
5353 :GET RKER RECVD
5354 :ONLY 'NXS' SHOULD BE SET
5355 :IN RKER. ANOTHER RKER BIT
5356 :WAS SET. (NOTE 'NXS' WAS
5357 :SIMULATED)

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5342 016420 022710 140204 25:  CMP      #140204,R0      ;DID HE & ERR BITS SET?
5343 016424 001403          BEQ      35          ;YES BRANCH
5344 016426 004737 021004  JSR      PC,GT2RG   ;GO GET RKCS, RKER
5345 016432 104106          ERROR    106       ;HE OR ERR BIT DID NOT SET WHEN
5346                                ;NXS ERROR OCCURED
5347                                ;CLEAR NXS, HE, ERR BITS
5348 -016434 104413 33:  CNT.RESET ;GO, DO CONTROL RESET
5349                                ;THIS IS A CALL FOR THE 'CNTRL-
5350                                ;RESET' ROUTINE. A CONTROL RESET IS
5351                                ;ISSUED AND AFTER A CERTAIN TIME
5352                                ;IF THE 'CNTRL RDY' DOES NOT SET
5353                                ;AN ERROR IS REPORTED. NOTE THAT
5354                                ;THE PC IN ERROR MESSAGE IS THE
5355                                ;PC WHERE 'CNT.RESET' IS LOCATED.
5356                                ;THIS IS A VERY BASIC ERR& IF IT
5357                                ;OCCURS GO BACK TO TEST 10
5358 016436 004737 021352  JSR      PC,CHKCLR  ;CHECK IF 'NXS' BIT WAS CLEARED BY
5359                                ;CON.RESET. IF NOT, RETURN HERE.
5360 016442 104102          ERROR    102       ;CNTRL RESET DID NOT CLEAR
5361                                ;NXS BIT IN RKER
5362 016444 004737 021376 45:  JSR      PC,CHKCCLR ;CHECK IF 'HE' & 'ERR' BITS WERE CLEARED
5363                                ;BY CON.RESET. IF NOT, RETURN HERE.
5364 016450 104102          ERROR    102       ;RKCS BITS ERR OR HE WERE NOT
5365                                ;CLEARED BY CNTRL RESET
5366
5367 ;*****
5368 ;*TEST 47 SIMULATE & CHECK WCE
5369 ;*THIS TEST SIMULATES A WRITE CH JK ERROR AND CHECKS THAT IT
5370 ;*IS DETECTED BY WCE BIT OF RKER. FOR COMPARISON IT USES
5371 ;*THE 256 WORDS DATA BLOCK WRITTEN ON SECTOR 0, CYLINDER 0
5372 ;*IN A PREVIOUS TEST. THIS BLOCK IS COMPARED WITH THE 256 WORDS
5373 ;*MEMORY BUFFER STARTING AT 'OUTBUF'. WCE IS SIMULATED BY
5374 ;*DROPPING A BIT FROM ONE OF THE WORDS IN THE MEMORY BUFFER.
5375 ;*****
5376 016452 000004          TST47: SCOPE
5377 016454 013700 001332  MOV      RKCS,R0
5378 016460 104413          CNT.RESET
5379                                ;GO, DO CONTROL RESET
5380                                ;THIS IS A CALL FOR THE 'CNTRL-
5381                                ;RESET' ROUTINE. A CONTROL RESET IS
5382                                ;ISSUED AND AFTER A CERTAIN TIME
5383                                ;IF THE 'CNTRL RDY' DOES NOT SET
5384                                ;AN ERROR IS REPORTED. NOTE THAT
5385                                ;THE PC IN ERROR MESSAGE IS THE
5386                                ;PC WHERE 'CNT.RESET' IS LOCATED.
5387                                ;THIS IS A VERY BASIC ERR& IF IT
5388                                ;OCCURS GO BACK TO TEST 10
5389                                ;CHECK IF SIN IS SET, IF
5390 016464 012701 033336  TST.SIN ;SET DO DRV-RESET TO CLR IT
5391 016470 012702 177400  MOV      #OUTBUF,R1 ;THIS CODE SETS UP A MEMORY
5392 016474 012703 177777  MOV      #-400,R2   ;BUFFER OF 256 WORDS STARING
5393                                ;AT OUTBUF
5394                                ;FIRST WORD 177400
5395                                ;SECOND 177001
5395 016500 062703 177401 15:  ADD      #177401,R3
5396 016504 010321          MOV      R3,(R1)+  ;LAST WORD 000377
5397 016506 005202          INC      R2        ;HAVE U GENERATED ALL 256 WORDS?

```



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5454                                     ;THIS IS A VERY BASIC ERR& IF IT
5455                                     ;OCCURS GO BACK TO TEST 10
5456 016634 104421 TST.SIN                                     ;CHECK IF SIN IS SET, IF
5457                                     ;SET DO DRIVE RESET TO CLR IT
5458 016636 013700 001332 MOV RKCS,R0
5459 016642 012737 170007 033354 MOV #170007,OUTBUF+16 ;WCE IS SIMULATED BY DROPPING A BIT
5460                                     ;IN THE EIGHTH WORD (WHICH IS ACTUALLY
5461                                     ;174007). NOTE THAT 256 WORD MEMORY
5462                                     ;BUFFER IS CREATED IN THE PREVIOUS TEST.
5463 016650 013701 001350 MOV DRIVAD,R1
5464 016654 012777 177000 162452 MOV #-1000,ARKWC ;WRT CHK 1000 (OCTAL) WORDS, 2 SECTORS
5465 016662 012777 033336 162446 MOV #OUTBUF,ARKBA ;FROM THIS BUS ADRES
5466 016670 010177 162444 MOV R1,ARKDA ;WITH THIS DISK ADRES, SEC 0, CYL 0
5467 016674 012710 000407 MOV #407,ARO ;WRT CHK, GO, SSE
5468 016700 104412 CHKCRDY ;GO CHECK IF CONTROL RDY IS SET
5469                                     ;IF SO, SKIP THE EROR MESSAGE.
5470 016702 104065 ERROR 65 ;CNTRL RDY DID NOT SET AFTER WRT
5471                                     ;CHK. A SOFT ERROR (WCE) IN
5472                                     ;SECTOR 0 SHOULD HAVE STOPPED
5473                                     ;ALL CONTROL ACTION.
5474 016704 022777 000001 162416 25: CMP #1,ARKER ;CHECK ONLY 'WCE' BIT SHOULD
5475                                     ;BE SET?
5476 016712 001407 BEQ 35 ;YES, BRANCH
5477 016714 012737 000001 001162 MOV #1,$REG0 ;GET EXPCTD RKER
5478 016722 017737 162402 001164 MOV ARKER,$REG1 ;GET RKER RECVD
5479 016730 104107 ERROR 107 ;ONLY BIT 'WCE' OF RKER
5480                                     ;SHOULD BE SET (WCE WAS
5481                                     ;SIMULATED ABOVE). ERROR
5482                                     ;IF IT'S NOT
5483 016732 005201 35: INC R1 ;CHECK THAT RKDA INCREMENTED BY
5484 016734 020177 162400 CMP R1,ARKDA ;1 SECTOR ONLY IMPLYING THAT
5485                                     ;CNTRL ACTION DID STOP AFTER
5486                                     ;SOFT ERROR IN SECTOR 0
5487 016740 001406 BEQ TST51 ;YES, EXIT
5488 016742 010137 001162 MOV R1,$REG0 ;GET EXPCTD RKDA
5489 016746 017737 162366 001164 MOV ARKDA,$REG1 ;GET RKDA RECVD
5490 016754 104070 ERROR 70 ;RKDA SHOULD HAVE INCRMNTD
5491                                     ;BY 1 SECTOR ONLY, IT DIDN'T.
5492                                     ;WCE WAS SIMULATED IN THE
5493                                     ;FIRST SECTOR & A WRT CHK
5494                                     ;OF 2 SECTORS WAS ISSUED.
5495                                     ;CONTROLLER SHOULD STOP AFTER
5496                                     ;DETECTING WCE IN THE FIRST
5497                                     ;SECTOR. HENCE RKDA SHOULD
5498                                     ;INCREMENT BY 1 SECTOR ONLY
5499
5500
5501 ;*****
5502 ;*TEST 51 CHECK THAT RK11 INTERRUPTS ON SOFT ERROR WHEN SSE & ICE ARE SET
5503 ;*THIS TEST CHECKS WHEN SSE BIT IS SET WITH IDE SET AND A SOFT
5504 ;*ERROR OCCURS, THEN ALL CONTROL ACTION WILL STOP AND A BUS
5505 ;*REQUEST (INTERRUPT) WILL OCCUR AT THE END OF THE CURRENT
5506 ;*SECTOR. SOFT ERROR IS SIMULATED BY WCE AS IN PREVIOUS
5507 ;*TEST. PREREQUISITES FOR THIS TEST ARE THE SAME AS THOSE
5508 ;*FOR THE PREVIOUS TEST.
5509 ;*****

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5510 016756 000004 *ST51: SCOPE
5511 016760 104413 CNT.RESET
5512
5513
5514
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5516
5517
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5520
5521 016762 104421 TST.SIM
5522
5523 016764 012737 170007 C33354 MOV #170007,OUTBUF+16
5524
5525
5526
5527
5528 016772 013701 001350 MOV DRIVAD,R1
5529 016776 012777 177000 162330 MOV #-1000,@RKWC
5530 017004 012777 033336 162324 MOV #OUTBUF,@RKBA
5531 017012 010177 162322 MOV R1,@RKDA
5532 017016 013700 001402 MOV RKVEC,R0
5533 017022 012720 017054 MOV #15,(R0)+
5534 017026 012710 000340 MOV #340,@R0
5535 017032 012777 000507 162272 MOV #507,@RKCS
5536 017040 104420 177777 WAT.INT,177777
5537
5538
5539 017044 004737 021004 JSR PC,GT2RG
5540 017050 104111 ERROR 111
5541
5542 017052 000417 BR 25
5543
5544 017054 022626 15: CMP (SP)+,(SP)+
5545 017056 022626 CMP (SP)+,(SP)+
5546 017060 012777 004600 162314 MOV #BADINT,@RKVEC
5547
5548 017066 005201 INC R1
5549 017070 020177 162244 CMP R1,@RKDA
5550
5551
5552 017074 001406 BEQ 25
5553 017076 010137 001162 MOV R1,$REG0
5554 017102 017737 162232 001164 MOV @RKDA,$REG1
5555 017110 104003 ERROR 3
5556
5557
5558
5559 017112 25:
5560 017112 012746 000340 MOV #340,-(SP)
5561 017116 012746 017124 MOV #645,-(SP)
5562 017122 000002 RTI
5563 017124 645:
5564 017124 005077 162202 CLR @RKCS
5565

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```

;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
;CHECK IF SIM IS SET, IF
;SET DO DRIVE RESET TO CLR IT
;WCE IS SIMULATED BY DROPPING A BIT
;IN THE EIGHTH WORD (WHICH IS 174007.
;NOTE THAT THE 256 WORD MEMORY
;BUFFER (STARTING AT OUTBUF) IS
;CREATED IN A PREVIOUS TEST.
;WRT CHK 1000 (OCTAL) WORDS, 2 SECTORS
;FROM THIS BUS ADRES
;WITH THIS DISK ADRES, SEC 0. CYL 0
;SET UP INTERRUPT VECTOR FOR RK11
;SET PSW ON INTERRUPT
;WRT CHK, GO, SSE, IDE SET
;WAIT FOR INTERRUPT FROM RK11
;TIME=485 MS FOR 11/20.
;97 MS FOR 11/45
;11/05
;RK11 DID NOT INTERRUPT AFTER A SCFT
;ERROR (SIMULATED) IN SECTOR 0
;RESTORE STACK POINTER (FROM RK11 INTRLP*),
;POP STACK (FROM WAT.INT)
;RESTORE RK11 INTERRUPT VECTOR
;ADRES FOR UNEXPECTED INTERRUPTS
;CHECK THAT RKDA INCREMENTED
;BY ONLY 1 SECTOR BEFORE INTERRUPT
;OCCURRED
;GET EXPCTD RKDA
;GET RKDA RECVD
;RKDA SHOULD HAVE INCREMENTED BY
;1 SECTOR ONLY, IF ALL CNTRL ACTION
;HAD STOPPED AFTER SOFT ERROR
;(SIMULATED) IN SECTOR 0. IT DID NOT.
;CLEAR THE IDE BIT

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5576 017130 000004
5577 017132 013700 001332
5578 017136 012701 177774
5579 017142 005002
5580 017144 012737 017152 001110
5581
5582 017152 104417 000142
5583 017156 004737 021432
5584 017162 104016
5585 017164 104413
5586
5587
5588
5589
5590
5591
5592
5593
5594
5595 017166 010210
5596 017170 012777 177777 162136
5597 017176 013777 001350 162134
5598 017204 012777 177776 162124
5599
5600 017212 052710 000007
5601
5602
5603
5604 017216 104412
5605
5606 017220 104065
5607 017222 010205
5608 017224 062705 000020
5609 017230 042705 000100
5610 017234 011004
5611 017236 042704 177717
5612 017242 020504
5613 017244 001405
5614 017246 010537 001162
5615 017252 010437 001164
5616 017256 104112
5617
5618
5619
5620
5621 017260 017703 162044

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*****
: TEST 52 CHECK THE MEX BITS IN RKCS
: *THIS TEST CHECKS OUT THE EXTENDED MEMORY BITS OF THE RKCS.
: *THE RKBA IS SET TO 177776 AND A ONE WORD WRITE CHECK IS TRIED.
: *THIS COULD GIVE RISE TO NXM ERROR, BUT EVEN THEN THE RKBA
: *SHOULD OVERFLOW INTO THE MEX BITS. SIMILIARLY IT IS CHECKED
: *THAT THE OVERFLOWING BIT CAN MAKE THE MEX BITS COUNT
: *01,10,1,00.
*****

```

```

†ST52 SCOPE
MOV RKCS,R0
MOV #-4,R1
CLR R2
MOV #1$, $LPERR
;SET UP THE COUNT
;INITIALIZE MEX BITS TO B SET IN RKCS
;SET RETURN ADRES FOR
;LUPING ON EROR (SW9)
1$: DELAY 142
JSR PC,TSTRWS
ERROR 16
CNT.RESET
;TIME DELAY
;WAIT FOR R/W/S RDY
;R/W/S RDY IS NOT SET
;GO, DO CONTROL RESET
;THIS IS A CALL FOR THE 'CNTRL-
;RESET' ROUTINE. A CONTROL RESET IS
;ISSUED AND AFTER A CERTAIN TIME
;IF THE 'CNTRL RDY' DOES NOT SET
;AN ERROR IS REPORTED. NOTE THAT
;THE PC IN ERROR MESSAGE IS THE
;PC WHERE 'CNT.RESET' IS LOCATED.
;THIS IS A VERY BASIC ERR& IF IT
;OCCURS GO BACK TO TEST 10
MOV R2,R0
MOV #-1,RKWC
MOV DRIVAD,ARKDA
MOV #177776,ARKBA
;SET MEX BITS (AS IN R2) IN RKCS
;WRT CHK 1 WORD
;THIS DISK ADRES, SEC 0, CYL 0
;THIS BUS ADRES. NOTE THIS BA
;IN CONJUCTION WITH MEX BITS OF RKCS
;WRT CHK, GO
;THERE MAY BE A NXM OR WCE BUT
;WHATEVER THE CASE RKBA SHOULD
;OVERFLOW MAKING THE MEX BITS COUNT
;GO CHECK IF CONTROL RDY IS SET
;IF SO, SKIP THE EROR MESSAGE.
;CNTRL RDY DID NOT SET AFTER WRT CHK
BIS #7,R0
;MEX BITS SHOULD INCREMENT BY 1 TO THIS
;MASK OUT IDE BIT POSITION, IF SET
;GET RKCS
;MASK OUT ALL BITS EXCEPT MEX
;DID MEX BITS INCREMENT CORRECTLY?
;YES, BRANCH
3$: ERROR 65
MOV R2,R5
ADD #20,R5
BIC #100,R5
MOV R0,R4
BIC #177717,R4
CMP R5,R4
BEQ 4$
MOV R5,$REGO
MOV R4,$REGI
ERROR 112
;GET EXPCTD MEX BITS
;GET MEX BITS RECVD
;MEX BITS DID NOT INCREMENT AS
;'EXPCTD' WHEN RKBA OVERFLOWED.
;NOTE THAT BIT POSITION 4 & 5
;REFLECT MEX BITS 0 & 1 IN THE
;ERROR MESSAGE.
4$: MOV ARKER,R3
;GET ARKER

```

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5622 017264 010305          MOV      R3,R5
5623 017266 042703 003001    BIC      #3001,R3      ;MASK WCE,DLT,NXM BIT, IF SET
5624 017272 001410          BEQ      5$           ;BRANCH IF REST OF RKER CLR
5625 017274 042705 177776    BIC      #177776,R5    ;MASK NON-WCE BITS
5626 017300 010537 001162    MOV      R5,$REG0     ;THIS IS THE EXPCTD RKER
5627 017304 017737 162020 001164  MOV      @RKER,$REG1  ;GET RKER RECVD
5628 017312 104107          ERROR    107         ;ERROR IN RKER. IT SHOULD
5629                                ;BE AS EXPECTED IN
5630                                ;ERROR MESSAGE
5631 017314 062702 000020    5$:     ADD      #20,R2    ;INCREMENT TO NXT MEX BIT
5632 017320 005201          INC      R1          ;HAVE U CHKD THE MEX BITS 4 TIMES?
5633 017322 001313          BNE     1$           ;IF NOT, LUP BACK
5634
5635 ::*****
5636 ;*TEST 53      TRANSFER FROM DISK TO TTY
5637 ;* THIS TEST CHECKS THE HIGH ORDER BITS OF THE ADDRESS
5638 ;* LINES. FIRST A ONE WORD (100) IS WRITTEN ON SECTOR,
5639 ;* 2, CYL 0. THEN IT IS READ BACK, BUT THE NPR IS DONE
5640 ;* NOT TO THE MEMORY, BUT THE TELETYPE BUFFER (TKS 177560)
5641 ;* AND IT CHECKED THAT THE WORD WAS RECIEVED CORRECTLY.
5642 ;*IF IT IS NOT, AN ERROR IS REPORTED. THIS TEST IS
5643 ;*SKIPPED ON AN 11/05.
5644 ::*****
5645 017324 000004          †T53:  SCOPE
5646 017326 012737 000001 001206  MOV      #1,$TIMES    ;DO 1 ITERATION
5647                                ;THIS CODE FINDS OUT IF THE CPU
5648                                ;IS AN 11/05 OR ELSE.
5649                                ;ON AN 11/05, R0 (177700) CAN BE
5650                                ;ADDRESSED AS A MEMORY LOCATION, BUT
5651                                ;ON ANY OTHER CPU IF 177700 IS REFERENCED
5652                                ;A TIME OUT WILL OCCUR.
5653 017334 012737 017356 000004    MOV      #5$,@#4     ;SET UP TIME OUT VECTOR
5654 017342 005737 177700          TST     @#177700    ;REFERENCE R0
5655 017346 012737 004534 000004    MOV      #BADTMO,@#4 ;R0 WAS REFERENCED W/O TIMEOUT
5656                                ;HENCE 11/05
5657 017354 000520          BR      TST54       ;SKIP THIS TEST
5658 017356 022626          5$:     CMP      (SP)+,(SP)+ ;RESTORE STACK POINTER
5659 017360 012737 004534 000004    MOV      #BADTMO,@#4 ;RESTORE TIMEOUT VECTOR
5660 017366 012746 000340          MOV      #340,-(SP)
5661 017372 012746 017400          MOV      #64$,-(SP)
5662 017376 000002          RTI
5663 017400          64$:
5664 017400 013700 001332    MOV      RKCS,R0
5665 017404 104413          CNT.RESET          ;GO, DO CONTROL RESET
5666                                ;THIS IS A CALL FOR THE 'CNTRL-
5667                                ;RESET' ROUTINE. A CONTROL RESET IS
5668                                ;ISSUED AND AFTER A CERTAIN TIME
5669                                ;IF THE 'CNTRL RDY' DOES NOT SET
5670                                ;AN ERROR IS REPORTED. NOTE THAT
5671                                ;THE PC IN ERROR MESSAGE IS THE
5672                                ;PC WHERE 'CNT.RESET' IS LOCATED.
5673                                ;THIS IS A VERY BASIC ERR& IF IT
5674                                ;OCCURS GO BACK TO TEST 10
5675 017406 012701 033336    MOV      #OUTBUF,R1
5676 017412 013704 001336    MOV      RKBA,R4
5677 017416 012711 000100    MOV      #100,@R1   ;WRITE THIS WORD

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```

5678 017422 012777 177777 161704      MOV      # -1, DRKWC      ;WRITE 1 WORD
5679 017430 013702 001350                MOV      DRIVAD, R2
5680 017434 052702 000002                BIS      #2, R2          ;ON CYL 0, SEC 2
5681 017440 010277 161674                MOV      R2, DRKDA
5682 017444 010114                MOV      R1, DR4        ;FROM THIS MEMORY LOC
5683 017446 012710 000003                MOV      #3, DR0        ;WRITE, GO
5684 017452 005003                CLR      R3
5685 017454 105710                1$:     TSTB     DR0
5686 017456 100410                BMI      2$
5687 017460 005203                INC      R3
5688 017462 001374                BNE     1$
5689 017464 004737 020770                JSR     PC, GT4RG        ;GET RKCS, ER, DS
5690 017470 010237 001202                MOV     R2, $REG10      ;GET THE STARTING ADRES
5691 017474 104416                BRKDAY  ;BREAK IT INTO DR# , CYL, SUR, SEC #
5692 017476 104031                ERROR   31              ;CNTRL RDY DID NOT SET AFTER
5693                                     ;WRITE OF 1 WORD ON CYL 0, SEC 2
5694 017500 012777 177777 161526 2$:     MOV     # -1, DRKWC      ;READ 1 WORD
5695 017506 010277 161626                MOV     R2, DRKDA        ;FROM SEC 2, CYL 0
5696 017512 013714 001144                MOV     STKS, DR4        ;INTO TTY STATUS REGISTER
5697 017516 005077 161422                CLR     $STKS           ;CLEAR TTY KEY BRD STATUS REG
5698
5699 017522 012710 000065                MOV     #65, DR0        ;READ, MEX BITS SET
5700 017526 005003                CLR     R3
5701 017530 105710                3$:     TSTB     DR0
5702 017532 100410                BMI     4$
5703 017534 005203                INC     R3
5704 017536 001374                BNE     3$
5705 017540 004737 020770                JSR     PC, GT4RG
5706 017544 010237 001202                MOV     R2, $REG10      ;GET THE STARTING ADRES
5707 017550 104416                BRKDAY  ;BREAK IT INTO DR# , CYL, SUR, SEC#
5708 017552 104045                ERROR   45              ;CNTRL RDY DIDN'T SET AFTER
5709                                     ;READ OF 1 WORD FROM CYL 0, SEC 2.
5710                                     ;IN EROR MSGE, <DSK-ADRES> GIVES
5711                                     ;ADRES WHERE READ BEGAN. 'RKDA'
5712                                     ;GIVES CONTENTS OF RKDA AT TIME OF EROR
5713 017554 032737 000100 001144 4$:     BIT     #100, $TKS      ;WAS THE CORRECT WORD READ INTO
5714                                     ;THE TTY STATUS REGISTER?
5715 017562 001015                BNE     TST54           ;; YES, EXIT
5716 017564 017705 161354                MOV     $TKS, R5        ;GET THE WORD RECVD FROM DISK
5717 017570 010537 001164                MOV     R5, $REG1
5718 017574 052705 000100                BIS     #100, R5        ;THIS WORD WAS EXPCTD
5719 017600 010537 001162                MOV     R5, $REG0      ;STORE EXPCTD WORD
5720 017604 011437 001166                MOV     DR4, $REG2
5721 017610 011037 001170                MOV     DR0, $REG3
5722 017614 104115                ERROR   115            ;GET RKCS
5723                                     ;DATA ERROR. A ONE WORD (100)
5724                                     ;NPR WAS TRIED FROM DISK TO
5725                                     ;TTY KEYBOARD STATUS REGISTER
5726                                     ;(17756) . BIT 6 SHOULD HAVE BEEN
5727                                     ;SET AS RESULT OF THIS
5728                                     ;BUT IT WAS NOT
5729
5730
5731
5732
5733

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:*****
:*TEST 54      CHECK THAT RKBA CAN COUNT CORRECTLY
:*THIS TEST CHECKS THAT RKBA CAN COUNT CORRECTLY. IT IS SET
:*TO THE DESIRED INITIAL VALUE. THEN A ONE WORD WRITE CHECK

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5734
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5740 017616 000004
5741 017620 012737 000005 001206
5742 017626 104421
5743 017630 005001
5744 017632 012702 000002
5745
5746 017636 012737 017650 001110
5747
5748
5749 017644 013705 001336
5750 017650 004737 021432
5751 017654 104016
5752 017656 104413
5753 017660 012777 177777 161446
5754 017666 010115
5755 017670 013777 001350 161442
5756 017676 012777 000067 161426
5757 017704 104412
5758
5759 017706 104065
5760
5761
5762
5763 017710 005237 001356
5764 017714 001417
5765
5766 017716 020215
5767
5768 017720 001410
5769 017722 010137 001162
5770 017726 011537 001164
5771 017732 104017
5772
5773
5774
5775
5776
5777
5778
5779 017734 005237 001360
5780 017740 001405
5781 017742 060201
5782 017744 010102
5783 017746 062702 000002
5784 017752 001336
5785
5786 017754
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; *IS TRIED, WITH MEX (MEMORY EXTENSION) BITS SET. IF THERE IS
; *NO MEMORY PRESENT (FOR CERTAIN BUS ADDRESSES), THERE
; *WILL BE AN NXM ERROR STOPPING CONTROLLER ACTION. BUT RKBA
; *SHOULD HAVE INCREMENTED BY 1 FROM ITS INITIAL VALUE. IF IT
; *HAS NOT, AN ERROR IS REPORTED.
;*****
1$: SCOPE
MOV #5,$TIMES ; DO 5 ITERATIONS
TST.SIN ; CHECK IF SIN SET, IF SET DRV RESET
CLR R1 ; INITIALIZE (VALUE OF RKBA)
MOV #2,R2 ; INITIALIZE (INCMNTD VALUE OF RKBA)

MOV #15,$LPERR ; SET RETURN ADRES FOR LUPING
; ON EROR

MOV RKBA,R5
JSR PC,$STRWS ; WAIT FOR R/W/S RDY
ERROR 16 ; R/W/S RDY IS NOT SET
CNT.RESET ; DO CONTROL RESET
MOV #1,$RKWC ; WRITE CHK 1 WORD
MOV R1,$R5 ; THIS BUS ADRES
MOV $DRVAD,$RKDA ; SET DISK ADRES
MOV #67,$RKCS ; WRITE CHECK, GO, MEX BITS SET
CHKCRDY ; GO CHECK IF CONTROL RDY IS SET
; IF SO, SKIP THE EROR MESSAGE.
ERROR 65 ; CNTRL RDY DID NOT SET AFTER
; WRT CHK WAS TRIED TO NXM LOC
; U MIGHT WANT TO USE TESTS
; CHECKING MEX BITS & NXM.
; ALLOW ONLY 5 ERRORS OF ABOVE KIND

INC INDX1
BEQ 5$

3$: CMP R2,$R5 ; DID RKBA INCREMENT BY 1 FROM
; ITS INITIAL VALUE?
; YES, BRANCH
BEQ 4$
MOV R1,$REGO ; GET EXPCTD RKBA
MOV $R5,$REG1 ; GET RKBA RECVD
ERROR 17 ; RKBA DID NOT INCREMENT BY
; 1 FROM ITS INITIAL VALUE.
; ONE WORD WRT CHK WAS TRIED
; TO A NXM LOCATION. THERE
; WILL BE AN NXM ERROR,
; BUT STILL RKBA SHOULD
; INCREMENT BY 1 FROM ITS
; INITIAL VALUE.
; ALLOW ONLY 5 ERRORS OF
; THE ABOVE KIND

INC INDX2
BEQ 5$
4$: ADD R2,R1 ; SET NXT VALUE OF RKBA
MOV R1,R2
ADD #2,R2 ; SET EXPCTD VALUE OF RKBA
BNE 1$ ; ALL DONE"

5$: ; DUMMY EXIT POINT

;*****

```

```

5790
5791
5792
5793
5794
5795 017754 000004
5796 017756 012737 000001 001206
5797 017764 005737 001404
5798 017770 001403
5799 017772 004537 025154
5800 017776 104120
5801
5802 020000
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5804
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5809
5810
5811
5812 020000 000004
5813 020002 012737 000001 001206
5814 020010 005237 001352
5815
5816 020014 004737 021500
5817 020020 104026
5818 020022 023737 001412 001352
5819
5820 020030 001405
5821 020032 062737 020000 001350
5822 020040 000137 005040
5823
5824 020044 005037 001112
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5841
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5843 020050 000004
5844 020052 012737 000005 001206
5845 020060 005237 001440

```

```

:*TEST 55 CHECK FOR RK-05F
; *THIS TEST CHECKS RK-05F TYPE DRIVES
; *TO INSURE THAT IF SEEKS ARE ISSUED ON ONE
; *DRIVE, THE OTHER DRIVE BECOMES BUSY
; *****
†ST55: SCOPE
MOV #1, $TIMES ; DO 1 ITERATION
TST FFLAG ; SEE IF RK-05F
BEQ IS ; NOT F
JSR R5, FCHECK ; SEE IF OTHER GOES BUSY
ERROR 120

IS:

; *****
:*TEST 56 END OF PROGRAM
; *THIS IS NOT A TEST, BUT A LINKAGE PROVIDED TO PERFORM
; *THE ABOVE SUB-TESTS FOR ALL DRIVES THAT ARE PRESENT.
; *NOTE THAT THE NEXT TEST- HARDWARE POLLING LOGIC-
; *IS DONE USING ALL THE DRIVES THAT ARE INDICATED PRESENT.
; *DO NOT LOOP ON THIS 'TEST'.
; *****
†ST56: SCOPE
MOV #1, $TIMES ; DO 1 ITERATION
INC DRVDON ; INCREMENT THE COUNT FOR THE NUMBER
; OF DRIVES THAT ARE CHECKED
JSR PC, DRESET ; RESET THE DRIVE
ERROR 26 ; R/W/S DIDN'T SET AFTER DRIVE RESET
BTEOP: CMP DRIVS, DRVDON ; HAVE U TESTED ALL THE DRIVES
; THAT ARE PRESENT?
BEQ IS ; IF YES, EXIT
ADD #20000, DRIVAD ; ADRES THE NXT POSSIBLE DRIVE
JMP NUDRV ; GO BACK AND TEST THE NEXT
; DRIVE PRESENT

IS: CLR $ERTTL

; *****
:*TEST 57 CHECK HARDWARE POLLING LOGIC
; *THIS TEST CHECKS THE HARDWARE POLL LOGIC, USING ALL THE DRIVES
; *PRESENT ON THE RK11. ATLEAST TWO DRIVES SHOULD BE PRESENT
; *TO DO A MEANINGFUL HARDWARE POLL. SEQUENCE OF OPERATIONS IS
; *AS FOLLOWING:
; *1) NUMBER OF DRIVES ON THE RK11 IS ASCERTAINED.
; *2) HAVING LOCKED OUT ALL INTERRUPTS (CPU PR 7), SEEK IS INITIATED
; *FOR ONE DRIVE AT A TIME, ONLY WHEN 'CNTRL RDY' IS SET.
; *3) CPU PRIORITY IS DROPPED TO 4 SO THAT RK11 CAN INTERRUPT. THE INCOMING
; *INTERRUPT IS PROCESSED TO CHECK IF IT WAS DUE TO 'SEEK DONE' BY
; *ONE OF THE DRIVES.
; *4) IF BY THE END OF THE SET TIME A DRIVE HAS NOT INTERRUPTED
; *AN ERROR MESSAGE IS GIVEN INDICATING WHICH DRIVE DID NOT
; *INTERRUPT AFTER SEEK WAS DONE.
; *****
†ST57: SCOPE
MOV #5, $TIMES ; DO 5 ITERATIONS
INC $IZYET ; FOLNR RK05F YET?

```

5846	020064	001002		BNE	25\$:YES
5847	020066	004737	025300	JSR	PC,SIZEF	:FIND WHICH ARE RK-05F
5848	020072	005037	001436	25\$: CLR	PHYDRV	:NUMBER OF ACTUAL DRIVES
5849	020076	012700	001414	MOV	#DRIVO,R0	:TABLE
5850	020102	005710		23\$: TST	(R0)	:DRIVE HERE+?
5851	020104	001405		BEQ	22\$:NO
5852	020106	005237	001436	INC	PHYDRV	:COUNT DRIVE
5853	020112	005710		TST	(R0)	:RK05F?
5854	020114	100001		BPL	22\$:NO
5855	020116	005720		TST	(R0)+	:DONT COUNT F TWICE
5856	020120	005720		22\$: TST	(R0)+	:NEXT DRIVE
5857	020122	020027	001433	CMP	R0,#DRIV7+1	:ALL YET
5858	020126	002765		BLT	23\$:NO
5859	020130	005037	001406	CLR	0DDEVN	:EVEN DRIVES FIRST IF F
5860	020134	005737	001412	T56: TST	DRIVS	:ANY DRIVES PRESENT?
5861	020140	001002		BNE	20\$:YES
5862	020142	000137	020646	JMP	\$EOP	:NO
5863	020146	005237	001434	20\$: INC	T56FLG	
5864	020152	013700	001332	MOV	RKCS,R0	
5865	020156	005037	001356	CLR	INDX1	:FLAG TO INDICATE:
5866						: (INDX1)=0 POLLING DONE AFTER ALL
5867						: DRIVES SEEK TO CYL 0
5868						: (INDX1)=1 POLLING DONE AFTER ALL
5869						: DRIVES SEEK TO CYL 4
5870	020162	005037	001360	15\$: CLR	INDX2	:FLAG INDICATING TYPE OF INTERRUPT
5871						:SET TO NON-ZERO TO INDICATE
5872						:THAT THE INTERRUPT IS DUE TO
5873						:SEEK DONE
5874	020166	104413			CNT.RESET	:GO, DO CONTROL RESET
5875						:THIS IS A CALL FOR THE 'CNTRL-
5876						:RESET' ROUTINE. A CONTROL RESET IS
5877						:ISSUED AND AFTER A CERTAIN TIME
5878						:IF THE 'CNTRL RDY' DOES NOT SET
5879						:AN ERROR IS REPORTED. NOTE THAT
5880						:THE PC IN ERROR MESSAGE IS THE
5881						:PC WHERE 'CNT.RESET' IS LOCATED.
5882						:THIS IS A VERY BASIC ERRS IF IT
5883						:OCCURS GO BACK TO TEST 10
5884	020170	005737	001356	TST	INDX1	:PERFORMING SEEKS TO CYL 4
5885	020174	001002		BNE	.+6	:YES, BRANCH
5886	020176	005002		BR	R2	:NO
5887	020200	000402		BP	.+6	
5888	020202	012702	000200	MOV	#200,R2	:SET ADRES FOR FOURTH CYLINDER
5889	020206	012701	001414	MOV	#DRIVO,R1	:INITIALIZE POINTER
5890	020212	012703	177770	MOV	#-10,R3	:SET COUNT FOR 8 DRIVES
5891	020216	012705	033336	MOV	#OUTBUF,R5	:INITIALIZE POINTER TO INDICATOR AREA
5892	020222	005025		CLR	(R5)+	:CLEAR OUT THE 8-WORD INDICATOR
5893	020224	005203		INC	R3	:AREA WHICH IS USED FOR DOING
5894	020226	001375		BNE	.-4	:SOFTWARE POLLING LATER ON
5895	020230	012703	177770	MOV	#-10,R3	:SET COUNT FOR 8 POSSIBLE DRIVES
5896	020234	012705	033336	MOV	#OUTBUF,R5	:INITIALIZE POINTER TO INDICATOR AREA
5897	020240			15\$:		
5898	020240	012746	000340	MOV	#340,-(SP)	
5899	020244	012746	020252	MOV	#64\$,-(SP)	
5900	020250	000002		RTI		
5901	020252			64\$:		

5902	020252	032711	000001		BIT	#BIT0,(R1)	: IS THIS DRIVE PRESENT?
5903	020256	001433			BEG	4\$: IF NOT, BRANCH
5904	020260	005711			TST	(R1)	: RK06F?
5905	020262	100012			BPL	17\$: NO, CONTINUE
5906	020264	032702	020000		BIT	#BIT13,R2	: DRIVE EVEN?
5907	020270	001404			BEG	16\$: YES
5908	020272	005737	001406		TST	00DEVN	: DO WE WANT ODD?
5909	020276	001423			BEG	4\$: NO, SO DO NOT TEST
5910	020300	000403			BR	17\$: ADD THIS DRIVE TO LIST
5911	020302	005737	001406	16\$:	TST	00DEVN	: DO WE WANT EVEN?
5912	020306	001017			BNE	4\$: NO, SO SKIP
5913	020310	010215		17\$:	MOV	R2,(R5)	: SET UP THIS WORD IN THE
5914							: INDICATOR AREA SHOWING THAT THIS
5915							: DRIVE (AS IN BITS 13-15 OF R2)
5916							: IS PRESENT
5917	020312	042725	017777		BIC	#17777,(R5)+	: MASK OUT UNWANTED BITS (CYL,SUR,SEC BITS
5918	020316	005004			CLR	R4	
5919	020320	105710		2\$:	TSTB	3R0	: IS CNTRL RDY SET?
5920	020322	100405			BMI	3\$: YES, BRANCH
5921	020324	005204			INC	R4	: NO, WAIT FOR IT
5922	020326	001374			BNE	2\$: IF WAITED LONG REPORT ERROR
5923	020330	004737	020770		JSR	PC,GT4R3	: GO, GET RKCS,ER,DS,DA
5924	020334	104021			ERROR	21	: CNTRL RDY DID NOT SET AFTER ACCEPTING
5925							: ADRES FROM PREVIOUS SEEK
5926	020336	010277	160776	3\$:	MOV	R2,3RKDA	: ADRES THIS DRIVE, CYL 0 OR CYL 4
5927							: (WHICHEVER THE CASE MAY BE)
5928	020342	012710	000111		MOV	#111,3R0	: SEEK,GO,IDE SET
5929	020346	005721		4\$:	TST	(R1)+	: NEXT DRIVE DATA
5930	020350	062702	020000		ADD	#20000,R2	: INCREMENT DRIVE ADRES (BITS 15,14,13
5931	020354	005203			INC	R3	: TO NEXT ONE
5932	020356	001330			BNE	1\$: BRANCH BACK IF ALL DRIVES ARE
5933							: NOT CHECKED TO SEE IF THE NEXT
5934							: DRIVE IS PRESENT (& IF SO ISSUE A
5935							: SEEK TO IT)
5936							: BY NOW SEEKS HAVE BEEN ISSUED
5937							: TO ALL DRIVES PRESENT & POLLING
5938							: HAS BEGUN
5939	020360	005004			CLR	R4	
5940	020362	013702	001402	5\$:	MOV	RKVEC,R2	
5941	020366	012722	020420		MOV	#6\$, (R2)+	: SET ADRES FOR RK11 TO INTERRUPT
5942	020372	012712	000340		MOV	#340,(R2)	: SET PSW ON INTERRUPT
5943	020376	013746	001400		MOV	RKPRI,-(SP)	: DROP CPU PRIORITY TO 4 SO THAT
5944	020402	012746	020410		MOV	#18\$,-(SP)	: ;RK11 CAN INTERRUPT
5945	020406	000002			RTI		
5946	020410	000240		18\$:	NOP		: THIS IS A TIME LOOP DURING
5947	020412	005204			INC	R4	: WHICH ALL DRIVES PRESENT SHOULD
5948	020414	001375			BNE	18\$: INTERRUPT
5949	020416	000452			BR	11\$: BRANCH AND CHECK IF ALL AVAILABLE
5950							: DRIVES INTERRUPTED CORRECTLY
5951	020420	022626		6\$:	CMP	(SP)+,(SP)+	: RESTORE STACK POINTER
5952	020422	005737	001360		TST	INDX2	: WAS THIS FIRST INTERRUPT
5953							: DUE TO 'ADRES ACK' AFTER INITIATION
5954							: OF SEEK?
5955	020426	001021			BNE	9\$: IF YES, CHECK THE FOLLOWING
5956							
5957	020430	032710	020000		BIT	#20000,3R0	: CHECK THAT SCP IS NOT SET

5958	020434	001403			BEQ	7\$: BRANCH IF SCP CLEAR
5959	020436	011037	001162		MOV	2R0,\$REGO		: GET RKCS
5960	020442	104076			ERROR	76		: AFTER THE FIRST INTERRUPT WHICH
5961								: IS DUE TO INITIATION OF SEEK, SCP
5962								: SHOULD NOT HAVE SET. IT DID
5963	020444	017701	160656	7\$:	MOV	2RKDS,R1		
5964	020450	032701	160000		BIT	#160000,R1		: RKDS BITS 15-13 SHLOULD BE CLR
5965	020454	001403			BEQ	8\$		
5966	020456	010137	001162		MOV	R1,\$REGO		: GET RKDS
5967	020462	104050			ERROR	50		: SEEK, WITH IDE SET WAS ISSUED TO
5968								: ALL AVAILABLE DRIVES. THE FIRST
5969								: INTERRUPT IS DUE TO SEEK INITIATED
5970								: BY FRST DRV. DRV ID BITS 13-15
5971								: SHOULD BE CLR AFTR THIS FRST INRUPT.
5972								: THEY WERE NOT IF THIS ERROR OCCURS.
5973	020464	005237	001360	8\$:	INC	INDX2		: SET UP FLAG INDICATING
5974								: THAT THE FIRST INTERRUPT DUE
5975								: TO INITIATION OF SEEK WAS
5976								: PROCESSED
5977	020470	000734			BR	5\$: GO BACK TO THE WAIT LOOP & WAIT
5978								: FOR NEXT INTERRUPT FROM RK11
5979	020472	013703	001436	9\$:	MOV	PHYDRV,R3		: SET COUNT OF # OF DRIVES PRESENT
5980	020476	012705	033336		MOV	#OUTBUF,R5		: INITIALIZE POINTER
5981	020502	017701	160620		MOV	2RKDS,R1		: GET RKDS
5982	020506	042701	017777		BIC	#17777,R1		: MASK BITS 0-12
5983								: THE FOLLOWING CODE IS A SOFTWARE
5984								: POLL WHICH FINDS OUT WHICH DRIVE
5985								: CAUSED THE PRESENT INTERRUPT
5986								: AND SETS UP A FLAG BIT FOR
5987								: THE DRIVE #, INDICATING THAT
5988								: THIS DRIVE # INTERRUPTED
5989	020512	020125			CMP	R1,(R5)+		
5990	020514	001411			BEQ	10\$: BRANCH IF INTERRUPTING DRIVE WAS FOUND
5991	020516	005303			DEC	R3		: HAVE U CHKD ALL DRIVS PRESENT?
5992	020520	001374			BNE	.-6		: IF NOT LUP BAK & CHK
5993								: REPORT ERROR IF THE INTERRUPTING
5994								: DRIVE # (AS IN RKDS 13-15) WAS NOT
5995								: ANY ONE OF THOSE THAT ARE PRESENT
5996	020522	010146			MOV	R1,-(R6)		: GET WORD TO B SHFTD RT
5997	020524	004737	021174		JSR	PC,SHFTRT		: GO SHIFT IT
5998	020530	012637	001162		MOV	(R6)+,\$REGO		: THIS DRIVE # WAS RECVD IN RKDS AS
5999								: THE INTERRUPTING DRIVE, BUT THIS
6000								: DRIVE IS NOT PHYSICALLY PRESENT
6001	020534	104051			ERROR	51		: RKDS INDICATES AN INTERRUPTING
6002								: DRIVE # (DURING H'WARE POLL) BUT
6003								: THAT DRIVE IS ACTUALLY NOT PRESENT
6004	020536	000401			BR	10\$+2		
6005	020540	005245		10\$:	INC	-(R5)		: SET UP FLAG INDICATING THAT
6006								: THE INTERRUPT FOR THIS DRIVE
6007								: (AFTER IT HAD COMPLETED ITS SEEK)
6008								: WAS PROCESSED
6009	020542	000707			BR	5\$: GO BAK & WAIT FOR FLRTHR INTRUPTS
6010	020544	013703	001436	11\$:	MOV	PHYDRV,R3		: GET # OF DRIVES
6011	020550	012705	033336		MOV	#OUTBUF,R5		: INITIALIZE POINTER
6012								
6013	020554	105715		14\$:	TSTB	(R5)		: DID THIS DRIVE INTERRUPT?

```

6014 020556 001006
6015 020560 011546
6016 020562 004737 021174
6017 020566 012637 001162
6018
6019 020572 104052
6020
6021 020574 062705 000002
6022 020600 005303
6023 020602 001364
6024
6025 020604 005737 001356
6026 020610 001004
6027 020612 005237 001356
6028 020616 000137 020162
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6039
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6052
6053 020622 005237 001406
6054 020626 022737 000002 001406
6055 020634 001402
6056 020636 000137 020134
6057 020642 005037 001434
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6062
6063
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6065
6066
6067
6068
6069 020646

```

```

BNE 13$
MOV (R5),-(R6)
JSR PC,SHFRT
MOV (R6)+,$REGO
ERROR 52
13$: ADD #2,R5
DEC R3
BNE 14$
TST INDX1
BNE TSTEND
INC INDX1
JMP 15$

```

```

: YES, BRANCH
: GET THIS DRIVE #
: SHIFT IT TO THE RIGHT
: THIS DRIVE # DID NOT INTERRUPT
: DURING H'WARE POLL
: DRIVE # (AS IN $REGO) DID NOT
: INTERRUPT DURING HARDWARE POLL
: INCREMENT POINTER TO THE NEXT FLAG
: CHKD FOR ALL DRIVES?
: IF NOT LUP BACK
: DONE POLLING FOR SEEKS TO CYL 312?
: IF YES, EXIT
: IF NOT, INCREMENT FLAG
: GO DO IT
: INDICATOR TABLE
: THE 8-WORD INDICATOR TABLE USED IN
: THE FORMER PART OF THIS SUB-TEST
: IS LOCATED STARTING AT 'OUTBUF'.
: WORDS ARE SET UP TO INDICATE
: PRESENCE OF A DRIVE EG: IF
: DRIVES C 1.2 ARE PRESENT, IT WILL
: LOOK LIKE
: OUTBUF: 000000 BITS 13,14,15
: 020000 CONTAIN THE
: 040000 DRIVE NO.
: 000000 REST 0'S
: WHEN A DRIVE INTERRUPTS AFTER SEEK
: IS DONE BIT 0 OF THE CORRESPONDING
: INDICATOR WORD IS SET. THUS FOR THE
: ABOVE EXAMPLE IF ALL DRIVES INTERRUPTED
: CORRECTLY THEN IT WILL LOOK LIKE:
: 12$: 000001 BIT 0 SET
: 020001 TO INDICATE
: 040001 DR INTERRUPTED
: 000000 REST 0'S

```

```

TSTEND: INC 00DEVN :NOW ODD IF RK05F
CMP #2,00DEVN :SEE IF DONE
BEQ 21$ :ALL DONE
JMP T56 :TEST AGAIN
21$: CLR T56FLG

```

.SBTTL END OF PASS ROUTINE

```

:*****
: *INCREMENT THE PASS NUMBER ($PASS)
: *INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
: *TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
: *IF THERES A MONITOR GO TO IT
: *IF THERE ISN'T JUMP TO ST4

```

\$EOP:

```

6070 020646 000004
6071 020650 005037 001102
6072 020654 005037 001206
6073 020660 005237 001100
6074 020664 042737 100000 001100
6075 020672 005327
6076 020674 000001
6077 020676 003022
6078 020700 012737
6079 020702 000001
6080 020704 020674
6081 020706 104401 020753
6082 020712 013746 001100
6083 020716 104405
6084 020720 104401 020750
6085 020724 013700 000042
6086 020730 001405
6087 020732 000005
6088 020734 004710
6089 020736 000240
6090 020740 000240
6091 020742 000240
6092 020744
6093 020744 000137
6094 020746 004456
6095 020750 377 377 000
6096 020753 015 042412 042116
6097 020760 050040 051501 020123
6098 020766 000043
6099
6100
6101
6102
6103
6104
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6111
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6113
6114
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6117
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6124
6125

```

```

SCOPE
CLR $STNM          ;; ZERO THE TEST NUMBER
CLR $TIMES        ;; ZERO THE NUMBER OF ITERATIONS
INC $PASS         ;; INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;; DON'T ALLOW A NEG. NUMBER
DEC (PC)+        ;; LOOP?
SEOPCT: .WORD 1
BGT $DOAGN       ;; YES
MOV (PC)+,2(PC)+ ;; RESTORE COUNTER
SENDCT: .WORD 1
SEOPCT
TYPE $ENDMG      ;; TYPE "END PASS #"
MOV $PASS,-(SP)  ;; SAVE $PASS FOR TYPEOUT
TYPDS           ;; GO TYPE--DECIMAL ASCII WITH SIGN
TYPE $NULL       ;; TYPE A NULL CHARACTER
SGET42: MOV #42,R0 ;; GET MONITOR ADDRESS
BEQ $DOAGN       ;; BRANCH IF NO MONITOR
RESET           ;; CLEAR THE WORLD
SENDAD: JSR PC,(R0) ;; GO TO MONITOR
NOP            ;; SAVE ROOM
NOP            ;; FOR
NOP            ;; ACT11
$DOAGN:
JMP 2(PC)+      ;; RETURN
SRTNAD: .WORD ST4
$NULL: .BYTE -1,-1,0 ;; NULL CHARACTER STRING
SENDMG: .ASCIZ <15><12>/END PASS #/

```

.SBTTL GT2RG: ROUTINE FOR GETTING RKCS,RKER

```

;SUBROUTINE FOR TRANSFERRING THE CONTENTS OF RKCS, RKER
;TO $REG0, $REG1 RESPECTIVELY BEFORE TYPING OUT AN ERROR MESSAGE.
;CALL: JSR PC,GT2RG

```

.SBTTL GT3RG: ROUTINE FOR GETTING RKCS, RKER, RKDS

```

;GT3RG
;SUBROUTINE FOR TRANSFERRING THE CONTENTS OF RKCS, RKER, RKDS
;TO $REG0, $REG1, $REG2 RESPECTIVELY BEFORE TYPING OUT AN
;ERROR MESSAGE.
;CALL: JSR PC,GT3RG

```

.SBTTL GT4RG: ROUTINE FOR GETTING RKCS, RKER, RKDS, RKDA

```

;GT4RG
;SUBROUTINE FOR TRANSFERRING CONTENTS OF RKCS, RKER, RKDS
;RKDA TO $REG0, $REG1, $REG2, $REG3 RESPECTIVELY BEFORE
;TYPING OUT AN ERROR MESSAGE.

```

6126
 6127
 6128 020770 017737 160344 001170
 6129 020776 017737 160324 001166
 6130 021004 017737 160320 001164
 6131 021012 017737 160314 001162
 6132 021020 000207
 6133
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 6145
 6146 021022
 6147 021022 104401 021030
 6148 021026 000406
 6149
 6150 021044
 6151 021044 010346
 6152 021046 104402
 6153 021050 000207
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 6164
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 6175
 6176
 6177
 6178 021052 010046
 6179 021054 012700 001172
 6180 021060 000403
 6181

```

;CALL: JSR PC,GT4RG
GT4RG: MOV BRKDA,$REG3 ;GET RKDA
GT3RG: MOV BRKDS,$REG2 ;GET RKDS
GT2RG: MOV BRKER,$REG1 ;GET RKER
MOV BRKCS,$REG0
RTS PC
  
```

.SBTTL TYERM: SPECIAL ERROR MESSAGE ROUTINE

```

:TYERM
:THIS ROUTINE TYPES OUT 'EROR AT PC=X'
:X IS THE PC WHERE THE EXPLANATION AS TO WHAT HAPPENED IS GIVEN. THIS ROUTINE
:IS USED ONLY FOR NON-MANUAL MODE OF THE PROGRAM.
:CALL: JSR TYERM
  
```

```

TYERM:
        TYPE      65$          ;;TYPE ASCIZ STRING
        BR        64$          ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <15><12>/EROR,PC=/
64$:   MOV        R3,-(SP)
        TYPOC
        RTS       PC
  
```

.SBTTL BDAO, BDA4: BREAK DISK ADDRESS INTO SEC, SUR, CYL. DRIVE

:BDAO, BDA4

```

:THIS ROUTINE BREAKS A DISK ADDRESS (BITS 0-15) INTO DRIVE #,
:CYLINDER #, SURFACE, SECTOR #. THE ROUTINE IS CALLED BY USING EITHER
:BRKDAO OR BRKDA4, BOTH BEING 'TRAP' INSTRUCTIOS WITH THEIR LOWER BYTES
:ENCODED TO PROVIDE INDEXING TO 'BDAO' OR 'BDA4'. BEFORE CALLING
:THE ROUTINE THE DISK ADDRESS WHICH IS TO BE BROKEN AS ABOVE
:IS DEPOSITED IN $REG10.
  
```

```

:'BRKDAO' PUTS THE
:DRIVE # INTO $REG0
:CYLINDER # INTO $REG1
:SURFACE # INTO $REG2
:SECTOR # INTO $REG3
:CALL: BRKDAO
BRKDA4 PUTS THE
DRIVE # INTO $REG4
CYLINDER # INTO $REG5
SURFACE # INTO $REG6
SECTOR # INTO $REG7
BRKDA4
  
```

```

BDAO:  MOV        R0,-(SP)      ;PUSH R0 ONTO THE STACK
        MOV        # $REG3+2,R0 ;SET JF POINTER
        BR        BDA4
  
```

```

6182 021062 010046          BDA4: MOV    R0,-(SP)      ;PUSH R0 ONTO THE STACK
6183 021064 012700 001202  MOV    #SREG7+2,R0    ;SET UP POINTER
6184
6185 021070 032777 020000 160042 BDA4: BIT    #20000,DSWR   ;INHIBIT TYPEOUT?
6186 021076 001034          BNE    2$            ;YES, BRANCH TO EXIT POINT
6187
6188 021100 010146          MOV    R1,-(SP)      ;PUSH R1 ON STACK
6189 021102 010246          MOV    R2,-(SP)      ;PUSH R2 ON STACK
6190 021104 013701 001202  MOV    $REG10,R1     ;GET THE ADDRESS WHICH
6191          ;HAS TO BE BROKEN
6192 021110 042701 177760  BIC    #177760,R1    ;EXTRACT SECTOR BITS 0-3
6193 021114 010140          MOV    R1,-(R0)     ;MOVE SECTOR BITS TO $REG3 OR $REG7
6194 021116 013701 001202  MOV    $REG10,R1     ;GET THE DSK-ADRES TO BE BROKEN
6195 021122 006201          ASR   R1            ;SHIFT RIGHT 4 TIMES
6196 021124 006201          ASR   R1
6197 021126 006201          ASR   R1
6198 021130 006201          ASR   R1
6199 021132 010102          MOV    R1,R2        ;STORE THIS
6200 021134 042702 177776  BIC    #177776,R2    ;EXTRACCT THE SURFACE BIT
6201 021140 010240          MOV    R2,-(R0)     ;MOVE SURFACE BIT TO $REG3 OR $REG6
6202 021142 006201          ASR   R1
6203 021144 010102          MOV    R1,R2        ;STORE IT
6204 021146 042702 177400  BIC    #177400,R2    ;EXTRACT THE CYLINDER BITS
6205 021152 010240          MOV    R2,-(R0)     ;MOVE CYLINDER BITS TO $REG1 OR $REG5
6206 021154 000301          SWAB  R1            ;SWAB HI-LO BYTES
6207 021156 042701 177770  BIC    #177770,R1    ;EXTRACT THE DRIVE #
6208 021162 010140          MOV    R1,-(R0)     ;MOVE DRIVE # TO $REG0 OR $REG4
6209
6210 021164 012602          MOV    (SP)+,R2     ;RESTORE R2
6211 021166 012601          MOV    (SP)+,R1     ;RESTORE R1
6212 021170 012600 2$: MOV    (SP)+,R0     ;RESTORE R0 FROM THE STACK
6213 021172 000002          RTI                ;RETURN FROM INTERRUPT, EXIT THIS
6214          ;ROUTINE
6215
6216
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6221          .SBTTL SHFTRT: SHIFT RIGHT ROUTINE
6222          ;SHFTRT
6223          ;THIS ROUTINE SHIFTS A WORD TO THE RIGHT 13 TIMES. THE WORD TO BE SHIFTEC
6224          ;IS PUT ON THE STACK BEFORE ENTERING THIS ROUTINE AND IT IS POPPED UP
6225          ;FROM THE STACK AFTER THE SHIFT HAS BEEN DONE.
6226          ;CALL: JSR    PC,SHFTRT
6227 021174 012737 177763 021220 SHFTRT: MOV    #-15,2$   ;SET UP A COUNT OF 13
6228 021202 000241          CLC                ;CLEAR THE C BIT
6229 021204 006066 000002 1$: ROR    2(R6)      ;ROTATE RIGHT THE WORD TO B SHFTRT
6230 021210 005237 021220  INC    2$          ;SHIFTED 13 TIMES?
6231 021214 001373          BNE    1$          ;IF NOT LUP BAK & SHIFT
6232 021216 000207          RTS                ;EXIT FROM THIS SJBROUTINE
6233 021220 000000 2$: 0
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.SBTTL CHKHE: CHECK FOR 'ERR' OR
.SBTTL CHKHE1: CHECK FOR 'ERR' OR

```

:CHKHE
:THIS ROUTINE CHECKS IF 'HE' OR 'ERR' BITS IN RKCS ARE SET. IF ANY OF THE
:TWO BITS ARE SET, THE CONTENTS OF RKCS, ER, DS, AND DA ARE SAVED AND A
:RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.
:AT THE TIME OF ENTRY 'DRIVAD' CONTAINS THE DISK ADDRESS WHICH IS TO
:BE BROCKEN DOWN INTO DRIVE #, CYLINDER, SURFACE AND SECTOR #. THIS INFORMATION
:IS SAVED TO BE USED LATER FOR ERROR REPORTING. IF THE BITS ARE NOT SET,
:RETURN IS MADE TO SKIP THE ERROR MESSAGE.
    
```

```

:CHKHE1
:THIS ROUTINE CHECKS IF 'HE' OR 'ERR' BITS IN RKCS ARE SET. IF ANY OF THE
:TWO BITS ARE SET, THE CONTENTS OF RKCS, ER, DS, AND DA ARE SAVED AND A
:RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.
:AT THE TIME OF ENTRY R1 CONTAINS THE DISK ADDRESS WHICH IS TO BE BROCKEN
:DOWN INTO DRIVE #, CYLINDER, SURFACE AND SECTOR #. THIS INFORMATION IS
:SAVED TO BE USED LATER FOR ERROR REPORTING. IF THE BITS ARE NOT SET,
:RETURN IS MADE TO SKIP THE ERROR MESSAGE.
    
```

```

CHKHE1: MOV    R1,$REGIO    ;SAVE THE DISK ADRES
        BR     CHE1
CHKHE:  MOV    DRIVAD,$REGIO ;SAVE THE DISK ADRES
CHE1:   BIT    #!40000,$RKCS ;IS 'HE' OR 'ERR' BIT SET?
        BEQ    CRETRN      ;NO
        JSR    PC,G'      ;GET RKCS,ER,DS, DA
        BRKDA4           ;GO TO 'BD04' & BREAK CONTENTS 0
        ;$REGIO INTO DR#,CYL,SUR,SEC BITS
        RTS    PC         ;RETURN TO THE ERROR MESSAGE
    
```

.SBTTL CHKDA: CHECK IF RKDA INCREMENTED CORRECTLY

```

:CHKDA
:THIS ROUTINE CHECKS IF RKDA INCREMENTED CORRECTLY. IF RKDA INCREMENTED
:CORRECTLY RETURN IS MADE TO SKIP THE ERROR MESSAGE.
:IF RKDA DID NOT INCREMENT CORRECTLY, THE EXPECTED AND RECIEVED VALUES
:OF RKDA ARE SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE
:'JSR' CALL.
    
```

```

CHKDA:  MOV    DRIVAD,R5    ;RKDA SHOULD INCREMENT TO THIS
        INC    R5          ;AFTER DATA TRANSFER IS DONE
CHKDA1: CMP    R5,$RKDA    ;DID RKDA INCREMENT CORRECTLY?
        BEQ    CRETRN      ;IF YES, BRANCH
        MOV    R5,$REGIO  ;IF NOT, REPORT ERROR
        BRKDA0           ;GET EXPCTD RKDA
        ;GO TO 'BD04' & BREAK CONTENTS OF
        ;$REGIO INTO DR#,CYL,SUR,SEC BITS
        MOV    $RKDA,$REGIO ;GET ACTUAL RKDA
        BRKDA4           ;GO TO 'BD04' & BREAK CONTENTS OF
        ;$REGIO INTO DR#,CYL,SUR,SEC BITS
        RTS    PC         ;RETJRN TO THE ERROR MESSAGE
    
```

.SBTTL CHKWC: CHECK IF RKWC OVERFLOWED

6294				
6295				
6296				
6297				
6298				
6299				
6300	021312	005777	160016	
6301	021316	001442		
6302				
6303	021320	017737	160010	001162
6304	021326	017737	160006	001164
6305	021334	000207		
6306				
6307				

```

;CHKWC
;THIS ROUTINE CHECKS IF RKWC OVERFLOWED TO 0. IF IT DID A RETURN IS MADE
;TO SKIP THE ERROR MESSAGE. IF NOT, THE CONTENTS OF RKWC AND RKDA ARE SAVED
;AND A RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE 'JSR' CALL.
CHKWC: TST      @RKWC          ;DID WORD COUNT OVERFLOW TO 0?
        BEQ      CRETRN       ;IF YES, BRANCH
                                ;IF NOT, ERROR
                                ;GET RKWC
        MOV      @RKWC,$REG0   ;GET RKWC
        MOV      @RKDA,$REG1   ;GET RKDA
        RTS      PC           ;RETURN TO THE ERROR MESSAGE

```

.SBTTL CHKER: CHECK RKER CONTENTS

6308				
6309				
6310				
6311				
6312				
6313				
6314	021336	005777	157766	
6315	021342	001430		
6316				
6317	021344	004737	020776	
6318				
6319	021350	000207		
6320				
6321				

```

;CHKER
;THIS ROUTINE CHECKS IF ANY BIT IN RKER SET. IF NOT RETURN IS MADE TO SKIP
;THE ERROR MESSAGE. IF ANY BIT IS SET THE CONTENTS OF RKCS, RKER, RKDS ARE
;SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE.
CHKER: TST      @RKER          ;DID ANY BIT IN RKER SET?
        BEQ      CRETRN       ;NO, BRANCH
                                ;YES, ERROR
        JSR      PC,GT3RG     ;GO, GET RKCS, ER, DS
        RTS      PC           ;RETURN TO THE ERROR MESSAGE

```

6322				
6323				
6324				
6325				
6326				
6327	021352	005777	157752	
6328	021356	001422		
6329	021360	013737	001330	001162
6330	021366	017737	157736	001164
6331	021374	000207		
6332				
6333				

```

;CHKECLR
;THIS ROUTINE CHECKS THAT RKER IS CLEAR. IF NOT, THE CONTENTS OF RKER
;ARE SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE FOLLOWING THE "JSR"
;CALL. IF RKER IS CLEAR THE ERROR MESSAGE IS SKIPPED ON RETURN.
CHKECLR: TST    @RKER          ;ANY BIT IN RKER SET?
          BEQ    CRETRN       ;NO
          MOV    @RKER,$REG0   ;GET ADRES OF RKER
          MOV    @RKER,$REG1   ;GET CONTENTS OF RKER
          RTS    PC           ;RETJRN TO THE ERROR MESSAGE

```

6334				
6335				
6336				
6337				
6338	021376	022777	000200	157726
6339	021404	001407		
6340	021406	013737	001332	001162
6341	021414	017737	157712	001164
6342	021422	000207		
6343				
6344	021424	062716	000002	
6345	021430	000207		
6346				
6347				
6348				
6349				

```

;CHKCCLR
;THIS ROUTINE CHECKS THAT RKCS IS CLEAR. IF NOT, THE CONTENTS OF RKCS ARE
;SAVED AND A RETURN IS MADE TO THE ERROR MESSAGE. IF RKCS IS CLEAR THE
;ERROR MESSAGE IS SKIPPED ON RETURN.
CHKCCLR: CMP    #200,@RKCS     ;IS RKCS CLEAR?
          BEQ    CRETRN       ;YES
          MOV    @RKCS,$REG0   ;SAVE ADRES OF RKCS
          MOV    @RKCS,$REG1   ;SAVE THE CONTENT OF RKCS
          RTS    PC           ;RETURN TO THE ERROR MESSAGE
CRETRN: ADD    #2,(SP)        ;SKIP ERROR MESSAGE ON
        RTS    PC           ;RETJRN

```

.SBTTL TSTRWS: WAIT FOR R/W/S RDY ROUTINE


```

6350 ;TSTRWS
6351 ;THIS ROUTINE WAITS FOR R/W/S RDY TO SET. WHEN IT SETS, THE RETURN PC
6352 ;IS INCREMENTED SO THAT ON RETURN (TO THE MAIN PROGRAM) THE ERROR
6353 ;MESSAGE FOLLOWING THE 'JSR' CALL IS SKIPPED. IF R/W/S RDY DOES NOT SET
6354 ;THEN A RETURN IS MADE TO THE ERROR MESSAGE (FOLLOWING THE 'JSR' CALL).
6355 ;WAITING TIME IS APPROX. 1040 MS FOR 11/20, APPROX. 208 MS FOR 11/45
6356 ;CALL: JSR TSTRWS
6357
6358 021432 013777 001350 157700 TSTRWS: MOV DRIVAD, @RKDA ;ADRES THE DRIVE
6359 021440 005037 001366 CLR TIMER ;INITIALIZE COUNT
6360 021444 032777 000100 157654 1$: BIT #100, @RKDS ;DID R/W/S RDY SET?
6361 021452 001007 BNE 2$ ;YES, BRANCH
6362 021454 005237 001366 INC TIMER ;WAIT FOR R/W/S RDY
6363 021460 001371 BNE 1$ ;ERROR IF IT'S NOT SET BY NOW
6364 021462 017737 157640 001162 MOV @RKDS, $REGD ;GET RKDS
6365 021470 000207 RTS PC ;EXIT (TO ERROR FOOLOWING 'JSR TSTRWS')
6366
6367 021472 062716 000002 2$: ADD #2, (SP) ;ADJUST RETURN ADRES TO SKIP OVER
6368 ;ERROR (FOLLOWING 'JSR TSTRWS')
6369 021476 000207 RTS PC ;EXIT
6370
6371
6372
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6374
6375 .SBTTL DRESET: DRIVE RESET ROUTINE
6376
6377 ;DRESET
6378 ;THIS ROUTINE DOES A DRIVE RESET ON THE DRIVE WHOOSE ADDRESS IS IN
6379 ;RKDA. MULTIPLE RETURN ADDRESSES FOR THIS ROUTINE ARE PROVIDED.
6380 ;IF THERE IS NO ERROR (R/W/S RDY SETS WITHIN CERTAIN TIME), THEN BEFORE
6381 ;EXITNG FROM THIS ROUTINE THE RETURN ADDRESS IS INCREMENTED BY 2, TO SKIP
6382 ;THE ERROR MESSAGE ON RETURN. IF THERE IS AN ERROR, THE 3 REGISTERS (CS, ER, DS)
6383 ;ARE STORED AND THEN A NORMAL EXIT IS MADE FROM THIS ROUTINE TO THE
6384 ;ERROR MESSAGE FOLLOWING THE CALL FOR THIS ROUTINE.
6385 ;CALL: JSR PC, DRESET
6386
6387
6388 021500 005037 001364 DRESET: CLR COUNT1 ;INITIALIZE THE COUNT
6389 021504 013777 001350 157626 MOV DRIVAD, @RKDA ;ADRES THE DRIVE
6390 021512 012777 000015 157612 MOV #15, @RKCS ;DRIVE RESET, GO
6391 021520 104414 CNT.RDY ;THIS IS A CALL FOR 'CN.RDY'
6392 ;ROUTINE WHICH WAITS FOR CNT
6393 ;RDY TO SET. IF CNTRL RDY DOES
6394 ;NOT SET WITHIN 883 MS/ 11-20
6395 ;(176 MS FOR 11-45 WITH BIPOLAR)
6396 ;AN ERROR IS REPORTED
6397 021522 032777 000100 157576 1$: BIT #100, @RKDS ;DID R/W/S RDY SET?
6398 021530 001013 BNE 2$
6399 021532 012746 177770 MOV #-10, -(SP) ;PUSH COUNT ON SP
6400 021536 005216 INC (SP) ;COUNT IT DOWN
6401 021540 001376 BNE -2
6402 021542 005726 TST (SP)+ ;POP UP $P
6403 021544 005237 001364 INC COUNT1 ;IF NOT WAIT
6404 021550 001364 BNE 1$ ;WAITED LONG"
6405 021552 004737 020770 JSR PC, GT4RG

```

6406 021556 000402
 6407 021560 062716 000002
 6408 021564 000207

BR 2\$+4
 2\$: ADD #2,DR6
 RTS PC

.SBTTL TSTSIN: CHECK 'SIN' ROUTINE

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:TSTSIN
 :THIS ROUTINE CHECKS IF 'SIN' IS SET, IF IT IS SET A
 :DRIVE RESET IS DONE TO CLEAR 'SIN' AND INITIALIZE POSITIONER.
 :CALL: TST.SIN
 :IF ON DOING DRIVE RESET R/W/S RDY DOES NOT SET A MESSAGE
 : ERROR PC=XXXXXX IS GIVEN.
 :XXXXXX=PC IN THE MAIN PROGRAM WHERE 'TST.SIN' CALL IS LOCATED.

021566 013777 001350 157544
 021574 032777 001000 157524
 021602 001403
 021604 004737 021500
 021610 000401
 021612 000002
 021614 032777 020000 157316
 021622 001373
 021624 104401 021632
 021630 000406
 021646
 021646 011646
 021650 062716 177776
 021654 104402
 021656 000755

TSTSIN: MOV DRIVAD,DRKDA ;ADRES THE DRIVE
 BIT #1000,DRKDS ;IS SIN SET?
 BEQ 1\$
 JSR PC,DRESET ;GO DO DRIVE RESET, SIN SET
 BR 2\$;REPORT ERROR
 1\$: RTI
 2\$: BIT #SW13,DSWR ;INHIBIT TYPEOUT?
 BNE 1\$;IF YES, SKIP TYPEOUT
 TYPE 65\$;TYPE ASCIZ STRING
 BR 64\$;GET OVER THE ASCIZ
 65\$: .ASCIZ /ERROR PC= /
 64\$: MOV (SP) -(SP)
 ADD #-2,(SP) ;GET THE PC WHERE 'TST.SIN' IS LOCATED
 TYPOC ;GO TYPE OUT PC
 BR 1\$

.SBTTL DELAY: TIME DELAY ROUTINE

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:DELAY
 :THIS ROUTINE PROVIDES A VARIABLE TIME DELAY. THE CALL FOR THIS
 :ROUTINE IS AN ENCODED 'TRAP' INSTRUCTION.
 :CALL: DELAY ,N N IS ANY OCTAL NO. FROM 1 TO 177777
 :THE DELAY PROVIDED IS 7.5N US (CONVERT N TO DECIMAL) FOR 11/20
 :1.5N US FOR 11/45
 :IF THE USER WANTS TO CHANGE THE DELAY TIME (EXMP: SHORTER DELAY TO
 :GET A TIGHTER SCOPE LOOP) THE VARIABLE 'N' FOLLOWING 'DELAY' SHOULD
 :BE CHANGED TO SUIT THE INDIVIDUAL NEED.

021660 017637 000000 001366
 021666 062716 000002
 021672 005337 001366
 021676 001375
 021700 000002

DELAY: MOV 2(SP),TIMER ;GET 'AMOUNT' (N) FOR WHICH
 ADD #2,(SP) ;DELAY IS TO BE PROVIDED
 ;ADJUST STACK POINTER TO SKIP OVER 'N'
 1\$: DEC TIMER ;COUNT DOWN TO 0
 BNE 1\$
 RTI ;RETURN TO MAIN PROGRAM

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.SBTTL WAT.INT: WAIT FOR INTERRUPT ROUTINE

;WAT.INT
 ;THIS ROUTINE PROVIDES A VARIABLE TIME WAIT LOOP DURING WHICH AN INTERRUPT
 ;FROM RK11 CAN OCCUR. THE CALL IS AN ENCODED 'TRAP' INSTRUCTION.

;CALL: WAT.INT ,N N IS ANY OCTAL NO. FROM 1 TO 177777

;WAIT LOOP TIME= APPROX. 7.5N US (CONVERT N TO DECIMAL) FOR 11/20
 ;APPROX. 1.5N US FOR 11/45
 ;UPON ENTERING THE ROUTINE THE CPU PRIORITY IS DROPPED SO THAT
 ;RK11 CAN INTERRUPT. NOTE THAT WHEN RK11 INTERRUPTS THIS ROUTINE
 ;IS EXITED WITHOUT POPPING THE STACK, THIS POPPING IS DONE AFTER GETTING
 ;TO RK11 INTERRUPT HANDLER.
 ;IF FOR ANY REASON THE WAIT LOOP TIME HAS TO BE CHANGED IT CAN BE DONE
 ;BY SIMPLY CHANGING THE VARIABLE 'N' FOLLOWING THE 'WAT.INT'.

021702	017637	000000	001366	WATINT: MOV	2(SP),TIMER	;GET 'AMOUNT' (N) FOR WHICH
021710	062716	000002		ADD	#2,(SP)	;WAITING IS TO BE DONE
021714	013746	001400		MOV	RKPRI, -(SP)	;ADJUST STACK POINTER FOR CORRECT RETURN
021720	012746	021726		MOV	#15, -(SP)	;DROP CPU PRIORITY SO THAT RK11 CAN
021724	000002			RTI		; INTERRUPT
021726	005337	001366		1\$: DEC	TIMER	;WAIT FOR RK11 TO INTERRUPT
021732	001375			BNE	1\$	
021734	000002			RTI		;IF INTERRUPT HAS NOT OCCURED BY NOW ;RETURN AND REPORT ERROR ;EXIT

;WATIME

021736	005000			WATIME: CLR	RO
021740	005001			CLR	R1
021742	005200			1\$: INC	RO
021744	001376			BNE	1\$
021746	105201			INCB	R1
021750	001374			BNE	1\$
021752	000207			RTS	PC

.SBTTL CHKCRDY: CHECK CONTROL READY

;CHK.CRDY
 ;THIS ROUTINE WAITS FOR THE CONTROL READY TO SET. IF THE CONTROL READY BIT
 ;DOES NOT SET WITHIN A CERTAIN TIME, THEN THE CONTENTS OF RKCS, RKER, RKDS
 ;AND RKDA ARE SAVED AND AN EXIT MADE TO THE ERROR MESSAGE FOLLOWING THE
 ;'JSR' CALL FOR THIS ROUTINE.
 ;IF CONTROL READY SETS THEN THE RETURN ADDRESS IS ADJUSTED TO SKIP THE
 ;ERROR MESSAGE ON RETURN.
 ;CALL: CHKCRDY
 ; ERROR ;RETURN HERE IF ERROR

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6518
6519
6520 021754 005037 001366
6521 021760 105777 157346
6522 021764 100406
6523 021766 005237 001366
6524 021772 001372
6525 021774 004737 020770
6526 022000 000002
6527
6528 022002 062716 000002
6529 022006 000002
6530
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6532
6533
6534
6535
6536
6537
6538
6539
6540
6541
6542
6543
6544
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6556
6557 022010 012777 000001 157314
6558 022016 012737 177500 001170
6559 022024 000402
6560 022026 005037 001170
6561 022032 105777 157274
6562 022036 100435
6563 022040 005237 001170
6564 022044 001372
6565 022046 032777 020000 157064
6566 022054 001026
6567 022056 104401
6568 022060 001245
6569 022062 104401 022070
6570 022066 000403
6571
6572 022076
6573 022076 011646

```

```

: --- ;RETJRN HERE IF NO ERROR
CH.CRDY: CLR TIMER
1$: TSTB 2RKCS ;CNTRL RDY SET?
BMI 2$ ;YES
INC TIMER
BNE 1$ ;NO, WAIT
JSR PC,GT4RG ;SAVE RKCS, ER, DS, DA
RTI

2$: ADD #2,(SP) ;ADJUST RETURN ADDRESS TO
RTI ;SKIP ERROR MESSAGE ON RETURN

.SBTTL CON.RESET: CONTROL REST ROUTINE

:CON.RESET
:THIS ROUTINE ISSUES A CONTROL RESET AND WAITS FOR
:THE 'CNTRL RDY' FLAG TO SET. WHEN THE FLAG SETS
:AN EXIT IS MADE OUT OF THE ROUTINE. IF 'CNTRL-RDY'
:DOES NOT SET WITHIN A CERTAIN TIME AN ERROR MESSAGE
: CNT RDY DIDN'T SET
: PC=XXXXXX RKCS=YYYYYY
: IS GIVEN. NOTE THAT XXXXXX IS THE PC WHERE 'CNT.RESET' OR 'CNT.RDY'
: IS CALLED.

:CALL: CNT.RESET

.SBTTL CNT.RDY: WAIT FOR CONTROL READY ROUTINE

:CN.RDY
:THIS ROUTINE WAITS FOR THE CONTROL READY BIT TO SET AND WHEN IT
:SETS EXITS OUT. IF WITHIN A CERTAIN TIME CNTRL RDY DOES
:NOT SET AN ERROR IS REPORTED. WAITING TIME IS 883 MS FOR 11-20
:175 MS FOR 11-45 WITH BIPOLAR MEMORY.
:CALL: CNT.RDY
CN.RST: MOV #1,2RKCS ;ISSUE A CONTROL RESET
MOV #-300,$REG3 ;SET UP COUNT
BR CN.RDY+4 ;SKIP OVER CN.RDY

CN.RDY: CLR $REG3
1$: TSTB 2RKCS ;DID CNTRL-RDY SET?
BMI 3$ ;YES, EXIT
INC $REG3 ;WAITED LONG?
BNE 1$ ;IF NOT, GO BAK & WAIT
2$: BIT #SW13,2SWR ;INHIBIT TIMEOUT?
BNE 3$ ;IF YES, SKIP TIMEOUT

:65$: .ASCIZ <15><12>.PC=
64$: BR 65$ ;:TYPE ASCIZ STRING
;:GET OVER THE ASCIZ

64$: MOV (SP),-(SP)

```

```

6574 022100 162716 000002          SUB      #2,(SP)
6575 022104 104402          TYPOC           ;GO TYPE PC IN THE MAIN PROGRAM,
6576                                ;WHERE ERROR OCCURRED
6577 022106 104401 022114          TYPE      67$   ;:TYPE ASCIZ STRING
6578 022112 000404          BR        66$   ;:GET OVER THE ASCIZ
6579                                ;:67$: .ASCIZ / RKCS=/
6580 022124                                66$:
6581 022124 017746 157202          MOV      @RKCS,-(SP) ;GET RKCS
6582 022130 104402          TYPOC           ;GO TYPE IT
6583
6584 022132 000002          3$: RTI         ;RETURN FROM THIS
6585                                ;ROUTINE TO THE MAIN
6586                                ;PROGRAM
6587
6588
6589                                ;THIS PART OF THE PROGRAM CONTAINS THE COMMON ROUTINES CALLED
6590                                ;FROM THE SYSMAC.SML PACKAGE
6591                                ;
6592
6593                                .SBTTL SCOPE HANDLER ROUTINE
6594
6595                                ;:*****
6596                                ;:THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
6597                                ;:AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
6598                                ;:AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
6599                                ;:THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
6600                                ;:SW14=1 LOOP ON TEST
6601                                ;:SW11=1 INHIBIT ITERATIONS
6602                                ;:SW09=1 LOOP ON ERROR
6603                                ;:SW08=1 LOOP ON TEST IN SWR<7:0>
6604                                ;:CALL
6605                                ;:* SCOPE ;:SCOPE=IOT
6606
6607                                $$SCOPE:
6608 022134 022134 104407          CKSWR
6609 022136 032777 040000 156774 1$: BIT      #BIT14,@SWR ;:TEST FOR CHANGE IN SOFT-SWR
6610 022144 001111          BNE      $OVER ;:LOOP ON PRESENT TEST?
6611                                ;:YES IF SW14=1
6612 022146 000416          XTSTR: BR ;:TESTER*****
6613                                ;:IF RUNNING ON THE "XOR" TESTER CHANGE
6614 022150 013746 000004          MOV      @ERRVEC,-(SP) ;:THIS INSTRUCTION TO A "NOP" (NOP=240)
6615 022154 012737 022174 000004          MOV      #5$,@ERRVEC ;:SAVE THE CONTENTS OF THE ERROR VECTOR
6616 022162 005737 177060          TST      @177060 ;:SET FOR TIMEOUT
6617 022166 012637 000004          MOV      (SP)+,@ERRVEC ;:TIME OUT ON XOR?
6618 022172 000463          BR        $$VLAD ;:RESTORE THE ERROR VECTOR
6619 022174 022626          5$: CMP      (SP)+,(SP)+ ;:GO TO THE NEXT TEST
6620 022176 012637 000004          MOV      (SP)+,@ERRVEC ;:CLEAR THE STACK AFTER A TIME OUT
6621 022202 000423          BR        7$ ;:RESTORE THE ERROR VECTOR
6622 022204                                6$: ;:LOOP ON THE PRESENT TEST
6623 022204 032777 000400 156726          BIT      #BIT08,@SWR ;:TESTER*****
6624 022212 001404          BEQ      2$ ;:LOOP ON SPEC. TEST?
6625 022214 127737 156720 001102          CMPB    @SWR,$STNM ;:BR IF NO
6626 022222 001462          BEQ      $OVER ;:ON THE RIGHT TEST? SWR<7:0>
6627 022224 105737 001103          2$: TSTB   $ERFLG ;:BR IF YES
6628 022230 001421          BEQ      3$ ;:HAS AN ERROR OCCURRED?
6629 022232 123737 001115 001103          CMPB    $ERMAX,$ERFLG ;:BR IF NO
6630                                ;:MAX. ERRORS FOR THIS TEST OCCURRED?

```

```

6630 022240 101015          BHI      3$          ;; BR IF NO
6631 022242 032777 001000 156670  BIT      #BIT09, $SWR  ;; LOOP ON ERROR?
6632 022250 001404          BEQ      4$          ;; BR IF NO
6633 022252 013737 001110 001106 7$: MOV    $LPERR, $LPADR ;; SET LOOP ADDRESS TO LAST SCOPE
6634 022260 000443          BR       $OVER
6635 022262 105037 001103 4$: CLR   $ERFLG      ;; ZERO THE ERROR FLAG
6636 022266 005037 001206          CLR   $TIMES      ;; CLEAR THE NUMBER OF ITERATIONS TO MAKE
6637 022272 000415          BR       1$          ;; ESCAPE TO THE NEXT TEST
6638 022274 032777 004000 156636 3$: BIT   #BIT11, $SWR  ;; INHIBIT ITERATIONS?
6639 022302 001011          BNE     1$          ;; BR IF YES
6640 022304 005737 001100          TST   $PASS      ;; IF FIRST PASS OF PROGRAM
6641 022310 001406          BEQ     1$          ;; INHIBIT ITERATIONS
6642 022312 005237 001104          INC   $ICNT      ;; INCREMENT ITERATION COUNT
6643 022316 023737 001206 001104  CMP   $TIMES, $ICNT ;; CHECK THE NUMBER OF ITERATIONS MADE
6644 022324 002021          BGE     $OVER     ;; BR IF MORE ITERATION REQUIRED
6645 022326 012737 000001 001104 1$: MOV   #1, $ICNT  ;; REINITIALIZE THE ITERATION COUNTER
6646 022334 013737 022404 001206  MOV   $MXCNT, $TIMES ;; SET NUMBER OF ITERATIONS TO DO
6647 022342 105237 001102          $SVLAD: INCB  $TSTNM  ;; COUNT TEST NUMBERS
6648 022346 011637 001106          MOV   (SP), $LPADR  ;; SAVE SCOPE LOOP ADDRESS
6649 022352 011637 001110          MOV   (SP), $LPERR  ;; SAVE ERROR LOOP ADDRESS
6650 022356 005037 001210          CLR   $ESCAPE     ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
6651 022362 112737 000001 001115  MOVB  #1, $ERMAX   ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
6652 022370 013777 001102 156544 $OVER: MOV  $TSTNM, $DISPLAY ;; DISPLAY TEST NUMBER
6653 022376 013716 001106          MOV   $LPADR, (SP) ;; FUDGE RETURN ADDRESS
6654 022402 000002          RTI
6655 022404 000050          $MXCNT: 50      ;; FIXES PS
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6666
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6669
6670 022406 104407          $ERROR: CKSWR      ;; CHECK FOR SOFTWARE SWITCH REGISTER REQUEST
6671 022410 105237 001103 7$: INCB  $ERFLG      ;; SET THE ERROR FLAG
6672 022414 001775          BEQ     7$          ;; DON'T LET THE FLAG GO TO ZERO
6673 022416 013777 001102 156516  MOV   $TSTNM, $DISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG
6674 022424 005237 001112 1$: INC   $ERTTL     ;; COUNT THE NUMBER OF ERRORS
6675
6676 022430 032777 000100 156502  BIT   #BIT6, $SWR  ;; DESELECT DRIVE SW SET?
6677 022436 001404          BEQ     6$          ;; NO
6678 022440 023727 001112 000005  CMP   $ERTTL, #5   ;; MORE THAN 5 ERRORS ON THIS DRIVE?
6679 022446 101053          BHI     8$          ;; YES, DESELECT THE DRIVE
6680
6681 022450 011637 001116 6$: MOV   (SP), $ERRPC ;; GET ADDRESS OF ERROR INSTRUCTION
6682 022454 162737 000002 001116  SUB   #2, $ERRPC
6683 022462 117737 156430 001114  MOVB  $ERRPC, $ITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE
6684 022470 032777 020000 156442  BIT   #SW13, $SWR  ;; SKIP TYPEOUT IF SET
6685 022476 001004          BNE     2$          ;; SKIP TYPEOUTS

```

.SBTTL ERROR HANDLER ROUTINE

```

; *SW15=1      HALT ON ERROR
; *SW13=1      INHIBIT ERROR TYPEOUTS
; *SW10=1      TESTING ON SIMULATOR
; *SW09=1      LOOP ON ERROR
; *SW12=1      CYCLE ON ERROR TO PREVIOUS 'SCOPE'
; *SW06=1      DROP DRIVE AFTER MAXIMUM (ALLOWABLE) ERRORS ON THE DRIVE
; *GO TO $ERRTYP ON ERROR

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6686 022500 004737 022730 JSR PC, @SERRTYP ;GO TO USER ERROR ROUTINE
6687 022504 104401 001213 TYPE $CRLF
6688 022510 023737 000042 000046 2$: CMP @#42, @#46 ;ARE WE IN ACT11 AUTO MODE?
6689 022516 001403 BEQ .+10 ;YES, HALT ON ERROR
6690 022520 005777 156414 TST @SWR ;HALT ON ERROR?
6691 022524 100002 BPL 3$ ;SKIP IF CONTINUE
6692 022526 000000 HALT ;HALT ON ERROR!
6693 022530 104407 CKSWR ;CHECK FOR SOFTWARE SWITCH REGISTER REQUEST
6694 022532 032777 010000 156400 3$: BIT #SW12, @SWR ;SW 12 SET?
6695 022540 001402 BEQ .+6 ;NO, BRANCH
6696 022542 013716 001106 MOV $LPADR, (SP) ;ADJUST RETURN ADRES FOR SW12
6697 022546 032777 001000 156364 BIT #SW09, @SWR ;LOOP ON ERROR SWITCH SET?
6698 022554 001402 BEQ 4$ ;BR IF NO
6699 022556 013716 001110 MOV $LPERR, (SP) ;FUDGE RETURN FOR LOOPING
6700 022562 005737 001210 4$: TST $ESCAPE ;CHECK FOR AN ESCAPE ADDRESS
6701 022566 001402 BEQ 5$ ;BR IF NONE
6702 022570 013716 001210 MOV $ESCAPE, (SP) ;FUDGE RETURN ADDRESS FOR ESCAPE
6703 022574 000002 5$: RTI ;RETURN
6704
6705 022576 005737 001434 8$: TST T56FLG ;IF EROR WAS IN LAST TEST (POLL)
6706 ;DROP ALL THE DRIVES
6707 022602 001407 BEQ 10$
6708 022604 104401 001303 TYPE MSG5
6709 022610 005037 001412 CLR DRIVS
6710 022614 022626 CMP (SP)+, (SP)+
6711 022616 000137 020646 JMP $EOP
6712 022622 013746 001354 10$: MOV DRVPTR, -(SP) ;DROP THE DRIVE FROM THE
6713 022626 162716 000002 SUB #2, (SP) ;SELECTION LIST
6714 022632 013746 001350 MOV DRIVAD, -(SP) ;DRIVE ADDR TO STACK
6715 022636 004737 021174 JSR PC, SHFRT ;RIGHT JUSTIFY
6716 022642 042716 000001 BIC #1, (R6) ;MAKE EVEN
6717 022646 062716 001414 ADD #DRIVO, (SP) ;POINTS TO TABLE FOR EVEN DRIVE
6718 022652 042776 100000 000000 BIC #BIT15, @ (R6) ;TEST REMAINING DRIVE AS RK05E
6719 022660 062716 000002 ADD #2, (R6) ;POINT TO ODD
6720 022664 042736 100000 BIC #BIT15, @ (SP)+ ;TEST AS RK-05E
6721 022670 012736 010000 MOV #BIT12, @ (SP)+ ;INDICATE THIS DRIVE DROPPED
6722 022674 104401 001272 TYPE MSG4
6723 022700 013746 001350 MOV DRIVAD, -(R6) ;PUSH DRIVE # ON STACK
6724 022704 004737 021174 JSR PC, SHFRT ;SHIFT IT BEFORE TYPING
6725 022710 104402 TYPOC ;TYPE OUT DRIVE #
6726 022712 104401 001315 TYPE MSG6
6727 022716 005337 001412 DEC DRIVS ;DECREMENT # OF DRIVES PRESNT
6728 022722 022626 9$: CMP (SP)+, (SP)+ ;RESTORE STACK
6729 022724 000137 020022 JMP BTEOP ;GO BACK TO THE END OF PROGRAM
;LINKAGE.

```

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

```

6730
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6733
6734 ;*****
6735 ;*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
6736 ;*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
6737 ;*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
6738

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```

$ERRTYP:
6739 022730 TYPE $CRLF ;;"CARRIAGE RETURN" & "LINE FEED"
6740 022730 104401 001213 MOV RO, -(SP) ;;SAVE RO
6741 022734 010046

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6742 022736 005000 CLR RO ;;PICKUP THE ITEM INDEX
6743 022740 153700 001114 BISB 2(S)ITEMB,RO
6744 022744 001004 BNE 1$ ;; IF ITEM NUMBER IS ZERO, JUST
6745 ;; TYPE THE PC OF THE ERROR
6746 022746 013746 001116 MOV $ERRPC,-(SP) ;;SAVE $ERRPC FOR TYPEOUT
6747 ;;ERROR ADDRESS
6748 022752 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS),
6749 022754 000426 BR 6$ ;;GET OUT
6750 022756 005300 1$: DEC RO ;;ADJUST THE INDEX SO THAT IT WILL
6751 022760 006300 ASL RO ;; WORK FOR THE ERROR TABLE
6752 022762 006300 ASL RO
6753 022764 006300 ASL RO
6754 022766 062700 001442 ADD #SERRTB,RO ;;FORM TABLE POINTER
6755 022772 012037 023002 MOV (RO)+,2$ ;;PICKUP "ERROR MESSAGE" POINTER
6756 022776 001404 BEQ 3$ ;;SKIP TYPEOUT IF NO POINTER
6757 023000 104401 TYPE ;;TYPE THE "ERROR MESSAGE"
6758 023002 000000 2$: .WORD 0 ;;"ERROR MESSAGE" POINTER GOES HERE
6759 023004 104401 001213 TYPE $SCRLF ;;"CARRIAGE RETURN" & "LINE FEED"
6760 023010 012037 023020 3$: MOV (RO)+,4$ ;;PICKUP "DATA HEADER" POINTER
6761 023014 001404 BEQ 5$ ;;SKIP TYPEOUT IF 0
6762 023016 104401 TYPE ;;TYPE THE "DATA HEADER"
6763 023020 000000 4$: .WORD 0 ;;"DATA HEADER" POINTER GOES HERE
6764 023022 104401 001213 TYPE $SCRLF ;;"CARRIAGE RETURN" & "LINE FEED"
6765 023026 011000 5$: MOV (RO),RO ;;PICKUP "DATA TABLE" POINTER
6766 023030 001004 BNE 7$ ;;GO TYPE THE DATA
6767 023032 012600 6$: MOV (SP)+,RO ;;RESTORE RO
6768 023034 104401 001213 TYPE $SCRLF ;;"CARRIAGE RETURN" & "LINE FEED"
6769 023040 000207 RTS PC ;;RETURN
6770 023042 7$:
6771 023042 013046 MOV 2(RO)+,-(SP) ;;SAVE 2(RO)+ FOR TYPEOUT
6772 023044 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
6773 023046 005710 TST (RO) ;;IS THERE ANOTHER NUMBER?
6774 023050 001770 BEQ 6$ ;;BR IF NO
6775 023052 104401 023060 TYPE 8$ ;;TYPE TWO(2) SPACES
6776 023056 000771 BR 7$ ;;LOOP
6777 023060 020040 000 8$: .ASCIZ / / ;;TWO(2) SPACES
6778 023064 .EVEN
6779
6780 .SBTTL TYPE ROUTINE
6781
6782 ;;*****
6783 ;;ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
6784 ;;THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
6785 ;;NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
6786 ;;NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
6787 ;;NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
6788 ;;
6789 ;;CALL:
6790 ;;#1) USING A TRAP INSTRUCTION
6791 ;; TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
6792 ;;
6793 ;;
6794 ;;
6795 ;;
6796 ;;
6797 023064 105737 001157 $TYPE: TSTB $TPFLG ;;IS THERE A TERMINAL?

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6798 023070 100002          BPL      1$          ;; BR IF YES
6799 023072 000000          HALT                     ;; HALT HERE IF NO TERMINAL
6800 023074 000407          BR      3$          ;; LEAVE
6801 023076 010046          1$: MOV    RO, -(SP)      ;; SAVE RO
6802 023100 017600 000002    MOV    2(SP), RO      ;; GET ADDRESS OF ASCIZ STRING
6803 023104 112046          2$: MOVB  (RO)+, -(SP)  ;; PUSH CHARACTER TO BE TYPED ONTO STACK
6804 023106 001005          BNE    4$          ;; BR IF IT ISN'T THE TERMINATOR
6805 023110 005726          TST   (SP)+          ;; IF TERMINATOR POP IT OFF THE STACK
6806 023112 012600          60$: MOV  (SP)+, RO      ;; RESTORE RO
6807 023114 062716 000002    3$: ADD  #2, (SP)      ;; ADJUST RETURN PC
6808 023120 000002          RTI                     ;; RETURN
6809 023122 122716 000011    4$: CMPB #HT, (SP)      ;; BRANCH IF <HT>
6810 023126 001430          BEQ   8$          ;; BRANCH IF NOT <CR LF>
6811 023130 122716 000200    CMPB  #CRLF, (SP)
6812 023134 001006          BNE   5$
6813 023136 005726          TST  (SP)+          ;; POP <CR><LF> EQUIV
6814 023140 104401          TYPE                     ;; TYPE A CR AND LF
6815 023142 001213          $CRLF
6816 023144 105037 023300    CLRB  $CHARCNT        ;; CLEAR CHARACTER COUNT
6817 023150 000755          BR   2$          ;; GET NEXT CHARACTER
6818 023152 004737 023234    5$: JSR  PC, $TYPEC      ;; GO TYPE THIS CHARACTER
6819 023156 123726 001156    6$: CMPB $FILLC, (SP)+  ;; IS IT TIME FOR FILLER CHARS.?
6820 023162 001350          BNE  2$          ;; IF NO GO GET NEXT CHAR.
6821 023164 013746 001154    MOV  $NULL, -(SP)    ;; GET # OF FILLER CHARS. NEEDED
6822                                AND THE NULL CHAR.
6823 023170 105366 000001    7$: DECB 1(SP)        ;; DOES A NULL NEED TO BE TYPED?
6824 023174 002770          BLT  6$          ;; BR IF NO--GO POP THE NULL OFF OF STACK
6825 023176 004737 023234    JSR  PC, $TYPEC      ;; GO TYPE A NULL
6826 023202 105337 023300    DECB  $CHARCNT        ;; DO NOT COUNT AS A COUNT
6827 023206 000770          BR   7$          ;; LOOP
6828
6829          :HORIZONTAL TAB PROCESSOR
6830
6831 023210 112716 000040    8$: MOVB #' (SP)      ;; REPLACE TAB WITH SPACE
6832 023214 004737 023234    9$: JSR  PC, $TYPEC      ;; TYPE A SPACE
6833 023220 132737 000007 023300    BITB #7, $CHARCNT    ;; BRANCH IF NOT AT
6834 023226 001372          BNE  9$          ;; TAB STOP
6835 023230 005726          TST  (SP)+          ;; POP SPACE OFF STACK
6836 023232 000724          BR   2$          ;; GET NEXT CHARACTER
6837 023234 105777 155710    $TYPEC: TSTB 2$STPS    ;; WAIT UNTIL PRINTER IS READY
6838 023240 100375          BPL  $TYPEC
6839 023242 116677 000002 155702    MOVB 2(SP), 2$STPB    ;; LOAD CHAR TO BE TYPED INTO DATA REG.
6840 023250 122766 000015 000002    CMPB #CR, 2(SP)      ;; IS CHARACTER A CARRIAGE RETURN?
6841 023256 001003          BNE  1$          ;; BRANCH IF NO
6842 023260 105037 023300    CLRB  $CHARCNT        ;; YES--CLEAR CHARACTER COUNT
6843 023264 000406          BR   $TYPEX
6844 023266 122766 000012 000002 1$: CMPB #LF, 2(SP)    ;; IS CHARACTER A LINE FEED?
6845 023274 001402          BEQ  $TYPEX        ;; BRANCH IF YES
6846 023276 105227          INCB (PC)+          ;; COUNT THE CHARACTER
6847 023300 000000          $CHARCNT: .WORD 0   ;; CHARACTER COUNT STORAGE
6848 023302 000207          $TYPEX: RTS PC
6849
6850
6851          .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
6852
6853          ;; *****

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6863 023304
6864 023304 010046
6865 023306 010146
6866 023310 010246
6867 023312 010346
6868 023314 010546
6869 023316 012746 020200
6870 023322 016605 000020
6871 023326 100004
6872 023330 005405
6873 023332 112766 000055 000001
6874 023340 005000 1$:
6875 023342 012703 023520
6876 023346 112723 000040
6877 023352 005002 2$:
6878 023354 016001 023510
6879 023360 160105 3$:
6880 023362 002402
6881 023364 005202
6882 023366 000774
6883 023370 060105 4$:
6884 023372 005702
6885 023374 001002
6886 023376 105716
6887 023400 100407
6888 023402 106316 5$:
6889 023404 103003
6890 023406 116663 000001 177777
6891 023414 052702 000060 6$:
6892 023420 052702 000040 7$:
6893 023424 110223
6894 023426 005720
6895 023430 020027 000010
6896 023434 002746
6897 023436 003002
6898 023440 010502
6899 023442 000764
6900 023444 105726 9$:
6901 023446 100003
6902 023450 116663 177777 177776
6903 023456 105013 9$:
6904 023460 012605
6905 023462 012603
6906 023464 012602
6907 023466 012601
6908 023470 012600
6909 023472 104401 023520

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: *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
: *SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
: *NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
: *BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
: *REPLACED WITH SPACES.
: *CALL:
: *   MOV   NUM,-(SP)   ;; PUT THE BINARY NUMBER ON THE STACK
: *   TYPDS ;; GO TO THE ROUTINE

$TYPDS:
MOV   R0,-(SP)   ;; PUSH R0 ON STACK
MOV   R1,-(SP)   ;; PUSH R1 ON STACK
MOV   R2,-(SP)   ;; PUSH R2 ON STACK
MOV   R3,-(SP)   ;; PUSH R3 ON STACK
MOV   R5,-(SP)   ;; PUSH R5 ON STACK
MOV   #20200,-(SP) ;; SET BLANK SWITCH AND SIGN
MOV   20(SP),R5  ;; GET THE INPUT NUMBER
BPL   1$         ;; BR IF INPUT IS POS.
NEG   R5         ;; MAKE THE BINARY NUMBER POS.
MOVB  #'-,1(SP)  ;; MAKE THE ASCII NUMBER NEG.
CLR   R0         ;; ZERO THE CONSTANTS INDEX
MOV   #5DBLK,R3  ;; SETUP THE OUTPUT POINTER
MOVB  #' ,(R3)+  ;; SET THE FIRST CHARACTER TO A BLANK
CLR   R2         ;; CLEAR THE BCD NUMBER
MOV   $DTBL(R0),R1 ;; GET THE CONSTANT
SUB   R1,R5      ;; FORM THIS BCD DIGIT
BLT   4$         ;; BR IF DONE
INC   R2         ;; INCREASE THE BCD DIGIT BY 1
BR    3$

4$:   ADD   R1,R5  ;; ADD BACK THE CONSTANT
TST   R2         ;; CHECK IF BCD DIGIT=0
BNE   5$         ;; FALL THROUGH IF 0
TSTB  (SP)       ;; STILL DOING LEADING 0'S?
BMI   7$         ;; BR IF YES
ASLB  (SP)       ;; MSD?
BCC   6$         ;; BR IF NO
MOVB  1(SP),-1(R3) ;; YES--SET THE SIGN
BIS   #'0,R2     ;; MAKE THE BCD DIGIT ASCII
BIS   #' ,R2     ;; MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB  R2,(R3)+   ;; PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST   (R0)+     ;; JUST INCREMENTING
CMP   R0,#10    ;; CHECK THE TABLE INDEX
BLT   2$         ;; GO DO THE NEXT DIGIT
BGT   8$         ;; GO TO EXIT
MOV   R5,R2     ;; GET THE LSD
BR    6$         ;; GO CHANGE TO ASCII
TSTB  (SP)+     ;; WAS THE LSD THE FIRST NON-ZERO?
BPL   9$         ;; BR IF NO
MOVB  -1(SP),-2(R3) ;; YES--SET THE SIGN FOR TYPING
CLRB  (R3)      ;; SET THE TERMINATOR
MOV   (SP)+,R5  ;; POP STACK INTO R5
MOV   (SP)+,R3  ;; POP STACK INTO R3
MOV   (SP)+,R2  ;; POP STACK INTO R2
MOV   (SP)+,R1  ;; POP STACK INTO R1
MOV   (SP)+,R0  ;; POP STACK INTO R0
TYPE ,5DBLK    ;; NOW TYPE THE NUMBER

```

```

6910 023476 016666 000002 000004
6911 023504 012616
6912 023506 000002
6913 023510 023420
6914 023512 001750
6915 023514 000144
6916 023516 000012
6917 023520 000004
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6944 023530 017646 000000
6945 023534 116637 000001 023753
6946 023542 112637 023755
6947 023546 062716 000002
6948 023552 000406
6949 023554 112737 000001 023753
6950 023562 112737 000006 023755
6951 023570 112737 000005 023752
6952 023576 010346
6953 023600 010446
6954 023602 010546
6955 023604 113704 023755
6956 023610 005404
6957 023612 062704 000006
6958 023616 110437 023754
6959 023622 113704 023753
6960 023626 016605 000012
6961 023632 005003
6962 023634 006105
6963 023636 000404
6964 023640 006105
6965 023642 006105

```

```

MOV 2(SP),4(SP) ;;ADJUST THE STACK
MOV (SP)+,(SP)
RTI ;;RETURN TO USER
$DTBL: 10000.
1000.
100.
10.
$DBLK: .BLKW 4
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE
;*****
;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
;OCTAL (ASCII) NUMBER AND TYPE IT.
;$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
;CALL:
;* MOV NUM,-(SP) ;;NUMBER TO BE TYPED
;* TYPOS ;;CALL FOR TYPEOUT
;* .BYTE N ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
;* .BYTE M ;;M=1 OR 0
; ;;1=TYPE LEADING ZEROS
; ;;0=SUPPRESS LEADING ZEROS
;$TYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
;$TYPOS OR $TYPOC
;CALL:
;* MOV NUM,-(SP) ;;NUMBER TO BE TYPED
;* TYPON ;;CALL FOR TYPEOUT
;$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
;CALL:
;* MOV NUM,-(SP) ;;NUMBER TO BE TYPED
;* TYPOC ;;CALL FOR TYPEOUT
$TYPOS: MOV 2(SP),-(SP) ;;PICKUP THE MODE
MOV 1,SP,$OFILL ;;LOAD ZERO FILL SWITCH
MOV 1,SP,$OMODE+1 ;;NUMBER OF DIGITS TO TYPE
ADD #2,SP ;;ADJUST RETURN ADDRESS
BR $TYPON
$TYPOC: MOV #1,$OFILL ;;SET THE ZERO FILL SWITCH
MOV #6,$OMODE+1 ;;SET FOR SIX(6) DIGITS
$TYPON: MOV #5,$OCNT ;;SET THE ITERATION COUNT
MOV R3,-(SP) ;;SAVE R3
MOV R4,-(SP) ;;SAVE R4
MOV R5,-(SP) ;;SAVE R5
MOV $OMODE+1,R4 ;;GET THE NUMBER OF DIGITS TO TYPE
NEG R4
ADD #6,R4 ;;SUBTRACT IT FOR MAX. ALLOWED
MOV R4,$OFILL ;;SAVE IT FOR USE
MOV $OFILL,R4 ;;GET THE ZERO FILL SWITCH
MOV 12(SP),R5 ;;PICKUP THE INPUT NUMBER
CLR R3 ;;CLEAR THE OUTPUT WORD
1$: ROL R5 ;;ROTATE MSB INTO "C"
BR 3$ ;;GO DO MSB
2$: ROL R5 ;;FORM THIS DIGIT
ROL R5

```

```

6966 023644 006105          ROL      R5
6967 023646 010503          MOV     R5,R3
6968 023650 006103          3$:    ROL     R3          ;; GET LSB OF THIS DIGIT
6969 023652 105337 023754    DECB   $OMODE          ;; TYPE THIS DIGIT?
6970 023656 100016          BPL    7$              ;; BR IF NO
6971 023660 042703 177770    BIC    #177770,R3      ;; GET RID OF JUNK
6972 023664 001002          BNE    4$              ;; TEST FOR 0
6973 023666 005704          TST    R4              ;; SUPPRESS THIS 0?
6974 023670 001403          BEQ    5$              ;; BR IF YES
6975 023672 005204          4$:    INC    R4          ;; DON'T SUPPRESS ANYMORE 0'S
6976 023674 052703 000060    BIS    #'0,R3          ;; MAKE THIS DIGIT ASCII
6977 023700 052703 000040    5$:    BIS    #' ,R3      ;; MAKE ASCII IF NOT ALREADY
6978 023704 110337 023750    MOVB   R3,8$           ;; SAVE FOR TYPING
6979 023710 104401 023750    TYPE   8$              ;; GO TYPE THIS DIGIT
6980 023714 105337 023752    7$:    DECB   $OCNT          ;; COUNT BY 1
6981 023720 003347          BGT    2$              ;; BR IF MORE TO DO
6982 023722 002402          BLT    6$              ;; BR IF DONE
6983 023724 005204          INC    R4              ;; INSURE LAST DIGIT ISN'T A BLANK
6984 023726 000744          BR     2$              ;; GO DO THE LAST DIGIT
6985 023730 012605          6$:    MOV    (SP)+,R5      ;; RESTORE R5
6986 023732 012604          MOV    (SP)+,R4      ;; RESTORE R4
6987 023734 012603          MOV    (SP)+,R3      ;; RESTORE R3
6988 023736 016666 000002 000004    MOV    2(SP),4(SP)    ;; SET THE STACK FOR RETURNING
6989 023744 012616          MOV    (SP)+,(SP)
6990 023746 000002          RTI
6991 023750          8$:    .BYTE 0          ;; RETURN
6992 023751          .BYTE 0          ;; STORAGE FOR ASCII DIGIT
6993 023752          .BYTE 0          ;; TERMINATOR FOR TYPE ROUTINE
6994 023753          .BYTE 0          ;; OCTAL DIGIT COUNTER
6995 023754 000000    $OCNT: .BYTE 0          ;; ZERO FILL SWITCH
6996          $OFILL: .BYTE 0          ;; NUMBER OF DIGITS TO TYPE
6997          $OMODE: .WORD 0
6998          .SBTTL TTY INPUT ROUTINE
6999          ;;*****
7000          .ENABL LSB
7001          ;;*****
7002          ;;*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
7003          ;;*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
7004          ;;*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
7005          ;;*WHEN OPERATING IN TTY FLAG MODE.
7006          $CKSWR: CMP    #SWREG,SWR          ;; IS THE SOFT-SWR SELECTED?
7007 023756 022737 000176 001140    BNE    15$           ;; BRANCH IF NO
7008 023764 001074          TSTB   2$TKS          ;; CHAR THERE?
7009 023766 105777 155152    BPL    15$           ;; IF NO, DON'T WAIT AROUND
7010 023772 100071          MOVB   2$TKB, -(SP)   ;; SAVE THE CHAR
7011 023774 117746 155146    BIC    #177, (SP)     ;; STRIP-OFF THE ASCII
7012 024000 042716 177600    CMP    #7, (SP)+      ;; IS IT A CONTROL G?
7013 024004 022726 000007    BNE    15$           ;; NO, RETURN TO USER
7014 024010 001062          CMPB   $AUTOB, #1     ;; ARE WE RUNNING IN AUTO-MODE?
7015 024012 123727 001134 000001    BEQ    15$           ;; BRANCH IF YES
7016 024020 001456
7017
7018 024022 104401 024643          TYPE   . $CNTLG      ;; ECHO THE CONTROL-G (↑G)
7019 024026 104401 024650    $GTSWR: TYPE   $MSWR   ;; TYPE CURRENT CONTENTS
7020 024032 013746 000176    MOV    SWREG, -(SP)   ;; SAVE SWREG FOR TYPEOUT
7021 024036 104402          TYPOC          ;; GO TYPE--OCTAL ASCII(ALL DIGITS)

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7078 024240 011646          $RDCHR: MOV      (SP), -(SP)      ;; PUSH DOWN THE PC
7079 024242 016666 000004 000002  MOV      4(SP), 2(SP)      ;; SAVE THE PS
7080 024250 105777 154670 1$:  TSTB     2$TKS          ;; WAIT FOR
7081 024254 100375          BPL      1$              ;; A CHARACTER
7082 024256 117766 154664 000004  MOVB     2$TKB, 4(SP)      ;; READ THE TTY
7083 024264 042766 177600 000004  BIC      #1C(177), 4(SP)   ;; GET RID OF JUNK IF ANY
7084 024272 026627 000004 000023  CMP      4(SP), #23       ;; IS IT A CONTROL-S?
7085 024300 001013          BNE      3$              ;; BRANCH IF NO
7086 024302 105777 154636 2$:  TSTB     2$TKS          ;; WAIT FOR A CHARACTER
7087 024306 100375          BPL      2$              ;; LOOP UNTIL ITS THERE
7088 024310 117746 154632          MOVB     2$TKB, -(SP)      ;; GET CHARACTER
7089 024314 042716 177600          BIC      #1C177, (SP)     ;; MAKE IT 7-BIT ASCII
7090 024320 022627 000021          CMP      (SP)+, #21       ;; IS IT A CONTROL-Q?
7091 024324 001366          BNE      2$              ;; IF NOT DISCARD IT
7092 024326 000750          BR       1$              ;; YES, RESUME
7093 024330 026627 000004 000140 3$:  CMP      4(SP), #140      ;; IS IT UPPER CASE?
7094 024336 002407          BLT      4$              ;; BRANCH IF YES
7095 024340 026627 000004 000175  CMF      4(SP), #175      ;; IS IT A SPECIAL CHAR?
7096 024346 003003          BGT      4$              ;; BRANCH IF YES
7097 024350 042766 000040 000004  BIC      #40, 4(SP)       ;; MAKE IT UPPER CASE
7098 024356 000002          RTI                      ;; GO BACK TO USER
7099                                     *****
7100                                     *THIS ROUTINE WILL INPUT A STRING FROM THE TTY
7101                                     *CALL:
7102                                     *      ROLIN                ;; INPUT A STRIN  FROM THE TTY
7103                                     *      RETURN HERE         ;; ADDRESS OF F:  CHARACTER WILL BE ON THE STACK
7104                                     *                                     ;; TERMINATOR WILL BE A BYTE OF ALL 0'S
7105
7106 024360 010346          $RDLIN: MOV      R3, -(SP)   ;; SAVE R3
7107 024362 005046          CLR      -(SP)           ;; CLEAR THE RUBOUT KEY
7108 024364 012703 024614 1$:  MOV      #TTYIN, R3      ;; GET ADDRESS
7109 024370 022703 024636 2$:  CMP      #TTYIN+22, R3   ;; BUFFER FULL?
7110 024374 101456          BLOS     4$              ;; BR IF YES
7111 024376 104410          RDCHR   ;; GO READ ONE CHARACTER FROM THE TTY
7112 024400 112613          MOVB     (SP)+, (R3)      ;; GET CHARACTER
7113 024402 122713 000177 10$:  CMPB     #177, (R3)       ;; IS IT A RUBOUT
7114 024406 001022          BNE      5$              ;; BR IF NO
7115 024410 005716          TST      (SP)            ;; IS THIS THE FIRST RUBOUT?
7116 024412 001007          BNE      6$              ;; BR IF NO
7117 024414 112737 000134 024612  MOVB     #' \, 9$        ;; TYPE A BACK SLASH
7118 024422 104401 024612          TYPE    9$              ;;
7119 024426 012716 177777          MOV      #-1, (SP)       ;; SET THE RUBOUT KEY
7120 024432 005303          DEC     R3               ;; BACKUP BY ONE
7121 024434 020327 024614 6$:  CMP      R3, #TTYIN      ;; STACK EMPTY?
7122 024440 103434          BLO     4$              ;; BR IF YES
7123 024442 111337 024612          MOVB     (R3), 9$        ;; SETUP TO TYPEOUT THE DELETED CHAR.
7124 024446 104401 024612          TYPE    9$              ;; GO TYPE
7125 024452 000746          BR       2$              ;; GO READ ANOTHER CHAR.
7126 024454 005716          5$:  TST      (SP)            ;; RUBOUT KEY SET?
7127 024456 001406          BEQ     7$              ;; BR IF NO
7128 024460 112737 000134 024612  MOVB     #' \, 9$        ;; TYPE A BACK SLASH
7129 024466 104401 024612          TYPE    9$              ;;
7130 024472 005016          CLR     (SP)            ;; CLEAR THE RUBOUT KEY
7131 024474 122713 000025 7$:  CMPB     #25, (R3)       ;; IS CHARACTER A CTRL U?
7132 024500 001003          BNE     8$              ;; BR IF NO
7133 024502 104401 024636          TYPE    , $CNTLU        ;; TYPE A CONTROL "U"

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7134 024506 000726
7135 024510 122713 000022
7136 024514 001011
7137 024516 105013
7138 024520 104401 001213
7139 024524 104401 024614
7140 024530 000717
7141 024532 104401 001212
7142 024536 000712
7143 024540 111337 024612
7144 024544 104401 024612
7145 024550 122723 000015
7146 024554 001305
7147 024556 105063 177777
7148 024562 104401 001214
7149 024566 005726
7150 024570 012603
7151 024572 011646
7152 024574 016666 000004 000002
7153 024602 012766 024614 000004
7154 024610 000002
7155 024612 000
7156 024613 000
7157 024614 000022
7158 024636 052536 005015 000
7159 024643 136 006507 000012
7160 024650 005015 053523 020122
7161 024656 020075 000
7162 024661 040 047040 053505
7163 024666 036440 000040
7164
7165
7166
7167
7168
7169
7170
7171
7172
7173 024672 010046
7174 024674 016600 000002
7175 024700 005740
7176 024702 111000
7177 024704 006300
7178 024706 016000 024726
7179 024712 000200
7180
7181
7182
7183
7184 024714 011646
7185 024716 016666 000004 000002
7186 024724 000002
7187
7188
7189
    BR 1$ ; GO START OVER
    8$: CMPB #22,(R3) ; IS CHARACTER A "R"?
    BNE 3$ ; BRANCH IF NO
    CLRB (R3) ; CLEAR THE CHARACTER
    TYPE $CRLF ; TYPE A "CR" & "LF"
    TYPE $TTYIN ; TYPE THE INPUT STRING
    BR 2$ ; GO PICKUP ANOTHER CHACTER
    4$: TYPE $QUES ; TYPE A '?'
    BR 1$ ; CLEAR THE BUFFER AND LOOP
    3$: MOVB (R3),9$ ; ECHO THE CHARACTER
    TYPE 9$
    CMPB #15,(R3)+ ; CHECK FOR RETURN
    BNE 2$ ; LOOP IF NOT RETURN
    CLRB -1(R3) ; CLEAR RETURN (THE 15)
    TYPE $LF ; TYPE A LINE FEED
    TST (SP)+ ; CLEAN RUBOUT KEY FROM THE STACK
    MOV (SP)+,R3 ; RESTORE R3
    MOV (SP),-(SP) ; ADJUST THE STACK AND PUT ADDRESS OF THE
    MOV 4(SP),2(SP) ; FIRST ASCII CHARACTER ON IT
    MOV $TTYIN,4(SP)
    RTI ; RETURN
    9$: .BYTE 0 ; STORAGE FOR ASCII CHAR. TO TYPE
    .BYTE 0 ; TERMINATOR
    $TTYIN: .BLKB 22 ; RESERVE 22 BYTES FOR TTY INPUT
    $CNTLU: .ASCIZ /TU/<15><12> ; CONTROL "U"
    $CNTLG: .ASCIZ /TG/<15><12> ; CONTROL "G"
    $MSWR: .ASCIZ <15><12>/SWR = /
    $MNEW: .ASCIZ / NEW = /
    ;CONTROL U, RUBOUT CAPABILITY
    .SBTTL TRAP DECODER
    ;*****
    ;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
    ;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
    ;*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
    ;*GO TO THAT ROUTINE.
    $TRAP: MOV RO, -(SP) ; SAVE RO
    MOV 2(SP),RO ; GET TRAP ADDRESS
    TST -(RO) ; BACKUP BY 2
    MOVB (RO),RO ; GET RIGHT BYTE OF TRAP
    ASL RO ; POSITION FOR INDEXING
    MOV $TRAPAD(RO),RO ; INDEX TO TABLE
    RTS RO ; GO TO ROUTINE
    ;:THIS IS USE TO HANDLE THE "GETPRI" MACRO
    $TRAP2: MOV (SP),-(SP) ; MOVE THE PC DOWN
    MOV 4(SP),2(SP) ; MOVE THE PSW DOWN
    RTI ; RESTORE THE PSW
    .SBTTL TRAP TABLE
    
```

:*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
:*BY THE "TRAP" INSTRUCTION.

7190				
7191				
7192				
7193				
7194				
7195	024726	024714		
7196	024730	023064		
7197	024732	023554		
7198	024734	023530		
7199	024736	023570		
7200	024740	023304		
7201				
7202	024742	024026		
7203				
7204	024744	023756		
7205	024746	024240		
7206	024750	024360		
7207				
7208	024752	021754		
7209				
7210	024754	022010		
7211				
7212	024756	022026		
7213				
7214	024760	021052		
7215				
7216	024762	021062		
7217				
7218	024764	021660		
7219				
7220	024766	021702		
7221				
7222	024770	021566		
7223				
7224				
7225				
7226				
7227				
7228				
7229	024772	012737	025136	000024
7230	025000	012737	000340	000026
7231	025006	010046		
7232	025010	010146		
7233	025012	010246		
7234	025014	010346		
7235	025016	010446		
7236	025020	010546		
7237	025022	017746	154112	
7238	025026	010637	025142	
7239	025032	012737	025044	000024
7240	025040	000000		
7241	025042	000776		
7242				
7243				
7244				
7245	025044	012737	025136	000024

: ROUTINE
:-----

\$TRPAD:	.WORD	\$TRAP2		
	\$TYPE	::CALL=TYPE	TRAP+1(104401)	TTY TYPEOUT ROUTINE
	\$TYPOC	::CALL=TYPOC	TRAP+2(104402)	TYPE OCTAL NUMBER (WITH LEADING ZEROS)
	\$TYPOS	::CALL=TYPOS	TRAP+3(104403)	TYPE OCTAL NUMBER (NO LEADING ZEROS)
	\$TYPON	::CALL=TYPON	TRAP+4(104404)	TYPE OCTAL NUMBER (AS PER LAST CALL)
	\$TYPDS	::CALL=TYPDS	TRAP+5(104405)	TYPE DECIMAL NUMBER (WITH SIGN)
	\$GTSWR	::CALL=GTSWR	TRAP+6(104406)	GET SOFT-SWR SETTING
	\$CKSWR	::CALL=CKSWR	TRAP+7(104407)	TEST FOR CHANGE IN SOFT-SWR
	\$RDCHR	::CALL=RDCHR	TRAP+10(104410)	TTY TYPEIN CHARACTER ROUTINE
	\$RDLIN	::CALL=RDLIN	TRAP+11(104411)	TTY TYPEIN STRING ROUTINE
	CH.CRDY	::CALL=CHKCRDY	TRAP+12(104412)	CHECK CONTROL READY
	CN.RST	::CALL=CN.RST	TRAP+13(104413)	CONTROL RESET ROUTINE
	CN.RDY	::CALL=CN.RDY	TRAP+14(104414)	WAIT FOR CNTRL RDY TO SET
	BDA0	::CALL=BRKDA0	TRAP+15(104415)	BREAK RKDA INTO DR #,CYL,SUR,SEC BITS
	BDA4	::CALL=BRKDA4	TRAP+16(104416)	BREAK RKDA INTO DR #,CYL,SUR,SEC BITS
	DELA.Y	::CALL=DELAY	TRAP+17(104417)	TIME DELAY ROUTINE
	WATINT	::CALL=WAT.INT	TRAP+20(104420)	WAIT FOR RK11 INTERRUPT ROUTINE
	TSTSIN	::CALL=TST.SIN	TRAP+21(104421)	TEST SIN ROUTINE

.SBTTL POWER DOWN AND UP ROUTINES

::*****

```

:POWER DOWN ROUTINE
$PWRDN: MOV    $SILLUP, @PWRVEC    ;; SET FOR FAST UP
        MOV    #340, @PWRVEC+2  ;; PRIO:7
        MOV    R0, -(SP)        ;; PUSH R0 ON STACK
        MOV    R1, -(SP)        ;; PUSH R1 ON STACK
        MOV    R2, -(SP)        ;; PUSH R2 ON STACK
        MOV    R3, -(SP)        ;; PUSH R3 ON STACK
        MOV    R4, -(SP)        ;; PUSH R4 ON STACK
        MOV    R5, -(SP)        ;; PUSH R5 ON STACK
        MOV    @SWR, -(SP)      ;; PUSH @SWR ON STACK
        MOV    SP, $SAVR6      ;; SAVE SP
        MOV    $SPWRUP, @PWRVEC ;; SET UP VECTOR
        HALT
        BR     .-2              ;; HANG UP

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::*****

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:POWER UP ROUTINE
$PWRUP: MOV    $SILLUP, @PWRVEC ;; SET FOR FAST DOWN

```



```

7246 025052 013706 025142      MOV    $SAVR6,SP      ;; GET SP
7247 025056 005037 025142      CLR    $SAVR6        ;; WAIT LOOP FOR THE TTY
7248 025062 005237 025142      1$:   INC    $SAVR6    ;; WAIT FOR THE INC
7249 025066 001375          BNE    1$            ;; OF WORD
7250 025070 012677 154044      MOV    (SP)+, @JSWR  ;; POP STACK INTO @JSWR
7251 025074 012605          MOV    (SP)+, R5     ;; POP STACK INTO R5
7252 025076 012604          MOV    (SP)+, R4     ;; POP STACK INTO R4
7253 025100 012603          MOV    (SP)+, R3     ;; POP STACK INTO R3
7254 025102 012602          MOV    (SP)+, R2     ;; POP STACK INTO R2
7255 025104 012601          MOV    (SP)+, R1     ;; POP STACK INTO R1
7256 025106 012600          MOV    (SP)+, R0     ;; POP STACK INTO R0
7257 025110 012737 024772 000024  MOV    #SPWRDN, @PWRVEC ;; SET UP THE POWER DOWN VECTOR
7258 025116 012737 000340 000026  MOV    #340, @PWRVEC+2 ;; PRIO:7
7259 025124 104401          TYPE                                ;; REPORT THE POWER FAILURE
7260 025126 025144      $PWRMG: .WORD    $POWER      ;; POWER FAIL MESSAGE POINTER
7261 025130 012716          MOV    (PC)+, (SP)    ;; RESTART AT PFSTRT
7262 025132 004702      $PWRAD: .WORD    PFSTRT    ;; RESTART ADDRESS
7263 025134 000002          RTI
7264 025136 000000      $ILLUP: HALT          ;; THE POWER UP SEQUENCE WAS STARTED
7265 025140 000776          BR    .-2            ;; BEFORE THE POWER DOWN WAS COMPLETE
7266 025142 000000      $SAVR6: 0            ;; PUT THE SP HERE
7267 025144 005015 047520 042527  $POWER: .ASCIZ  <15><12>"POWER"
7268 025152 000122          .EVEN
7269
7270
7271 025154 004737 021500      FCHECK: JSR    PC, DRESET      ;; RESETB DRIVE
7272 025160 104026          ERROR    26
7273 025162 104413          CNT.RESET
7274 025164 013737 001350 025276      MOV    DRIVAD, DRHOLD    ;; SAVE DRIVE ADDR
7275 025172 032737 020000 001350      BIT    #20000, DRIVAD    ;; SEE IF ODD
7276 025200 001404          BEQ    1$
7277 025202 042737 020000 001350      BIC    #20000, DRIVAD    ;; MAKE EVEN
7278 025210 000403          BR    2$
7279 025212 052737 020000 001350 1$:   BIS    #20000, DRIVAD    ;; MAKE ODD
7280 025220 013777 001350 154112 2$:   MOV    DRIVAD, @RKDA    ;; DRIVE ADDR
7281 025226 012777 000011 154076      MOV    #11, @RKCS      ;; DRIVE SEEK
7282 025234 104414          CNT.RDY
7283 025236 013777 025276 154074      MOV    DRHOLD, @RKDA    ;; OTHER DRIVE
7284 025244 104414          CNT.RDY
7285 025246 032777 000100 154052      BIT    #100, @RKDS      ;; HEAEDS IN MOTIONN?
7286 025254 001001          BNE    3$
7287 025256 005725          TST   (R5)+           ;; NO 50 RK-05J
7288 025260 013737 025276 001350 3$:   MOV    DRHOLD, DRIVAD    ;; YES RK-05F
7289 025266 004737 021500          JSR    PC, DRESET      ;; RESTORE ADDR
7290 025272 104026          ERROR    26           ;; WAIT FOR RESET
7291 025274 000205          RTS    R5
7292 025276 000000      DRHOLD: 0
7293 025300 005037 001350      SIZEF: CLR    DRIVAD      ;; START AT DRO
7294 025304 012700 001414          MOV    #DRIVO, R0      ;; TABLE OF AVAIL DRIVES
7295 025310 005710          TST   (R0)            ;; THIS DRIVE HERE?
7296 025312 001413          BEQ    2$            ;; NO
7297 025314 005760 000002          TST   2(R0)           ;; COMPLEMENT HERE?
7298 025320 001410          BEQ    2$            ;; NO
7299 025322 004537 025154          JSR    R5, FCHECK      ;; SEE IF F MODEL
7300 025326 000405          BR    2$            ;; J MODEL
7301 025330 052710 100000          BIS    #100000, (R0)   ;; SET SIGN FOR F

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7302	025334	052760	100000	000002		BIS	#100000,2(RO)	;BOTH DRIVES
7303	025342	005720			2\$:	TST	(RO)+	
7304	025344	005720				TST	(RO)+	;NEXT PAIR OF DRIVES
7305	025346	062737	040000	001350		ADD	#40000,DRIVAD	;NEXT ACTUL ADDR
7306	025354	022700	001433			CMP	#DRIV7+1,RO	;CHECKED ALL?
7307	025360	003353				BGT	4\$;NOT YET
7308	025362	000207				RTS	PC	
7309								
7310								;ERROR MESSAGES
7311								
7312								.SBTTL ERROR MESSAGES
7313								
7314	025364	045522	041527	042440	EM11:	.ASCIZ	/RKWC EROR/	
7315	025372	047522	000122					
7316								
7317								
7318	025376	044523	020116	051511	EM12:	.ASCIZ	/SIN IS SET/	
7319	025404	051440	052105	000				
7320								
7321	025411	122	041113	020101	EM13:	.ASCIZ	/RKBA EROR/	
7322	025416	051105	051117	000				
7323								
7324	025423	122	042113	020101	EM16:	.ASCIZ	/RKDA WRONG AFTER 'SSE'/	
7325	025430	051127	047117	020107				
7326	025436	043101	042524	020122				
7327	025444	051447	042523	000047				
7328								
7329	025452	045522	051504	042440	EM21:	.ASCIZ	/RKDS EROR/	
7330	025460	047522	000122					
7331								
7332	025464	050104	020114	042523	EM30:	.ASCIZ	/DPL SET/	
7333	025472	000124						
7334								
7335	025474	051104	020125	042523	EM31:	.ASCIZ	/DRU SET/	
7336	025502	000124						
7337								
7338	025504	045522	032460	041040	EM32:	.ASCIZ	/RKOS BIT NOT SET/	
7339	025512	052111	047040	052117				
7340	025520	051440	052105	000				
7341								
7342	025525	104	054522	041040	EM33:	.ASCIZ	/DRY BIT NOT SET/	
7343	025532	052111	047040	052117				
7344	025540	051440	052105	000				
7345								
7346	025545	123	045517	042040	EM34:	.ASCIZ	/SOK DIDN'T SET/	
7347	025552	042111	023516	020124				
7348	025560	042523	000124					
7349								
7350	025564	042523	026503	047103	EM35:	.ASCIZ	/SEC-CNTR DIDN'T COUNT TO 0/	
7351	025572	051124	042040	042111				
7352	025600	023516	020124	047503				
7353	025606	047125	020124	047524				
7354	025614	030040	000					
7355								
7356	025617	123	041505	041455	EM36:	.ASCIZ	/SEC-CNTR DIDN'T INCRMNT/	
7357	025624	052116	020122	044504				

7358	025632	047104	052047	044440		
7359	025640	041516	046522	052116		
7360	025646	000				
7361						
7362	025647	123	041505	041455	EM37:	.ASCIZ /SEC-COUNTR INCRMENTED WRONG/
7363	025654	052517	052116	020122		
7364	025662	047111	051103	042515		
7365	025670	052116	042105	053440		
7366	025676	047522	043516	000		
7367						
7368	025703	104	042111	023516	EM40:	.ASCIZ /DIDN'T GET SC=SA FOR THIS SECTR/
7369	025710	020124	042507	020124		
7370	025716	041523	051475	020101		
7371	025724	047506	020122	044124		
7372	025732	051511	051440	041505		
7373	025740	051124	000			
7374						
7375	025743	105	047522	026522	EM41:	.ASCIZ "EROR-R/W/S RDY SHOULD BE SET"
7376	025750	027522	027527	020123		
7377	025756	042122	020131	044123		
7378	025764	052517	042114	041040		
7379	025772	020105	042523	000124		
7380						
7381	026000	047125	054105	042520	EM43:	.ASCIZ /UNEXPECTED RK11 INTERRUPT/
7382	026006	052103	042105	051040		
7383	026014	030513	020061	047111		
7384	026022	042524	051122	050125		
7385	026030	000124				
7386						
7387	026032	047103	051124	020114	EM44:	.ASCIZ /CNTRL RDY DIDN'T SET AFTER SEEK OR DR RESET/
7388	026040	042122	020131	044504		
7389	026046	047104	052047	051440		
7390	026054	052105	040440	052106		
7391	026062	051105	051440	042505		
7392	026070	020113	051117	042040		
7393	026076	020122	042522	042523		
7394	026104	000124				
7395						
7396	026106	051105	020122	051117	EM45:	.ASCIZ /ERR OR HE BIT SET ON SEEK OR DR RESET
7397	026114	044040	020105	044502		
7398	026122	020124	042523	020124		
7399	026130	047117	051440	042505		
7400	026136	020113	051117	042040		
7401	026144	020122	042522	042523		
7402	026152	000124				
7403						
7404	026154	045522	051105	041040	EM46:	.ASCIZ /RKER BIT, ON SEEK OR DR RESET/
7405	026162	052111	020054	047117		
7406	026170	051440	042505	020113		
7407	026176	051117	042040	020122		
7408	026204	042522	042523	000124		
7409						
7410	026212	045522	051503	041440	EM47:	.ASCIZ /RKCS CHNGC AFTR FUNCTION WAS DCNE/
7411	026220	047110	042107	040440		
7412	026226	052106	020122	052506		
7413	026234	041516	044524	047117		

7414	026242	053440	051501	042040		
7415	026250	047117	000105			
7416						
7417	026254	027522	027527	020123	EM50:	.ASCIZ "R/W/S RDY DIDN'T CLEAR"
7418	026262	042122	020131	044504		
7419	026270	047104	052047	041440		
7420	026276	042514	051101	000		
7421						
7422	026303	122	053457	051457	EM51:	.ASCIZ "R/W/S RDY DIDN'T SET AFTR SEEK OR DR RESET"
7423	026310	051040	054504	042040		
7424	026316	042111	023516	020124		
7425	026324	042523	020124	043101		
7426	026332	051124	051440	042505		
7427	026340	020113	051117	042040		
7428	026346	020122	042522	042523		
7429	026354	000124				
7430						
7431	026356	045522	040504	041440	EM52:	.ASCIZ /RKDA CHNGD AFTR SEEK/
7432	026364	047110	042107	040440		
7433	026372	052106	020122	042523		
7434	026400	045505	000			
7435						
7436	026403	103	052116	046122	EM53:	.ASCIZ CNTRL RDY DIDN'T CLR AS GO WAS SET/
7437	026410	051040	054504	042040		
7438	026416	042111	023516	020124		
7439	026424	046103	020122	051501		
7440	026432	043440	020117	040527		
7441	026440	020123	042523	000124		
7442						
7443	026446	047103	051124	020114	EM54:	.ASCIZ "CNTRL RDY DIDN'T SET ON WRT/FMT STARTING FROM /DSK-ADRES "
7444	026454	042122	020131	044504		
7445	026462	047104	052047	051440		
7446	026470	052105	047440	020116		
7447	026476	051127	027524	046506		
7448	026504	020124	052123	051101		
7449	026512	044524	043516	043040		
7450	026520	047522	020115	042074		
7451	026526	045523	040455	051104		
7452	026534	051505	000076			
7453						
7454	026540	042510	047440	020122	EM55:	.ASCIZ "HE OR ERR ON WRT/FMT STARTING FROM /DSK-ADRES"
7455	026546	051105	020122	047117		
7456	026554	053440	052122	043057		
7457	026562	052115	051440	040524		
7458	026570	052122	047111	020107		
7459	026576	051106	046517	036040		
7460	026604	051504	026513	042101		
7461	026612	042522	037123	000		
7462						
7463	026617	122	042113	020101	EM56:	.ASCIZ /RKDA INCRMNTD WRONG ON WRT-FMT
7464	026624	047111	051103	047115		
7465	026632	042124	053440	047522		
7466	026640	043516	047440	020116		
7467	026646	051127	026524	046506		
7468	026654	000124				
7469						

7470	026656	045522	041527	042040	EM57:	.ASCIZ	'RKWC DIDN'T OVRFLO ON WRT FMT'
7471	026664	042111	023516	020124			
7472	026672	053117	043122	047514			
7473	026700	047440	020116	051127			
7474	026706	020124	046506	000124			
7475							
7476	026714	045522	040502	044440	EM60:	.ASCIZ	'RKBA INCRMNTD WRONG ON WRT FMT'
7477	026722	041516	046522	052116			
7478	026730	020104	051127	047117			
7479	026736	020107	047117	053440			
7480	026744	052122	043040	052115			
7481	026752	000					
7482							
7483	026753	122	042513	020122	EM61:	.ASCIZ	'RKER SET.ON WRT OR RD OR FMT'
7484	026760	042523	026124	047117			
7485	026766	053440	052122	047440			
7486	026774	020122	042122	047440			
7487	027002	020122	046506	000124			
7488							
7489	027010	045522	041104	042440	EM62:	.ASCIZ	'RKOB EROR'
7490	027016	047522	020122				
7491							

7492	027022	045522	040504	044440	EM63:	.ASCIZ	/RKDA INCRMNTD WRONG ON RD OR RD FMT/
7493	027030	041516	046522	052116			
7494	027036	020104	051127	047117			
7495	027044	020107	047117	051040			
7496	027052	020104	051117	051040			
7497	027060	020104	046506	000124			
7498							
7499	027066	045522	041527	042040	EM64:	.ASCIZ	/RKWC DIDN'T OVRFLO ON RD OR RD FMT/
7500	027074	042111	023516	020124			
7501	027102	053117	043122	047514			
7502	027110	047440	020116	042122			
7503	027116	047440	020122	042122			
7504	027124	043040	052115	000			
7505							
7506	027131	122	041113	020101	EM65:	.ASCIZ	/RKBA INCRMNTD WRONG ON RD OR RD FMT/
7507	027136	047111	051103	047115			
7508	027144	042124	053440	047522			
7509	027152	043516	047440	020116			
7510	027160	042122	047440	020122			
7511	027166	042122	043040	052115			
7512	027174	000					
7513							
7514	027175	111	041516	051117	EM66:	.ASCIZ	/INCORRECT HEADER FROM 'SECTOR' /
7515	027202	042522	052103	044040			
7516	027210	040505	042504	020122			
7517	027216	051106	046517	023440			
7518	027224	042523	052103	051117			
7519	027232	000047					
7520							
7521	027234	040504	040524	042440	EM67:	.ASCIZ	/DATA ERROR/
7522	027242	051122	051117	000			
7523							
7524	027247	103	052116	046122	EM70:	.ASCIZ	"CNTRL RDY DIDN'T SET ON RD/FMT STARTING FROM (DSK-ADRES)"
7525	027254	051040	054504	042040			
7526	027262	042111	023516	020124			
7527	027270	042523	020124	047117			
7528	027276	051040	027504	046506			
7529	027304	020124	052123	051101			
7530	027312	044524	043516	043040			
7531	027320	047522	020115	042074			
7532	027326	045523	040455	051104			
7533	027334	051505	000076				
7534							
7535	027340	042510	047440	020122	EM71:	.ASCIZ	"HE OR ERR ON RD/FMT STARTING FROM (DSK-ADRES)"
7536	027346	051105	020122	047117			
7537	027354	051040	027504	046506			
7538	027362	020124	052123	051101			
7539	027370	044524	043516	043040			
7540	027376	047522	020115	042074			
7541	027404	045523	040455	051104			
7542	027412	051505	000076				
7543							
7544	027416	051127	047117	020107	EM72:	.ASCIZ	/WRONG DRIVE ID IN RKDS AFTER SEEK/
7545	027424	051104	053111	020105			
7546	027432	042111	044440	020116			
7547	027440	045522	051504	040440			

7548	027446	052106	051105	051440	
7549	027454	042505	000113		
7550					
7551	027460	051110	053504	042522	EM73: .ASCIZ /HRDWRE POLL-DRV ID BITS(13-15) SHLDBE CLR/
7552	027466	050040	046117	026514	
7553	027474	051104	020126	042111	
7554	027502	041040	052111	024123	
7555	027510	031461	030455	024465	
7556	027516	051440	046110	041104	
7557	027524	020105	046103	000122	
7558					
7559	027532	051110	053504	042522	EM74: .ASCIZ /HRDWRE POLL-INTRUPTING DRV * NOT PRSNT/
7560	027540	050040	046117	026514	
7561	027546	047111	051124	050125	
7562	027554	044524	043516	042040	
7563	027562	044522	020126	020043	
7564	027570	047516	020124	051120	
7565	027576	047123	000124		
7566					
7567	027602	051104	053111	021440	EM75: .ASCIZ /DRV * DIDN'T INTRUPT AFTER HRDWRE POLL/
7568	027610	042040	042111	023516	
7569	027616	020124	047111	051124	
7570	027624	050125	020124	043101	
7571	027632	042524	020122	051110	
7572	027640	053504	042522	050040	
7573	027646	046117	000114		
7574					
7575	027652	041523	020120	044504	EM76: .ASCIZ /SCP DIDN'T SET AFTER SEEK WAS DONE.
7576	027660	047104	052047	051440	
7577	027666	052105	040440	052106	
7578	027674	051105	051440	042505	
7579	027702	020113	040527	020123	
7580	027710	047504	042516	000	
7581					
7582	027715	122	042113	020101	EM77: .ASCIZ /RKDA CHANGD AFTER DRV RESET/
7583	027722	044103	047101	042107	
7584	027730	040440	052106	051105	
7585	027736	042040	044522	020126	
7586	027744	042522	042523	000124	
7587					
7588	027752	040504	040524	042440	EM100: .ASCIZ /DATA EROR AT WORD#/
7589	027760	047522	020122	052101	
7590	027766	053440	051117	021504	
7591	027774	000			
7592					
7593	027775	103	052116	046122	EM101: .ASCIZ /CNTRL RDY DIDN'T SET AFTER RD CHK/
7594	030002	051040	054504	042040	
7595	030010	042111	023516	020124	
7596	030016	042523	020124	043101	
7597	030024	042524	020122	042122	
7598	030032	041440	045510	000	
7599					
7600	030037	105	051122	047440	EM102: .ASCIZ /ERR OR HE ON RD CHK/
7601	030044	020122	042510	047440	
7602	030052	020116	042122	041440	
7603	030060	045510	000		

7604					
7605	030063	103	042523	047440	EM103: .ASCIZ /CSE ON RD CHK/
7606	030070	020116	042122	041440	
7607	030076	045510	000		
7608					
7609	030101	122	053513	020103	EM104: .ASCIZ /RKWC DIDN'T OVERFLO ON RD CHK OR WRT CHK/
7610	030106	044504	047104	052047	
7611	030114	047440	042526	043122	
7612	030122	047514	047440	020116	
7613	030130	042122	041440	045510	
7614	030136	047440	020122	051127	
7615	030144	020124	044103	000113	
7616					
7617	030152	045522	040504	044440	EM105: .ASCIZ RKDA INCRMNTD WRONG ON RD CHK/
7618	030160	041516	046522	052116	
7619	030166	020104	051127	047117	
7620	030174	020107	047117	051040	
7621	030202	020104	044103	000113	
7622					
7623	030210	045522	040502	041440	EM106: .ASCIZ /RKBA CHANGD AFTER RD CHK/
7624	030216	040510	043516	020104	
7625	030224	043101	042524	020122	
7626	030232	042122	041440	045510	
7627	030240	000			
7628					
7629	030241	115	046505	051117	EM107: .ASCIZ /MEMORY WORD CHANGED AFTER RD CHK/
7630	030246	020131	047527	042122	
7631	030254	041440	040510	043516	
7632	030262	042105	040440	052106	
7633	030270	051105	051040	020104	
7634	030276	044103	000113		
7635					
7636	030302	047103	051124	020114	EM110: .ASCIZ /CNTRL RDY DIDN'T SET AFTER WRT CHK/
7637	030310	042122	020131	044504	
7638	030316	047104	052047	051440	
7639	030324	052105	040440	052106	
7640	030332	051105	053440	052122	
7641	030340	041440	045510	000	
7642					
7643	030345	110	020105	051117	EM111: .ASCIZ /HE OR ERR ON WRT CHK/
7644	030352	042440	051122	047440	
7645	030360	020116	051127	020124	
7646	030366	044103	000113		
7647					
7648	030372	051127	052111	020105	EM112: .ASCIZ /WRITE CHECK EROR/
7649	030400	044103	041505	020113	
7650	030406	051105	051117	000	
7651					
7652	030413	122	042113	020101	EM113: .ASCIZ /RKDA INCRMNTD WRONG ON WRT CHK/
7653	030420	047111	051103	047115	
7654	030426	042124	053440	047522	
7655	030434	043516	047440	020116	
7656	030442	051127	020124	044103	
7657	030450	000113			
7658					
7659	030452	045522	040502	044440	EM114: .ASCIZ /RKBA INCRMNTD WRONG ON WRT CHK/

7660	030460	041516	046522	052116	
7661	030466	020104	051127	047117	
7662	030474	020107	047117	053440	
7663	030502	052122	041440	045510	
7664	030510	000			
7665					
7666	030511	122	041113	020101	EM115: .ASCIZ /RKBA INCRMNTD, WITH IBA SET/
7667	030516	047111	051103	047115	
7668	030524	042124	020054	044527	
7669	030532	044124	044440	040502	
7670	030540	051440	052105	000	
7671					
7672	030545	127	047522	043516	EM116: .ASCIZ WRONG MEMORY LOCATION CHANGED WITH IBA SET/
7673	030552	046440	046505	051117	
7674	030560	020131	047514	040503	
7675	030566	044524	047117	041440	
7676	030574	040510	043516	042105	
7677	030602	053440	052111	020110	
7678	030610	041111	020101	042523	
7679	030616	000124			
7680					
7681	030620	045522	030461	042040	EM117: .ASCIZ /RK11 DIDN'T INTRUPT WHEN IDE WAS SET/
7682	030626	042111	023516	020124	
7683	030634	047111	051124	050125	
7684	030642	020124	044127	047105	
7685	030650	044440	042504	053440	
7686	030656	051501	051440	052105	
7687	030664	000			
7688					
7689	030665	122	030513	020061	EM120: .ASCIZ /RK11 DIDN'T INTRUPT AFTER SK WAS INITIATED/
7690	030672	044504	047104	052047	
7691	030700	044440	052116	052522	
7692	030706	052120	040440	052106	
7693	030714	051105	051440	020113	
7694	030722	040527	020123	047111	
7695	030730	052111	040511	042524	
7696	030736	000104			
7697					
7698	030740	041523	020120	042523	EM121: .ASCIZ /SCP SET BEFORE SEEK COMPLETED/
7699	030746	020124	042502	047506	
7700	030754	042522	051440	042505	
7701	030762	020113	047503	050115	
7702	030770	042514	042524	000104	
7703					
7704	030776	045522	030461	042040	EM122: .ASCIZ /RK11 DIDN'T INTRUPT AFTER SK COMPLETED/
7705	031004	042111	023516	020124	
7706	031012	047111	051124	050125	
7707	031020	020124	043101	042524	
7708	031026	020122	045523	041440	
7709	031034	046517	046120	052105	
7710	031042	042105	000		
7711					
7712	031045	103	052116	046122	EM123: .ASCIZ /CNTRL RESET DIDN'T CLEAR 'SCP'/
7713	031052	051040	051505	052105	
7714	031060	042040	042111	023516	
7715	031066	020124	046103	040505	

7716	031074	020122	051447	050103	
7717	031102	000047			
7718					
7719	031104	045522	030461	042040	EM124: .ASCIZ /RK11 DIDN'T INTRUPT AFTER RD DONE/
7720	031112	042111	023516	020124	
7721	031120	047111	051124	050125	
7722	031126	020124	043101	042524	
7723	031134	020122	042122	042040	
7724	031142	047117	000105		
7725					
7726	031146	047103	051124	020114	EM125: .ASCIZ /CNTRL RESET DIDN'T CLR REGISTR/
7727	031154	042522	042523	020124	
7728	031162	044504	047104	052047	
7729	031170	041440	051114	051040	
7730	031176	043505	051511	051124	
7731	031204	000			
7732					
7733	031205	122	030513	020061	EM126: .ASCIZ /RK11 DIDN'T INTRUPT AT CPU LEVEL/
7734	031212	044504	047104	052047	
7735	031220	044440	052116	052522	
7736	031226	052120	040440	020124	
7737	031234	050103	020125	042514	
7738	031242	042526	000114		
7739					
7740	031246	045522	030461	044440	EM127: .ASCIZ /RK11 INTRUPTED AT WRONG CPU LEVEL/
7741	031254	052116	052522	052120	
7742	031262	042105	040440	020124	
7743	031270	051127	047117	020107	
7744	031276	050103	020125	042514	
7745	031304	042526	000114		
7746					
7747	031310	042447	051122	041040	EM130: .ASCIZ /'ERR BIT' DIDN'T SET IN RKER/
7748	031316	052111	020047	044504	
7749	031324	047104	052047	051440	
7750	031332	052105	044440	020116	
7751	031340	045522	051105	000	
7752					
7753	031345	110	020105	051117	EM131: .ASCIZ /HE OR ERR DIDN'T SET/
7754	031352	042440	051122	042040	
7755	031360	042111	023516	020124	
7756	031366	042523	000124		
7757					
7758	031372	045522	051105	042440	EM132: .ASCIZ /RKER EROR/
7759	031400	047522	000122		
7760					
7761	031404	054116	020103	044502	EM133: .ASCIZ /NXC BIT DIDN'T SET/
7762	031412	020124	044504	047104	
7763	031420	052047	051440	052105	
7764	031426	000			
7765					
7766	031427	122	030513	020061	EM134: .ASCIZ /RK11 DIDN'T INTRUPT ON SOFT EROR/
7767	031434	044504	047104	052047	
7768	031442	044440	052116	052522	
7769	031450	052120	047440	020116	
7770	031456	047523	052106	042440	
7771	031464	047522	000122		

7772						
7773	031470	042515	020130	044502	EM135:	.ASCIZ /MEX BITS INCRMNTD WRONG-RKCS/
7774	031476	051524	044440	041516		
7775	031504	046522	052116	020104		
7776	031512	051127	047117	026507		
7777	031520	045522	051503	000		
7778						
7779	031525	127	051520	047040	EM137:	.ASCIZ /WPS NOT CLEAR/
7780	031532	052117	041440	042514		
7781	031540	051101	000			
7782						
7783	031543	104	052101	020101	EM140:	.ASCIZ /DATA EROR ON TRANSFER FROM DISK TO TTY/
7784	031550	051105	051117	047440		
7785	031556	020116	051124	047101		
7786	031564	043123	051105	043040		
7787	031572	047522	020115	044504		
7788	031600	045523	052040	020117		
7789	031606	052124	000131			
7790						
7791	031612	042047	044522	020126	EM141:	.ASCIZ /'DRIV #' PRESENT, BUT NOT INDICATED/
7792	031620	023443	050040	042522		
7793	031626	042523	052116	020054		
7794	031634	052502	020124	047516		
7795	031642	020124	047111	044504		
7796	031650	040503	042524	000104		
7797	031656	047040	020117	052502	EM142:	.ASCIZ / NO BUSY ON OTHER HAL F OF RK-05F/
7798	031664	054523	047440	020116		
7799	031672	052117	042510	020122		
7800	031700	040510	043114	047440		
7801	031706	020106	045522	030055		
7802	031714	043065	000			
7803						
7804						
7805						
7806						
7807						
7808		031720				.EVEN
7809						.SBTTL ERROR DATA POINTERS
7810						
7811						
7812	031720	001116	001162	000000	DT1:	.WORD \$ERRPC,\$REG0,0
7813						
7814	031726	001116	001162	001164	DT2:	.WORD \$ERRPC,\$REG0,\$REG1,0
7815	031734	000000				
7816						
7817	031736	001116	001162	001164	DT20:	.WORD \$ERRPC,\$REG0,\$REG1,\$REG2,\$REG3,0
7818	031744	001166	001170	000000		
7819						
7820	031752	001116	000000		DT21:	.WORD \$ERRPC,0
7821						
7822	031756	001116	001162	001164	DT26:	.WORD \$ERRPC,\$REG0,\$REG1,\$REG2,0
7823	031764	001166	000000			
7824						
7825	031770	001116	001162	001164	DT54:	.WORD \$ERRPC,\$REG0,\$REG1,\$REG2,\$REG3,\$REG4,\$REG5,\$REG6,\$REG7,0
7826	031776	001166	001170	001172		
7827	032004	001174	001176	001200		

7996
7997
7998 033336 000400
7999
9000
9001
9002 000001

:DATA BUFFER

OUTBUF: .BLKW 256.

:THIS 256 WORD BUFFER IS FOR
:DATA TRANSFERS FROM AND
:TO THE DISK.

.END

BADINT	004600	CR	= 000015	DT20	031736	EM40	025703	PFSTR	004702
BADTMO	004534	CRETRN	021424	DT21	031752	EM41	025743	PHYDRV	001436
BDAR	021070	CRLF	= 000200	DT26	031756	EM43	026000	PIRQ	= 177772
BDQ0	021052	DDISP	= 177570	DT54	031770	EM44	026032	PIRQVE	= 000240
BDQ4	021062	DDPCH	= 001410	EFLG1	001370	EM45	026106	PRO	= 000000
BIT0	= 000001	DELAY	= 104417	EMTVEC	= 000030	EM46	026154	PR1	= 000040
BIT00	= 000001	DELA.Y	021660	EM100	027752	EM47	026212	PR2	= 000100
BIT01	= 000002	DH100	032747	EM101	027775	EM50	026254	PR3	= 000140
BIT02	= 000004	DH103	033004	EM102	030037	EM51	026303	PR4	= 000200
BIT03	= 000010	DH104	033020	EM103	030063	EM52	026356	PR5	= 000240
BIT04	= 000020	DH107	033044	EM104	030101	EM53	026403	PR6	= 000300
BIT05	= 000040	DH117	033102	EM105	030152	EM54	026446	PR7	= 000340
BIT06	= 000100	DH126	033116	EM106	030210	EM55	026540	PS	= 177775
BIT07	= 000200	DH130	033144	EM107	030241	EM56	026617	PSW	= 177776
BIT08	= 000400	DH131	033203	EM11	025364	EM57	026656	PWRVEC	= 000024
BIT09	= 001000	DH133	033231	EM110	030302	EM60	026714	RDCHR	= 104410
BIT1	= 000002	DH14	032107	EM111	030345	EM61	026753	RDLIN	= 104411
BIT10	= 002000	DH140	033267	EM112	030372	EM62	027010	RESVEC	= 000010
BIT11	= 004000	DH2	032014	EM113	030413	EM63	027022	RKBA	001336
BIT12	= 010000	DH21	032144	EM114	030452	EM64	027066	RKCS	001332
BIT13	= 020000	DH30	032151	EM115	030511	EM65	027131	RKDA	001340
BIT14	= 040000	DH34	032207	EM116	030545	EM66	027175	RKOB	001342
BIT15	= 100000	DH35	032225	EM117	030620	EM67	027234	RKOS	001326
BIT2	= 000004	DH36	032245	EM12	025376	EM70	027247	RKER	001330
BIT3	= 000010	DH4	032043	EM120	030665	EM71	027340	RKPRI	001400
BIT4	= 000020	DH40	032275	EM121	030740	EM72	027416	RKVEC	001402
BIT5	= 000040	DH44	032323	EM122	030776	EM73	027460	RKWC	001334
BIT6	= 000100	DH5	032071	EM123	031045	EM74	027532	R6	=%000006
BIT7	= 000200	DH54	032370	EM124	031104	EM75	027602	R7	=%000007
BIT8	= 000400	DH56	032477	EM125	031146	EM76	027652	SEEK0	001372
BIT9	= 001000	DH64	032604	EM126	031205	EM77	027715	SEEK1	001374
BPTVEC	= 000014	DH66	032630	EM127	031246	ERRVEC	= 000004	SEEK2	001376
BRKDA0	= 104415	DH67	032666	EM13	025411	FCHECK	025154	SHFTRT	021174
BRKDA4	= 104416	DH74	032726	EM130	031310	FFLAG	001404	SIMUL	001344
BTEOP	020022	DISPLA	001142	EM131	031345	FTITLE	001346	SIZEF	025300
CHE1	021236	DISPRE	000174	EM132	031372	GTSWR	= 104406	SIZYET	001440
CHKCCL	021376	DRESET	021500	EM133	031404	GT2RG	021004	STACK	= 001100
CHKCRD	= 104412	DRHOLD	025276	EM134	031427	GT3RG	020776	START	002636
CHKDA	021256	DRIVAD	001350	EM135	031470	GT4RG	020770	STARTA	002704
CHKDA1	021264	DRIVS	001412	EM137	031525	HT	= 000011	START1	003334
CKECL	021352	DRIVO	001414	EM140	031543	INDX1	001356	STKLMT	= 177774
CHKER	021336	DRIV1	001416	EM141	031612	INDX2	001360	ST2	003736
CHKHE	021230	DRIV2	001420	EM142	031656	IOTVEC	= 000020	ST3	004262
CHKHE1	021222	DRIV3	001422	EM16	025423	LF	= 000012	ST4	004456
CHKWC	021312	DRIV4	001424	EM21	025452	MSG1	001216	SWR	001140
CH.CRD	021754	DRIV5	001426	EM30	025464	MSG2	001236	SWREG	000176
CKSWR	= 104407	DRIV6	001430	EM31	025474	MSG3	001245	SW0	= 000001
CNT.RD	= 104414	DRIV7	001432	EM32	025504	MSG4	001272	SW00	= 000001
CNT.RE	= 104413	DRVON	001352	EM33	025525	MSG5	001303	SW01	= 000002
CN.RDY	022026	DRVPTR	001354	EM34	025545	MSG6	001315	SW02	= 000004
CN.RST	022010	DSWR	= 177570	EM35	025564	NUDRV	005040	SW03	= 000010
COUNT	001362	CT1	031720	EM36	025617	ODDEVN	001406	SW04	= 000020
COUNT:	001364	DT2	031726	EM37	025647	OUTBUF	033336	SW05	= 000040

SW06 = 000100
SW07 = 000200
SW08 = 000400
SW09 = 001000
SW1 = 000002
SW10 = 002000
SW11 = 004000
SW12 = 010000
SW13 = 020000
SW14 = 040000
SW15 = 100000
SW2 = 000004
SW3 = 000010
SW4 = 000020
SW5 = 000040
SW6 = 000100
SW7 = 000200
SW8 = 000400
SW9 = 001000
TBITVE = 000014
TIMER = 001366
TKVEC = 000060
TPVEC = 000064
TRAPVE = 000034
TATVEC = 000014
TSTENO = 020622
TSTRWS = 021432
TSTSTN = 021566
TST.SI = 104421
TST1 = 004706
TST10 = 005742
TST11 = 006030
TST12 = 006104
TST13 = 006246
TST14 = 006412
TST15 = 006504
TST16 = 006742
TST17 = 007170
TST2 = 005040

TST20 = 007500
TST21 = 007676
TST22 = 010150
TST23 = 010520
TST24 = 011000
TST25 = 011410
TST26 = 011716
TST27 = 012236
TST3 = 005236
TST30 = 012524
TST31 = 012760
TST32 = 013164
TST33 = 013432
TST34 = 013714
TST35 = 014172
TST36 = 014324
TST37 = 014650
TST4 = 005350
TST40 = 015064
TST41 = 015270
TST42 = 015452
TST43 = 015560
TST44 = 015734
TST45 = 016072
TST46 = 016300
TST47 = 016452
TST5 = 005436
TST50 = 016630
TST51 = 016756
TST52 = 017130
TST53 = 017324
TST54 = 017616
TST55 = 017754
TST56 = 020000
TST57 = 020050
TST6 = 005464
TST7 = 005522
TYERM = 021022
TYPDS = 104405

TYPE = 104401
TYPDC = 104402
TYPON = 104404
TYPOS = 104403
T56 = 020134
T56FLG = 001434
WATIME = 021736
WATINT = 021702
WAT.IN = 104420
\$AUTOB = 001134
\$BODADR = 001122
\$BODAT = 001126
\$CHARC = 023300
\$CKSWR = 023756
\$CMTAG = 001100
\$CM1 = 000012
\$CM2 = 000024
\$CM3 = 000012
\$CNTLG = 024643
\$CNTLU = 024636
\$CRLF = 001213
\$DLK = 023520
\$DOAGN = 020744
\$DTBL = 023510
\$ENDAD = 020734
\$ENDCT = 020702
\$ENDMG = 020753
\$ENULL = 020750
\$EOP = 020646
\$EOPCT = 020674
\$ERFLG = 001103
\$ERMAX = 001115
\$ERROR = 022406
\$ERRPC = 001116
\$ERRTB = 001442
\$ERRTY = 022730
\$ERTTL = 001112
\$ESCAP = 001210
\$FILLC = 001156

\$FILLS = 001155
\$GDADR = 001120
\$GODAT = 001124
\$GET42 = 020724
\$GTSWR = 024026
\$HD = 000000
\$ICNT = 001104
\$ILLUP = 025136
\$INTAG = 001135
\$ITEMB = 001114
\$LF = 001214
\$LPADR = 001106
\$LPERR = 001110
\$MNEW = 024661
\$MSWR = 024650
\$MXCNT = 022404
\$NULL = 001154
\$NWTST = 000001
\$OCNT = 023752
\$OMODE = 023754
\$OVER = 022370
\$PASS = 001100
\$POWER = 025144
\$PWAD = 025132
\$PWADN = 024772
\$PWRMG = 025126
\$PWRUP = 025044
\$QUES = 001212
\$RDCHR = 024240
\$RDLIN = 024360
\$RDSZ = 000022
\$REGAD = 001160
\$REGD = 001162
\$REG1 = 001164
\$REG10 = 001202
\$REG11 = 001204
\$REG2 = 001166
\$REG3 = 001170
\$REG4 = 001172

\$REG5 = 001174
\$REG6 = 001176
\$REG7 = 001200
\$RTNAD = 020746
\$SAVR6 = 025142
\$SCOPE = 022134
\$SETJP = 000117
\$STUP = 177777
\$SVLAD = 022342
\$SVPC = 000204
\$SWR = 165400
\$SWRMK = 000000
\$TIMES = 001206
\$TKB = 001146
\$TKS = 001144
\$TN = 000060
\$TPB = 001152
\$TPFLG = 001157
\$TPS = 001150
\$TRAP = 024672
\$TRAP2 = 024714
\$TRP = 000022
\$TRPAD = 024726
\$TSTNM = 001102
\$TTYIN = 024614
\$TYPDS = 023304
\$TYPE = 023064
\$TYPEC = 023234
\$TYPEX = 023302
\$TYPDC = 023554
\$TYPON = 023570
\$TYPOS = 023530
\$XTSTR = 022146
\$GET4 = 000000
\$FILL = 023753
 = 034336

. ABS. 034336 000

ERRORS DETECTED: 0

DSKM:DZRKKE/SOL=DSKZ:SYSMAC.SML,DSKZ:DZRKKE.P11
RUN-TIME: 19 27 .5 SECONDS
RUN-TIME RATIO: 1063/47=22.3
CORE USED: 34K (67 PAGES)