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IDENTIFICATION

PRODUCT CODE: AC-E742G-MC
PRODUCT NAME: CXFPAGO DEC/X11 FP11 (11/40 & 45) MODULE
DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT:

FPA IS A BKMOD THAT EXERCISES THE FP11-C
AND THE FIS OPTION IN THE 11/40 OR LSI-11.

2. REQUIREMENTS:

HARDWARE: PDP11/45 WITH THE FPP INSTRUCTION SET, OR AN 11/40 WITH FIS.
STORAGE:: FPA REQUIRES:
1. DECIMAL WORDS: 360
2. OCTAL WORDS: 0550
3. OCTAL BYTES: 1320

3. PASS DEFINITION:

ONE PASS OF THE FPA MODULE CONSISTS OF EXECUTING EACH
INSTRUCTION 25,000 TIMES.

4. EXECUTION TIME:

FPA MODULE RUNNING ALONE ON A PDP11 PROCESSOR TAKES
APPROXIMATELY 30 SECONDS.

5. CONFIGURATION REQUIREMENTS:

SET SR1 EQUAL TO A 1 IF AN 11/40 IS USED OR EQUAL TO 0
IF AN 11/45 IS USED.

6. DEVICE/OPTION SETUP:

MAKE SURE FP11-C OR FIS IS INSTALLED.

7. MODULE OPERATION:

- A. SETUP CYCLE COUNTER.
- B. TEST SRI, IF EQUAL TO 1 GO TO D.
- C. ELSE TEST ALL FPP INSTRUCTIONS, GO TO E.
- D. TEST ALL FIS INSTRUCTIONS.
- E. IF NOT EOP, GO TO B.
- F. ELSE DO EOP, GO TO A.

8. OPERATING OPTIONS:

SRI=0 11/45, 11/70 FP11-C OPTION INSTRUCTIONS
SRI=1 11/40 OR LSI, FIS OPTION INSTRUCTIONS

9. NON-STANDARD PRINTOUTS:

NONE

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000000  -      EKMOD <FPAG > 25000,16
000000  -      MODULE 40020,FPAG 1,6666,25000,16
          ;      TITLE FPAG DEC/X11 SYSTEM EXERCISER MODULE
          ;      DDXCOR VERSION 6 23-MAY-78
          ;      *****-LIST BIN*****
000000  -      BEGIN:*****
000000  -      050106 043501 040 MODNAM: .ASCII /FPAG / ;MODULE NAME
000005  -      000 XFLAG: .BYTE OPEN USED TO KEEP TRACK OF WBUFF USAGE
000006  -      000000 ADDR: +0 ;1ST DEVICE ADDR
000010  -      000000 VECTOR: +0 ;1ST DEVICE VECTOR.
000012  -      000 BR1: .BYTE PRTY+0 ;1ST BR LEVEL.
000013  -      000 BR2: .BYTE PRTY+0 ;2ND BR LEVEL.
000014  -      000001 DVID1: +1 ;DEVICE INDICATOR 1.
000016  -      000000 SR1: OPEN ;SWITCH REGISTER 1
000020  -      000000 SR2: OPEN ;SWITCH REGISTER 2
000022  -      000000 SR3: OPEN ;SWITCH REGISTER 3
000024  -      000000 SR4: OPEN ;SWITCH REGISTER 4
          ;*****
000026  -      040020 STAT: 40020 ;STATUS WORD.
000030  -      000224 INIT: START ;MODULE START ADDR.
000032  -      000224 SPOINT: MODSP ;MODULE STACK POINTER.
000034  -      000000 PASSCNT: 0 ;PASS COUNTER.
000036  -      025000 ICONT: 25000 ;# OF ITERATIONS PER PASS=25000
000040  -      000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000042  -      000000 SDFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044  -      000000 HRDCT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046  -      000000 SDPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050  -      000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052  -      000000 SVS CNT: 0 ;# OF SVS ERRORS ACCUMULATED
000054  -      000000 RANUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056  -      000000 CONFIG: 0 ;RESERVED FOR MONITOR USE
000058  -      000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060  -      000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062  -      000000 SVR0: OPEN ;LOC TO SAVE R0.
000064  -      000000 SVR1: OPEN ;LOC TO SAVE R1.
000066  -      000000 SVR2: OPEN ;LOC TO SAVE R2.
000070  -      000000 SVR3: OPEN ;LOC TO SAVE R3.
000072  -      000000 SVR4: OPEN ;LOC TO SAVE R4.
000074  -      000000 SVR5: OPEN ;LOC TO SAVE R5.
000076  -      000000 SVR6: OPEN ;LOC TO SAVE R6.
000100  -      000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102  -      000000 SADR: OPEN ;ADDR OF GOOD DATA, OR
          ;CONTENTS OF CSR.
000104  -      000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
          ;STATUS REG CONTENTS.
000106  -      000000 ERRTYP: OPEN ;TYPE OF ERROR
          ;EXPECTED DATA.
000110  -      000000 AWAS: OPEN ;ACTUAL DATA.
000112  -      000224 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000114  -      000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
000116  -      000000 WDFP: OPEN ;WORDS FROM MEMORY PER ITERATION
000120  -      000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000122  -      000016 IDNUM: 16 ;MODULE IDENTIFICATION NUMBER=16
          ;*****
          ;.REPT SPSIZ ;MODULE STACK STARTS HERE.

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```

          ;.NLIST 0
          ;.WORD 0
          ;.LIST
          ;.ENDR
000224  -      MODSP:*****
          ;*****

```

```
160 000000 000000
161 000000 000000
162 000001 000001
163 000002 000002
164 000003 000003
165 000004 000004
166 000005 000005
167 000224 000000
168 000224 000000
169 000224 005767 177566
170 000230 001402
171 000233 000167 000552
172 000236 170127 047400
173 000236 170127 047400
174 000244 177027 077777
175 000250 177127 002525
176 000250 174402 000012
177 000260 174402
178 000260 171001
179 000264 172001
180 000266 173000
181 000270 173000
182 000272 001372
183 000274 175467 000776
184 000300 170200
185 000309 022767 077777 000766
186 000310 001403
187
188 000312 104405 000000 000000
189
190 000320 172467 000242
191 000324 012702 001276
192 000330 174027 000230
193 000336 174122
194 000336 174122 000742
195 000340 172767 000724
196 000344 170200
197 000348 022767
198 000352 170000
199 000354 001403
200
201 000356 104405 000000 000000
202
203 000364 172567 000200
204 000370 174142
205 000374 173042
206 000374 173042
207 000376 173001
208 000400 175467 000672
209 000404 170200 000000 000664
210 000412 170200
211 000414 001403
212
213 000416 104405 000000 000000
214
215 000424 170001
EX4:
LDD D1010,AC0
MOV #ANS1,R2
STD D1010,AC1
STD AC1,(R2)+
LDD ANS5,AC3
STFPS FFS
CMPD ANS1,AC3
BFCC
BEQ 1$
;*****
HRDERS,BEGIN,NULL
;*****
LDD D0101,AC1
STD AC1,(R2)
SUBD (R2),AC0
SUBD -(R2),AC0
SUBDI AC1,AC0
STCFI AC0,ANS1
CMP FFS
STFPS FFS
BEQ MORE
;*****
HRDERS,BEGIN,NULL
;*****
1$:
LDD D0101,AC1
ADD AC0,(R2),AC0
SUBD -(R2),AC0
SUBDI AC1,AC0
STCFI AC0,ANS1
CMP FFS
STFPS FFS
BEQ 2$
;*****
HRDERS,BEGIN,NULL
;*****
2$:
LDDI #21,AC0
MULF AC0,AC0
DIVF #2,AC0
MODF #1,AC0
STFPS FFS
STCFI AC1,ANS1
CMP #220,ANS1
BEQ 3$
;*****
HRDERS,BEGIN,NULL
;*****
3$:
JMP CYCLE
D1010: -1,0,0
D0101: 0,-1,0
D1001: -1,0,0
DSMALL: -1,1,0,-1
WEIRD: -1,1,0,-1
AD0101: D0101
AD1001: D1001
AD1000: D1000
DBIG: 77777,0,-1,0
DMZERO: 100000,0,0,0
AANS1: ANS1
DALTA: 40252,125252,125252,125252
DALTB: 40325,52525,52525,52525
DALTC: 40325,52525,52525,52525
D40: 40000,0,0,0
D37: 37400,0,0,0
```

```
216 000426 177027 000525
217 000432 177127 000252
218 000436 174104
219 000440 172104
220 000444 173003
221 000446 175067 000624
222 000450 170200
223 000454 170200 000001 000614
224 000462 001403
225
226 000464 104405 000000 000000
227
228 000472 177027 000021
229 000476 171000
230 000500 174427 040400
231 000504 174427 040200
232 000510 170200
233 000512 175567 000560
234 000516 022767 000220 000552
235 000524 001403
236
237 000526 104405 000000 000000
238
239 000534 171427 041040
240 000540 175567 000532
241 000544 022767 000005 000524
242 000552 001403
243
244 000554 104405 000000 000000
245
246 000562 000167 000436
247 000566 177127
248 000570 000000 177777 000000
249 000576 177777 000000 000000
250 000604 000000
251 000604 177777 000001 000000
252 000612 177777
253 000614 000570
254 000616 000576
255 000620 000736
256 000622 077777 000000 177777
257 000630 000000
258 000632 100000 000000 000000
259 000640 000000
260 000642 001276
261 000644 040252 125252 125252
262 000650 125252
263 000654 052525 052525
264 000660 052525
265 000664 040325 052525
266 000670 052525
267 000674 040000 000000 000000
268 000702 000000
269 000704 037400 000000 000000
270 000712 000000
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272 000714* 040600 000000 000000 D46: 40600,0,0,0
273 000722* 000000
274 000724* 020000 000000 000000 D20: 20000,0,0,0
275 000732* 000000
276 000734* 000000
277 000736* 177777 000000 000000 D1000: 0
278 000744* 000000 D1000: -1,0,0,0
279 000746* 001000 000000 000000 D0100X: 100,0,0
280 000754* 000000 177777 177777 D0111: 0,-1,-1,-1
281 000762* 177777
282 000764* 000000 054321 D5T01: 0,54321
283 000770* 000764* 121000 000000 AD5T01: 5T01
284 000772* 043661 121000 000000 P5T01: 43661,121000,0,0
285 001000* 000000
286 001002* 000000 SAVADR: 0
287 001004* 000000 SAVSTS: 0
288 001006* 170200 FLTERR: STFPS FPS
289
290
291 075000 FADD=75000 ;11/40 FIS FLOATING ADD
292 075010 PSUB=75010 ;11/40 FIS FLOATING SUBTRACT
293 075020 FMUL=75020 ;11/40 FIS FLOATING MULTIPLY
294 075030 FDIV=75030 ;11/40 FIS FLOATING DIVIDE
295 ; FIS MODULE TEST
296
297
298 001010* 012704 001274* START2: MOV #FISSTK,R4 ;SET STACK POINTER
299 001014* 012744 107070 MOV #107070,-(R4) ;LOAD DATA ONTO STACK
300 001018* 012744 134343 MOV #134343,-(R4)
301 001022* 012744 065432 MOV #065432,-(R4)
302 001026* 012744 032107 MOV #032107,-(R4)
303 001030* 012744 134343 MOV #134343,-(R4)
304 001034* 012744 045670 MOV #045670,-(R4)
305 001038* 012744 125252 MOV #125252,-(R4)
306 001042* 012744 135252 MOV #135252,-(R4)
307 001046* 012744 016161 MOV #016161,-(R4)
308 001050* 012744 040616 MOV #040616,-(R4)
309 001064* 000240 NOP
310 001066* 005067 000210 CLR ANS3 ;CLEAR A LOCATION FOR PSW
311 001072* 075014 PSUB+ R4
312 001074* 075034 FDIV+ R4
313 001076* 075024 FMUL+ R4
314 001100* 075004 FADD+ R4
315 001102* 103406 BCS 102 ;IF C SET, LEAVE ANS3=0
316 001104* 102405 BVS 102 ;IF V SET, LEAVE ANS3=0
317 001106* 001404 BEQ 102 ;IF Z SET, LEAVE ANS3=0
318 001110* 100003 BPL 102 ;IF N = 0, LEAVE ANS3=0
319 001112* 012767 000010 000162 MOV #10,ANS3 ;IF ABOVE NOT TRUE, THEN PS=10
320 ;AND N=1,Z=V=C=0, SO MAKE #NS3=10
321 001120* 10$: MOV (R4)+,ANS1 ;SAVE FIRST HALF OF ANSWER
322 001124* 012467 000152 MOV (R4)+,ANS2 ;SAVE SECOND HALF OF ANSWER
323 001128* 012467 000150 MOV R4,ANS4 ;SAVE FINAL STACK POINTER
324 001132* 010467 000150 CMPB #10,ANS3 ;CHECK FINAL PSW
325 001134* 127677 000010 000140 BEQ 1$
326 001142* 001403
327

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328 001144* 104405 000000* 000000 ;*****
329 ;RDERS,BEGIN,NULL ;CONDITION CODES INCORRECT
330 ;*****
331 001152* 022767 001274* 000124 1$: CMP #FISSTK,ANS4 ;CHECK STACKPOINTER POSITION
332 001160* 001403 BEQ 2$ ;*****
333 ;RDERS,BEGIN,NULL ;*****
334 001162* 104405 000000* 000000 ;*****
335 ;*****
336 001170* 022767 137201 000100 2$: CMP #137201,ANS1 ;CHECK FIRST HALF OF ANSWER
337 001176* 001403 BEQ 3$ ;*****
338 ;RDERS,BEGIN,NULL ;*****
339 ;*****
340 001200* 104405 000000* 000000 ;RDERS,BEGIN,NULL ;*****
341 ;*****
342 001206* 022767 115230 000064 3$: CMP #115230,ANS2 ;CHECK SECOND HALF OF ANSWER
343 001214* 001403 BEQ 4$ ;*****
344 ;RDERS,BEGIN,NULL ;*****
345 ;*****
346 001216* 104405 000000* 000000 ;*****
347 ;*****
348 001224* 4$: ;*****
349 ;+
350 ;CHECK FOR END OF PASS
351 ;-
352
353 001224* CYCLE: ENDITS,BEGIN ;SIGNAL END OF ITERATION.
354 001224* 104413 000000* ;MONITOR SHALL TEST END OF PASS
355 001230* 000167 176770 JMP RESTRT ;CONTINUE TESTING
356
357
358
359
360
361
362
363 001274* 001274* =.+40
364 001276* 000000 FISSTK: 0
365 001300* 000000 ANS1: 0 ;FIPST ANSWER (SEE CODE)
366 001302* 000000 ANS2: 0
367 001304* 000000 ANS3: 0
368 001306* 000000 ANS4: 0
369 001310* 000000 ANS5: 0
370 001312* 000000 ANS6: 0
371 001314* 000000 ANS7: 0
372 001316* 000000 ANS8: 0
373 CNT: 0
374
375
376 000001 .END

```


PRTY6 = 000300	160#		
PRTY7 = 000340	160#		
PS = 177776	160#		
PS# = 177776	160#		
PUSH = 005746	160#		
PUSH2 = 024646	160#		
RANDS = 104417	160#		
RANNUM = 000054R	129#		
RSTRRT = 000274R	148#	168#	356
RES1 = 000056R	131#		
RES2 = 000060R	132#		
RSTRT = 000112R	148#		
SAVADR = 011002R	286#		
SAVSTS = 001004R	287#		
SBADR = 000102R	141#		
SOPCNT = 000042R	124#		
SOPERS = 104406	160#		
SOPPAS = 000046R	126#		
SPOINT = 000032R	120#		
SPSIZ = 000040	1#		
SR1 = 000016R	113#	153	
SR2 = 000020R	114#	169	
SR3 = 000022R	115#		
SR4 = 000024R	116#		
START = 000024R	119#	167#	
START1 = 000256R	170#	172#	
START2 = 001010R	171#	298#	
STAT = 000026R	118#		
SVR0 = 000062R	133#		
SVR1 = 000064R	134#		
SVR2 = 000066R	135#		
SVR3 = 000070R	136#		
SVR4 = 000072R	137#		
SVR5 = 000074R	138#		
SVR6 = 000076R	139#		
SYSCHT = 000052R	128#		
TRPDFD = 000022	160#		
VECTDR = 000010R	109#		
WASADR = 000104R	143#		
WDFR = 000116R	150#		
WDIO = 000114R	149#		
WEIRD = 000064R	252#		
XFLAG = 000005R	107#		
.	001320R	362#	

- ABS. 000000 000
001320 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0
XFPAGO,XFPAGO/SOL/CRF:SYM=DDXCOM,XFPAGO
RUN-TIME: 1 1 .2 SECONDS
RUN-TIME RATIO: 19/3=6.3

CORE USED: 7K (13 PAGES)