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.SBTTL DOCUMENTATION  
.REM \_

IDENTIFICATION

PRODUCT CODE: AC-E881R-MC  
PRODUCT NAME: CXBBAR0 KIT-11D MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT  
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BBA IS AN IOMOD THAT EXERCISES THE KIT11D. DURING A SINGLE PASS, IT CHECKS THAT THE KIT WILL PERFORM A PROGRAMMED DATA TRANSFER (PDT) FOLLOWED BY A NON PROCESSOR REQUEST (NPR) TRANSFER. ANY ERRORS DETECTED DURING THE PASS ARE REPORTED ON THE CONSOLE TTY. THE PROGRAM WILL RUN INDEFINITELY UNTIL HALTED BY THE USER (PROVIDED THAT HARD FAIL SYSTEM ERRORS HAVE NOT BEEN ENCOUNTERED).

2. REQUIREMENTS  
\*\*\*\*\*

HARDWARE: KIT11D AND AA SPECIAL WRAP-AROUND CABLE.  
STORAGE:: BBA REQUIRES:  
1. DECIMAL WORDS: 476  
2. OCTAL WORDS: 0734  
3. OCTAL BYTES: 1670

3. PASS DEFINITION  
\*\*\*\*\*

ONE PASS OF THE KITDO CONSISTS OF 70000 ITERATIONS 1 PROGRAMMED DATA TRANSFER AND ONE 64 WORDS.

4. EXECUTION TIME  
\*\*\*\*\*

RUNNING ALONE ON A PDP11/05 TAKES APPROXIMATELY 1 MINUTE PER PASS.

5. CONFIGURATION REQUIREMENTS  
\*\*\*\*\*

DEFAULT PARAMETERS:

BR1: 5  
BR2: 0

REQUIRED PARAMETERS:  
NONE, EXCEPT FOR THE ADDRESS AND VECTOR MUST BE ASSIGNED AT CONFIGURATION TIME (OR RUN TIME VIA THE MOD COMMAND). THE PROGRAM WILL NOT RUN UNLESS YOU SUPPLY A ADDRESS AND FLOATING VECTOR.

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6. DEVICE/OPTION SETUP  
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- A. THE KIT11D UNDER TEST MUST CONTAIN THE SPECIAL  
\*\*\*\*\* WRAP-AROUND CABLE FOR THIS TEST. \*\*\*\*\*
- B. THE VECTOR ADDRESS MAY BE SPECIFIED AT RUN TIME BY USING  
THE FOLLOWING MODIFY COMMAND AT RUN TIME:

.MODIFY KITDO 10(CR)

THE SYSTEM WILL ISSUE A CR/LF AND PRINT OUT THE  
PRESENT CONTENTS OF THAT LOCATION (CALLED VECTOR).

AT THAT TIME, YOU MAY ENTER THE NEW VECTOR ADDRESS AND  
FOLLOW IT BY A CARRIAGE RETURN (CR).

IF A DEVICE ADDRESS OTHER THAN 160000 IS USED.  
THAT ADDRESS MAY BE UPDATED BY USING THE  
MODIFY COMMAND:

.MODIFY KTDO 6 <CR>

- C. THE MODULE IS SELECTED AT RUN TIME BY TYPING:  
.SEL KITDO <CR>
- D. THE MODULE IS RUN BY TYPING:  
.RUN <CR>  
AT RUN TIME.
- E. A LOAD MAP IS OBTAINED AT RUN TIME BY SAYING:  
.MAP <CR>
- F. PROGRAMMERS GUIDE TO DECX11 IS AVAILABLE FROM PROGRAM  
LIBRARY.

7. MODULE OPERATION  
\*\*\*\*\*

- A. INITIALIZE THE PROGRAM TO ALLOW FOR PREVIOUS DEVICE AND/  
OR VECTOR ADDRESS MODIFICATIONS.
- B. CLEAR THE CSR AND DBR AND TEST FOR DEVICE READY
- C. BEGIN (PDT) TEST 1. ENABLE AND CHECK THE INTERRUPT.
- D. TRANSMIT A WORD (#52525) AND CHECK THAT THE INTERRUPT  
SERVICE ROUTINE WAS ENTERED. (SWITCH ENTERI WILL BE  
SET WHEN THE SERVICE ROUTINE IS ENTERED).
- E. CHECK THE CSR FOR CORRECT STATUS THEN CHECK THAT THE  
CORRECT WORD WAS RETURNED. END TEST1.
- F. BEGIN (NPR) TEST2, CHECK THECSR FOR CORRECT STATUS.

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- SET UP THE WORST CASE DATA PATTERN AND LOAD IT INTO TRANSMIT BUFFER.
- G. SET UP THE NPR PARAMETERS AND START THE NPR TRANSFER. CHECK THAT THE INTERRUPT ROUTINE WAS ENTERED. CHECK THE CSR FOR CORRECT STATUS. CHECK THAT ALL 64 WORDS WERE TRANSMITTED AND RECEIVED. CHECK THAT TRANSMITTED BUFFER MATCHES RECEIVED BUFFER.
- H. IF TEST IS SUCCESSFUL, PRINT OUT "ENDPASS" AFTER 130000 ITERATIONS OF THE TEST, AND LOOP TO BEGINNING OF TEST.
8. OPERATION OPTIONS  
\*\*\*\*\*
- NONE
9. NON-STANDARD PRINTOUTS  
\*\*\*\*\*
- MOST PRINTOUTS HAVE STANDARD FORMATS AS DESCRIBED IN THE DEC/X11 DOCUMENT.
- THE ONLY EXCEPTION IS THE KIT11D PROGRAM ERROR PRINTOUTS. ON THESE PRINTOUTS:
- THE CSRA WILL INDICATE THE CONTENTS OF THE WORD COUNT REGISTER. THE ACSR WILL INDICATE THE CONTENTS OF THE CSR AND THE ASTAT WILL BE THE KIT11D PROGRAM ERROR NUMBER.
10. LOADING THE BINARY (IF CONFIGURED EXERCISER) PROGRAM  
THE PROGRAM IS LOADED VIA THE ABSOLUTE LOADER.  
START ADDRESS=200  
RESTART ADDRESS=1000

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041102 041101 040  
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000000  
000001  
000001  
000012 240  
000013 000  
000014 000001  
000015 000000  
000020 000000  
000022 000000  
000024 000000  
000026 140000  
000030 000254

```
***** KIT 11D *****  
***** ERROR MESSAGES *****  
*****  
THE FORMAT OF KIT11D ERROR PRINTOUTS IS:  
NAME PC ASSEMBLED PC PASS # SYSTEM ERROR COUNT  
WC REGISTER CSR CONTENTS PROGRAM ERROR #  
THE POSSIBLE PROGRAM ERRORS ARE:  
***** ERROR ***** EXPLANATION *****  
1 CSR NOT 610 OR 210 (PDT) DEVICE NOT READY  
2 CSR NOT 710 OR 310 (PDT) INT NOT ENABLED  
3 CSR NOT 126510 OR 126110 (PDT)  
4 DATA MISMATCH, DBR NOT =52525 (PDT)  
5 CSR NOT 710 OR 210 (NPR) CSR NOT READY  
6 CSR NOT 310 (NPR) INT OK BUT CSR NOT READY.  
7 WORD COUNT NOT ZERO (NPR)  
8 ALL WORDS NOT MITTED (NPR)  
9 DATA MISMATCH (NPR)  
10  
11  
12 TIMEOUT ERROR (NO INT OCCURRED) (NPR/PDT)  
13 SLAVE SYNCH ERROR (NPR/PDT)
```

```
*****  
*KIT/11D EXERCISER MODULE  
IOMOD <BBAB> 140000,52  
MODULE 140000, BBAB 140000, 70000,62  
-TITLE BBAR DEC/X11 SYSTEM EXERCISER MODULE  
DDXCDM VERSION 6 23-MAY-78  
LIST BIT  
*****  
BEGIN: *****  
MODNAM: -ASCII /BBAB / ;MODULE NAME.  
XFLAG: -BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE  
ADDR: +0 ;1ST DEVICE ADDR.  
VECTOR: +0 ;1ST DEVICE VECTOR.  
BRI: -BYTE PRV5+0 ;1ST BR LEVEL.  
BR2: -BYTE PRV+0 ;2ND BR LEVEL.  
DYD1: +1 ;DEVICE INDICATOR 1.  
SR1: OPEN ;SWITCH REGISTER 1  
SR2: OPEN ;SWITCH REGISTER 2  
SR3: OPEN ;SWITCH REGISTER 3  
SR4: OPEN ;SWITCH REGISTER 4  
*****  
STAT: 140000 ;STATUS WORD.  
INIT: START ;MODULE START ADDR.
```

251 000032 000224  
252 000034 000000  
253 000036 070000  
254 000040 000000  
255 000042 000000  
256 000044 000000  
257 000046 000000  
258 000050 000000  
259 000052 000000  
260 000054 000000  
261 000056 000000  
262 000058 000000  
263 000060 000000  
264 000062 000000  
265 000064 000000  
266 000066 000000  
267 000070 000000  
268 000072 000000  
269 000074 000000  
270 000076 000000  
271 000100 000000  
272 000102 000000  
273 000104 000000  
274 000104 000000  
275 000104 000000  
276 000106 000000  
277 000110 000000  
278 000112 000344  
279 000114 000000  
280 000116 000000  
281 000120 000000  
282 000122 000062  
283 000124 000040  
284  
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290 000224  
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```
SPPOINT: MODSP ;MODULE STACK POINTER.  
PASCNT: 0 ;PASS COUNTER  
ICOUNT: 0 ;# OF ITERATIONS PER PASS=70000  
SOFCNT: 0 ;LOC TO COUNT ITERATIONS  
HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS  
SOPPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS  
HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS  
SVSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS  
RANUM: 0 ;# OF SYS ERRORS ACCUMULATED  
CONFIC: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED  
RES1: 0 ;RESERVED FOR MONITOR USE  
RES2: 0 ;RESERVED FOR MONITOR USE  
SVR0: OPEN ;LOC TO SAVE P0.  
SVR1: OPEN ;LOC TO SAVE R1.  
SVR2: OPEN ;LOC TO SAVE R2.  
SVR3: OPEN ;LOC TO SAVE R3.  
SVR4: OPEN ;LOC TO SAVE R4.  
SVR5: OPEN ;LOC TO SAVE R5.  
SVR6: OPEN ;LOC TO SAVE R6.  
CSRA: OPEN ;ADDR OF CURRENT CSR  
SRADR: OPEN ;ADDR OF GOOD DATA, OR  
ACSR: OPEN ;CONTENTS OF CSR.  
WASADR: OPEN ;ADDR OF BAD DATA, OR  
ASTAT: OPEN ;STATUS REG CONTENTS.  
ERMTYP: 0 ;TYPE OF ERROR  
ASB: OPEN ;EXPECTED DATA.  
AWAS: OPEN ;ACTUAL DATA.  
RSTRT: RSTRT ;RESTART ADDR PSS AFTER END OF PASS  
MOTO: OPEN ;WORDS TO MEMORY PER ITERATION  
MOPR: OPEN ;WORDS FROM MEMORY PER ITERATION  
INTR: OPEN ;# OF INTERRUPTS PER ITERATION  
IDNUM: 62 ;MODULE IDENTIFICATION NUMBER=62  
-REPT SPSIZ ;MODULE STACK STARTS HERE.  
-LIST 0  
-WORD  
-LIST  
-FNDR  
MODSP: *****
```

```

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298 000224* 160006
299 000226* 160004
300 000230* 160002
301 000232* 160000
302 000234* 000000
303 000236* 177700
304
305
306 000240* 000000
307 000242* 000000
308
309
310
311 000244* 000200
312 000246* 000000
313 000250* 000000
314 000252* 000000
315
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318 000254* 012767 000002 177636
319 000262* 012767 000100 177624
320 000270* 012767 000100 177620
321 000278* 012767 177506
322 000302* 012721 001432
323 000306* 116721 177500
324
325
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329
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331 000312* 016702 177470
332 000316* 010267 177710
333 000318* 005122
334 000324* 010267 177700
335 000330* 005722
336 000332* 010267 177670
337 000336* 005122
338 000340* 010267 177660
339
340 000344* 005077 177654
341 000350* 005077 177652
342 000354* 005067 177670
343 000360* 005067 177666
344
345 000364* 012777 177777 177640
346 000372* 012777 001466 177630
347 000400* 022777 000210 177620
348

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```

***** INITIALIZATION *****
LOCATIONS LOCAL TO THIS MODULE.
DBR: 160006 ;DATA BUFFER REGISTER.
CSR: 160004 ;CONTROL STATUS REGISTER.
BAR: 160002 ;BUS ADDRESS REGISTER.
WC: 160000 ;WORD COUNT REGISTER.
NPRMAX: -WORD 1 ;NPR WORDS IN WORST CASE LOOP LIMIT.
NPRLAT: -WORD -100 ;(NPR), #OF WORDS IN WORST CASE PATTERN
;64=32X2 TRANSFERS FOR WORD COUNT.
NPRCTR: -WORD 0 ;NPR COUNTER FOR NPR PATTERN.
ERROR: -WORD 0 ;KIT11D ERROR NUMBER.
;THIS NUMBER IS PRINTED DURING KIT11
;PROGRAM GENERATED ERROR MESSAGES.
TLIMIT: -WORD 200 ;TIME-OUT LIMIT.
TTEMP: -WORD 0 ;WORKING LOCATION.
ENTERI: -WORD 0 ;INTERRUPT ENTERED FLAG.
SLVSYN: -WORD 0 ;SLAVE SYNCH ERROR FLAG.
;FLAG IS SET UPON ENTERING INTERRUPT SERVICE ROUTINE.
;(NORMALLY CLEARED).
;WILL EQUAL ONE ENDPASS.
START: MOV #2,INTR ;2 INTERRUPTS/ITERATION
MOV #100,WDTO ;100 WORDS TO MEM/ITERATION
MOV #100,WDFR ;100 WORDS FROM MEM/ITERATION
MOV #VECTOR,R1 ;SET UP INTERRUPT
MOV #KITSRV,(R1)+ ;INFORMATION.
MOV# BRL(R1)+
;
;SET UP THE REGISTER ADDRESSES FOR THE KIT-11 D.
;
MOV ADDR,R2 ;GET BASE ADDRESS.
TST R2,WC
MOV R2,BAR ;BUS ADDRESS.
TST (R2)+
MOV R2,CSR ;CONTROL STATUS
MOV R2,DBR ;DATA BUFFER REG.
RESTR: CLR #DBR ;START OFF BY CLEARING
CLR #CSR ;THE CONTROL STATUS REGISTER.
CLR ENTERI ;ZERO THE "INTERRUPT ENTERED" FLAG.
CLR SLVSYN ;ZERO THE "SLAVE SYNCH" ERROR FLAG.
MOV #1,BCW ;SET UP FOR PDT.
MOV #BUFFER,BAR ;SAME AS ABOVE.
CMP #210,ACSR ;CHECK TO SEE IF BIT7
;CAME UP (READY BIT).

```

```

349
350 000406* 001420
351 000418* 022777 000610 177610
352 000416* 001414
353 000420* 004787 000756
354
355 000424* 012767 000001 000630
356 000432* 012767 000003 177446
357
358 000440* 104405 000000* 000000
359
360
361 000446* 000702
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ERR: BEQ PDT1
CMP #610,ACSR ;OPTIONAL CASE.
BEQ PDT1 ;YES, CSR=610 (FIRST TIME THRU THIS IS TRUE)
JSR PC,SETUP ;LOAD CSR, ASSR,ASTAT
;PRIOR TO ERROR CALL.
MOV #1,ERRORN ;ERROR NO. 1
MOV #3,ERRTY ;CONTROLLER NOT READY
;*****
HRDRS,BEGIN,NULL ;CSR NOT READY ---- ERROR #1----
;*****
BR START ;GO TRY AGAIN.
.SBTTL TEST 1 (PDT)

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437 000650 042777 002000 177350
438 000656 027727 177344 000210
439 000664 001431 177334 000610
440 000668 027727 177334 000610
441 000674 001415
442 000676 012767 000005 000356
443 000704 004767 000472
444 000710 012767 000003 177170
445
446 000716 104405 000000 000000
447
448 000724 000167 177324
    
```

```

***** TEST 2 (NPR) *****
;AT THIS POINT:
;A PROGRAMMED DATA XFER WAS SUCCESSFUL. DATA
;MITTED WAS EQUAL TO DATA RCVD.
;INTERRUPTS ARE WORKING CORRECTLY.
;NOW DO A NPR TRANSFER.
PDT2:  BIT  #2000,BCSR ;CLEAR BIT 10. (INPUT BIT)
      CMP  BCSR,#210 ;READY BIT UP?
      BEQ  NPR1
      CMP  BCSR,#610 ;CHECK FOR 2ND CASE.
      BEQ  NPR1 ;YES, BIT READY.
      MOV  #5,ERRORN ;ERROR #5.
      JSR  PC,SETUP ;GET READY FOR ERROR MESSAGE.
      MOV  #1,ERRTYP ;CONTROLLER NOT READY
      *****
      HDRRS,BEGIN,NULL ;CSR NOT READY AFTER PDT ---- ERROR #3----
      *****
      JMP  START ;AND RETURN.
    
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481 000730 012701 001466
482
483 000734 005067 177300
484 000740 012700 000001
485
486
487 000744 005100
488 000746 010021
489 000750 005021
490
491 000752 005100
492 000754 010021
493
494 000756 005021
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;SET UP THE WORST CASE DATA PATTERN AND
;MIT IT UNDER NPR CONTROL.
;
;SET UP THE WORST CASE DATA PATTERN BELOW:
;(THIS PATTERN WILL BE LOADED INTO ODD LOCATIONS)
;
;WORD:      PATTERN:
;3          1
;5          177775
;7          177763
;11         177767
;13         177767
;15         177767
;17         177757
;21         177757
;23         177757
;...
;75         077777
;77         100000
;
NPR1:  MOV  #BUFFER,R1 ;BUFFER ADDRESS FOR
      CLR  NPRCTR ;MITTED/RCVD DATA.
      MOV  #1,R0 ;START WITH FRESH COUNTER
      ;START FORMING DATA ITS
      ;COMPLEMENT.
NPR2:  COM  R0 ;INVERT IT (NEG #).
      MOV  R0,(R1)+ ;AND PUT IT INTO ADDRESS AT R1
      CLR  (R1)+ ;OPEN A SPOT IN BUFFER FOR
      ;RETURNED DATA (FREE LOCATION).
      COM  R0 ;INVERT IT AGAIN (POS #) AND
      MOV  R0,(R1)+ ;OPEN A FREE SPOT FOR
      CLR  (R1)+ ;RETURNED DATA (TO BE USED LATER).
      ;(FREE LOCATION)
;
;WE HAVE FORMED A NEGATIVE NUMBER AND ITS
;POSITIVE COMPLEMENT IN THE BUFFER. WE HAVE ALSO
;STAGGERED THE BUFFER TO ALLOW EACH NUMBER FORMED
;TO BE FOLLOWED BY A FREE LOCATION DUE TO THE
;NATURE OF THE "WRAP-AROUND" CABLE USED IN THIS
;TEST.
;THUS OUR BUFFER WILL CONTAIN DATA TRANSMITTED INTERLAVED
    
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```
505 ;WITH THE DATA THAT WAS RECEIVED.
506 ;
507 ASL RO ;START SHIFTING RO TO FORM
508 ;THE NEXT SET OF DATA TO BE
509 ;LOADED.
510 000760 006300
511 BNE NPR2 ;UP COUNTER TO
512 000764 005267 177250 INC NPRCTR ;LOOP X TIMES TO
513 ;OBTAIN 32X
514 ;WORDS TO XFER.
515
516
517 000770 026767 177240 177242 CMP NPRMAX,NPRCTR ;X TIMES THRU LOOP?
518 ;(XFER-1)
519 000776 001360 BNE NPR1A ;NO, SO DO IT AGAIN.
520 ;
521 ;YES, THE XMIT BUFFER HAS BEEN SET UP.
522 ;
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```
523 ;NOW SETUP THE TRANSFER (NPR) PARAMETERS.
524 ;
525 ;
526
527 001000 016777 177232 177224 MOV NPRLMT,QWC ;2X WORDS
528 001006 012777 001466 177214 MOV #BUFFER,QBAR ;SET UP THE ADDRESS
529 ;OF THE BUFFER.
530
531 ;START THE ACTUAL TRANSFER HERE.
532 BTS #103,QCSR ;SET IN AND OUT.
533 ;ENABLE THE INTERRUPT.
534
535 001022 004767 000236 JSR PC,TIMOUT ;CHECK THE INTERRUPT SERVICE
536 ;ROUTINE VIA TIME-
537 ;OUT LOOP.
538
539 ;
540 ;AT THIS POINT, WE HAVE VERIFIED THAT AN INTERRUPT HAS
541 ;OCCURRED. CHECK THE CSR FOR ERRORS.
542 ;
543
544 001026 027727 177174 000210 IS: CMP QCSR,#210 ;CSR=310?
545 001034 001415 BREQ NPR4A ;YES, GOTD WORD COUNT OVERFLOW CHECK
546 001036 012767 000006 000216 MOV #6,ERRORN ;ERROR #6
547 001044 004767 000332 JSR PC,SETUP ;NO, GET READY TO RPORT ERROR.
548
549 001050 012767 000025 177030 MOV #25,ERRTYP ;BIT STUCK IN REG.
550 ;*****
551 001056 104405 000000 000000 HRDRFRS,BEGIN,NULL ;CSR NOT 310 ---- ERROR #6----
552 ;*****
553 001064 000167 177164 JMP START ;RESTART
554
555 ;NOW CHECK THE BAR TO MAKE SURE THAT THE
556 ;INTERUPT WAS NOT DUE TO A PREMATURE WC OVERFLOW.
557
558 NPR4A: TST QWC
559 001074 001413 BREQ NPR4 ;ERROR 7.
560 001076 012767 MOV #7,ERRORN ;ILLEGAL INTERRUPT
561 001104 012767 000011 176774 MOV #11,ERRTYP ;*****
562 ;*****
563 001112 104405 000000 000000 HRDRFRS,BEGIN,NULL ;WORD COUNT NOT ZERO ----ERROR #7----
564 ;*****
565 001120 000167 177130 JMP START ;RESTART.
566
567 ;NOW CHECK TO MAKE SURE THAT 100 WORDS WERE TRANSFERRED.
568 ;THE BAR SHOULD BE EQUAL TO BUFFER START ADDRESS PLUS 200.
569
570 NPR4: MOV QBAR,TEMP ;SAVE BAR.
571 001132 162767 SUB #BUFFER,TEMP ;GET THE OFFSET.
572 001140 026727 177102 000200 CMP TEMP,#200 ;ALL WORDS XFERRED?
573 ;YES.
574 001146 001415 BFG NPR5 ;ERROR #10
575 001150 012767 000010 000104 MOV #10,ERRORN ;NO
576 001156 004767 000220 JSR PC,SETUP ;NO
577 001162 012767 000011 176716 MOV #11,ERRTYP ;ILLEGAL INTERRUPT
578 001170 104405 000000 000000 HRDRFRS,BEGIN,NULL ;ALL WORDS NOT TRANSFERRED ----ERROR #10----
```



```
682 ;*****  
683 ;***** SETUP ROUTINE *****  
684 ;  
685 ;ROUTINE TO LOAD THE CSRA,ACSR,ASTAT  
686 ;FOR DEC/X11 ERROR ROUTINES.  
687 ;PRINTOUT INCLUDES: WC,CONTENTS OF CSR, ERROR #  
688 ;  
689 ;  
690 ;  
691 001402* 017767 176624 176470 SETUP: MOV QWC,CSRA ;SAVE THE CSR.  
692 001410* 017767 176612 176464 MOV QCSR,ACSR ;SAVE CONTENTS OF CSR.  
693 001416* 016767 177640 176460 MOV ERRORN,ASTAT ;GET ERRORN.  
694 001424* 005077 176576 CLR QCSR ;CLEAR THE (INTERRUPT ENARLE)CSR  
695 001430* 000207 ;AND RETURN.  
696 ;  
697 ;.SBTTL INTERRUPT SERVICE ROUTINE
```

```
698 ;*****  
699 ;***** INTERRUPT SERVICE ROUTINE *****  
700 ;  
701 ;*****  
702 ;  
703 ;SET THE INTERRUPT ENTERED FLAG (ENTERI) AND  
704 ;RETURN.  
705 ;  
706 001432* 042777 000100 176566 KITSRV: BIC #100,QCSR ;DISABLE THE INTERRUPT.  
707 001440* 012767 000001 176602 MOV #1,ENTERI ;SET THE "INTERRUPT ENTERED" FLAG.  
708 001446* 032777 040000 176552 BIT #40000,QCSR ;DO US SLAVE SYNCH FAILURE??.  
709 001454* 001403 ;BRANCH IF NOT  
710 001456* 012767 000001 176566 BFG #1,SLVSYN ;ELSE SET THE SLAVE ERROR FLAG.  
711 ;  
712 001464* 000002 ;  
713 ;  
714 ;.KIT INTERRUPT VECTOR ADDRESS  
715 ;  
716 ;  
717 ;.SBTTL DATA BUFFER  
718 001466* 000100 BUFFER: .BLKW 100  
719 001666* 000000 ;  
720 000001 ;.WORD 0  
721 ;.END
```



SOPPAS	000046R	257#																	
SPOINT	000032R	251#																	
SPSIZ =	000040	1#	284																
SR1	000016R	244#																	
SR2	000020R	245#																	
SR3	000022R	246#																	
SR4	000024R	247#																	
START	000254R	250#	318#	361	393	413	426	448	553	565	580	601	667	678					
STAT	000026R	249#																	
SVR0	000062R	264#																	
SVR1	000064R	265#																	
SVR2	000066R	266#																	
SVR3	000070R	267#																	
SVR4	000072R	268#																	
SVR5	000074R	269#																	
SVR6	000076R	270#																	
SVSCNT	000052R	259#																	
TIMOK	001352R	655#	669#																
TIMOUT	001264R	399#	535	642#															
TIMI	001292R	648#	658																
TLIMIT	000244R	310#	642																
TRPDPN=	000022	291#																	
TEMP	000246R	311#	570*	572	642*	657*													
VECTOR	000010R	240#	321																
WASADR	000104R	274#																	
WC	000232R	300#	332*	345*	527*	558	691												
WDFR	000116R	281#	320*																
WDT0	000118R	280#	319*																
XFLAG =	000005R	238#																	
.	001670R	718#																	

. ARS. 000000 000  
 001670 001

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0  
 XBBABO,XBBABO/SOL/CRF:SYM=DDXCOM,XBBABO  
 RUN-TIME: 1 2 .3 SECONDS  
 RUN-TIME RATIO: 19/4=4.4  
 CORE USED: 7K (13 PAGES)