

## Program Logic

### DOS IPL and Job Control

#### Program Number 360N-CL-453

This reference publication describes the internal logic of the IBM Operating System, Initial Program Load (IPL) and Job Control Programs. It is intended for use by persons involved in program maintenance and by system programmers who are altering the program design. Program logic information is not needed for normal operation of the IPL and Job Control Programs. It is designed to be used as a supplement to the program listing.

Effective use of this manual requires an understanding of IBM System/360 or System/370 operation and of the IBM Disk Operating System control and service programs, macro instructions, and operating procedures. Reference publications for this information are listed in the Preface of this manual.

For titles and abstracts of other associated publications, see the IBM System/360 and System/370 Bibliography, GA22-6822.

| Fifth Edition (June 1971)

This publication was formerly titled IBM System/360 Disk Operating System IPL and Job Control Programs. Although titles of some DOS publications (including this one) have been simplified, the change does not affect the contents of the publications.

This edition applies to Release 25 of the IBM Disk Operating System and to all subsequent releases until otherwise indicated in new editions or Technical Newsletters. Changes are continually made to the specifications herein; before using this publication in connection with the operation of IBM systems, consult the latest System/360 and System/370 SRL Newsletter, GN20-0360, for the editions that are applicable and current.

| This edition is a major revision of, and obsoletes, Y24-5086-3.

| Summary of Amendments

This edition contains maintenance changes, and it reflects Job Accounting Interface, OLTEP, Data Set Security, PCIL (Private Core Image Library), MCAR/CCH support, and RDE (Reliability Data Extractor). The IBM 3211 Printer and the IBM 1255/1259 Magnetic Character Readers are also supported in this edition.

The flowchart symbols used in this manual conform with American National Standards Institute, Inc., flowcharting standards. See Appendix C for an explanation of the new symbols.

Changes or additions to the text and illustrations are indicated by a vertical line to the left of the change.

Requests for copies of IBM publications should be made to your IBM representative or to the IBM branch office serving your locality.

A form for readers' comments is provided at the back of this publication. If the form has been removed, comments may be addressed to IBM Laboratory, Publications Dept., P.O. Box 24, Uithoorn, Netherlands. Comments become the property of IBM.

This Program Logic Manual (PLM) is a detailed guide to the IBM Disk Operating System IPL and Job Control programs. It supplements the program listings by providing descriptive text and flowcharts.

Note: Although titles of some DOS publications have been simplified, the change does not affect the contents of the publications.

For overall system control logic description, this PLM is used with six other PLMs:

- Introduction to DOS Logic, GY24-5017.
- DOS Linkage Editor, GY24-5080.
- DOS Supervisor and Related Transients, GY24-5151.
- DOS Logical Transients, GY24-5152.
- DOS System Service Programs, GY24-5153.
- DOS Librarian, GY24-5079.

Prerequisite publications that will aid in the use of this manual are:

- IBM System/360 Principles of Operation, GA22-6821.
- DOS System Control and Service, GC24-5036.
- IBM System/360 Disk and Tape Operating Systems, Assembler Specifications, GC24-3414.

Publications related in subject matter to the seven system control PLMs are:

- DOS Supervisor and I/O Macros, GC24-5037.
- DOS System Generation, GC24-5033.
- DOS Operating Guide, GC24-5022.
- DOS Messages, GC24-5074.
- DOS Data Management Concepts, GC24-3427.

Titles and abstracts of other related publications are listed in the IBM System/360 and System/370 Bibliography, GA22-6822.

This manual consists of four major sections. The first section is an introduction to the IPL and Job Control programs. The next two sections are a discussion of contents of the IPL and Job Control phases. The last section of the manual, the appendixes, contains label lists, error messages, charts, and tables for use as references in analyzing program details.

The detailed flowcharts are identified by letters AA through ZZ. Numerals such as 00 for the program level flowcharts identify the more general flowcharts.



CONTENTS

INTRODUCTION . . . . .	11	Initial Program Load (\$\$A\$IPL1, \$\$A\$IPL2, \$IPLRT2, \$IPLRT3, and \$IPLRT4) Charts AA-AZE . . . . .	197
Initial Program Load (IPL) . . . . .	11	Job Control (\$JOBCTLA, \$JOBCTL <sub>D</sub> , \$JOBCTL <sub>F</sub> , \$JOBCTL <sub>G</sub> , \$JOBCTL <sub>J</sub> , \$JOBCTL <sub>K</sub> , \$JOBCTL <sub>M</sub> , \$JOBCTL <sub>N</sub> , and \$\$BLSTIO) Charts BA-LD . . . . .	199
Job Control Program (\$JOBCTLA-\$JOBCTLN) . . . . .	11		
IPL PROGRAM . . . . .	12	APPENDIX B: ERROR MESSAGE CROSS REFERENCE . . . . .	215
Initial Program Load Program (IPL), Chart 01 . . . . .	12	APPENDIX C: EXPLANATION OF FLOWCHART SYMBOLS . . . . .	216
\$IPLRT2, Chart 02 . . . . .	14	APPENDIX D: SAMPLE LISTIO PRINTOUTS .	217
\$IPLRT3, Chart 03 . . . . .	14	APPENDIX E: COMREG AND CCB . . . . .	218
\$IPLRT4, Chart 03 . . . . .	15	APPENDIX F: I/O TABLES . . . . .	227
JOB CONTROL PROGRAM . . . . .	17	APPENDIX G: LABEL INFORMATION CYLINDER RECORD FORMAT . . . . .	245
I/O Flow . . . . .	17	APPENDIX H: MICROFICHE CROSS-REFERENCE INDEX . . . . .	247
Program Flow . . . . .	17	GLOSSARY . . . . .	248
\$JOBCTLA (Chart 04) . . . . .	17	INDEX . . . . .	251
\$JOBCTL <sub>D</sub> (Charts 05 and 06) . . . . .	18		
\$JOBCTL <sub>F</sub> (Chart 07) . . . . .	18		
\$JOBCTL <sub>G</sub> (Charts 08 and 09) . . . . .	18		
\$JOBCTL <sub>J</sub> (Charts 10, 11, and 12) . . . . .	18		
\$JOBCTL <sub>K</sub> (Charts 13 and 14) . . . . .	18		
\$JOBCTL <sub>M</sub> (Chart 15) . . . . .	18		
\$JOBCTL <sub>N</sub> (Chart 16) . . . . .	18		
\$\$BLSTIO . . . . .	18		
CHARTS . . . . .	21		
APPENDIX A: LABEL LIST . . . . .	197		

## CHARTS

Chart 00. Disk Operating System		Chart AR. \$IPLRT3 - ADD a Device	
Program Flow . . . . .	10	(Part 1 of 2) . . . . .	52
Chart 01. Initial Program Load (\$\$A\$IPL1 and \$\$A\$IPL2) . . . . .	21	Chart AS. \$IPLRT3 - ADD a Device	
Chart 02. Initial Program Load (\$IPLRT2) . . . . .	22	(Part 2 of 2) . . . . .	53
Chart 03. Initial Program Load (\$IPLRT3 and \$IPLRT4) . . . . .	23	Chart AT. \$IPLRT3 - Delete a PUB	54
Chart 04. Job Control (\$JOBCTLA) Root Phase . . . . .	24	Chart AU. \$IPLRT3 - Build PUB Table	
Chart 05. Job Control (\$JOBCTL0)		Subroutine . . . . .	55
Statement Processor (Part 1 of 2) . . . . .	25	Chart AV. \$IPLRT3 - Device Type	
Chart 06. Job Control (\$JOBCTL0)		Conversion Subroutine . . . . .	56
Statement Processor (Part 2 of 2) . . . . .	26	Chart AW. \$IPLRT3 - Conversion and Update FOCL Subroutines . . . . .	57
Chart 07. Job Control (\$JOBCTLF)		Chart AX. \$IPLRT4 - SET Statement	
Statement Processor . . . . .	27	Processor and Assign SYSLOG . . . . .	58
Chart 08. Job Control (\$JOBCTLG)		Chart AY. \$IPLRT4 - Assign SYSRES and Move I/O Tables . . . . .	59
Statement Processor (Part 1 of 2) . . . . .	28	Chart AZ. \$IPLRT4 - I/O and Check	
Chart 09. Job Control (\$JOBCTLG)		Device Type Subroutines . . . . .	60
Statement Processor (Part 2 of 2) . . . . .	29	Chart AZA. \$IPLRT4 - Find PUB and I/O Subroutines . . . . .	61
Chart 10. Job Control (\$JOBCTLJ)		Chart AZB. \$IPLRT4 - Date and Time	
Statement Processor (Part 1 of 3) . . . . .	30	Subroutines . . . . .	62
Chart 11. Job Control (\$JOBCTLJ)		Chart AZC. \$IPLRT4 - Copy Subroutine	63
Statement Processor (Part 2 of 3) . . . . .	31	Chart AZD. \$IPLRT4 - Set Job Control	
Chart 12. Job Control (\$JOBCTLJ)		Flags Subroutine . . . . .	64
Statement Processor (Part 3 of 3) . . . . .	32	Chart AZE. \$IPLRT4 - Reorder MPX	
Chart 13. Job Control (\$JOBCTLK)		Channel LUBs and PUBs . . . . .	65
Statement Processor (Part 1 of 2) . . . . .	33	Chart BA. \$JOBCTLA - Initialization	
Chart 14. Job Control (\$JOBCTLK)		Chart BB. \$JOBCTLA - Initialization and Control Statement Read . . . . .	66
Statement Processor (Part 2 of 2) . . . . .	34	Chart BC. \$JOBCTLA - Phase Vector	
Chart 15. Job Control (\$JOBCTLM)		Table Lookup . . . . .	68
Statement Processor . . . . .	35	Chart BD. \$JOBCTLA - DSKINT Subroutine	69
Chart 16. Job Control (\$JOBCTLN)		Chart BE. \$JOBCTLA - Message	
Statement Processor . . . . .	36	Subroutines . . . . .	70
Chart AA. \$\$A\$IPL1 - IPL Bootstrap . . . . .	37	Chart BF. \$JOBCTLA - Operand Scan	
Chart AB. \$\$A\$IPL2 - Clear Storage and Load Supervisor (Part 1 of 2) . . . . .	38	Subroutines . . . . .	71
Chart AC. \$\$A\$IPL2 - Load Supervisor (Part 2 of 2) . . . . .	39	Chart BG. \$JOBCTLA - Miscellaneous	
Chart AD. \$\$A\$IPL2 - Build Two-Device System . . . . .	40	Subroutines . . . . .	72
Chart AE. \$\$A\$IPL2 - Move I/O Tables . . . . .	41	Chart BH. \$JOBCTLA - EXCP Subroutines (Part 1 of 2) . . . . .	73
Chart AF. \$\$A\$IPL2 - Build PUB Table . . . . .	42	Chart BJ. \$JOBCTLA - EXCP Subroutines (Part 2 of 2) . . . . .	74
Chart AG. \$\$A\$IPL2 - Common Move		Chart BK. \$JOBCTLA - Miscellaneous	
Subroutine . . . . .	43	Subroutines . . . . .	75
Chart AH. \$\$A\$IPL2 - Update		Chart BL. \$JOBCTLA - Error	
Subroutines . . . . .	44	Subroutines (Part 1 of 2) . . . . .	76
Chart AJ. \$IPLRT2 - Initialization Routine . . . . .	45	Chart BM. \$JOBCTLA - Error	
Chart AK. \$IPLRT2 - Monitor, Read Control Card, and Operation Scan		Subroutines (Part 2 of 2) . . . . .	77
Routines . . . . .	46	Chart BN. \$JOBCTLA - Relocation	
Chart AL. \$IPLRT2 - Monitor Core Usage for ADD and DEL Cards and Allocation Subroutine . . . . .	47	Subroutines . . . . .	78
Chart AM. \$IPLRT2 - Monitor Core Usage for SET Card and Allocation		Chart CA. \$JOBCTL0 - ASSGN Statement	
Subroutine . . . . .	48	Processor (Part 1 of 10) . . . . .	79
Chart AN. \$IPLRT2 - Move Routine . . . . .	49	Chart CB. \$JOBCTL0 - ASSGN Statement	
Chart AP. \$IPLRT2 - Update LUB, Get Operand, and Conversion Subroutines . . . . .	50	Processor (Part 2 of 10) . . . . .	80
Chart AQ. \$IPLRT2 - I/O Subroutines . . . . .	51	Chart CC. \$JOBCTL0 - ASSGN Statement	

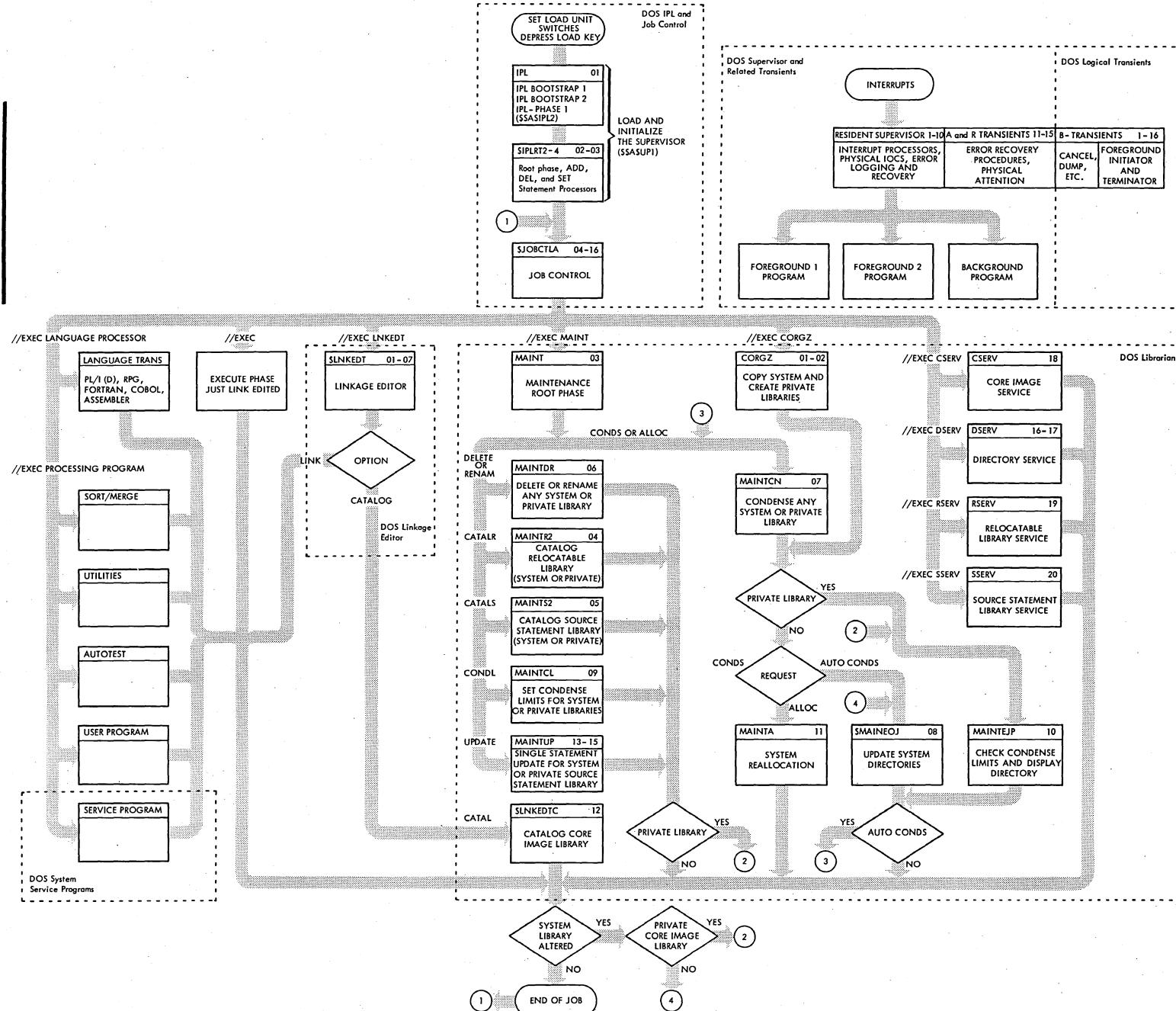
Chart CG. \$JOBCTL0 - ASSGN Statement	
Processor (Part 7 of 10) . . . . .	85
Chart CH. \$JOBCTL0 - ASSGN Statement	
Processor (Part 8 of 10) . . . . .	86
Chart CJ. \$JOBCTL0 - ASSGN Statement	
Processor (Part 9 of 10) . . . . .	87
Chart CK. \$JOBCTL0 - ASSGN Statement	
Processor (Part 10 of 10) . . . . .	88
Chart CL. \$JOBCTL0 - CLOSE Statement	
Processor . . . . .	89
Chart CM. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 1 of 3) . . . . .	90
Chart CN. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 2 of 3) . . . . .	91
Chart CP. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 3 of 3) . . . . .	92
Chart CQ. \$JOBCTL0 - Close Subroutine	. 93
Chart CR. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 1 of 5) . . . . .	94
Chart CS. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 2 of 5) . . . . .	95
Chart CT. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 3 of 5) . . . . .	96
Chart CU. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 4 of 5) . . . . .	97
Chart CV. \$JOBCTL0 - Miscellaneous	
Subroutines (Part 5 of 5) . . . . .	98
Chart CW. \$JOBCTL0 - SYSXXX Operand	
Processor . . . . .	99
Chart CX. \$JOBCTL0 - EXCP Subroutines	. 100
Chart CY. \$JOBCTL0 - Error Subroutines	. 101
Chart DA. \$JOBCTLF - LISTIO Statement	
Processor (Part 1 of 5) . . . . .	102
Chart DB. \$JOBCTLF - LISTIO Statement	
Processor (Part 2 of 5) . . . . .	103
Chart DC. \$JOBCTLF - LISTIO Statement	
Processor (Part 3 of 5) . . . . .	104
Chart DD. \$JOBCTLF - LISTIO Statement	
Processor (Part 4 of 5) . . . . .	105
Chart DE. \$JOBCTLF - LISTIO Statement	
Processor (Part 5 of 5) . . . . .	106
Chart DF. \$JOBCTLF - RESET Statement	
Processor (Part 1 of 2) . . . . .	107
Chart DG. \$JOBCTLF - RESET Statement	
Processor (Part 2 of 2) . . . . .	108
Chart DH. \$JOBCTLF - DVCDN Statement	
Processor (Part 1 of 2) . . . . .	109
Chart DJ. \$JOBCTLF - DVCDN Statement	
Processor (Part 2 of 2) . . . . .	110
Chart DK. \$JOBCTLF - DVCUP and MAP	
Statement Processors . . . . .	111
Chart DL. \$JOBCTLF - UNBATCH Statement	
Processor . . . . .	112
Chart DM. \$JOBCTLF - Error Subroutines	
and UNA Statement Processor	
(Part 1 of 2) . . . . .	113
Chart DN. \$JOBCTLF - UNA Statement	
Processor (Part 2 of 2) . . . . .	114
Chart DP. \$JOBCTLF - Miscellaneous	
Subroutines (Part 1 of 4) . . . . .	115
Chart DQ. \$JOBCTLF - Miscellaneous	
Subroutines (Part 2 of 4) . . . . .	116
Chart DR. \$JOBCTLF - Miscellaneous	
Subroutines (Part 3 of 4) . . . . .	117
Chart DS. \$JOBCTLF - Miscellaneous	
Subroutines (Part 4 of 4) . . . . .	118
Chart EA. \$JOBCTLG - JOB Statement	
Processor (Part 1 of 2) . . . . .	119
Chart EB. \$JOBCTLG - JOB Statement	
Processor (Part 2 of 2) . . . . .	120
Chart EC. \$JOBCTLG - /& Statement	
Processor (Part 1 of 3) . . . . .	121
Chart ED. \$JOBCTLG - /& Statement	
Processor (Part 2 of 3) . . . . .	122
Chart EE. \$JOBCTLG - /& Statement	
Processor (Part 3 of 3) and CANCEL	
Statement Processor . . . . .	123
Chart EF. \$JOBCTLG - EXEC Statement	
Processor (Part 1 of 5) . . . . .	124
Chart EG. \$JOBCTLG - EXEC Statement	
Processor (Part 2 of 5) . . . . .	125
Chart EH. \$JOBCTLG - EXEC Statement	
Processor (Part 3 of 5) . . . . .	126
Chart EJ. \$JOBCTLG - EXEC Statement	
Processor (Part 4 of 5) . . . . .	127
Chart EK. \$JOBCTLG - EXEC Statement	
Processor (Part 5 of 5) . . . . .	128
Chart EL. \$JOBCTLG - OPTION Statement	
Processor (Part 1 of 4) . . . . .	129
Chart EM. \$JOBCTLG - OPTION Statement	
Processor (Part 2 of 4) . . . . .	130
Chart EN. \$JOBCTLG - OPTION Statement	
Processor (Part 3 of 4) . . . . .	131
Chart EP. \$JOBCTLG - OPTION Statement	
Processor (Part 4 of 4) . . . . .	132
Chart FA. \$JOBCTLG - Time Stamping	
Subroutines . . . . .	133
Chart FB. \$JOBCTLG - Miscellaneous	
Subroutines (Part 1 of 3) . . . . .	134
Chart FC. \$JOBCTLG - Miscellaneous	
Subroutines (Part 2 of 3) . . . . .	135
Chart FD. \$JOBCTLG - Miscellaneous	
Subroutines (Part 3 of 3) . . . . .	136
Chart FE. \$JOBCTLG - Label Processing	
Subroutines (Part 1 of 2) . . . . .	137
Chart FF. \$JOBCTLG - Label Processing	
Subroutines (Part 2 of 2) . . . . .	138
Chart FG. \$JOBCTLG - Error Subroutines	139
Chart GA. \$JOBCTLJ - RELSE and HOLD	
Statement Processors . . . . .	140
Chart GB. \$JOBCTLJ - UCS Statement	
Processor (Part 1 of 2) . . . . .	141
Chart GC. \$JOBCTLJ - UCS Statement	
Processor (Part 2 of 2) . . . . .	142
Chart GD. \$JOBCTLJ - ACTION and	
INCLUDE Statement Processors . . . . .	143
Chart GE. \$JOBCTLJ - MTC Statement	
Processor (Part 1 of 2) . . . . .	144
Chart GF. \$JOBCTLJ - MTC Statement	
Processor (Part 2 of 2) . . . . .	145
Chart GG. \$JOBCTLJ - SET Statement	
Processor (Part 1 of 3) . . . . .	146
Chart GH. \$JOBCTLJ - SET Statement	
Processor (Part 2 of 3) . . . . .	147
Chart GJ. \$JOBCTLJ - SET Statement	
Processor (Part 3 of 3) . . . . .	148
Chart GK. \$JOBCTLJ - UPSI Statement	
Processor . . . . .	149
Chart GL. \$JOBCTLJ - PAUSE, LOG, and	
NOLOG Statement Processors . . . . .	150
Chart GM. \$JOBCTLJ - STOP Statement	
Processor . . . . .	151
Chart GN. \$JOBCTLJ - CATALR Card	
Processor . . . . .	152
Chart GP. \$JOBCTLJ - ALLOC Statement	
Processor (Part 1 of 3) . . . . .	153

Chart GQ. \$JOBCTLJ - ALLOC Statement Processor (Part 2 of 3) . . . . .	154	Chart HS. \$JOBCTLK - Error Subroutines 175 Chart JA. \$JOBCTLM - Recorder File Initialization . . . . . 176
Chart GR. \$JOBCTLJ - ALLOC Statement Processor (Part 3 of 3) . . . . .	155	Chart JB. \$JOBCTLM - Create Recorder File (Part 1 of 2) . . . . . 177
Chart GS. \$JOBCTLJ - Miscellaneous Subroutines (Part 1 of 2) . . . . .	156	Chart JC. \$JOBCTLM - Create Recorder File (Part 2 of 2) . . . . . 178
Chart GT. \$JOBCTLJ - Miscellaneous Subroutines (Part 2 of 2) . . . . .	157	Chart JD. \$JOBCTLM - Check Recorder File (Part 1 of 4) . . . . . 179
Chart GU. \$JOBCTLJ - Error Subroutines 158 Chart HA. \$JOBCTLK - LBLTYP, VOL, & TPLAB Statement Processors . . . . .	159	Chart JE. \$JOBCTLM - Check Recorder File (Part 2 of 4) . . . . . 180
Chart HB. \$JOBCTLK - TLBL Statement Processor . . . . .	160	Chart JF. \$JOBCTLM - Check Recorder File (Part 3 of 4) . . . . . 181
Chart HC. \$JOBCTLK - Label Processing Subroutines (Part 1 of 2) . . . . .	161	Chart JG. \$JOBCTLM - Check Recorder File (Part 4 of 4) . . . . . 182
Chart HD. \$JOBCTLK - Label Processing Subroutines (Part 2 of 2) . . . . .	162	Chart JH. \$JOBCTLM - ROD Statement Processor . . . . . 183
Chart HE. \$JOBCTLK - DLBL Statement Processor . . . . .	163	Chart JJ. \$JOBCTLM - Miscellaneous Subroutines (Part 1 of 2) . . . . . 184
Chart HF. \$JOBCTLK - DLAB Statement Processor . . . . .	164	Chart JK. \$JOBCTLM - Miscellaneous Subroutines (Part 2 of 2) . . . . . 185
Chart HG. \$JOBCTLK - XTENT Statement Processor (Part 1 of 2) . . . . .	165	Chart JL. \$JOBCTLM - I/O Subroutines (Part 1 of 2) . . . . . 186
Chart HH. \$JOBCTLK - XTENT Statement Processor (Part 2 of 2) . . . . .	166	Chart JM. \$JOBCTLM - I/O Subroutines (Part 2 of 2) . . . . . 187
Chart HJ. \$JOBCTLK - EXTENT Statement Processor (Part 1 of 3) . . . . .	167	Chart JN. \$JOBCTLM - RMS and Error Subroutines . . . . . 188
Chart HK. \$JOBCTLK - EXTENT Statement Processor (Part 2 of 3) . . . . .	168	Chart JP. \$JOBCTLM - Miscellaneous Subroutines (Part 1 of 2) . . . . . 189
Chart HL. \$JOBCTLK - EXTENT Statement Processor (Part 3 of 3) . . . . .	169	Chart JQ. \$JOBCTLM - Miscellaneous Subroutines (Part 2 of 2) . . . . . 190
Chart HM. \$JOBCTLK - Label Processing Subroutines (Part 1 of 3) . . . . .	170	Chart KA. \$JOBCTLN - Job Accounting Interface (Part 1 of 2) . . . . . 191
Chart HN. \$JOBCTLK - Label Processing Subroutines (Part 2 of 3) . . . . .	171	Chart KB. \$JOBCTLN - Job Accounting Interface (Part 2 of 2) . . . . . 192
Chart HP. \$JOBCTLK - Label Processing Subroutines (Part 3 of 3) . . . . .	172	Chart LA. \$\$BLSTIO - Initialization . 193
Chart HQ. \$JOBCTLK - RSTRRT Statement Processor . . . . .	173	Chart LB. \$\$BLSTIO - Operand Identification Subroutine . . . . . 194
Chart HR. \$JOBCTLK - Miscellaneous Subroutines . . . . .	174	Chart LC. \$\$BLSTIO - Build Print Line Subroutine . . . . . 195
		Chart LD. \$\$BLSTIO - Build Header Subroutine . . . . . 196

FIGURES

Figure 1. I/O Table for One-Device System . . . . .	12	Figure 17. Disk Information Block (DIB) Table . . . . .	230
Figure 2. I/O Table for Two-Device System . . . . .	12	Figure 18. First Part of PIB Table . . . . .	231
Figure 3. IPL Main Storage Map . . . . .	13	Figure 19. PIB Flag Expansions . . . . .	232
Figure 4. ADD, DEL, and SET Statements . . . . .	16	Figure 20. Second Part of PIB Table . . . . .	233
Figure 5. Job Control Storage Allocation . . . . .	19	Figure 21. TEBV Table Showing Status Block and Error Blocks (Part 1 of 2) . . . . .	234
Figure 6. DFB Format . . . . .	202	Figure 22. I/O Table Interrelationship . . . . .	236
Figure 7. Format for NICLS and FICLS . . . . .	204	Figure 23. Job Information Block (JIB) Table . . . . .	237
Figure 8. Phase-Vector Table Entry Format . . . . .	212	Figure 24. Job Accounting Interface Partition Table . . . . .	238
Figure 9. Sample LISTIO Printouts . . . . .	217	Figure 25. Job Accounting Interface Common Table (ACCTCOMN) . . . . .	239
Figure 10. Supervisor Communications Region (Part 1 of 5) . . . . .	218	Figure 26. RMS Machine Check Record on SYSREC . . . . .	240
Figure 11. Background Communications Region Extension . . . . .	223	Figure 27. RMS Monitor Table - RASTAB (Part 1 of 2) . . . . .	241
Figure 12. SDR Communications Region - SDRTABLE (Part 1 of 2) . . . . .	224	Figure 28. RMS Linkage Area (RASLINK) . . . . .	243
Figure 13. Command Control Block (CCB) . . . . .	226	Figure 29. RMS Channel Check Record on SYSREC . . . . .	244
Figure 14. PUB Table . . . . .	227	Figure 30. Format of SYSRES Tape Label Information . . . . .	245
Figure 15. NICL, FICL, and LUB Tables . . . . .	228	Figure 31. SYSRES DASD Label Information . . . . .	246
Figure 16. Tape Error Block (TEB) . . . . .	229		

Chart 00. Disk Operating System Program Flow



INITIAL PROGRAM LOAD (IPL)

The IPL program must be executed each time it is necessary to load a new supervisor control program or to change the channel and unit assignment for SYSRES. The IPL program:

1. Operates in the supervisor mode.
2. Loads supervisor into core from SYSRES.
3. Performs any ADDs and DELETEs of devices to the supervisor PUB table.
4. Sets the date and time of day (if supported) into supervisor communications region.
5. Places the system in the problem mode.
6. Exits to EOJ when it is finished.

Additional information is supplied in the section, IPL Program.

JOB CONTROL PROGRAM (\$JOBCTLA-\$JOBCTLN)

The job control program provides job-to-job transition for:

- Background programs

- Foreground programs if BJF (Batch-Job-Foreground) option is specified. This program also prepares job steps for execution. (One or more programs can be executed within a single job. Each such execution is called a job step.)

On the basis of information provided in job control statements, Job Control performs the following functions:

- Prepares the system for execution of programs in a batched job environment.
- Assigns device address to symbolic units.
- Sets up fields in the communications region(s).
- Edits and stores volume and file label information.
- Prepares for restarting checkpointed programs.
- Clears the problem program area to binary zero between job steps.

Job Control is executed in the problem program area and is overlaid by the job step it is preparing to execute. For additional information, refer to the section, Job Control Program.

## IPL PROGRAM

### INITIAL PROGRAM LOAD PROGRAM (IPL), CHART 01

IPL is a 2-phase program consisting of:

- **\$\$A\$IPL1** (a 64-byte bootstrap routine), and
- **\$\$A\$IPL2** (less than 4096 bytes).

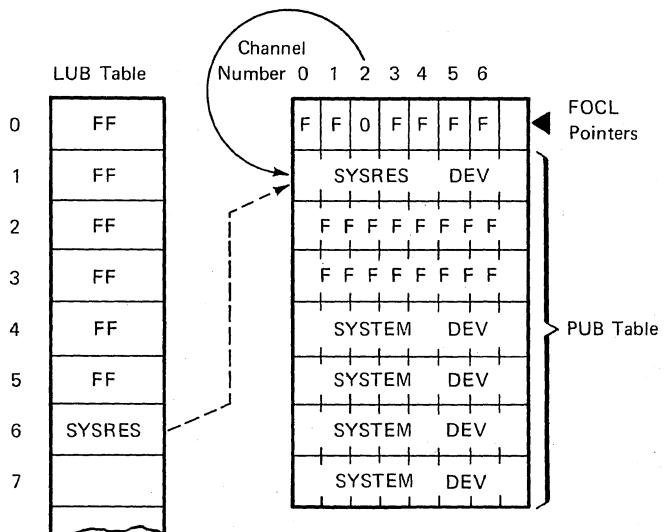
The **\$\$A\$IPL1** bootstrap program is located on SYSRES at 00 00 1 (CC HH R). The operator sets the channel and unit of SYSRES in the load unit switches and presses the load key. Microprogramming reads the first record (24 bytes) from SYSRES into main storage starting at location 00. This 24-byte record consists of a PSW starting at location 0 and two chained CCWs starting at location 8.

Microprogramming executes the first CCW at location 8, which reads in the next 40 bytes (3 more chained CCWs and a seek address) from SYSRES (cylinder 0, track 0, record 2). The second CCW is a seek for the **\$\$A\$IPL2** program on SYSRES (cylinder 0, track 01, record 5). The next three CCWs are a search, transfer in channel, and read for cylinder 0, track 01, record 5 to load the **\$\$A\$IPL2** program. Control is transferred to the **\$\$A\$IPL2** program by loading the PSW at location 0. This PSW was loaded as part of **\$\$A\$IPL1**.

**\$\$A\$IPL2** examines the CPU type. If it is a System/360 CPU, a switch is set indicating that a hard wait will occur when a machine check is encountered. **\$\$A\$IPL2** clears storage from its own end to the end of main storage. A program check is forced and the program check new PSW returns control to the **\$\$A\$IPL2** program. The address at which the program check occurred is saved as the end-of-storage address. There is no provision in the **\$\$A\$IPL2** program to clear main storage below location 12,288.

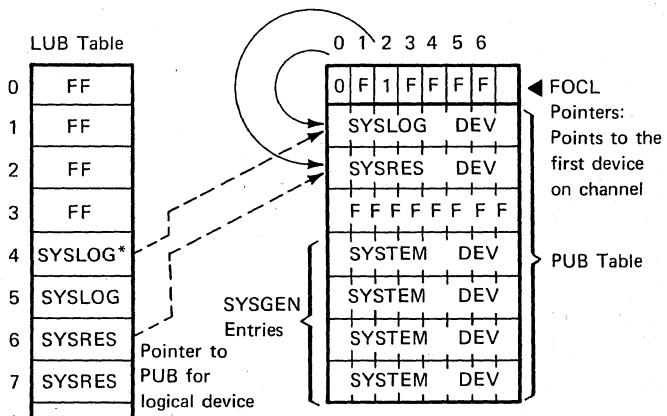
The transient directory is searched for the core image library disk address of the supervisor. The supervisor is read into main storage starting at location 00. The I/O tables that are located within the supervisor are moved to the end of the supervisor (see Figure 3, step 3). A 2-device system is then built in low storage for the IPL operation.

Figures 1 and 2 show examples of I/O tables built by **\$\$A\$IPL2**. Figure 1 shows the I/O tables for a 1-device system, and Figure 2 shows the I/O tables for a 2-device system. Figure 3 is a map of main storage.



Note: It is assumed that SYSRES is on channel 2.

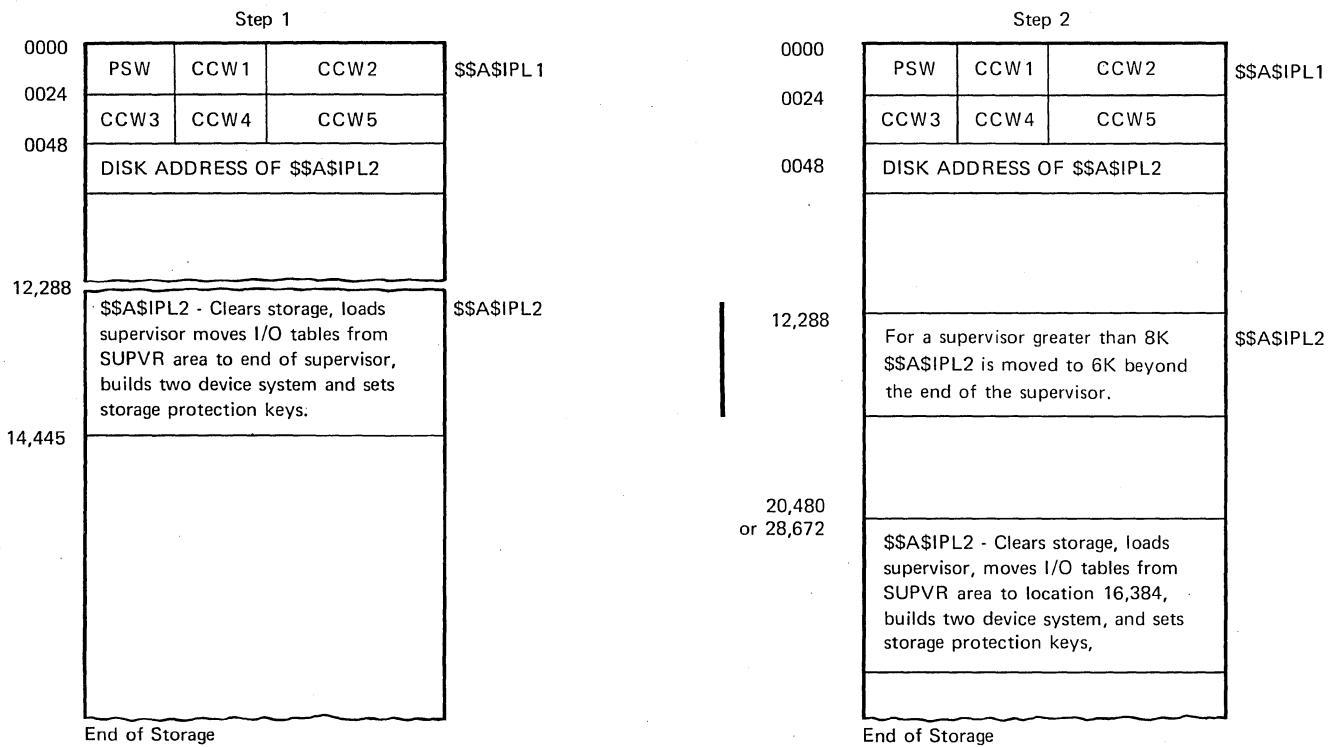
Figure 1. I/O Table for One-Device System



Note: It is assumed that SYSRES is on channel 2 and that the communication device SYSLOG is on channel 0.

\* SYSUSE LUB (displacement 18 in the LUB table) is assigned for SYSRDR or SYSLOG.

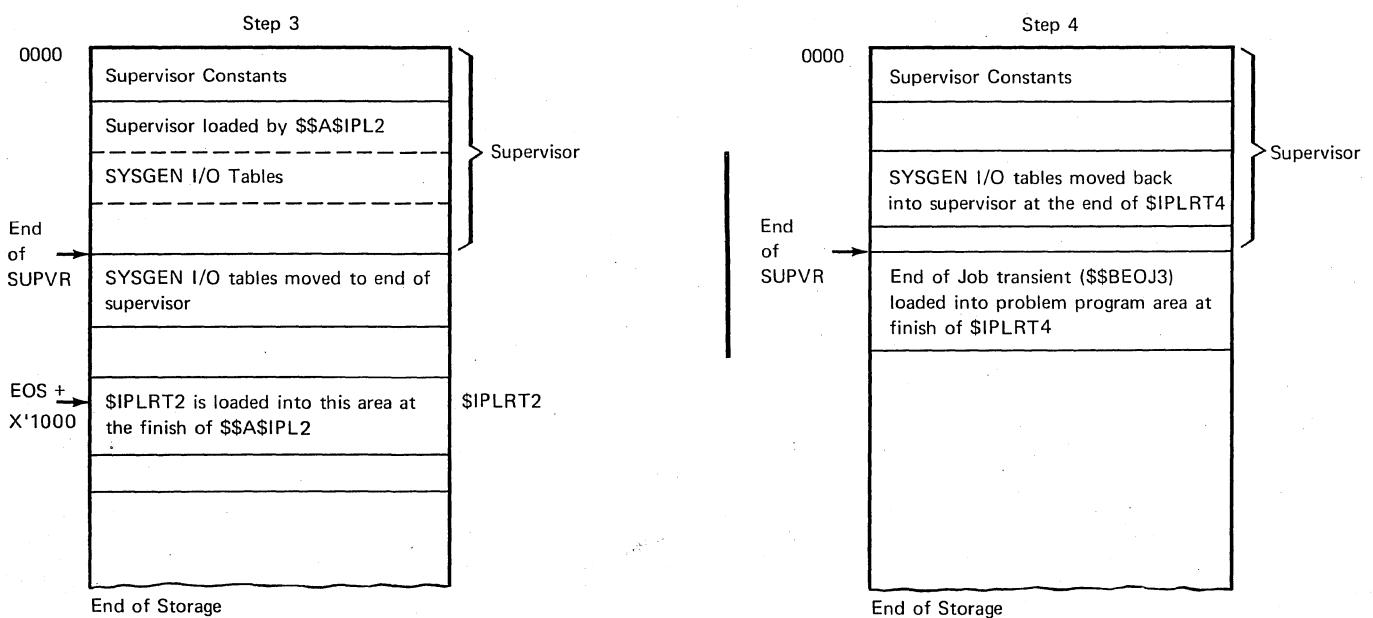
Figure 2. I/O Table for Two-Device System



**Step 1** - represents the main storage map after `$$A$IPL2` is loaded.

NOTE: Storage addresses are in decimal notation.

Step 2 - represents the main storage map after \$\$A\$ IPL2 is loaded.



Step 3 - represents the main storage map after \$A\$ IPL2 loads the supervisor.

Step 4 - represents the main storage map after \$IPLRT4 is executed.

**Figure 3.** IPL Main Storage Map

After the system I/O tables have been moved, a PUB is built in low storage for SYSRES. A LUB is assigned for this PUB and the FOCL is set to point to the PUB for the SYSRES device. The system is put into the wait state and the operator has the option of selecting the communication device desired for IPL. If the desired communication device is:

1. A card reader, and it is already assigned as SYSRDR, the operator presses the external interrupt key causing an external interrupt.
2. A card reader, and it is not assigned as SYSRDR, the operator presses the start key on the reader causing an I/O (device end) interrupt.
3. A console printer-keyboard, the operator presses the request key causing an I/O attention interrupt.

After the operator has taken the appropriate action for choosing a communication device, a PUB and LUB are added and the FOCL is updated to show the new device. This completes building of the 2-device system for IPL.

A check is made to determine if the storage protection feature is supported. If so, the storage protection keys are set. The supervisor area, in blocks of 2K, receives a storage protection key of 0. The upper part of the supervisor that is not an even multiple of 2K and the remainder of main storage are not protected. They receive a storage protection key of 1. The \$\$A\$IPL2 issues a SVC of 4 to load the \$IPLRT2 program, overlaying the \$\$A\$IPL2 program.

#### \$IPLRT2, CHART 02

The \$IPLRT2 program is loaded and executed every time the operator chooses to IPL the system. It is loaded (by the \$\$A\$IPL2 program) starting at location EOS+4096 (see Figure 3, step 3). Before loading \$IPLRT2, the \$\$A\$IPL2 program has moved the system I/O tables to high core. A 2-device system, SYSRES and SYSUSE (SYSUSE=SYSRDR or SYSLOG), has been built by the \$\$A\$IPL2 program for IPL operations.

The \$IPLRT2 program performs the following functions:

- Scans the LUB table entries in each partition and establishes partition ownership for each entry.

- Checks for RMS support. When the IPL program is executed on a System/360 CPU, System/370 functions are negated, RETAIN/370 support is turned off (if present), and coding to simulate System/360 support is moved into the supervisor. In the case of a System/370 CPU, the address of the extended logout area is found and \$\$BCCHHR is fetched to build the load list.
- Monitors core usage and indicates whether there is enough core storage available to load both \$IPLRT3 and \$IPLRT4 into core at the same time. When there is not enough core storage available, a switch is set, indicating that \$IPLRT3 and \$IPLRT4 will be separately loaded into the same area as needed.
- Contains the error subroutines to issue the error messages when necessary.
- Loads the appropriate phase(s) into core storage after the \$IPLRT2 program. If the first control card is a SET card, \$IPLRT4 is loaded. If the first control card read is a DEL or an ADD card and there is sufficient core storage available to allow both \$IPLRT3 and \$IPLRT4 to be loaded, both are loaded. If there is not sufficient core storage available to have \$IPLRT4 loaded after \$IPLRT3, each is loaded into the same area as it is needed.

The ADD, DEL, and SET statements are entered from the IPL communication device (SYSRDR or SYSLOG). The formats for these statements are described in Figure 4.

After a card is read, the operation code is evaluated by a translate and test instruction to determine the type of statement.

#### \$IPLRT3, CHART 03

The \$IPLRT3 program is loaded and executed only when ADD or DEL control cards are submitted to the IPL program. It is loaded by the \$IPLRT2 program.

The \$IPLRT3 program performs the following functions:

- Adds a device to the system.
- Deletes a device from the system.

### Add Routine (\$IPLRT3)

The add routine checks to ensure the device is not already assigned. It then determines where to add the PUB in the PUB table and moves all the PUB entries beyond this point down one PUB length to make room for the new PUB. The new PUB is then inserted in the area just vacated. The LUB table and FOCL pointers are updated to reflect the new entry and the routine returns to read another control statement.

### Delete Routine (\$IPLRT3)

The delete routine first checks to see if the device to be deleted is in the PUB table and then determines the location in the PUB table of the PUB to be deleted. All PUBs beyond this point are moved up one PUB length overlaying the PUB to be deleted. The LUB table and FOCL pointers are updated so they no longer point to a nonexisting PUB entry. The routine returns to read another control statement.

### \$IPLRT4, CHART 03

The \$IPLRT4 program is loaded and executed every time the operator decides to IPL the system. It is loaded by the \$IPLRT2 program.

The \$IPLRT4 program performs the following functions:

- Sets the system date.
- Sets the system time of day, if the timer feature is supported.

- Checks the channels for file-protect support for each device when DASDFP option is specified.
- Scans the PUB table. If a 3211 is found, \$IPLRT4 loads \$\$BUFLDR.

### Set Routine (\$IPLRT4)

The set time of day routine determines the operand format of the set statement.

- The DATERT subroutine converts the month, day, and year to decimal. This information is then stored in the system date field of the communication region (displacement 79).
- The TIMERT subroutine is used if the timer feature is supported. It converts the hours, minutes, and seconds to decimal and determines the time of day in total seconds. The total seconds, multiplied by 300, is stored at core location X'54'.

The SET card signals the end of the control statements. The system assignments for SYSRES and the communication device (SYSRDR or SYSLOG) are checked and permanently assigned. The system I/O tables are moved from their temporary location in high core to their permanent location in the supervisor area. This move overlays the two-device IPL I/O tables (that were built by \$\$A\$IPL2) and finishes the IPL operation.

The end-of-job transient is loaded with a SVC 14 to initiate normal job processing. If the supervisor contains teleprocessing support, \$\$BEOJ3 is fetched; otherwise, \$\$BEOJ4 is fetched.

ADD -- Add a Device to the PUB Table

Operation	Operand
ADD	X'cuu' [(k)], devicetype [,X'ss']

X'cuu' = Channel and unit numbers in hexadecimal

k = S, if the device is switchable (is physically attached to two adjacent channels).  
The designated channel is the lower of the two channels.

k = 0-255 indicates the priority of the device, if the device cannot be switched. The highest priority is 0.  
If k is not given, a priority of 255 is assumed.

devicetype\* = 2400T7 for 7-track, IBM 2400 Series Magnetic Tape Units.  
2400T9 for 9-track, IBM 2400 Series Magnetic Tape Units.  
1442N1 for 1442N1, IBM Card Read Punch.

X'ss' = Device specifications used for tape mode. If device specifications are not specified, X'ss' has the following set values:

X'C0' for 9-track tape  
X'90' for 7-track tape  
X'00' for nontapes

If you specify X'C8' for an 800 BPI single-density 9-track tape drive, you will save time during the tape OPEN.  
X'00', X'01', X'02', and X'03' are invalid as X'ss' for magnetic tape. These four values are used to specify SADxxx requirements for IBM 2702 lines\*. X'ss' is required for the IBM 1255, 1259, 1270\*\*, 1275\*\*, 1412, 1419, and 1419P device types. It specifies the external interrupt bit (in the old PSW) used by this device to indicate READ COMPLETE. The specifications are:

X'01' PSW bit 31      X'08' PSW bit 28  
X'02' PSW bit 30      X'10' PSW bit 27  
X'04' PSW bit 29      X'20' PSW bit 26

\* Device type codes and a complete list of density settings can be obtained from the Supervisor and Transients PLM.

\*\* This device is not available in the United States of America.

The end-of-block character (B) (alter code 5) must be given after each ADD statement if the communication device is a printer-keyboard.

DEL -- Delete a Device from PUB Table

Operation	Operand
DEL	X'cuu'

Where cuu is the channel and unit numbers, in hex, of the device to be deleted.

The end-of-block character (B) (alter code 5) must be given after each DEL statement if the communication device is a printer-keyboard.

SET -- Set Date and Time of Day

Operation	Operand
SET	[DATE=n1] [,CLOCK=n2]

The entries in the operand represent the following:

DATE = n1      Sets the system date to the specified value. n1 has one of the following formats:

mm/dd/yy  
dd/mm/yy

Where mm specifies the month, dd specifies the day, and yy specifies the year.  
The format used is that selected when the system was generated.

CLOCK = n2      Must be given at IPL time if the timer feature is present.  
Sets the system clock to the specified value. n2 has the following format:

hh/mm/ss

Where hh specifies hours (00-23), mm specifies minutes (00-59), and ss specifies seconds (00-59).

Figure 4. ADD, DEL, and SET Statements

The job control program provides job-to-job transition for background programs and, optionally, for foreground programs. It also prepares program job steps for execution. (One or more programs can be executed within a single job. Each such execution is called a job step.) To obtain job-to-job transition for foreground programs, Batch-Job-Foreground (BJF) support must be specified at system generation time, and a BATCH command must be issued by the operator. Job Control then provides this feature in the partition specified by the operand of the BATCH command if the following minimum requirements are met:

- At least 10K of main storage in the partition.
- Separate system I/O files for the partition.

Job Control performs various functions on the basis of information provided in job control statements:

- Prepares programs for execution.
- Assigns device addresses to symbolic units.
- Sets up field in the communication region(s).
- Edits and stores volume and file label information.
- Prepares for restarting checkpointed programs.
- Clears the program area to binary zeros between job steps.
- Prepares input for the linkage editor program if the LINK option has been specified. The statements: ENTRY, ACTION, PHASE, and INCLUDE, when present in input stream, are copied to SYSLNK as card images. An INCLUDE statement with a blank operand causes the contents of SYSIPT to be copied to SYSLNK until a /\* statement is read from SYSIPT. Blank cards from SYSIPT are ignored. This preparation for the linkage editor program is valid for the BG partition only, unless the supervisor includes the PCIL option. If the LINK option is not specified and the CATALR card precedes the PHASE card, these cards are copied to SYSPCH

as card images preceding the compilation.

The job control program is executed in the program area and is overlaid by the job step it is preparing for execution. A JOB statement in the input stream marks the beginning of a job and a /\* statement marks the end of a job. An EXEC statement calls for execution of a job step. A job step is normally ended with the EOJ macro.

#### I/O FLOW

The I/O flow for the job control program consists of:

- Input  
    SYSRDR  
    SYSIPT
- Output  
    SYSLST  
    SYSLNK
- I/O  
    SYSLOG

#### PROGRAM FLOW

Functionally, Job Control consists of eight phases and one B-transient that are identified as \$JOBCTLA, \$JOBCTLB, \$JOBCTLF, \$JOBCTLG, \$JOBCTLJ, \$JOBCTLK, \$JOBCTLM, \$JOBCTLN, and \$BLSTIO.

#### \$JOBCTLA (CHART 04)

This phase is the initial entry into Job Control. It is loaded every time Job Control is fetched and is considered the root phase. (It is resident in main storage at all times during job control execution and contains routines that are used by the other phases of Job Control.)

Job control input is read from SYSRDR or SYSLOG depending on the setting of the job control input switch (COMREG+56, bit 2). As each control statement is read, it is analyzed to determine which of the processing routines is to be used. The phase containing the correct processing

routine is loaded if it is not already in main storage as a result of the previous control statement.

Figure 5 represents the storage allocation for Job Control.

10. HOLD
11. RELSE
12. CATALR card (not a control statement)
13. NOLOG
14. LOG
15. STOP
16. PAUSE
17. ALLOC

#### \$JOBCTL0 (CHARTS 05 AND 06)

Contains the processing routines for the following control statements:

1. ASSGN
2. CLOSE

#### \$JOBCTLF (CHART 07)

Contains the processing routines for the following control statements:

1. DVCNDN
2. DVCUP
3. LISTIO
4. RESET
5. UNA
6. UNBATCH
7. MAP

#### \$JOBCTLG (CHARTS 08 AND 09)

Contains the processing routines for the following control statements:

1. CANCEL
2. /& (EOJ)
3. EXEC
4. JOB
5. OPTION

#### \$JOBCTLJ (CHARTS 10, 11, AND 12)

Contains processing routines for the following control statements:

1. ACTION
2. ENTRY
3. PHASE
4. INCLUDE
5. DATE
6. SET
7. UPSI
8. MTC
9. UCS

#### \$JOBCTLK (CHARTS 13 AND 14)

Contains processing routines for the following control statements:

1. RSTRRT
2. LBLTYP
3. VOL
4. TPLAB
5. DLAB
6. XTENT
7. TLBL
8. DLBL
9. EXTENT

#### \$JOBCTLM (CHART 15)

Initializes the recorder file and contains processing routines for the ROD control statement.

#### \$JOBCTLN (CHART 16)

Provides interface between the DOS system and \$JOBACCT to allow the user to access Job Accounting information. This phase contains processing routines for the following conditions:

1. Normal end of job.
2. Simulated end of job.
3. End of job and pause.
4. EXEC card encountered.

Note: \$JOBCTLN interfaces with \$JOBACCT phase supplied by user.

#### \$\$BLSTIO

This B-transient contains subroutines used by DVCNDN and LISTIO control statement processors of \$JOBCTLF. When required by these processors, \$\$BLSTIO is fetched (SVC 2) into supervisor B-transient area.

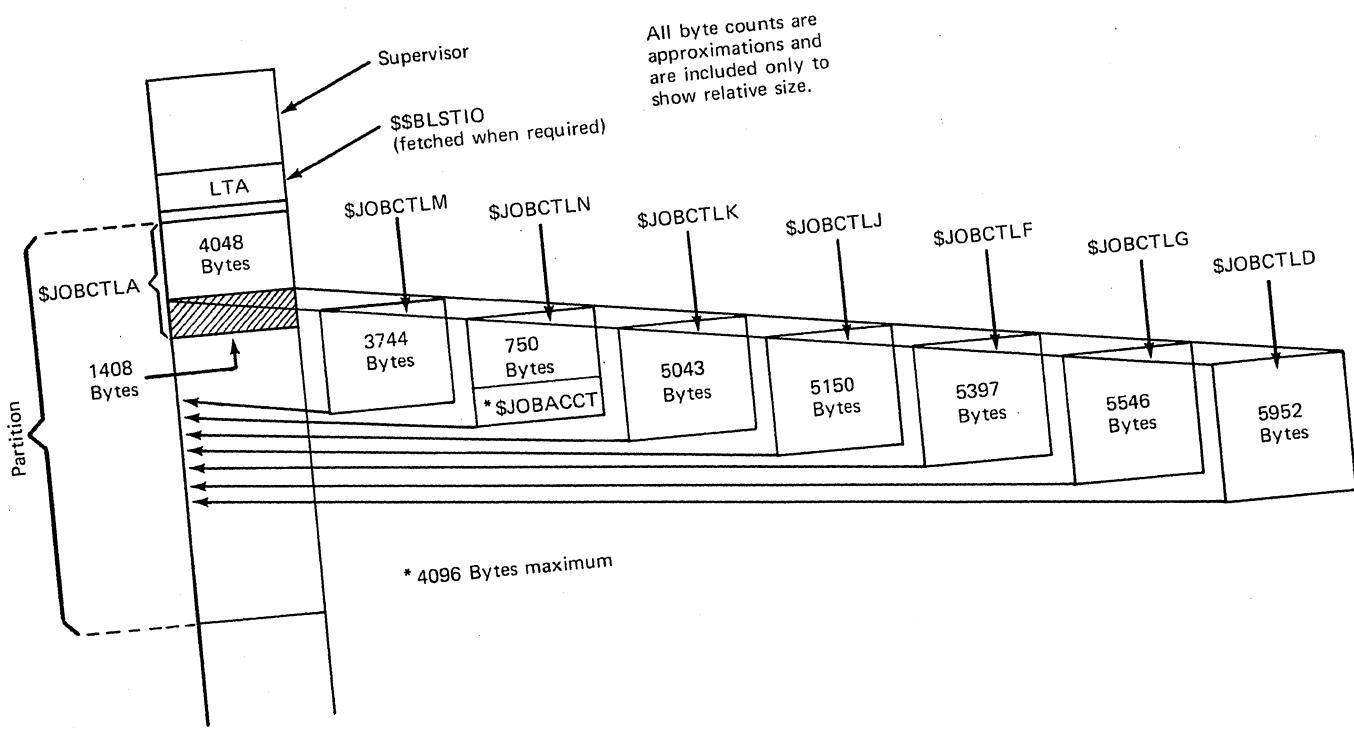
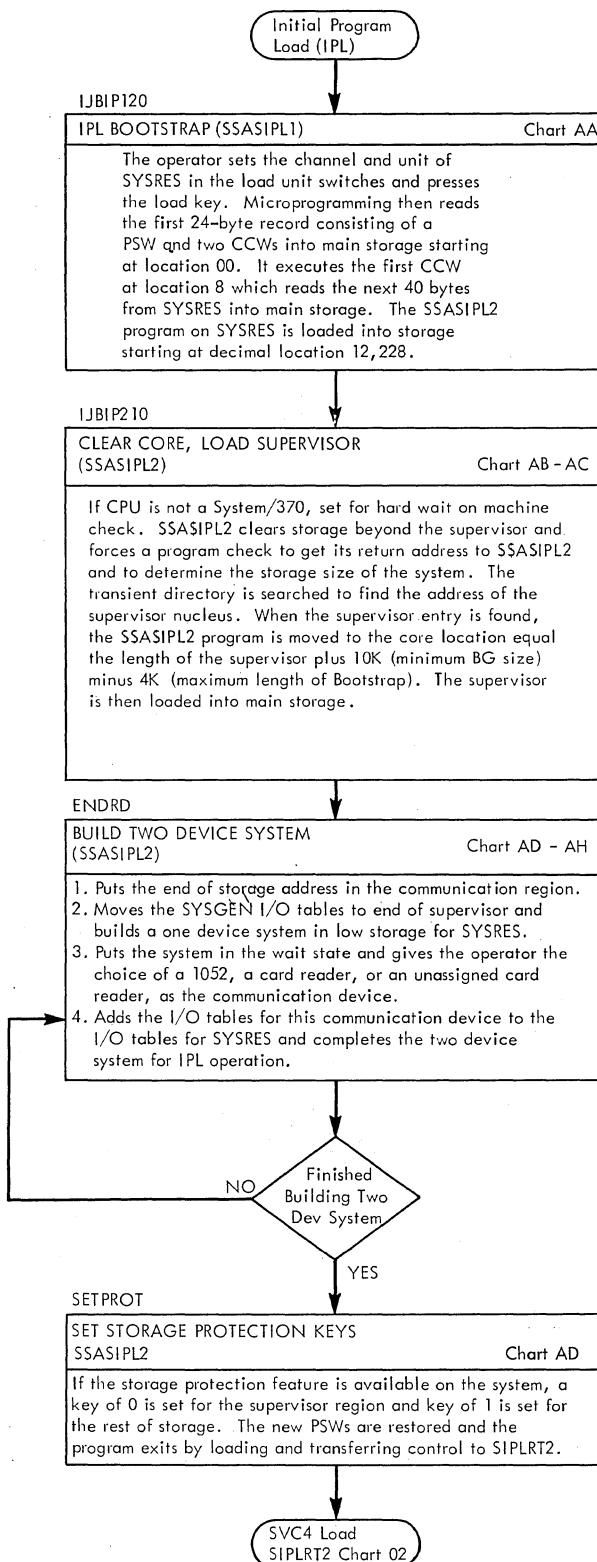


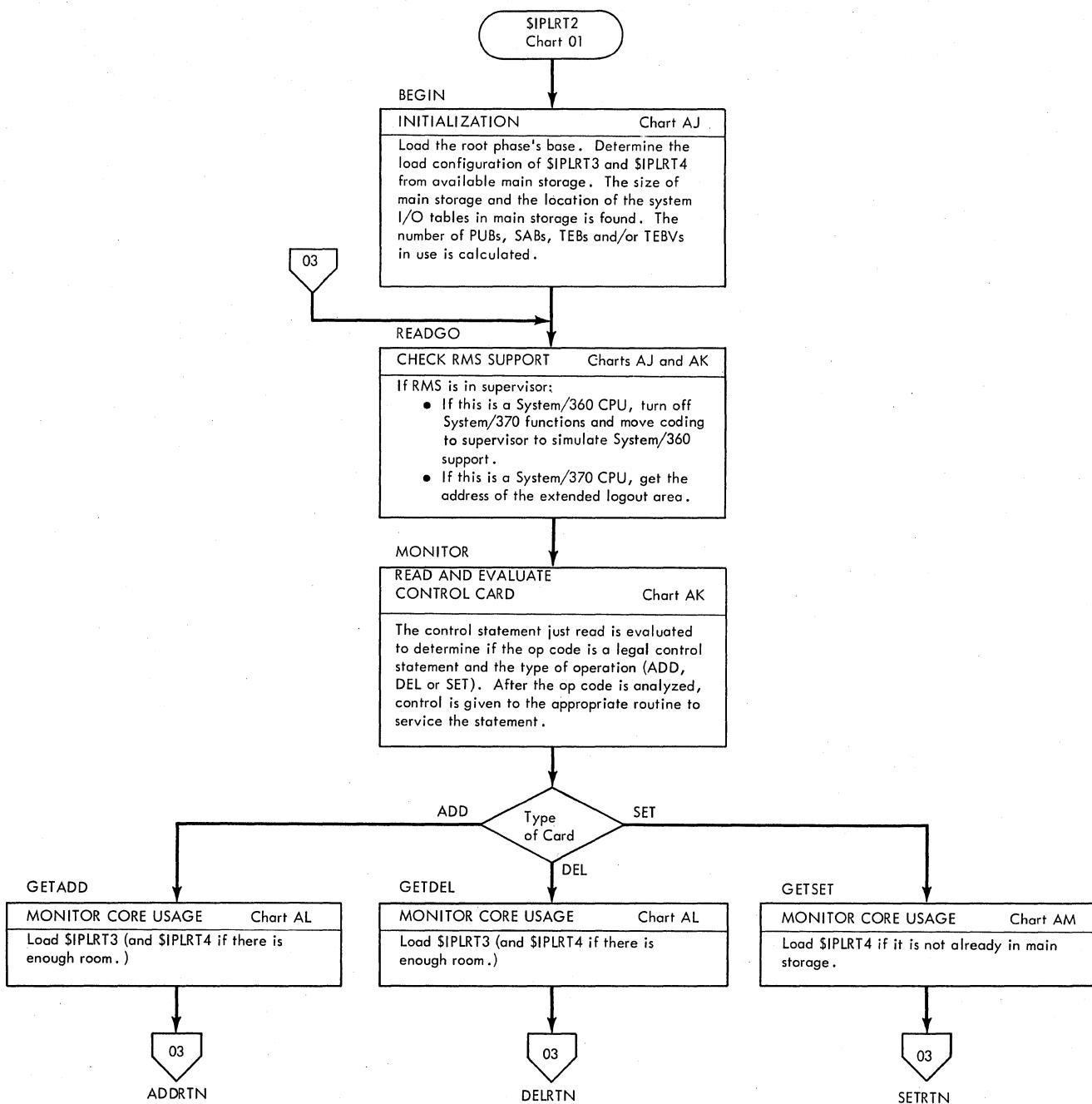
Figure 5. Job Control Storage Allocation



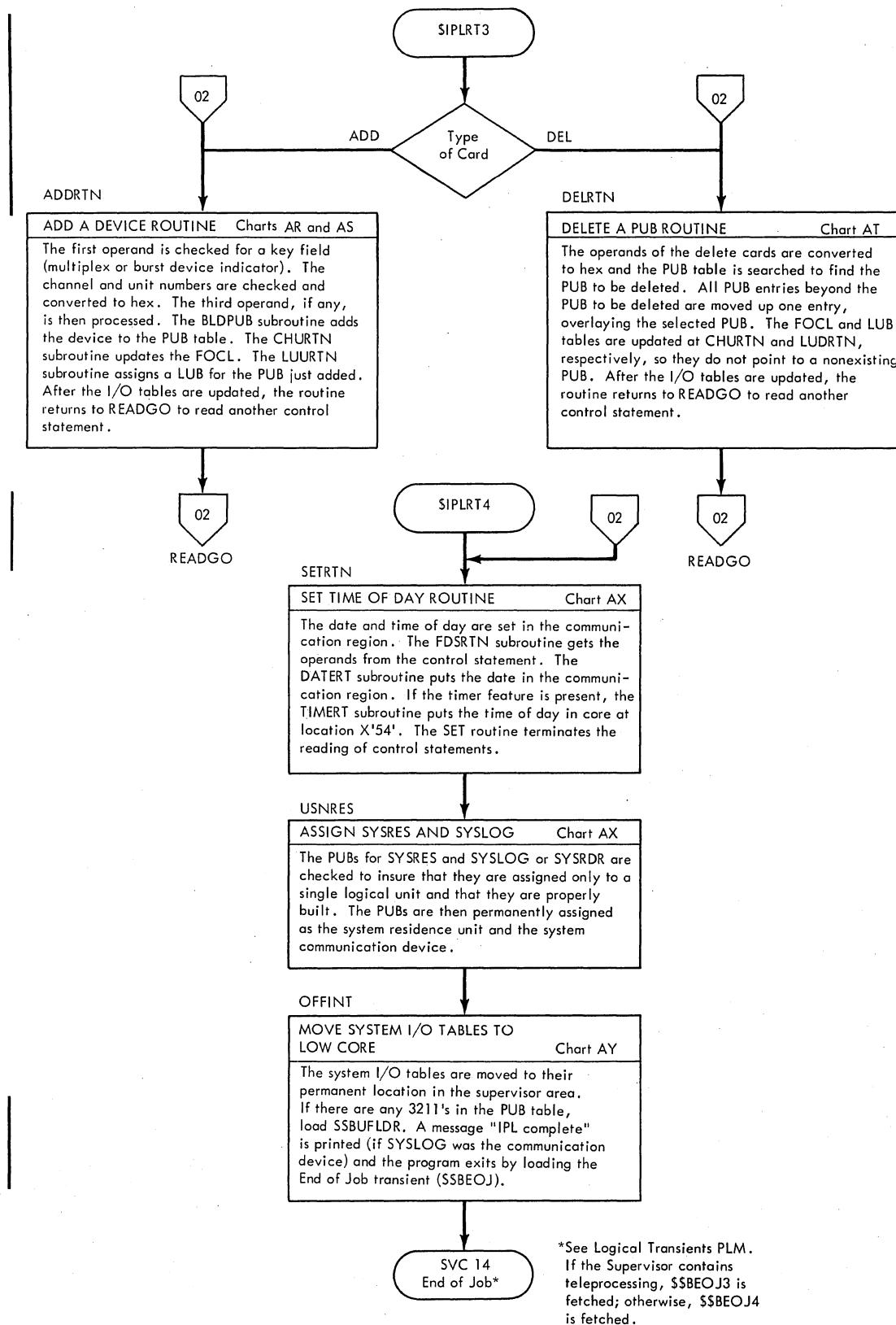
Chart 01. Initial Program Load (\$\$A\$IPL1 and \$\$A\$IPL2)



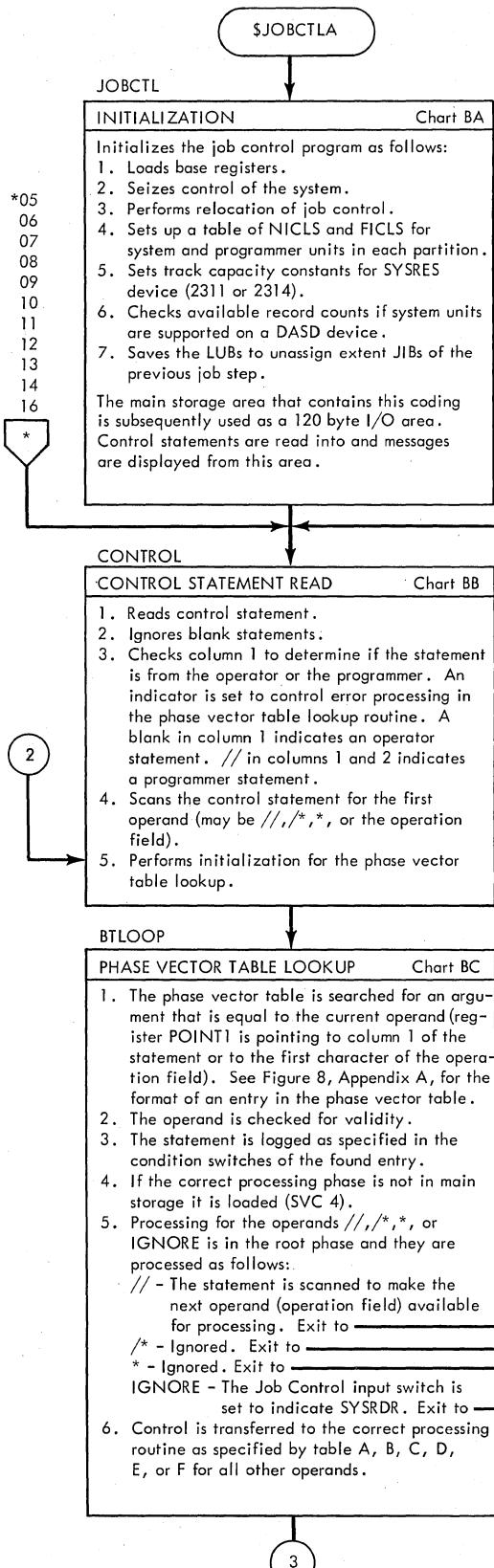
**Chart 02. Initial Program Load (\$IPLRT2)**



**Chart 03. Initial Program Load (\$IPLRT3 and \$IPLRT4)**



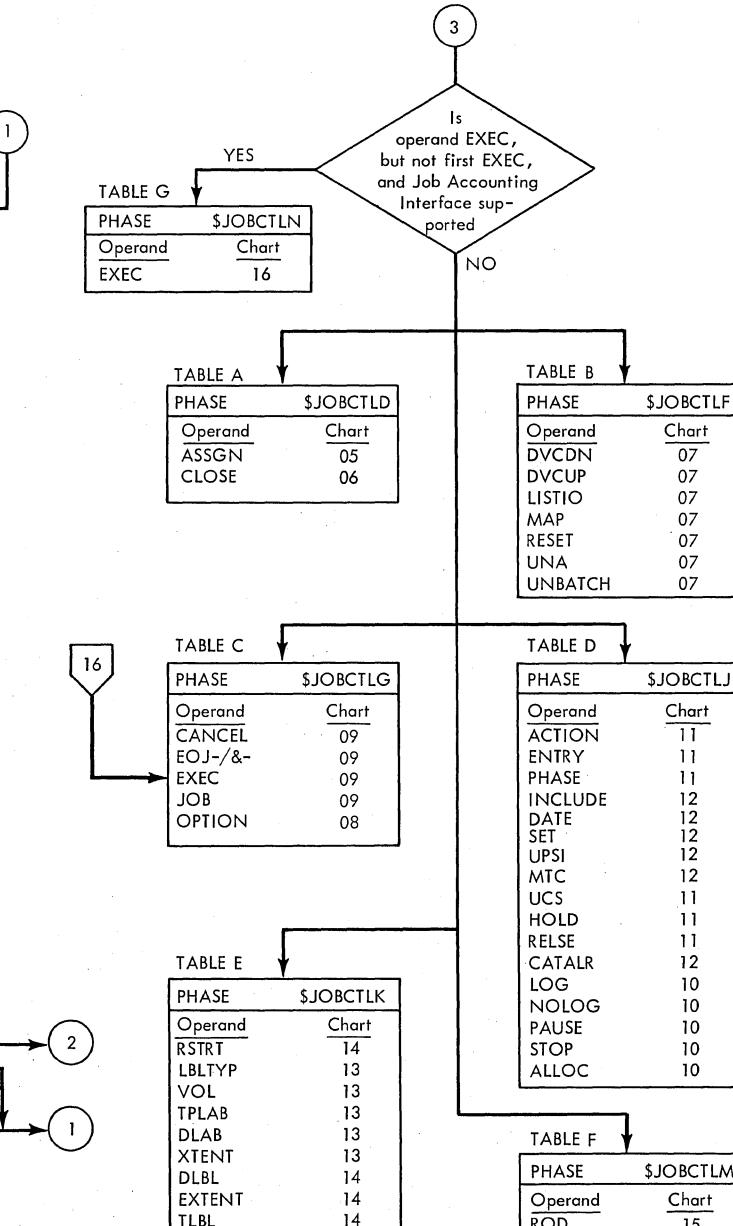
## Chart 04. Job Control (\$JOBCTLA) Root Phase



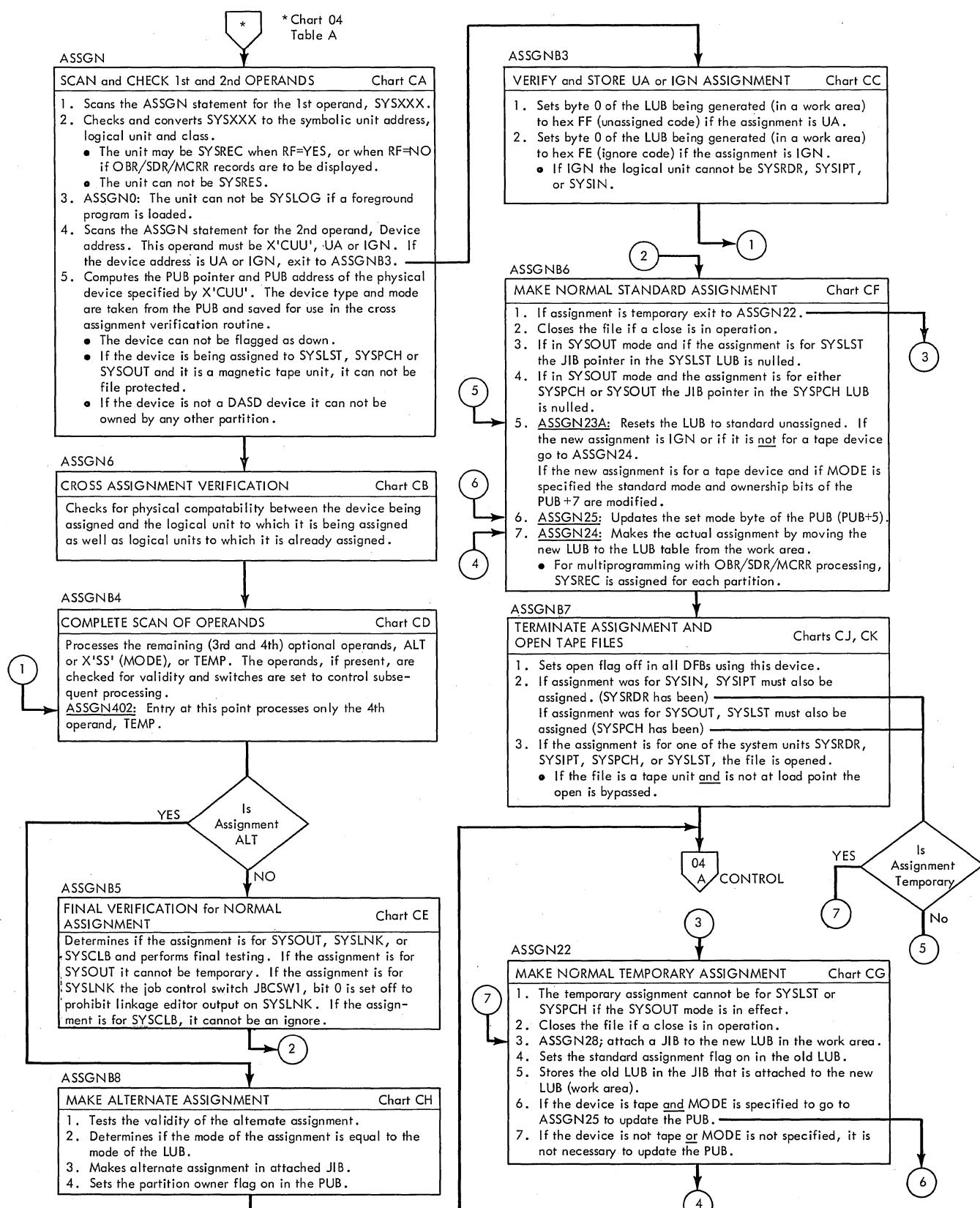
Note:

Job control is entered from the supervisor fetch routine. It can be entered normally by means of the EOJ macro or abnormally from the B-transients \$\$BILSVC, \$\$SBPCHK, \$\$SBTERM, \$\$\$BEOJ, \$\$\$BEOJI. In all cases the B-transient \$\$BEOJ is used to issue the actual fetch for Job Control (\$JOBCTLA). This phase includes:

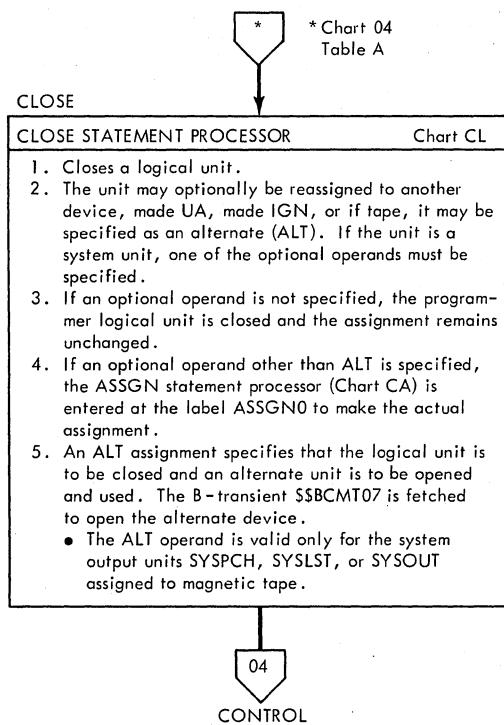
1. The initialization routine (JOBCTL).
  2. The control statement read routine (CONTROL).
  3. The phase vector table (see Figure 8 in Appendix A).
  4. The root phase subroutines (Charts BD - BK).
  5. The root phase error message routines (Charts BL - BM).
  6. Relocation subroutine Chart BN.
- Items 4 and 5 are used by other job control phases.



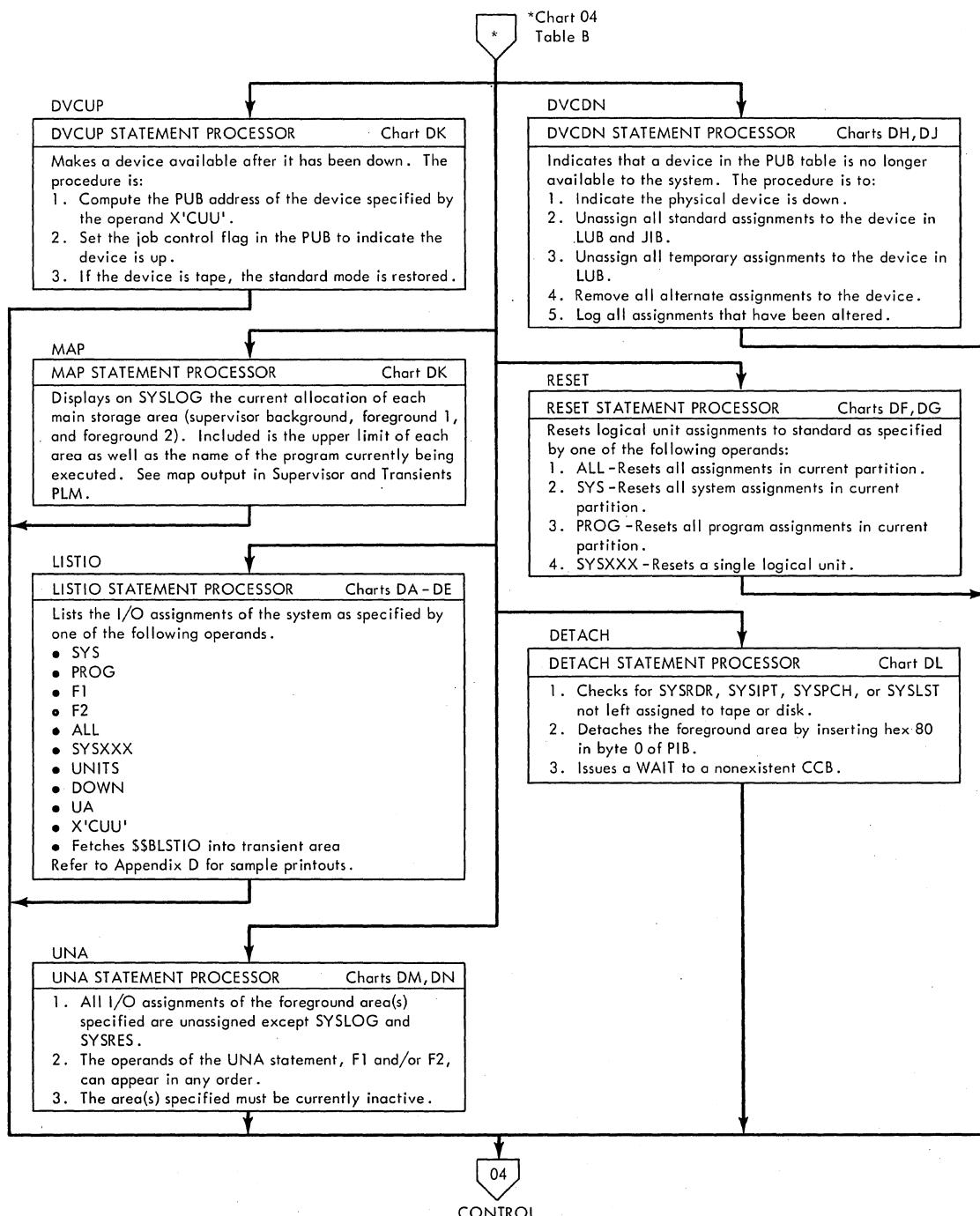
**Chart 05. Job Control (\$JOBCTL) Statement Processor (Part 1 of 2)**



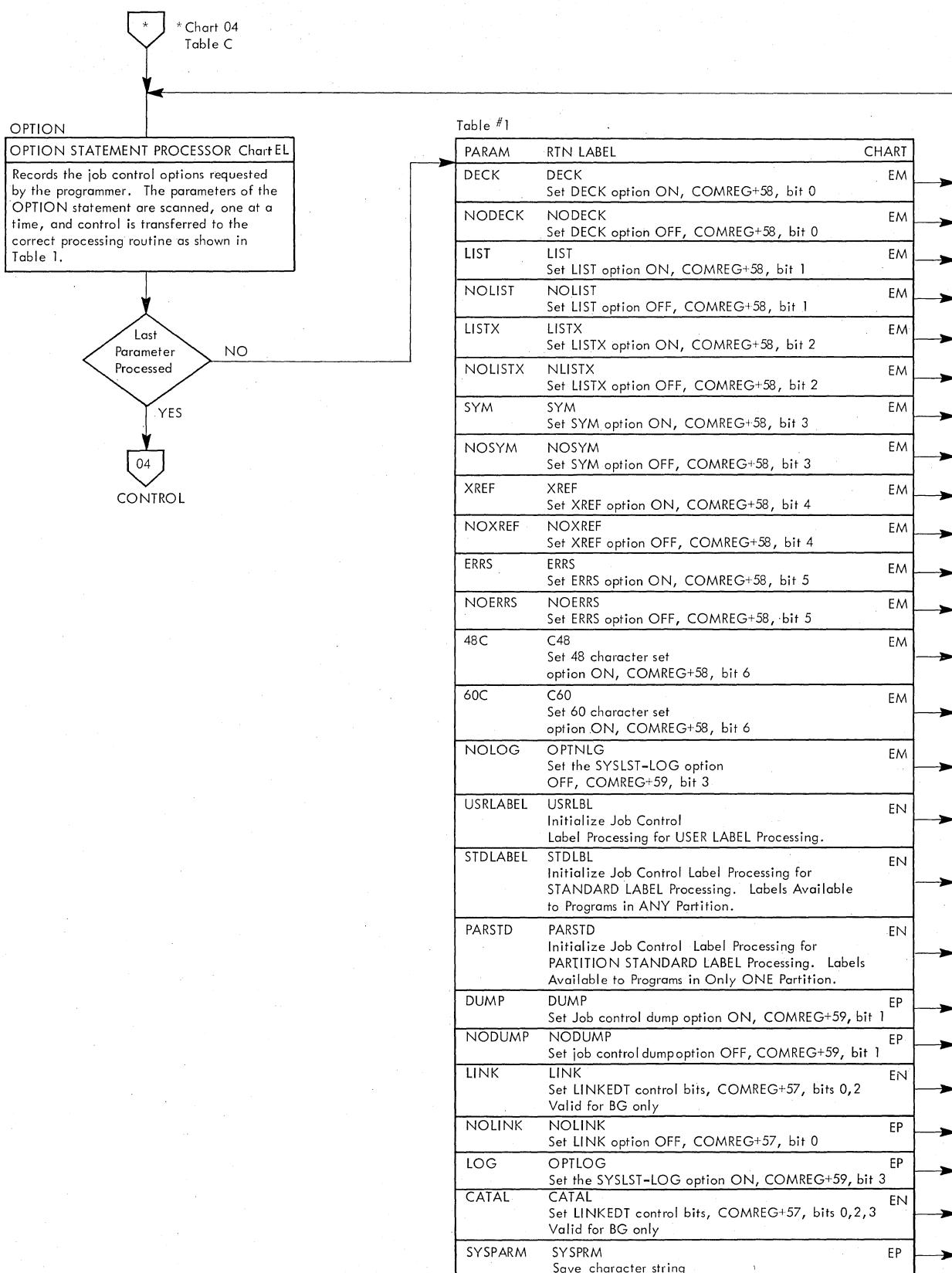
**Chart 06. Job Control (\$JOBCTL0) Statement Processor (Part 2 of 2)**



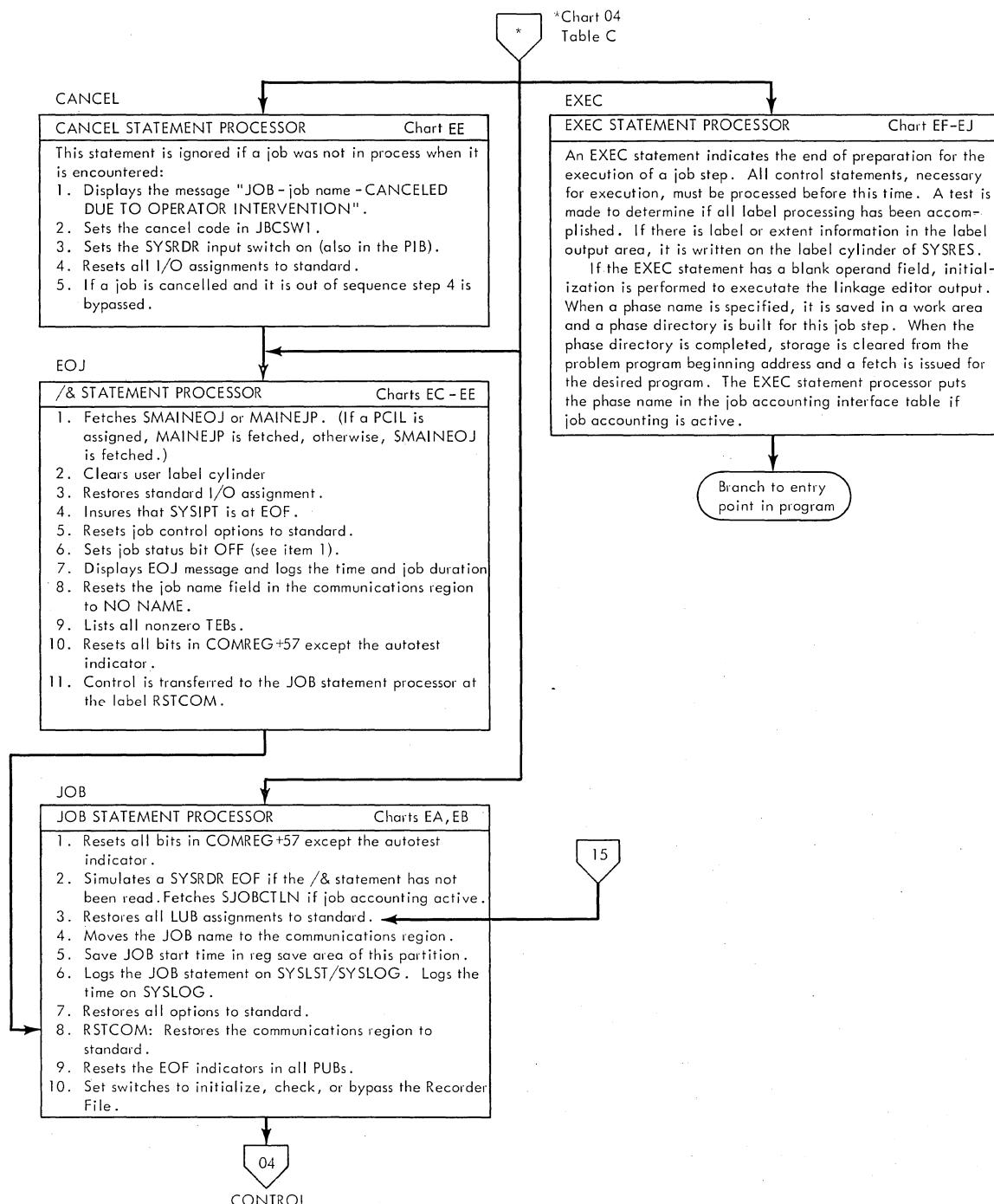
## Chart 07. Job Control (\$JOBCTLF) Statement Processor



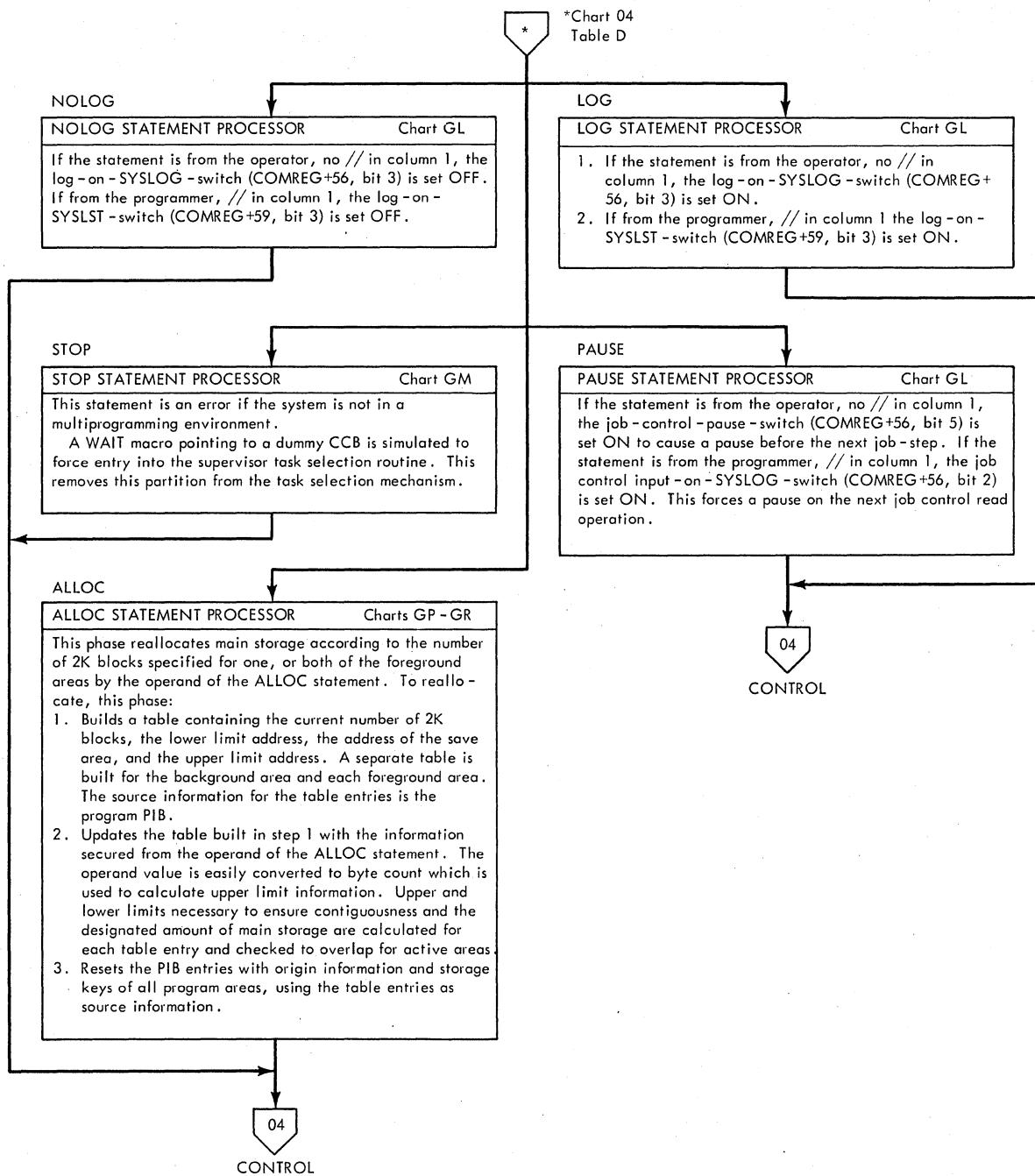
## Chart 08. Job Control (\$JOBCTLG) Statement Processor (Part 1 of 2)



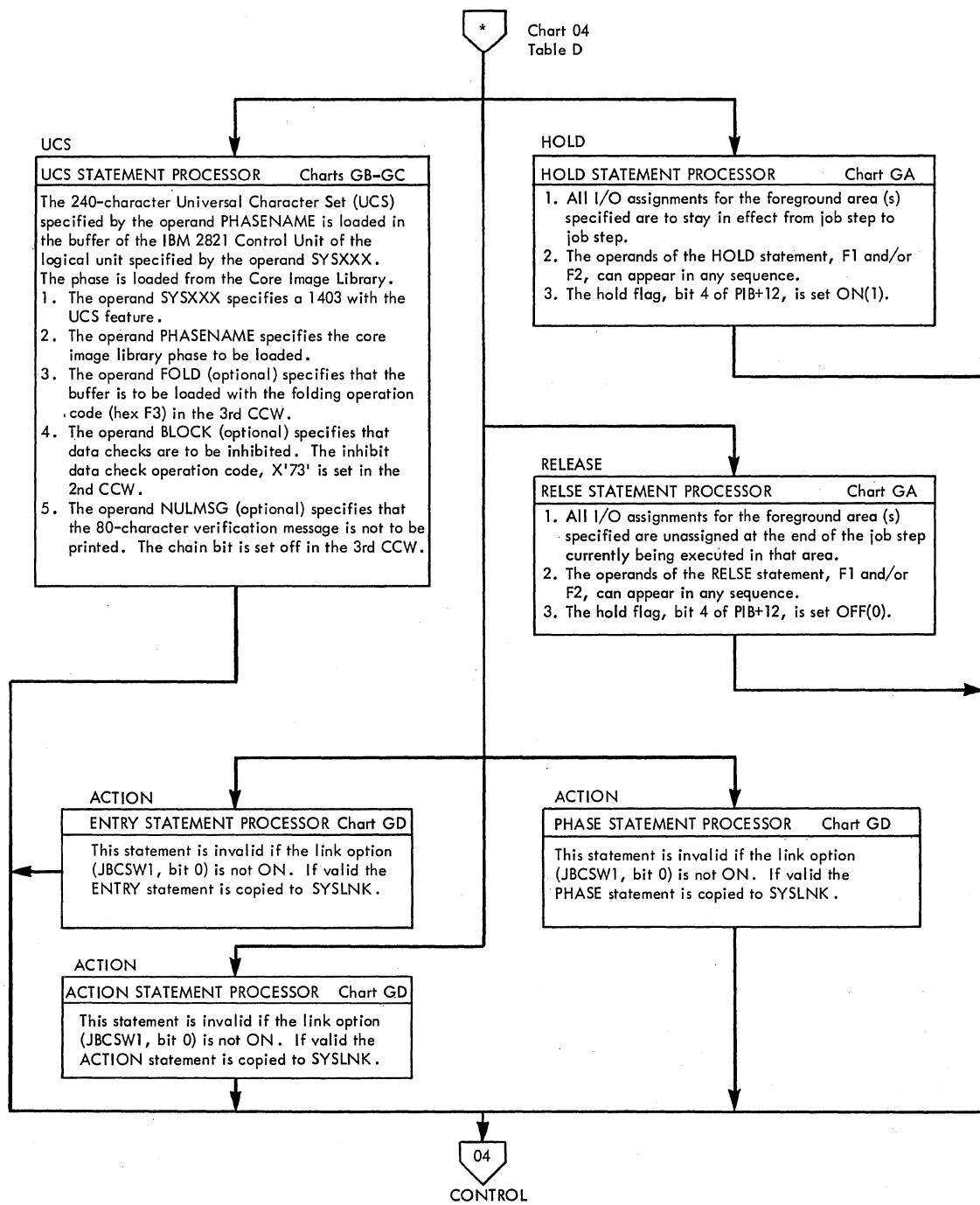
**Chart 09. Job Control (\$JOBCTLG) Statement Processor (Part 2 of 2)**



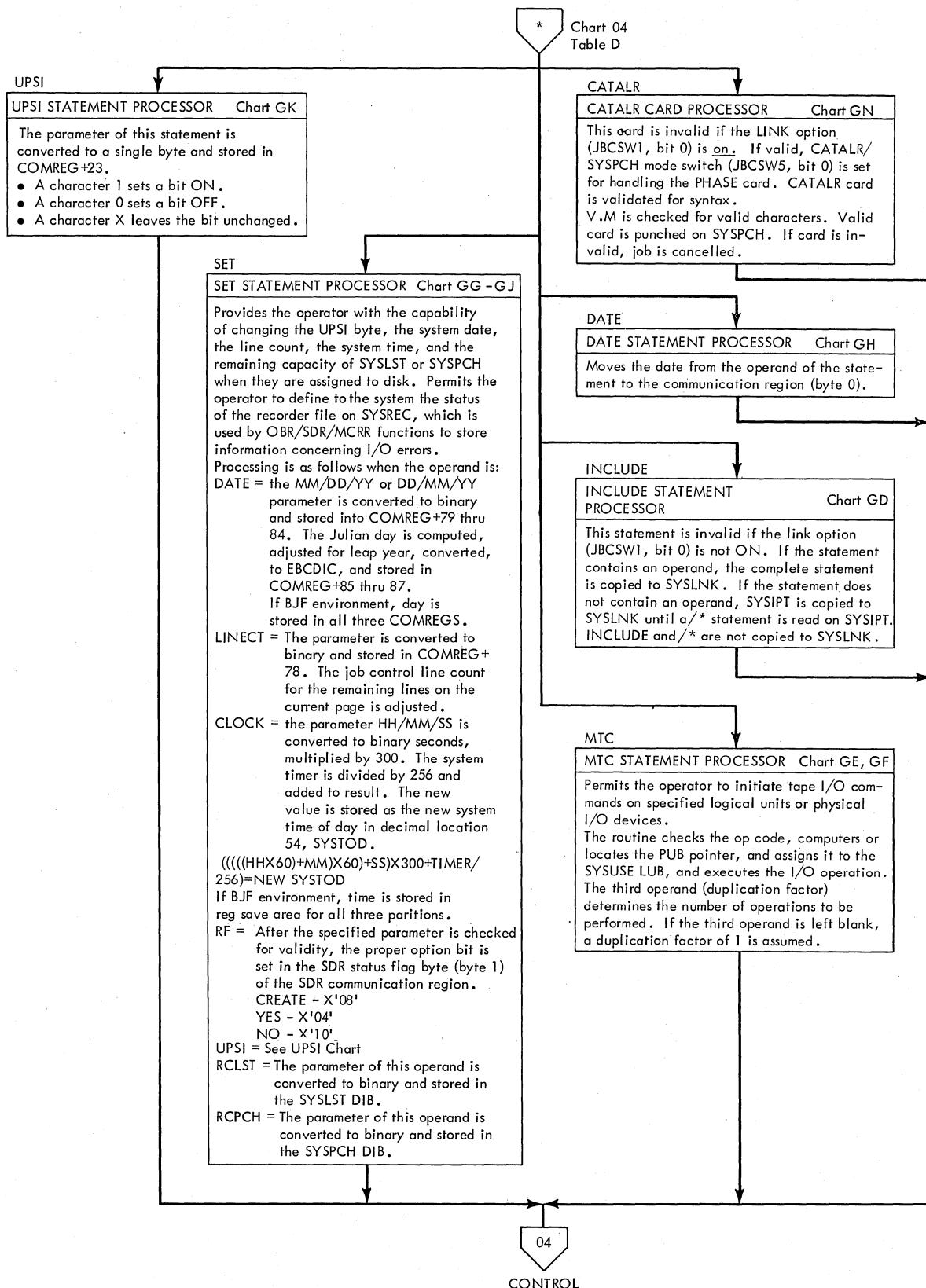
### Chart 10. Job Control (\$JOBCTLJ) Statement Processor (Part 1 of 3)



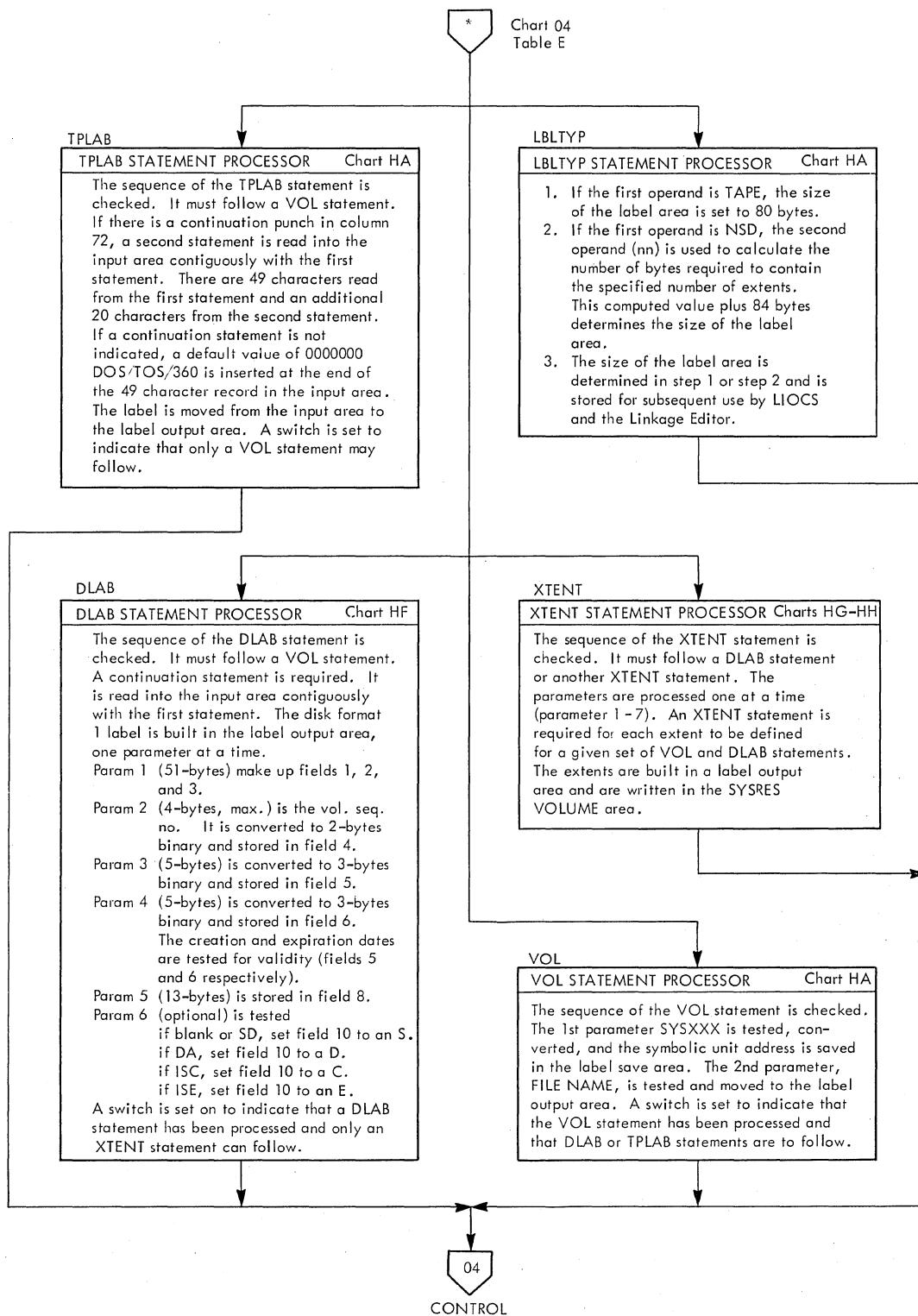
**Chart 11. Job Control (\$JOBCTLJ) Statement Processor (Part 2 of 3)**



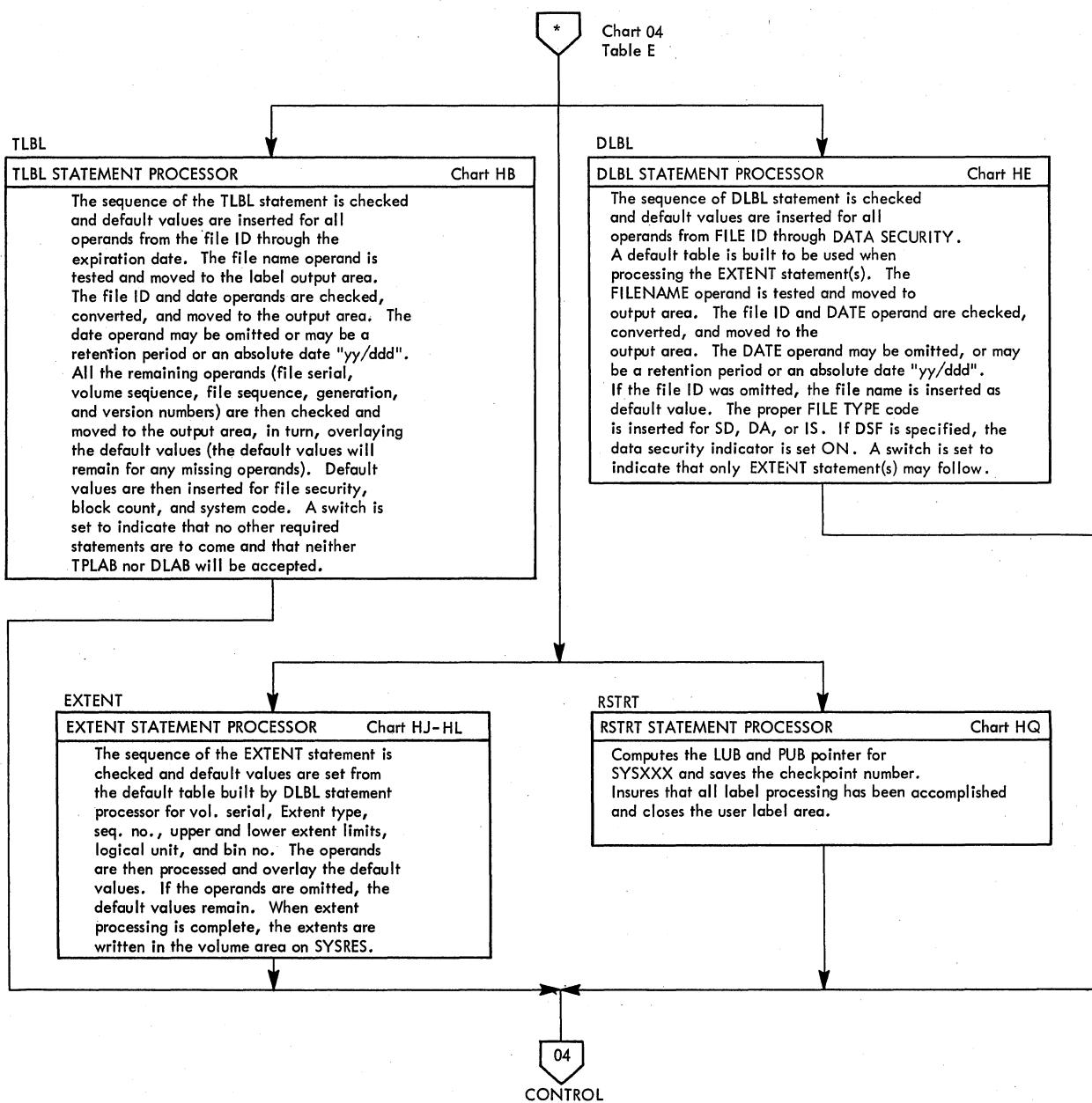
**Chart 12. Job Control (\$JOBCTLJ) Statement Processor (Part 3 of 3)**



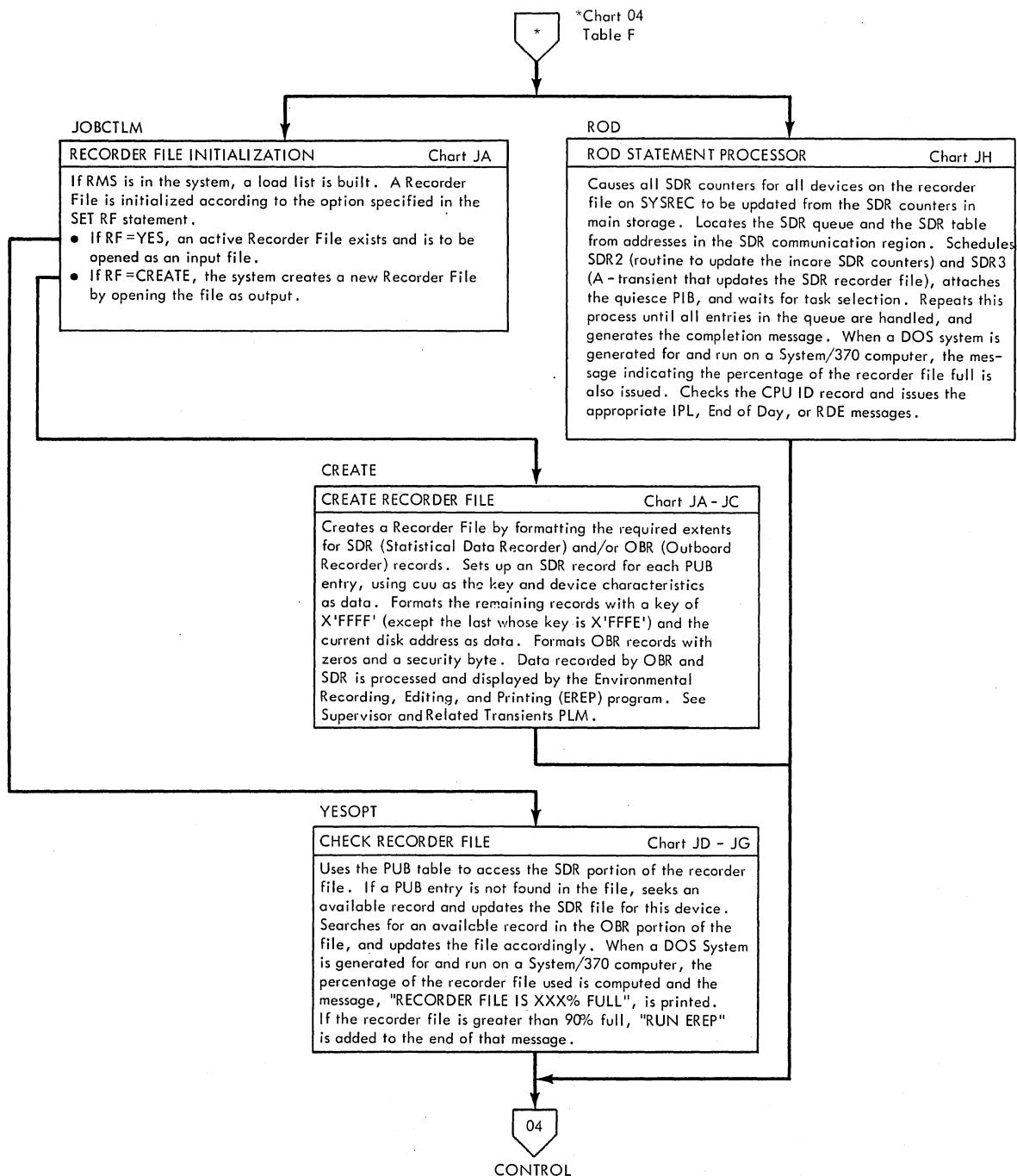
**Chart 13. Job Control (\$JOBCTLK) Statement Processor (Part 1 of 2)**



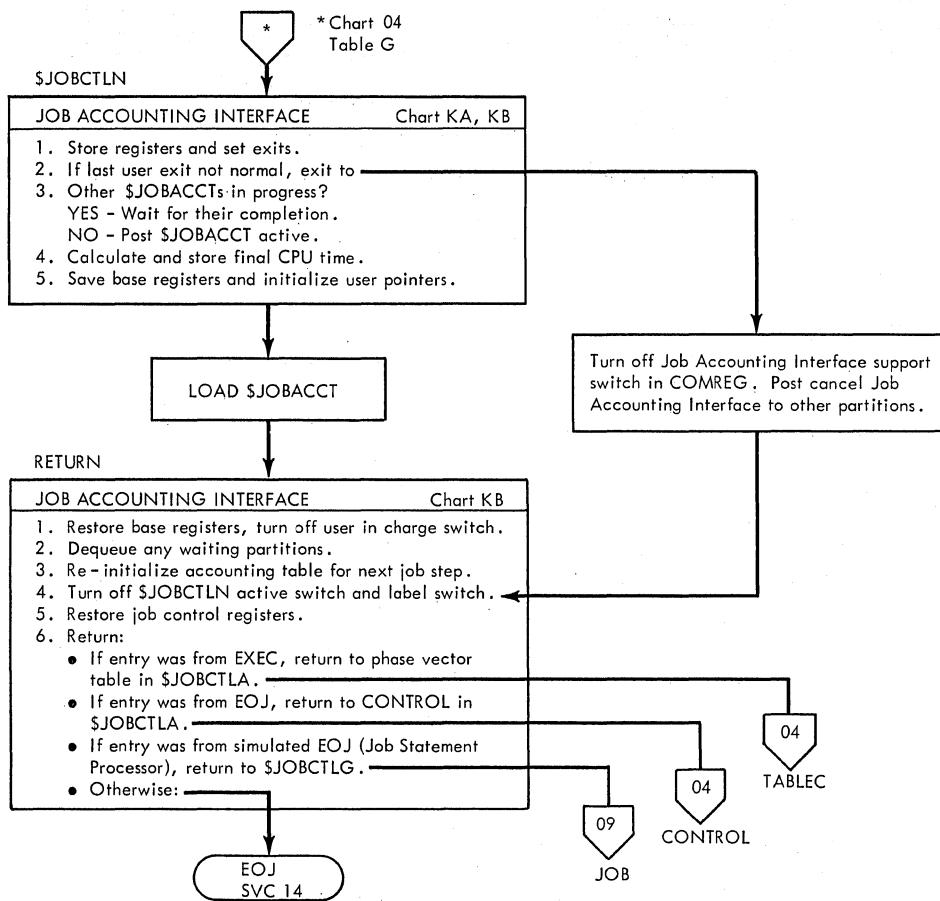
**Chart 14. Job Control (\$JOBCTLK) Statement Processor (Part 2 of 2)**



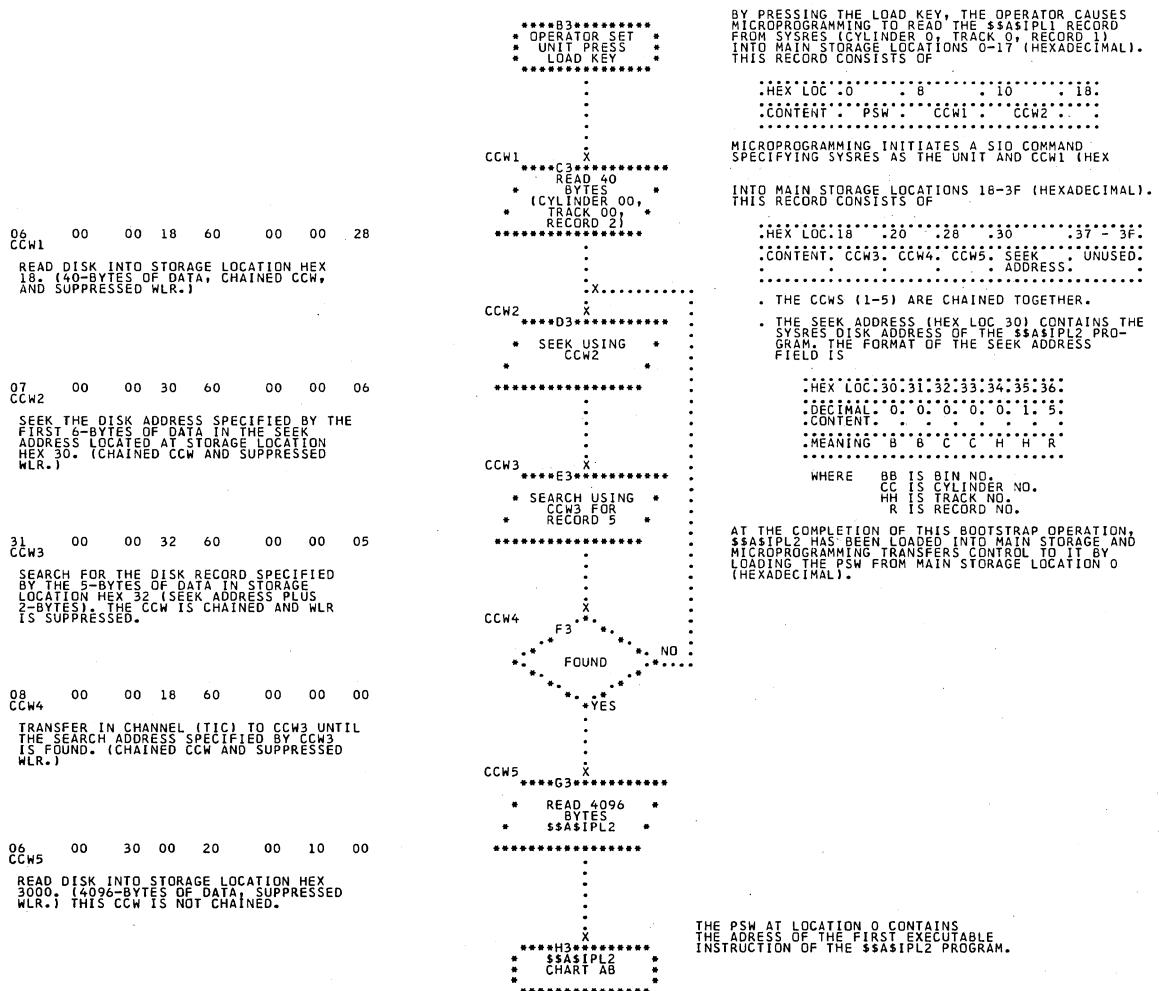
**Chart 15. Job Control (\$JOBCTLM) Statement Processor**



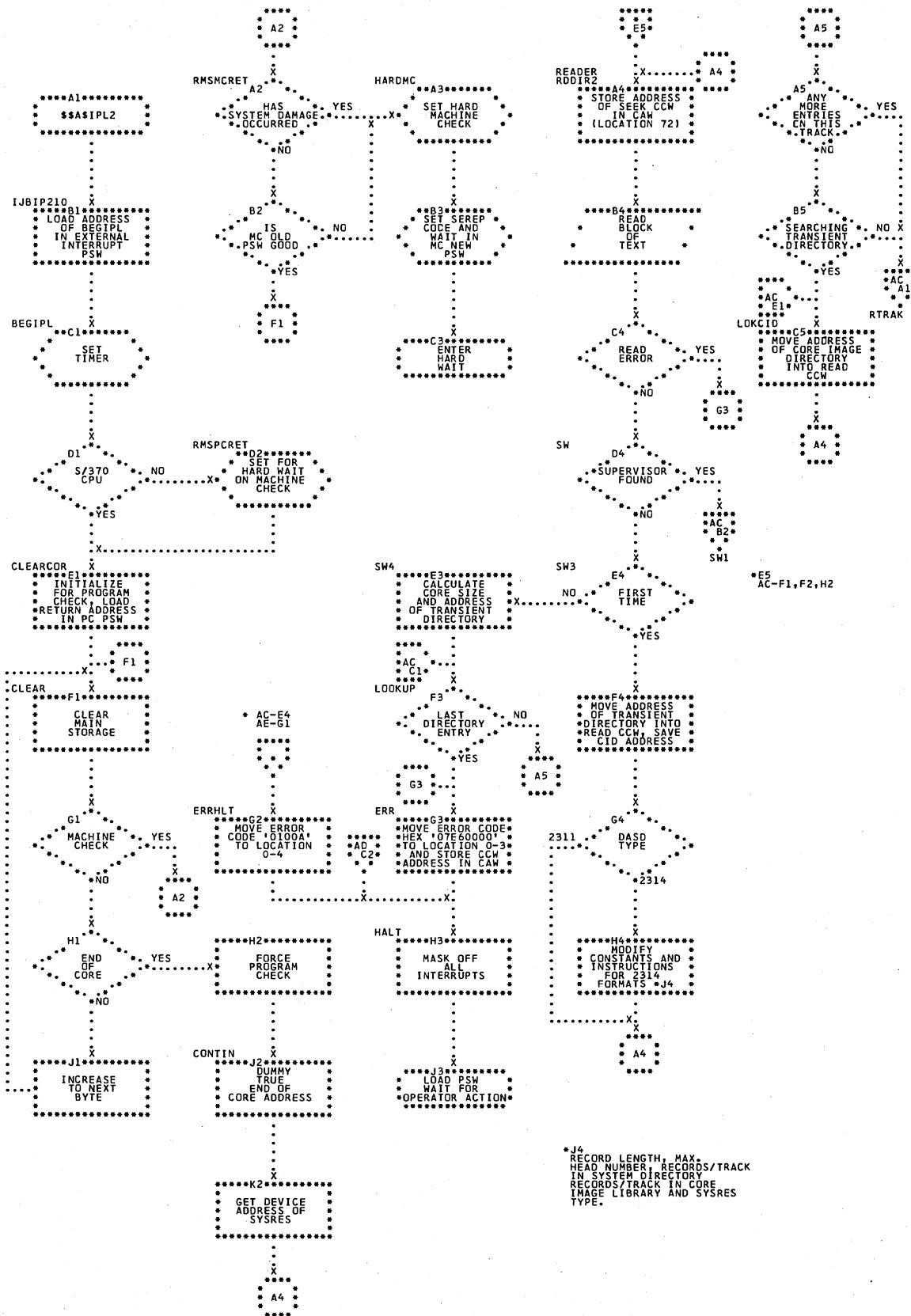
**Chart 16. Job Control (\$JOBCTLN) Statement Processor**



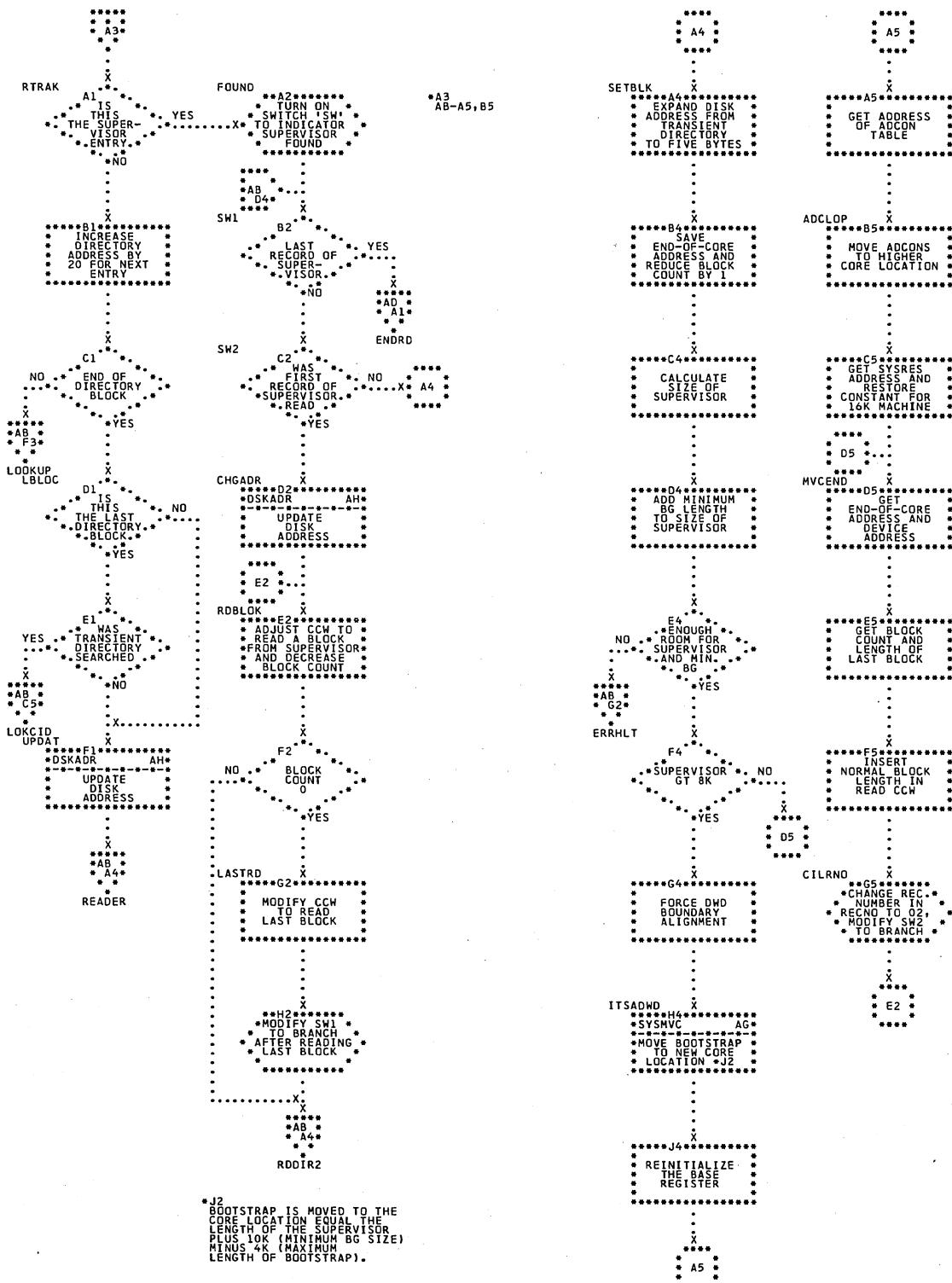
**Chart AA. \$\$A\$IPL1 - IPL Bootstrap**  
Refer to Chart 01.



**Chart AB. §§A\$ IPL2 - Clear Storage and Load Supervisor (Part 1 of 2)**  
Refer to Chart 01.



**Chart AC.    \$SA\$IPL2 - Load Supervisor (Part 2 of 2)**  
Refer to Chart 01.



**Chart AD. \$\$A\$ IPL2 - Build Two-Device System**  
 Refer to Chart 01.

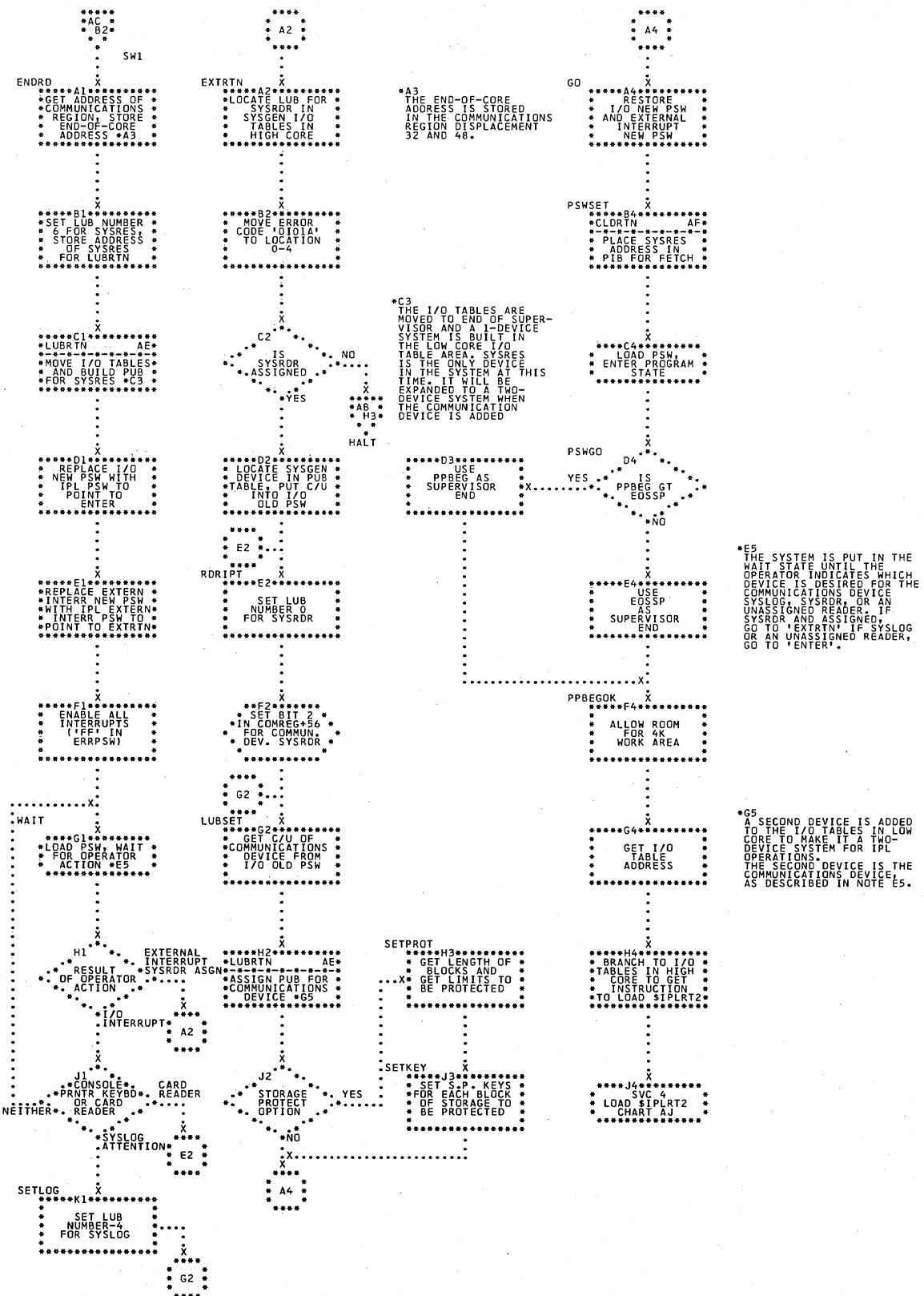
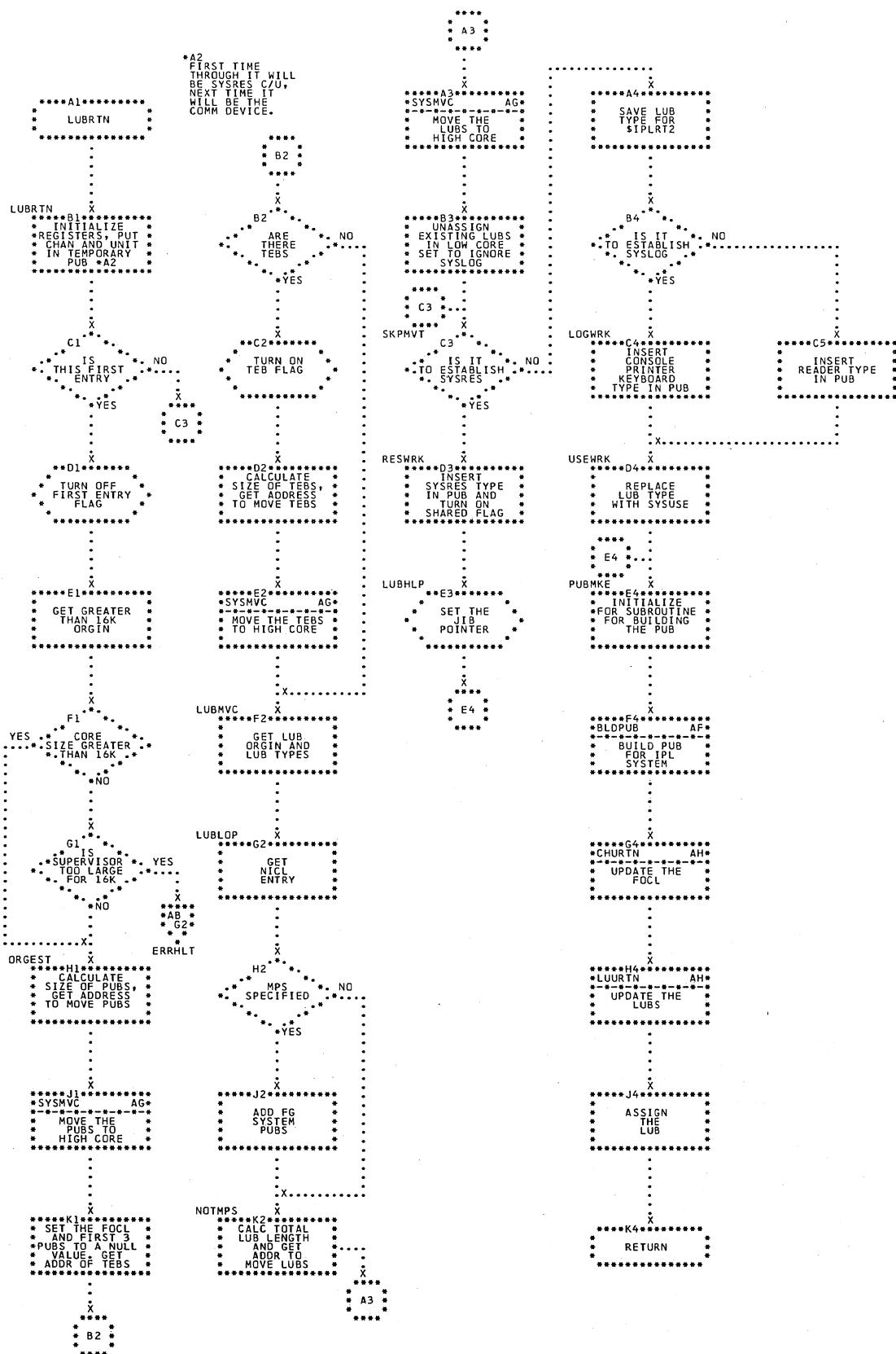


Chart AE. \$\$A\$IPL2 - Move I/O Tables  
Refer to Chart 01.



**Chart AF. \$\$A\$IPL2 - Build PUB Table  
Refer to Chart 01.**

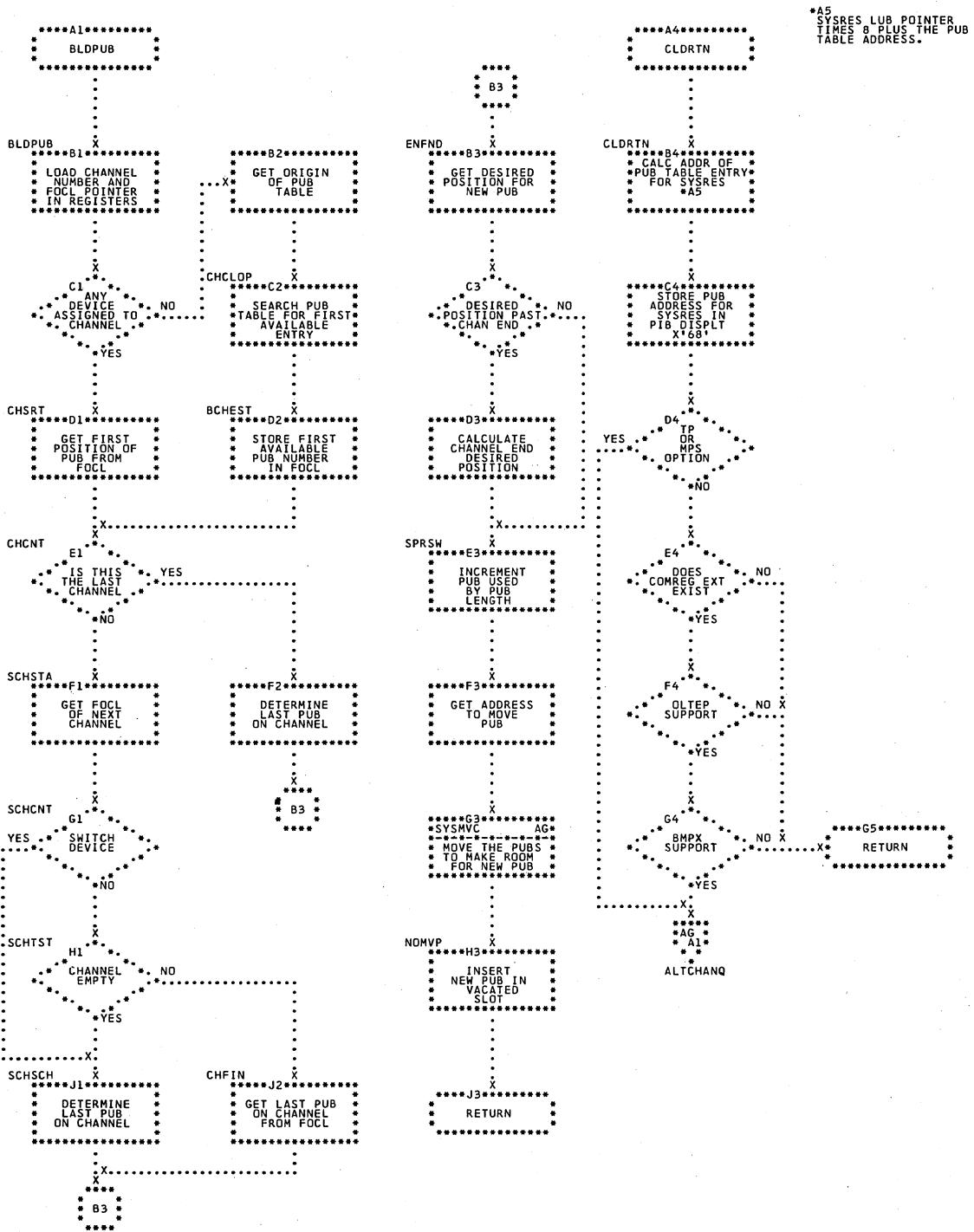
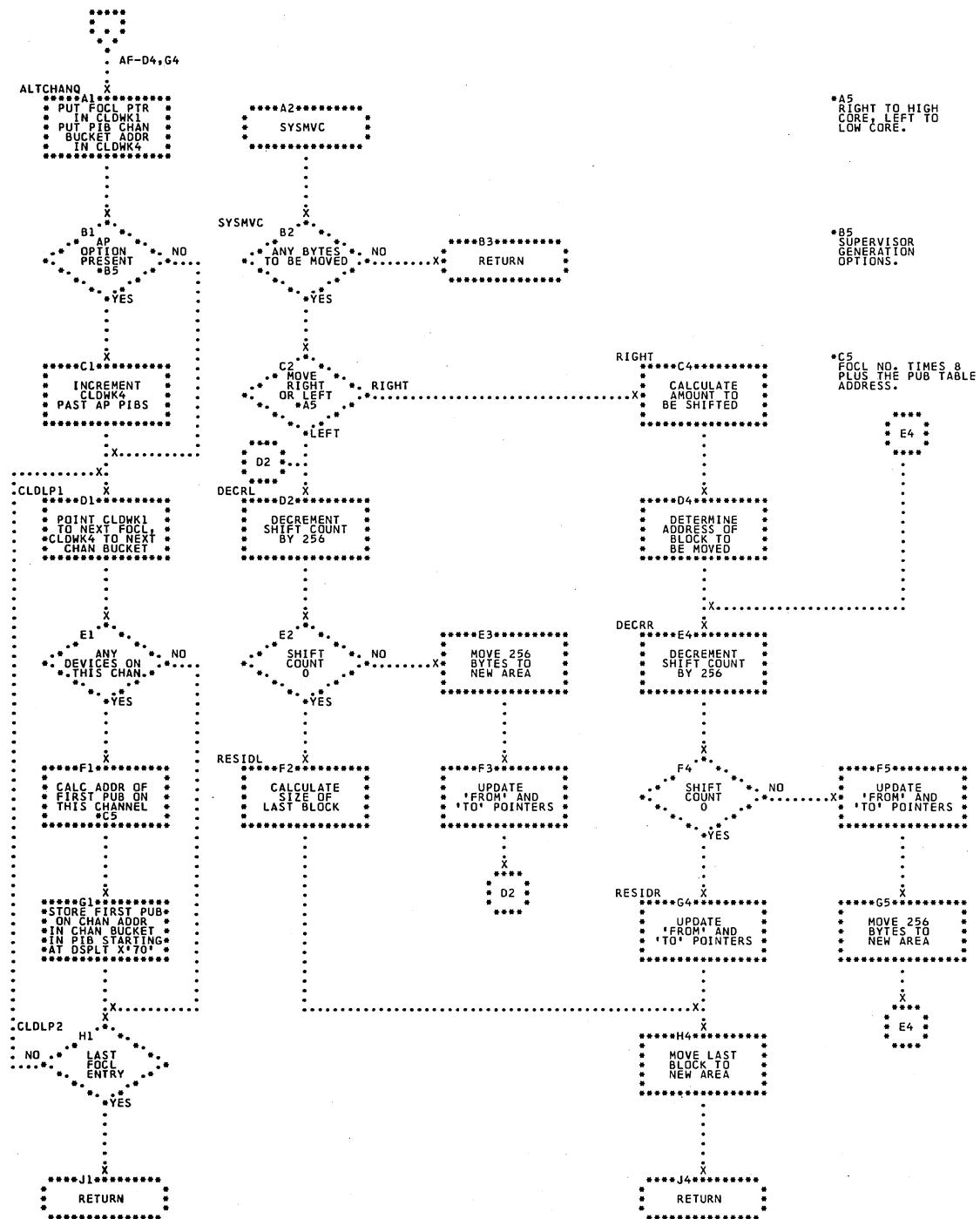


Chart AG. \$\$A\$IPL2 - Common Move Subroutine  
Refer to Chart 01.



**Chart AH. \$\$A\$IPL2 - Update Subroutines**  
 Refer to Chart 01.

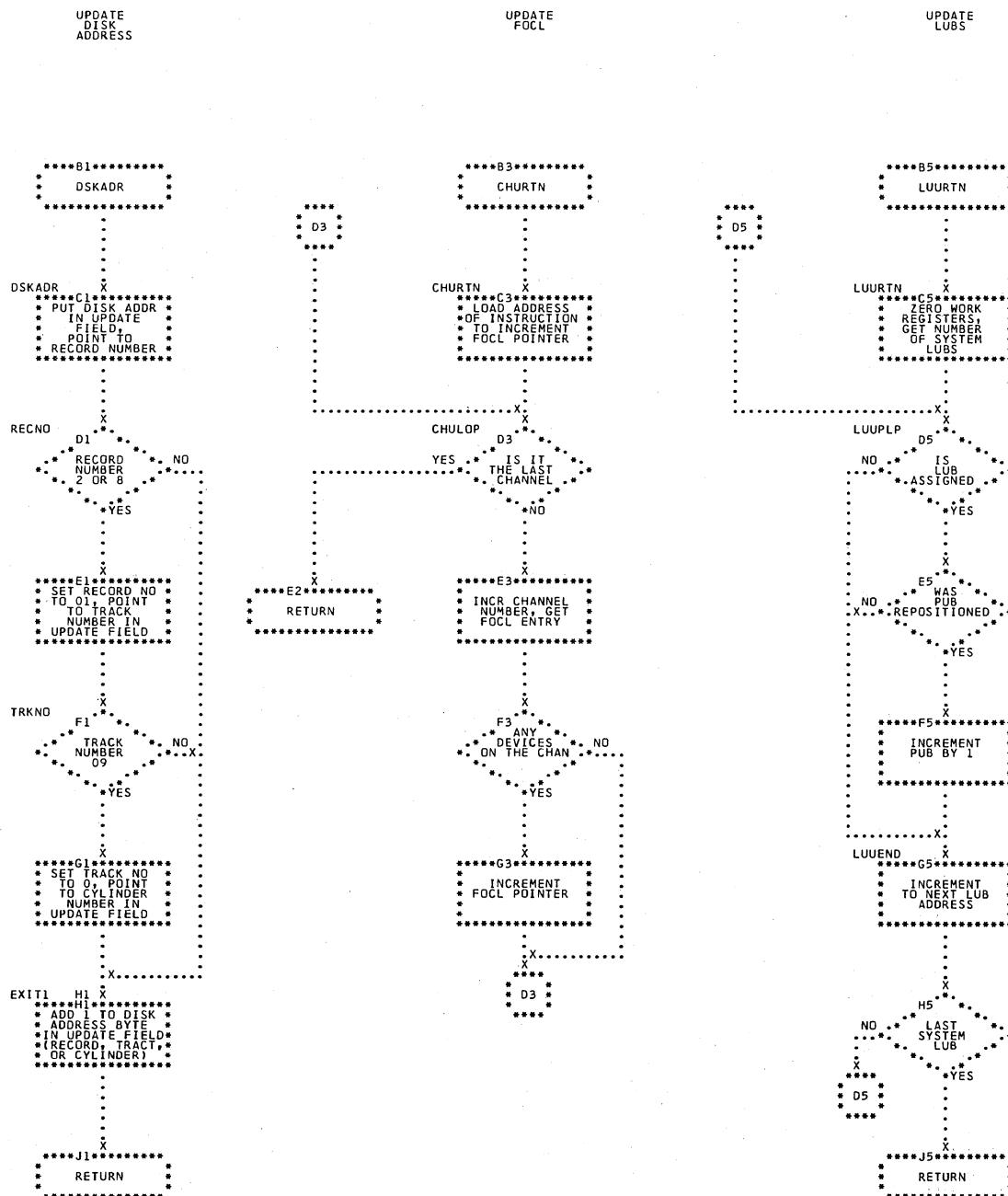


Chart AJ. \$IPLRT2 - Initialization Routine  
Refer to Chart 02.

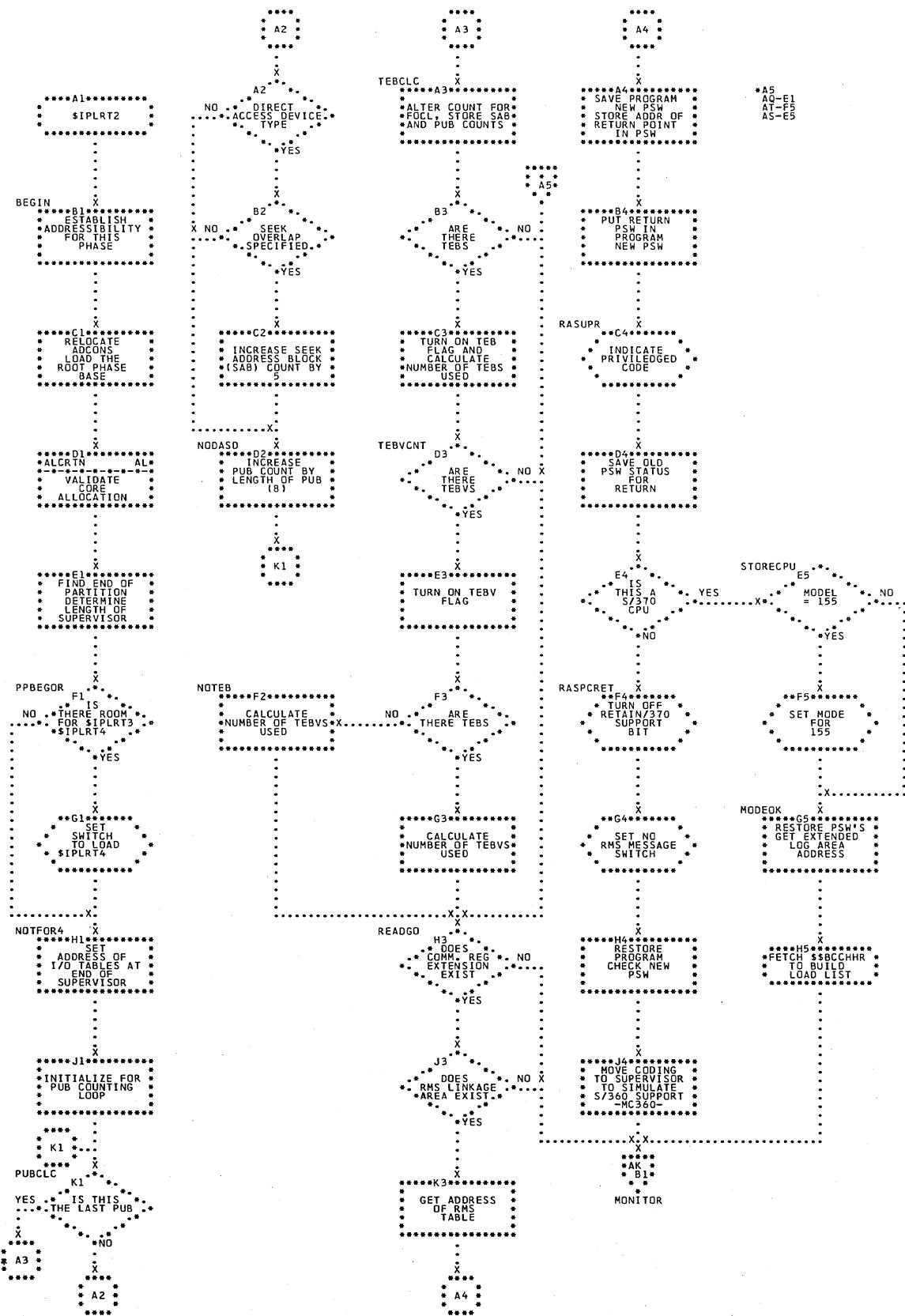


Chart AK. \$IPLRT2 - Monitor, Read Control Card, and Operation Scan Routines  
Refer to Chart 02.

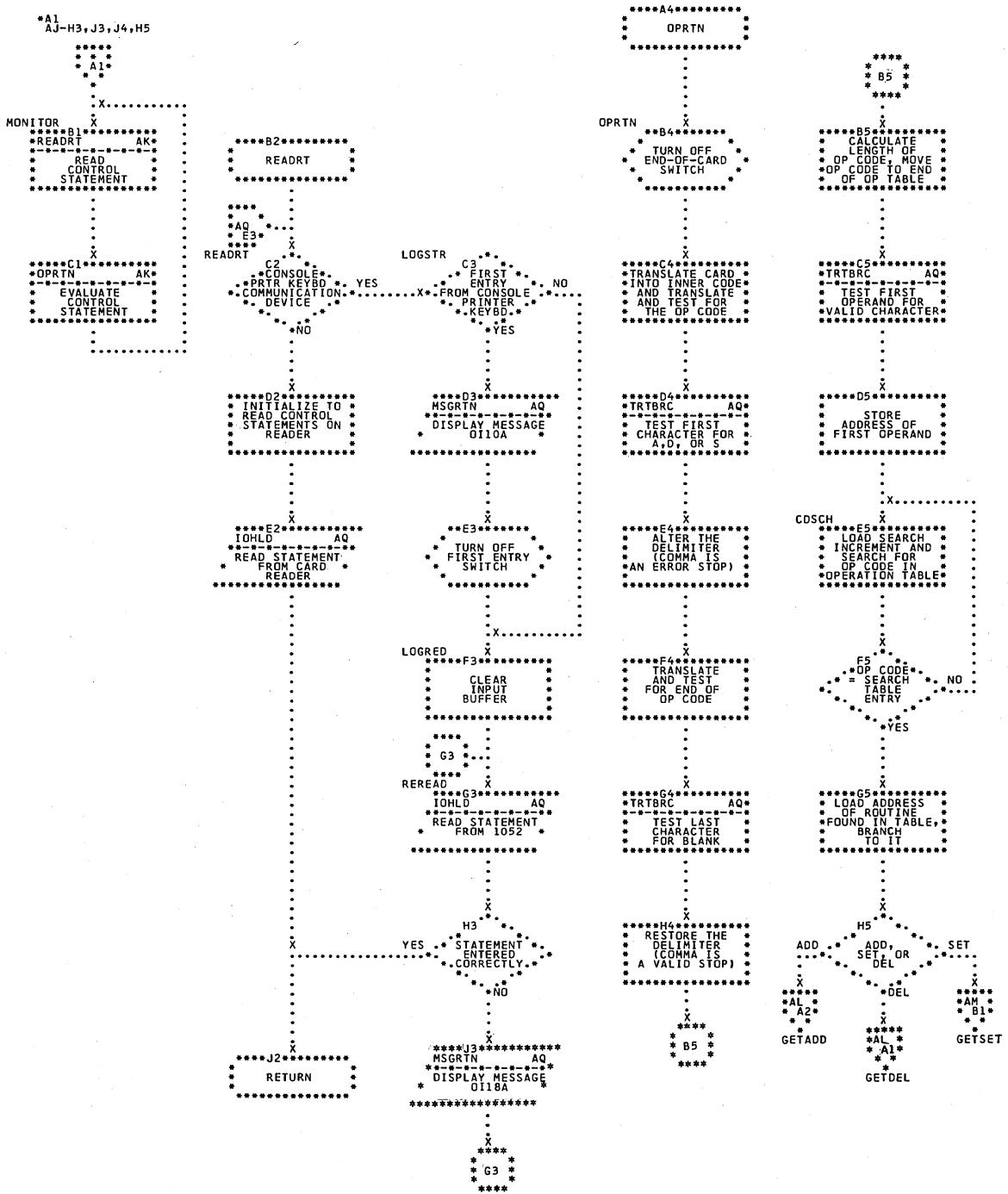
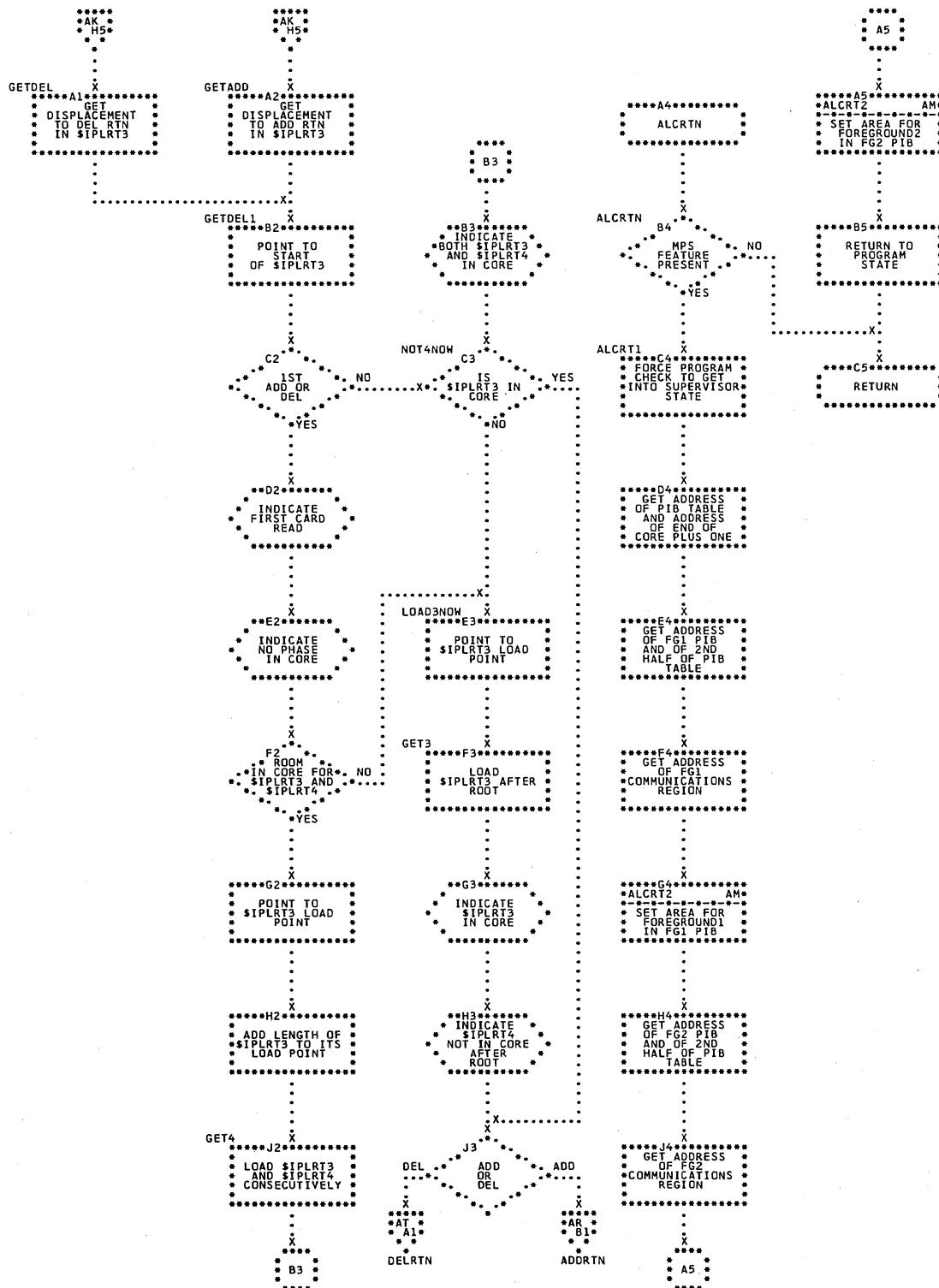


Chart AL. \$IPLRT2 - Monitor Core Usage for ADD and DEL Cards and Allocation Subroutine  
Refer to Chart 02.



**Chart AM. \$IPLRT2 - Monitor Core Usage for SET Card and Allocation Subroutine**  
 Refer to Chart 02.

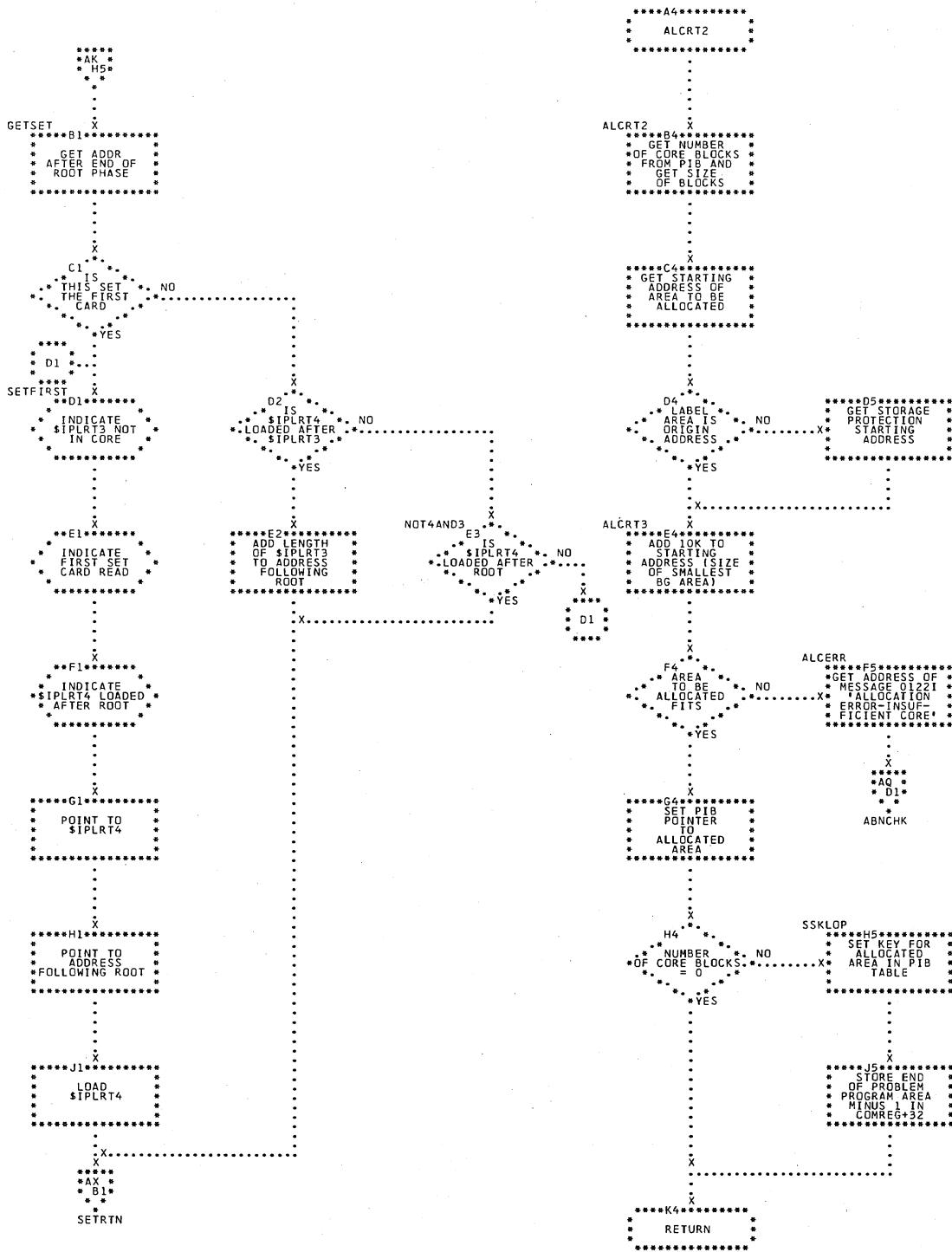
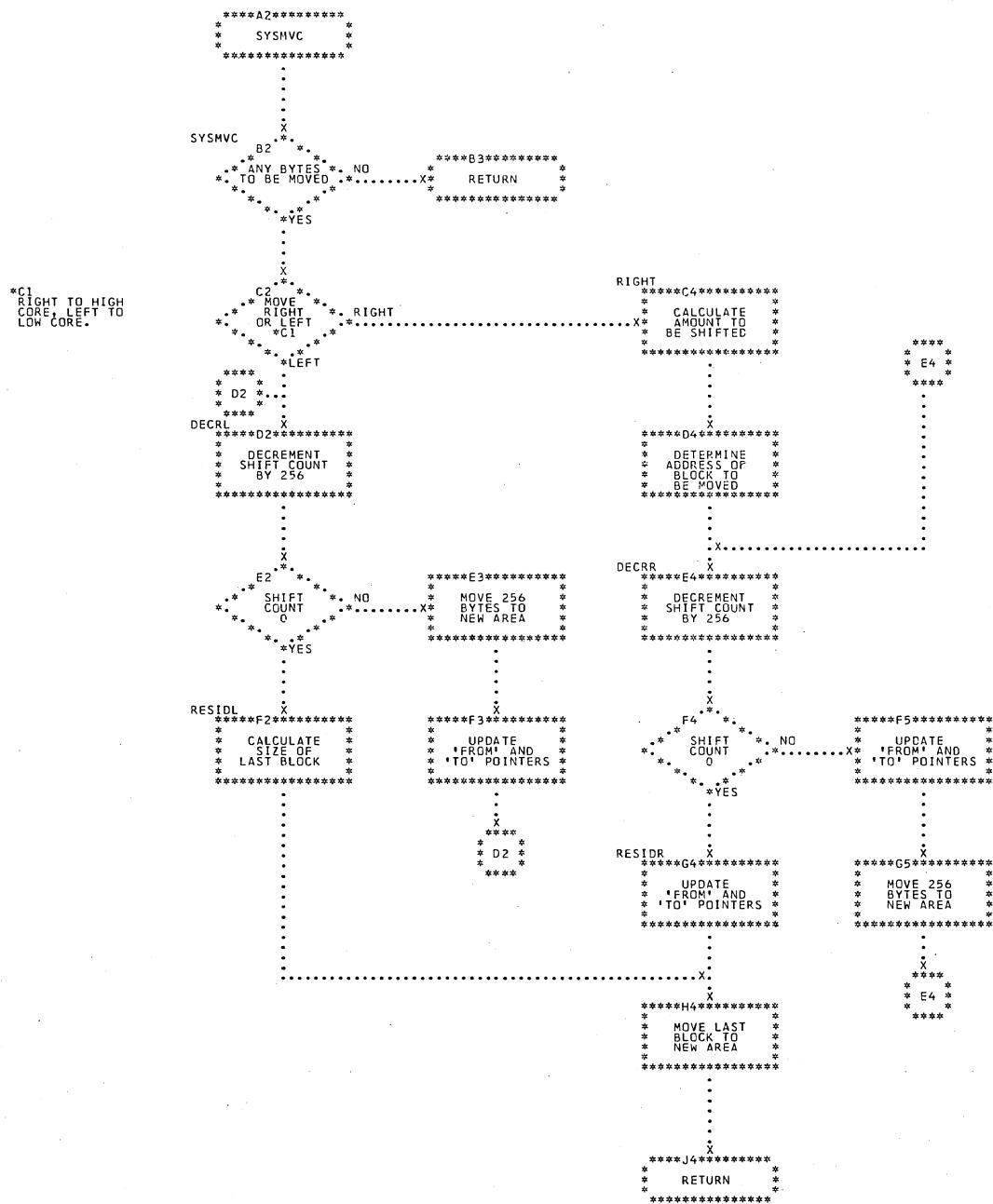


Chart AN. \$IPLRT2 - Move Routine  
Refer to Chart 02.



**Chart AP. \$IPLRT2 - Update LUB, Get Operand, and Conversion Subroutines**  
 Refer to Chart 02.

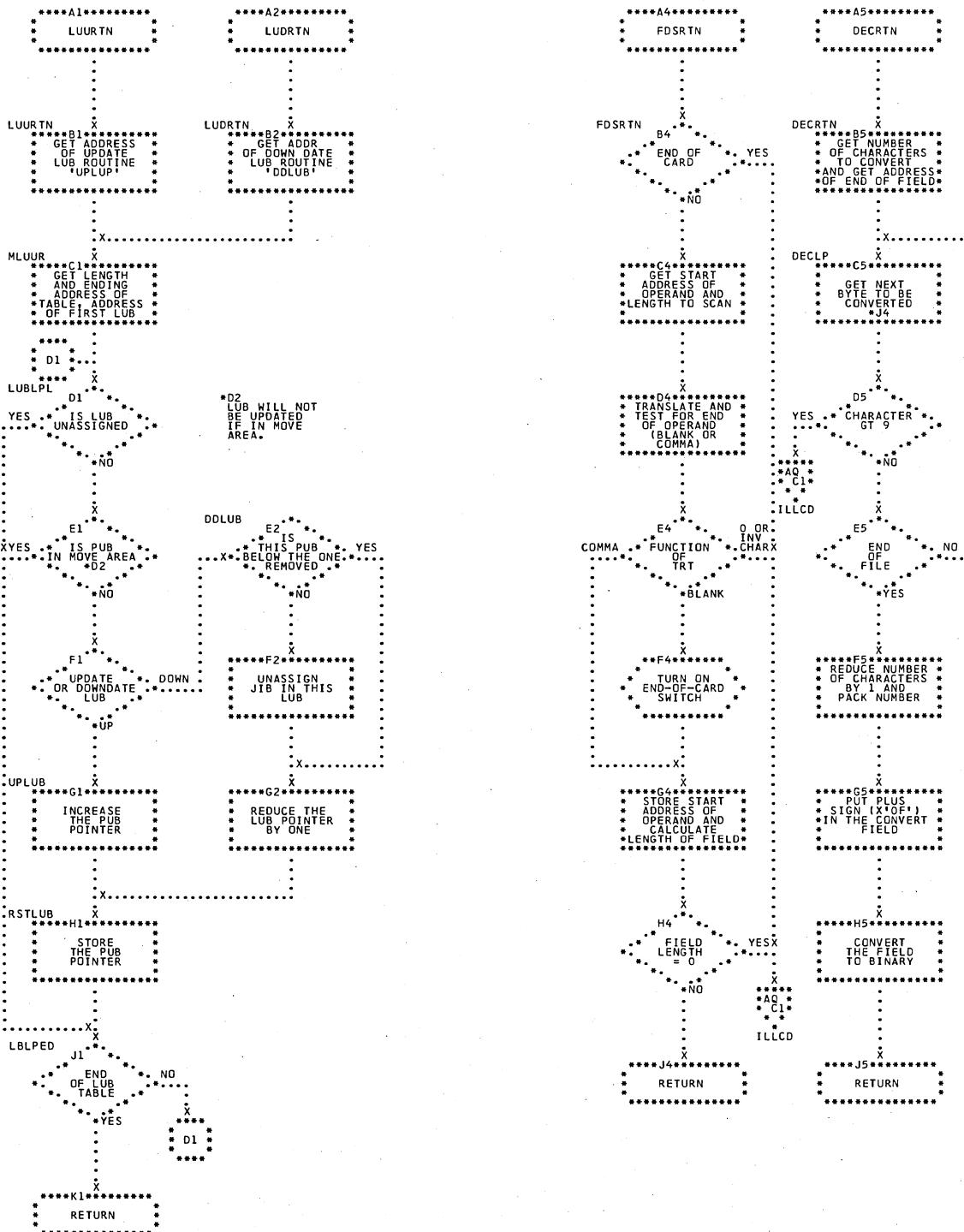
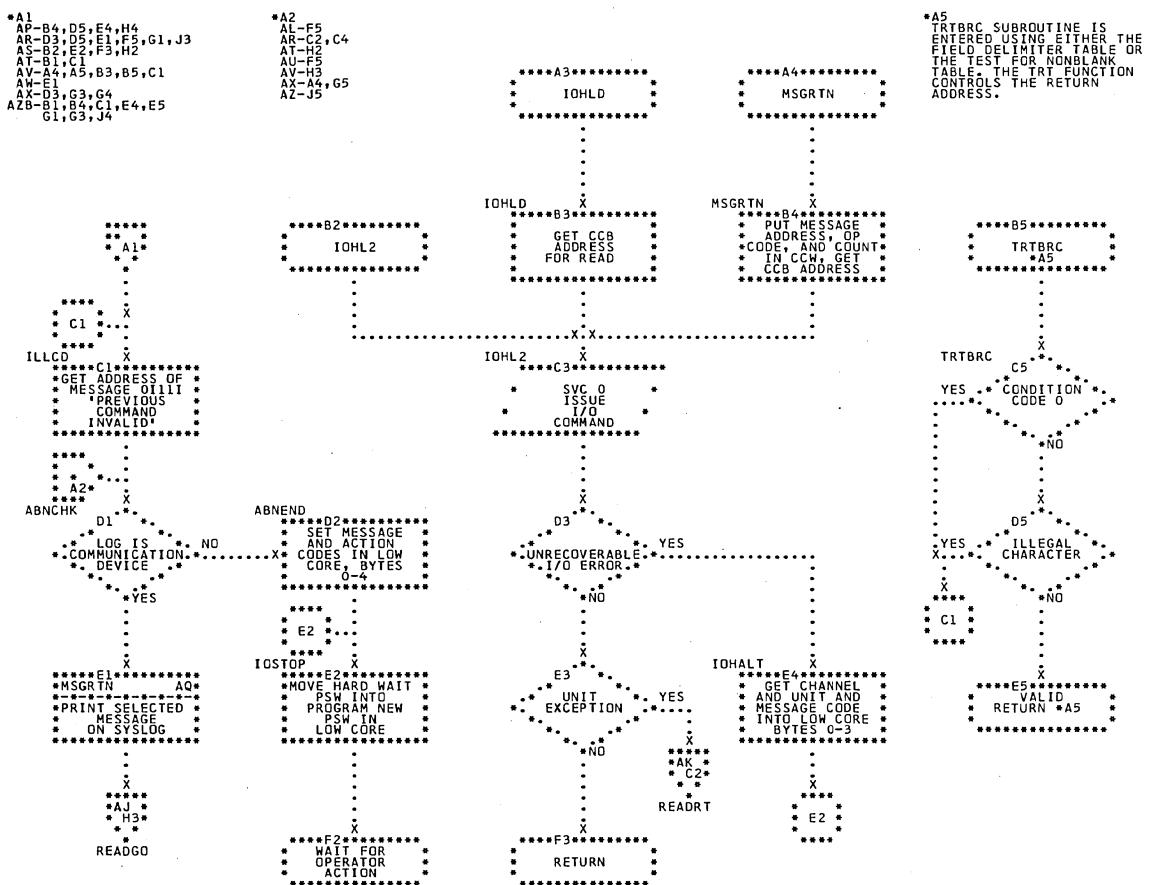


Chart AQ. \$IPLRT2 - I/O Subroutines  
Refer to Chart 02.



**Chart AR. \$IPLRT3 - ADD a Device (Part 1 of 2)**  
**Refer to Chart 03.**

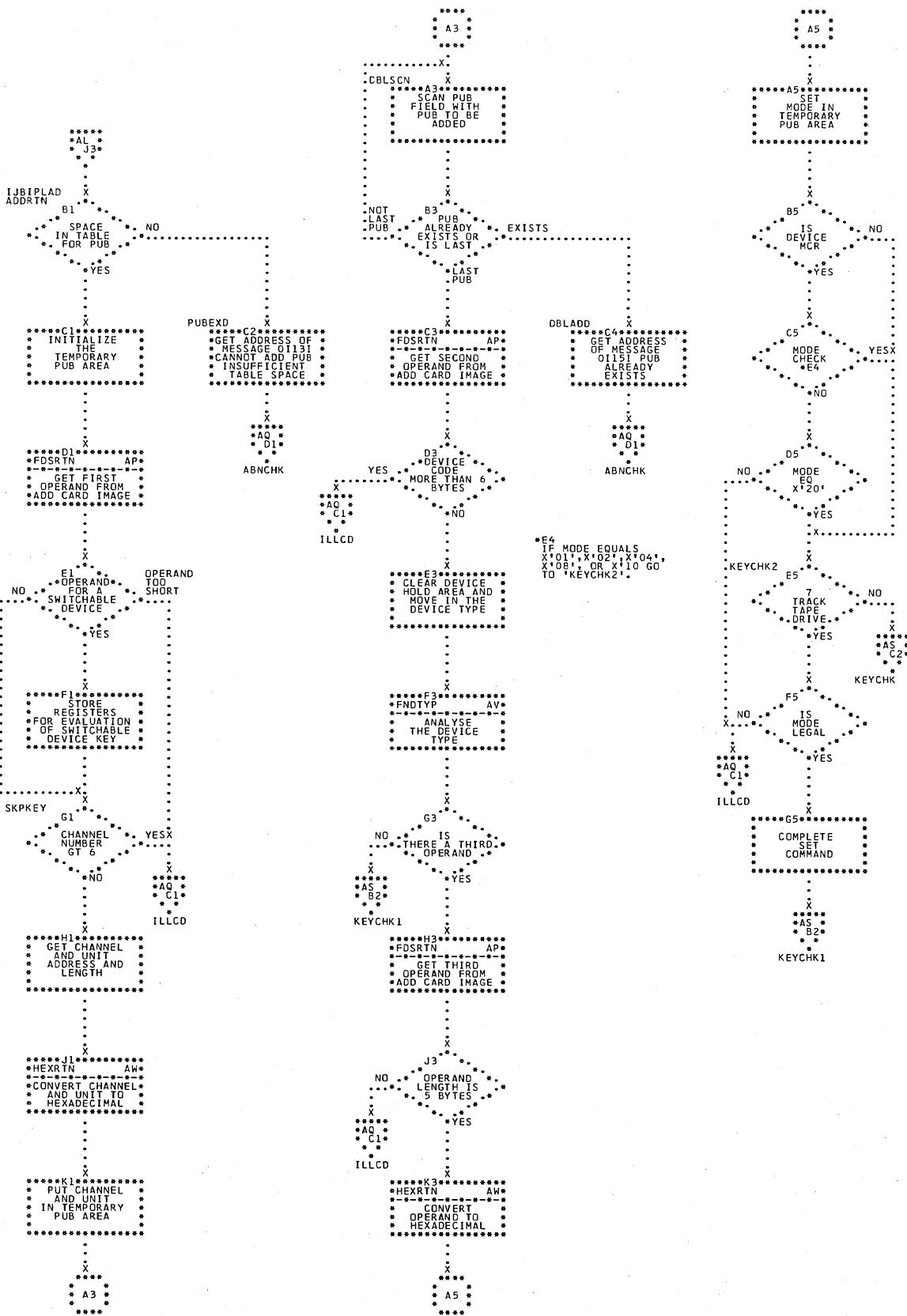
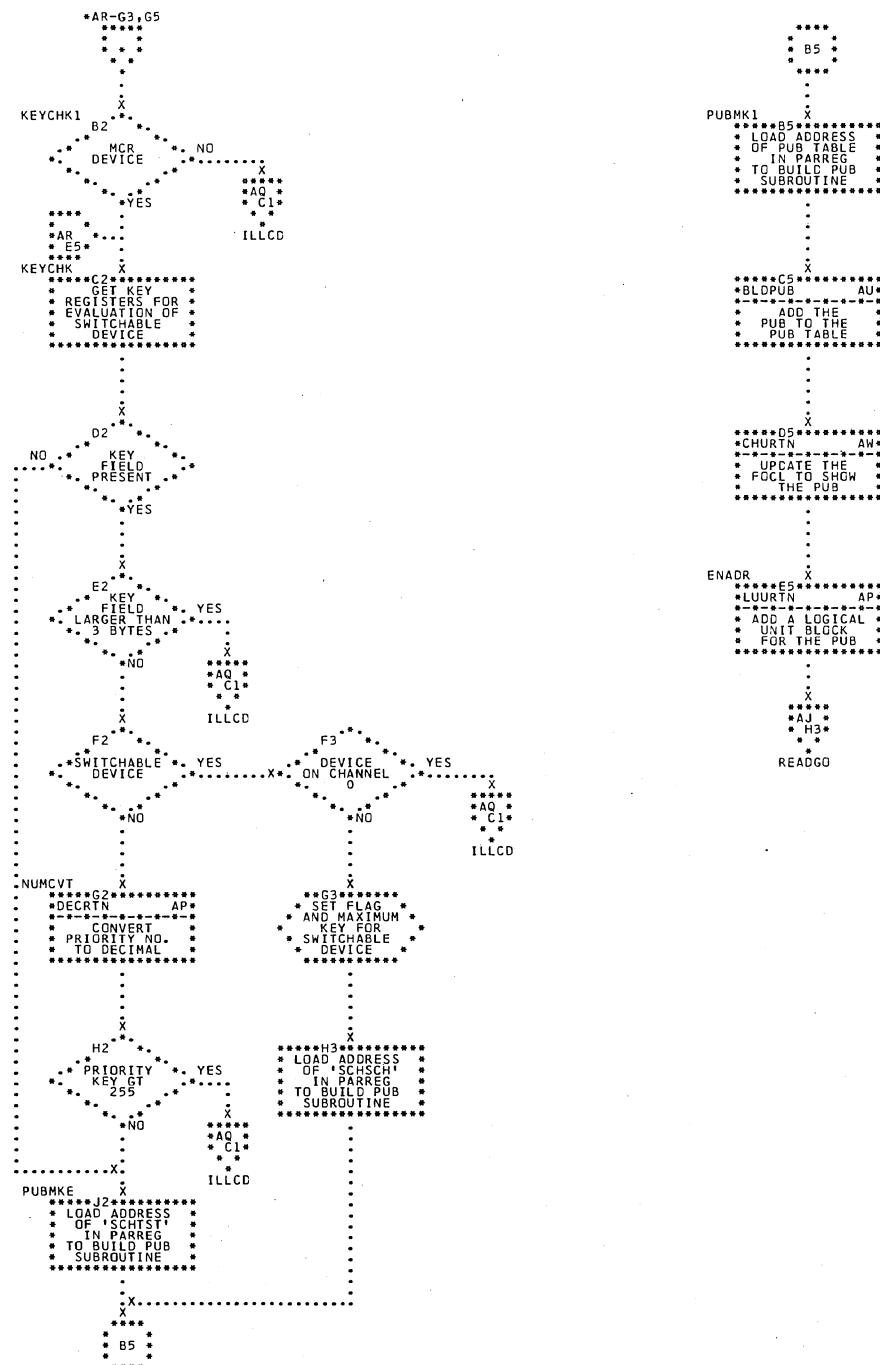


Chart AS. \$IPLRT3 - ADD a Device (Part 2 of 2)  
Refer to Chart 03.



**Chart AT. \$IPLRT3 - Delete a PUB**  
Refer to Chart 03.

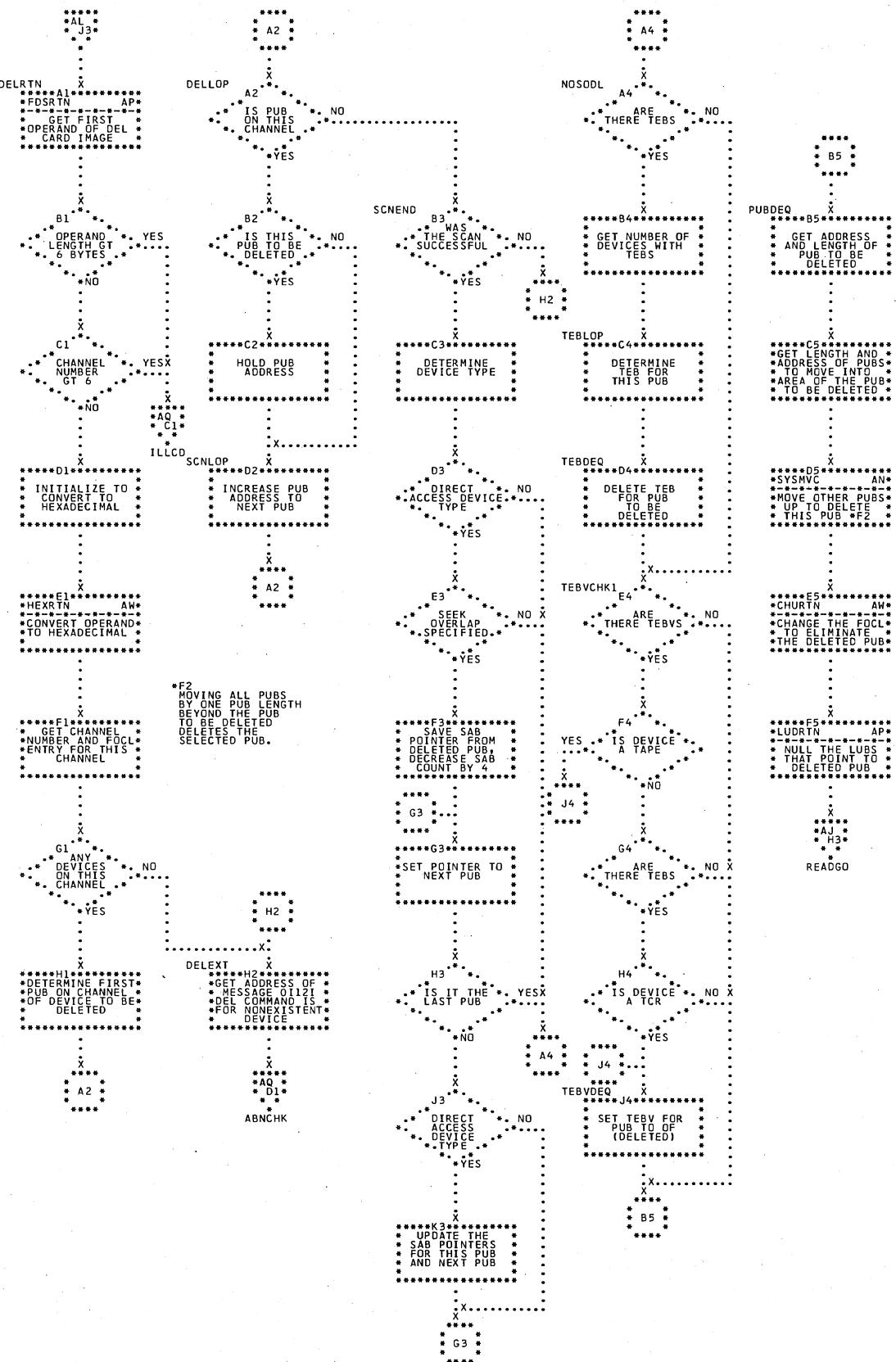
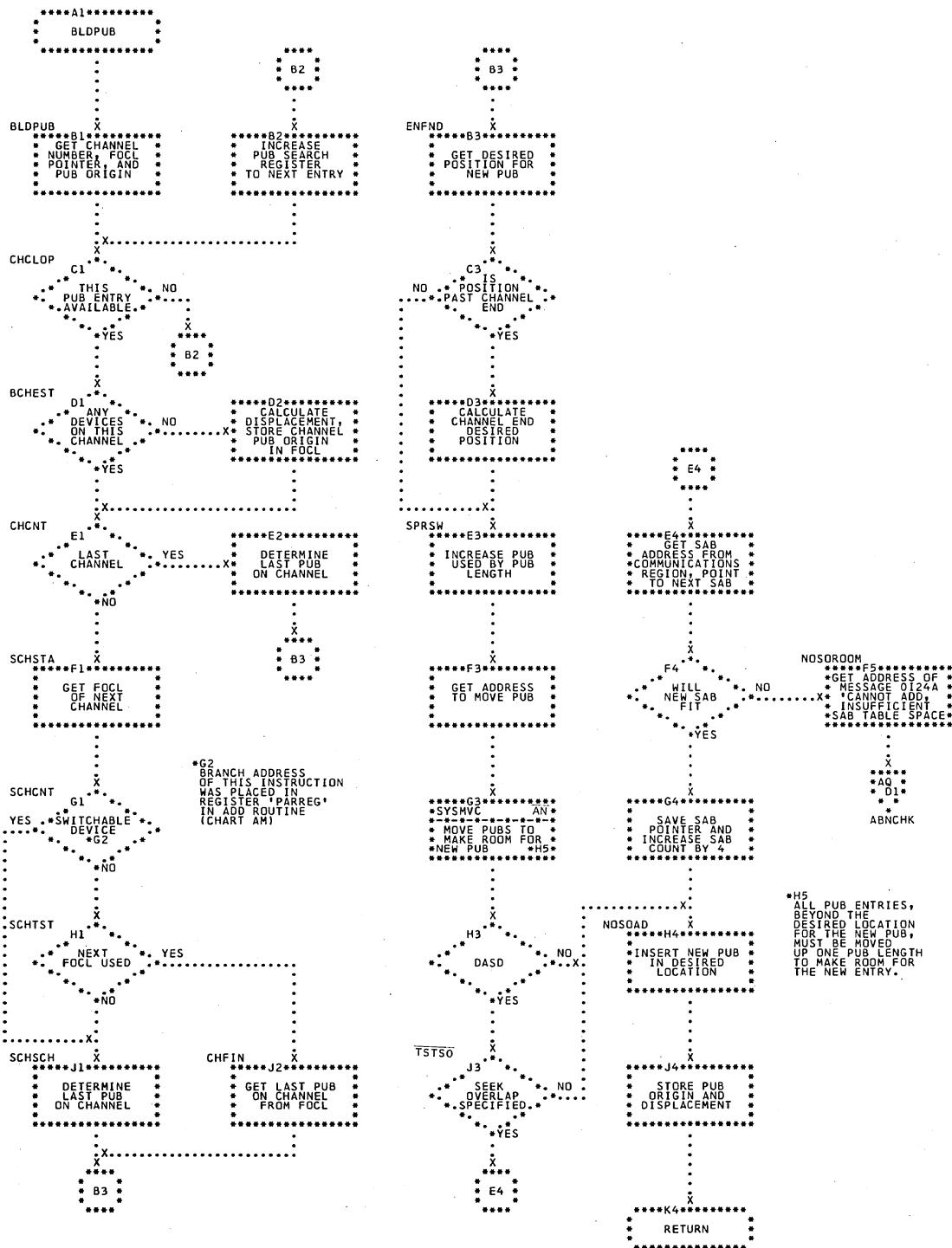


Chart AU. \$IPLRT3 - Build PUB Table Subroutine  
Refer to Chart 03.



**Chart AV. \$IPLRT3 - Device Type Conversion Subroutine**  
Refer to Chart 03.

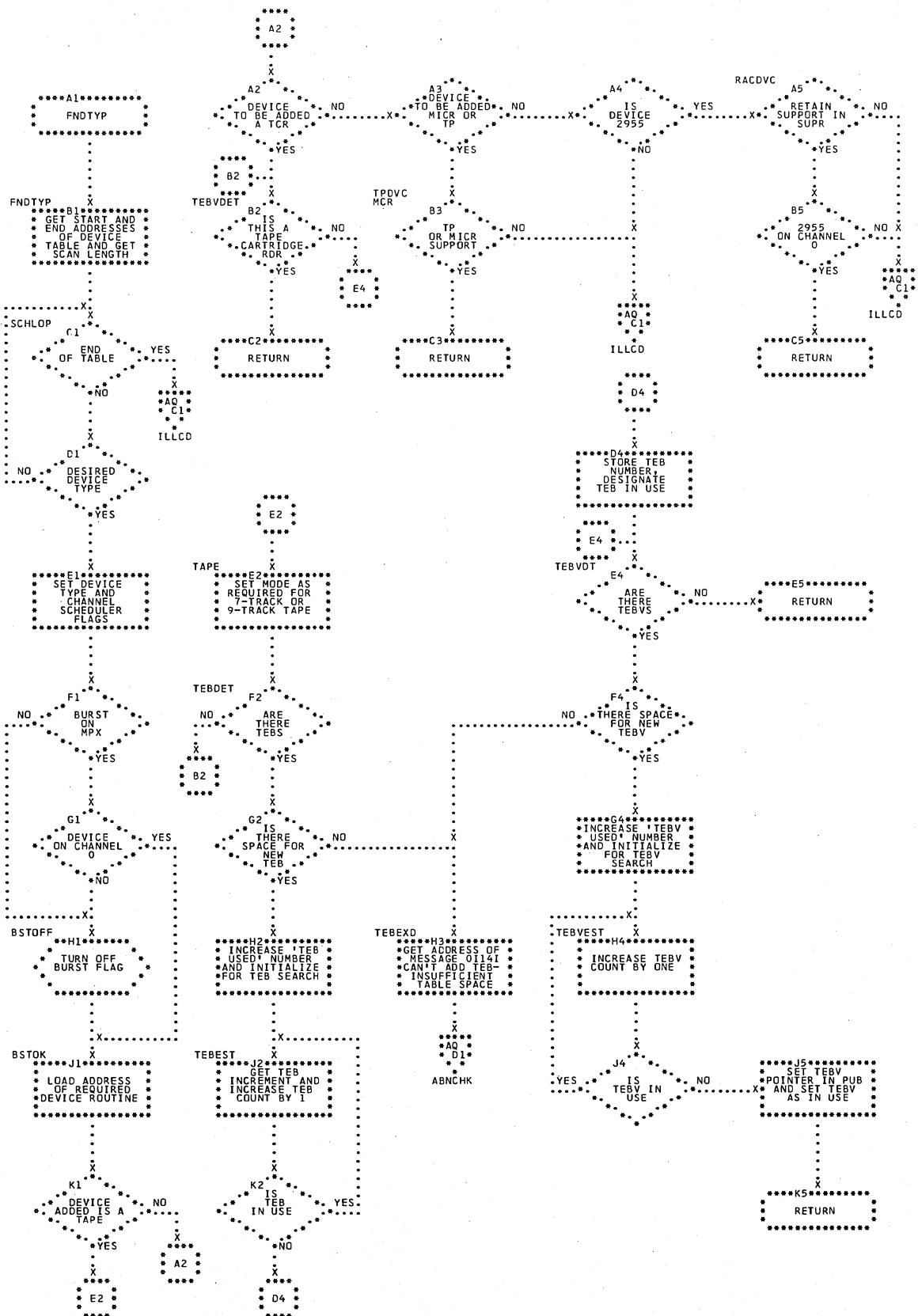


Chart AW. \$IPLRT3 - Conversion and Update FOCL Subroutines  
Refer to Chart 03.

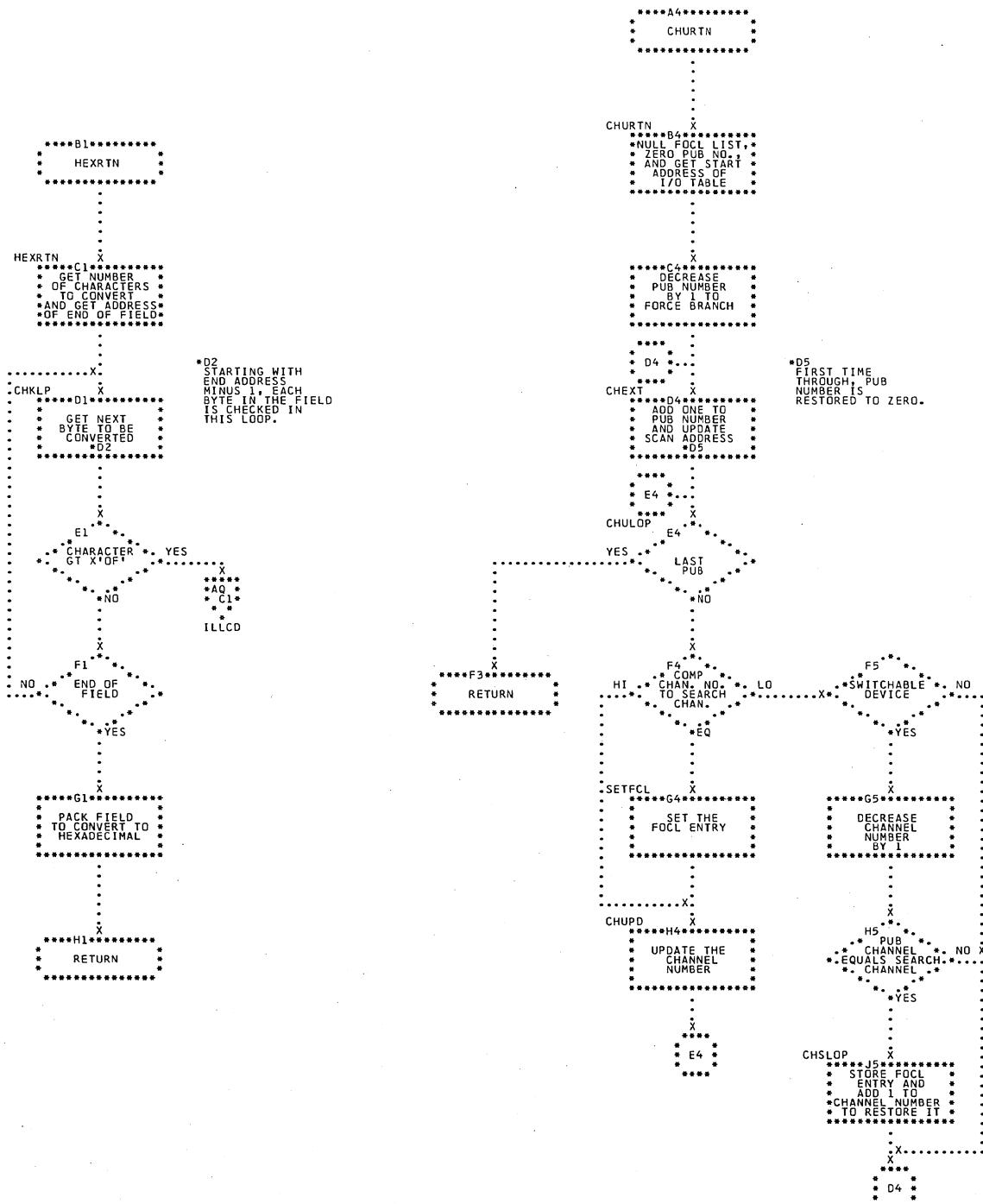


Chart AX. \$IPLRT4 - SET Statement Processor and Assign SYSLOG  
Refer to Chart 03.

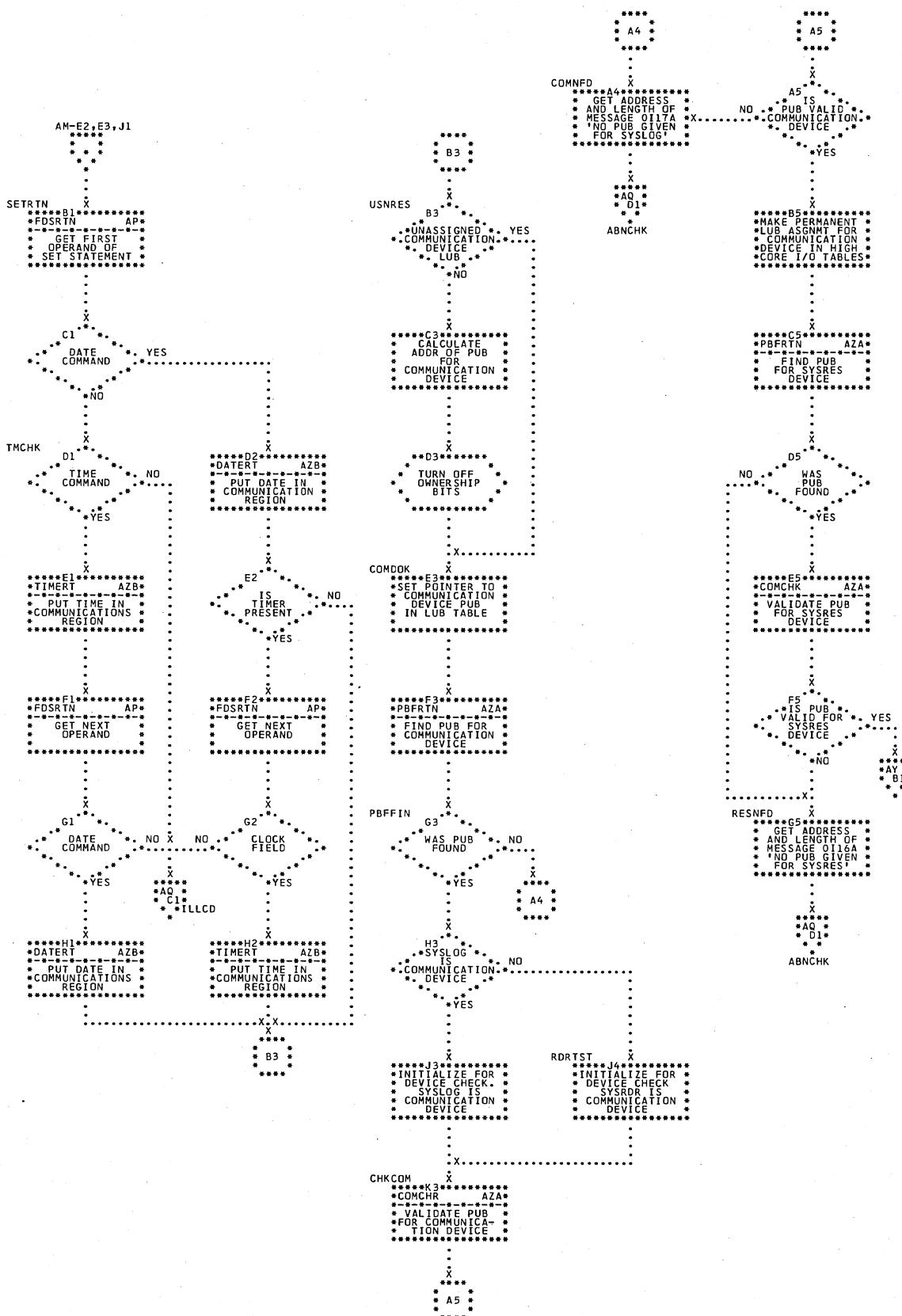


Chart AY. \$IPLRT4 - Assign SYSRES and Move I/O Tables  
Refer to Chart 03.

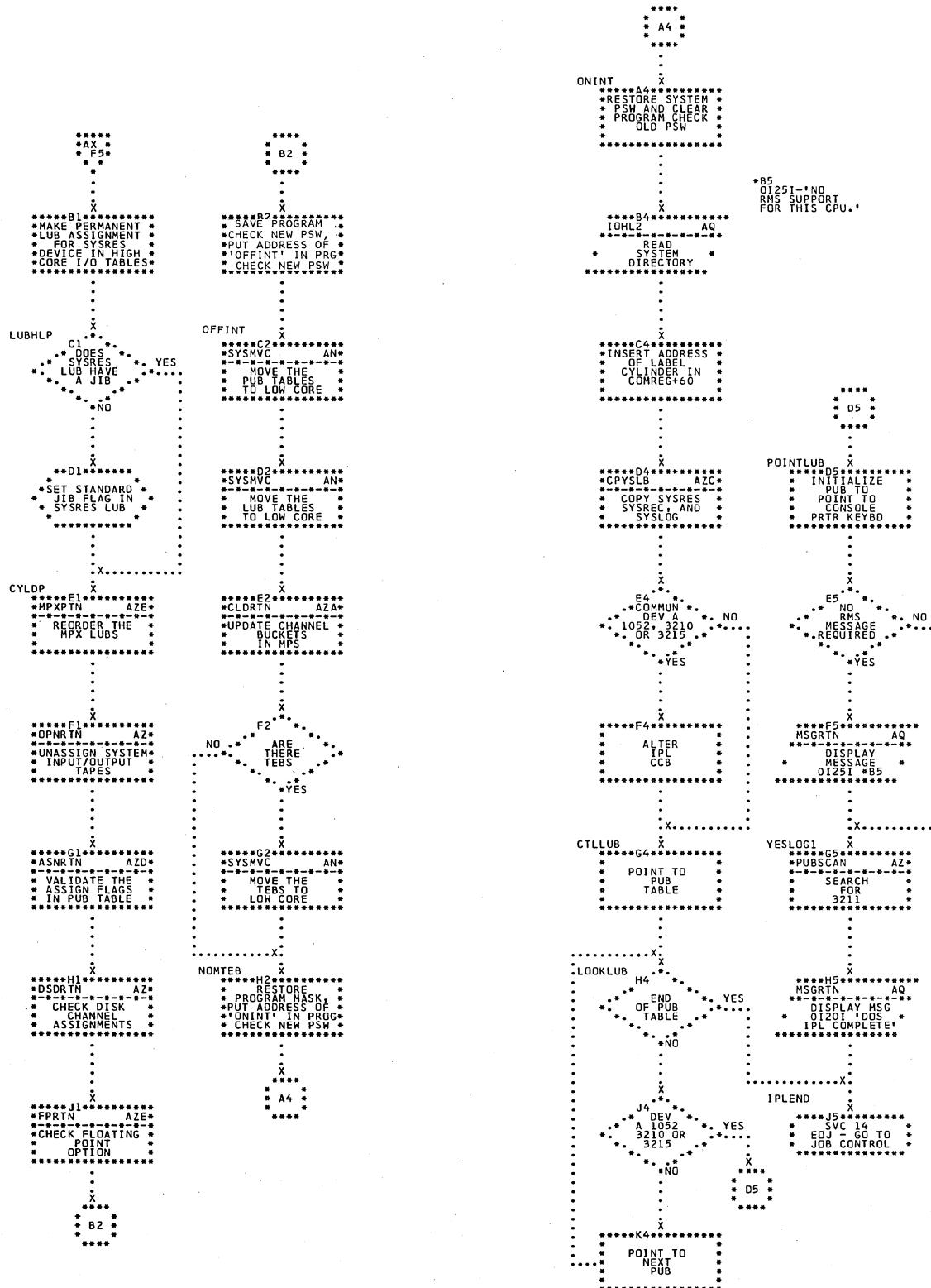


Chart AZ. \$IPLRT4 - I/O and Check Device Type Subroutines  
Refer to Chart 03.

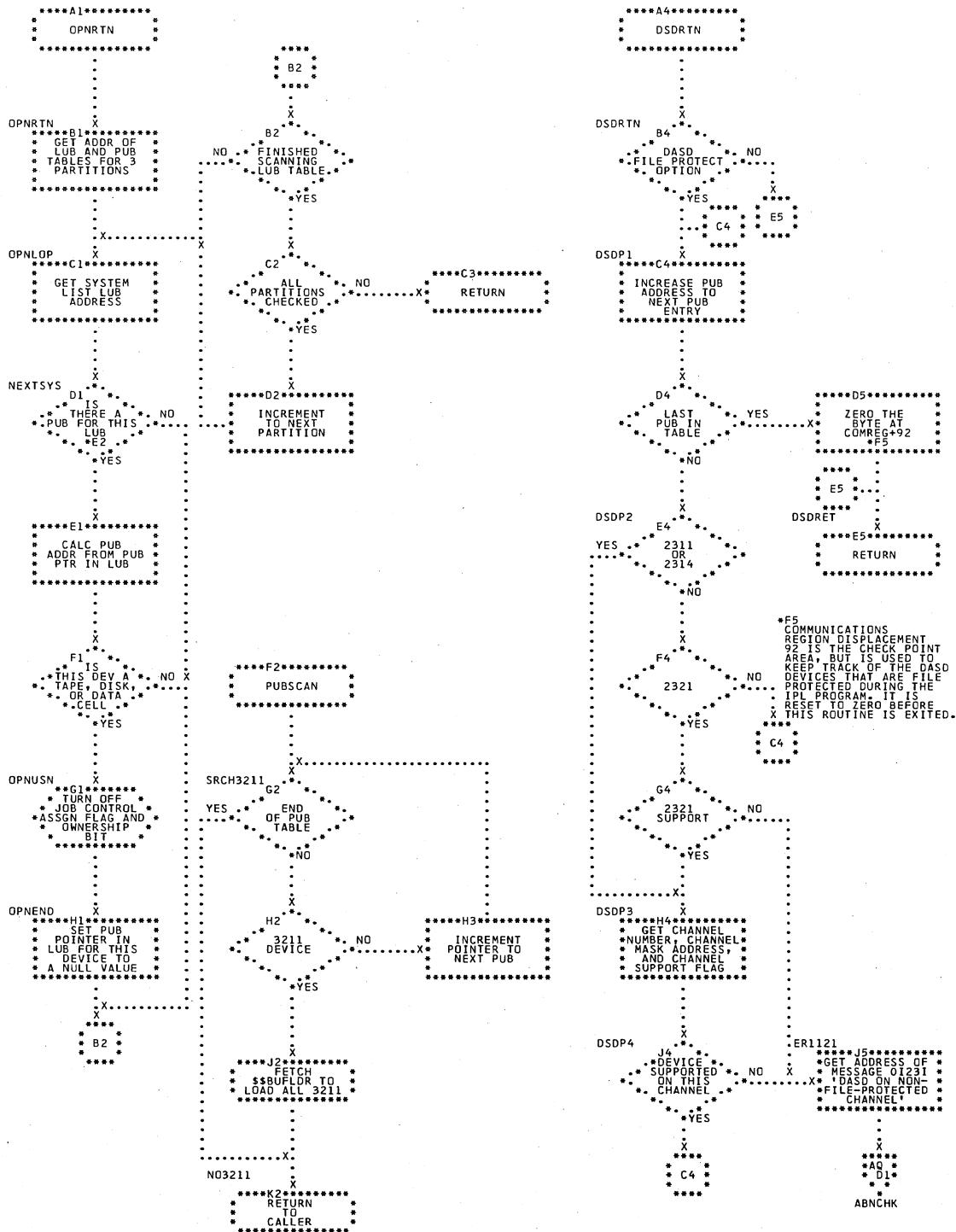


Chart AZA. \$IPLRT4 - Find PUB and I/O Subroutines  
Refer to Chart 03.

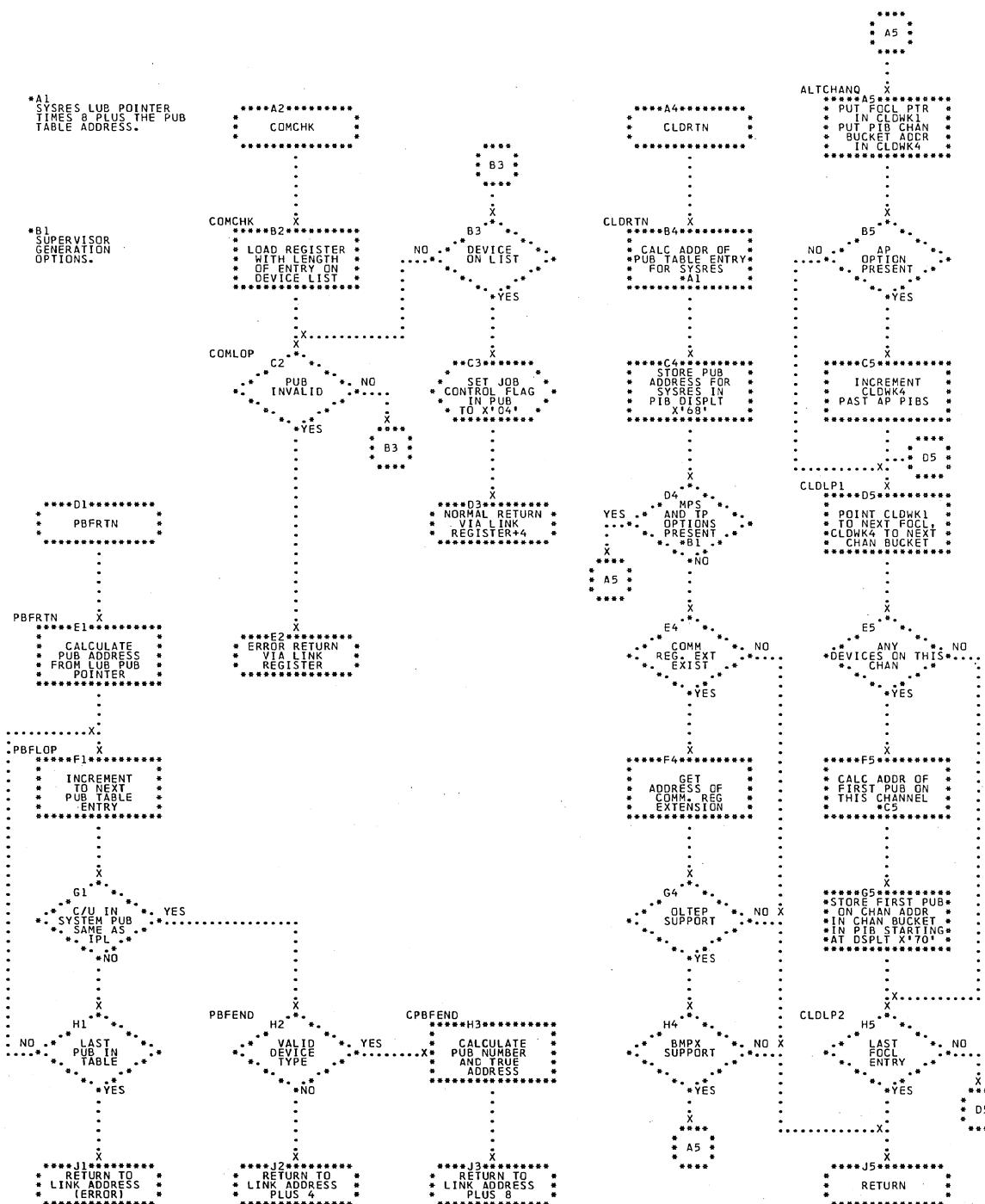
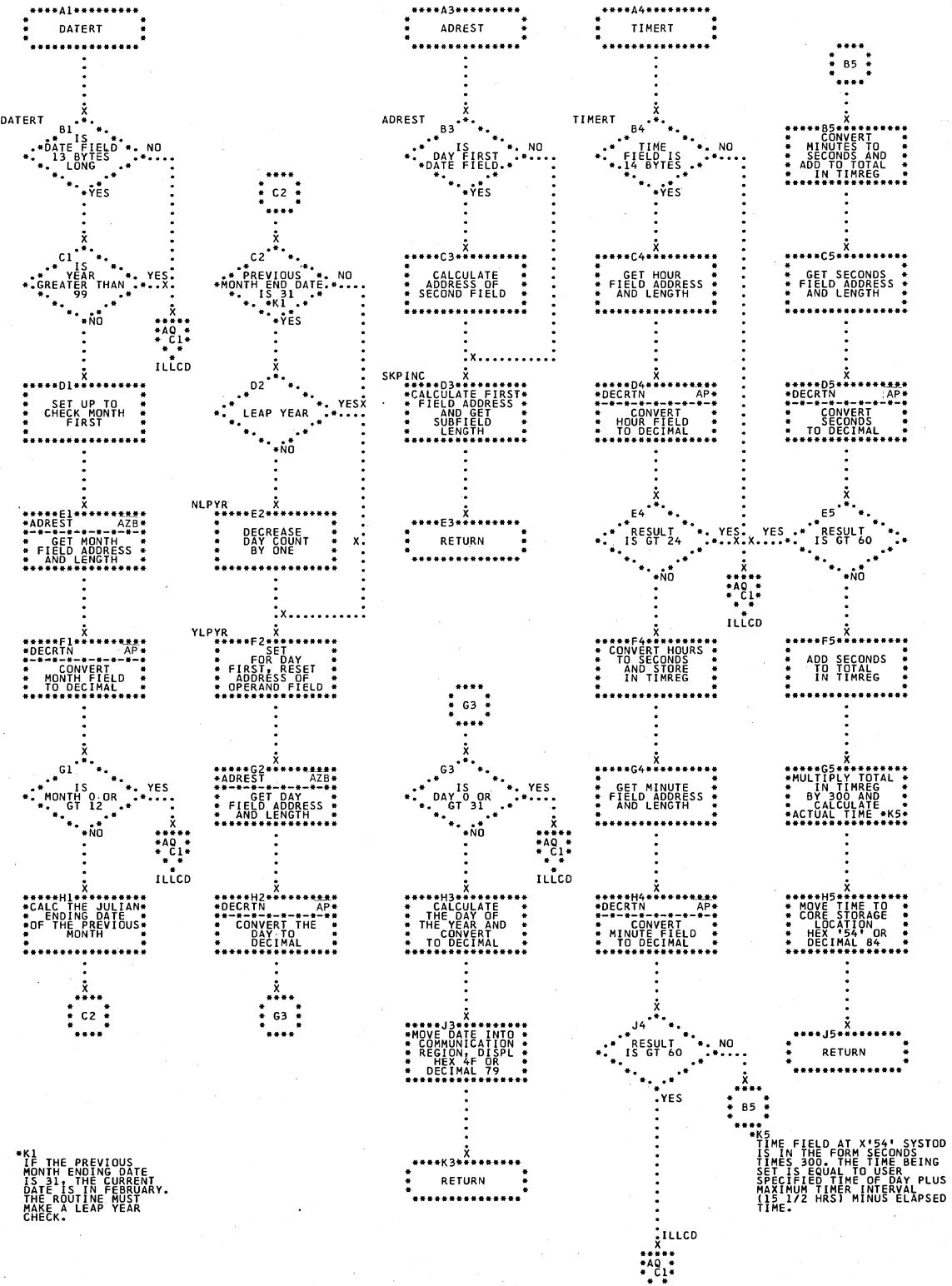
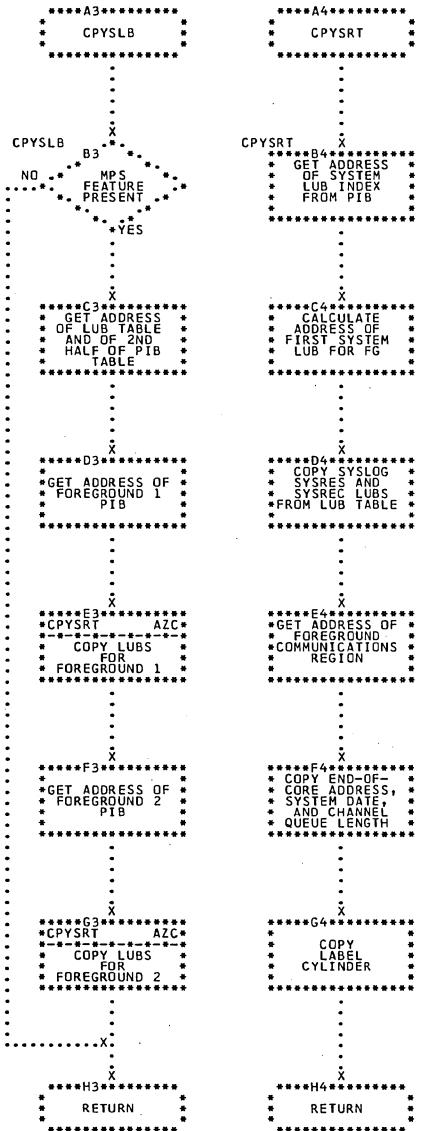


Chart AZB. \$IPLRT4 - Date and Time Subroutines  
Refer to Chart 03.



\*K1  
IF THE PREVIOUS  
MONTH ENDING DATE  
IS 31, THE CURRENT  
DATE IS IN FEBRUARY.  
THE ROUTINE MUST  
MAKE A LEAP YEAR  
CHECK.

Chart AZC. \$IPLRT4 - Copy Subroutine  
Refer to Chart 03.



**Chart AZD. \$IPLRT4 - Set Job Control Flags Subroutine**  
 Refer to Chart 03.

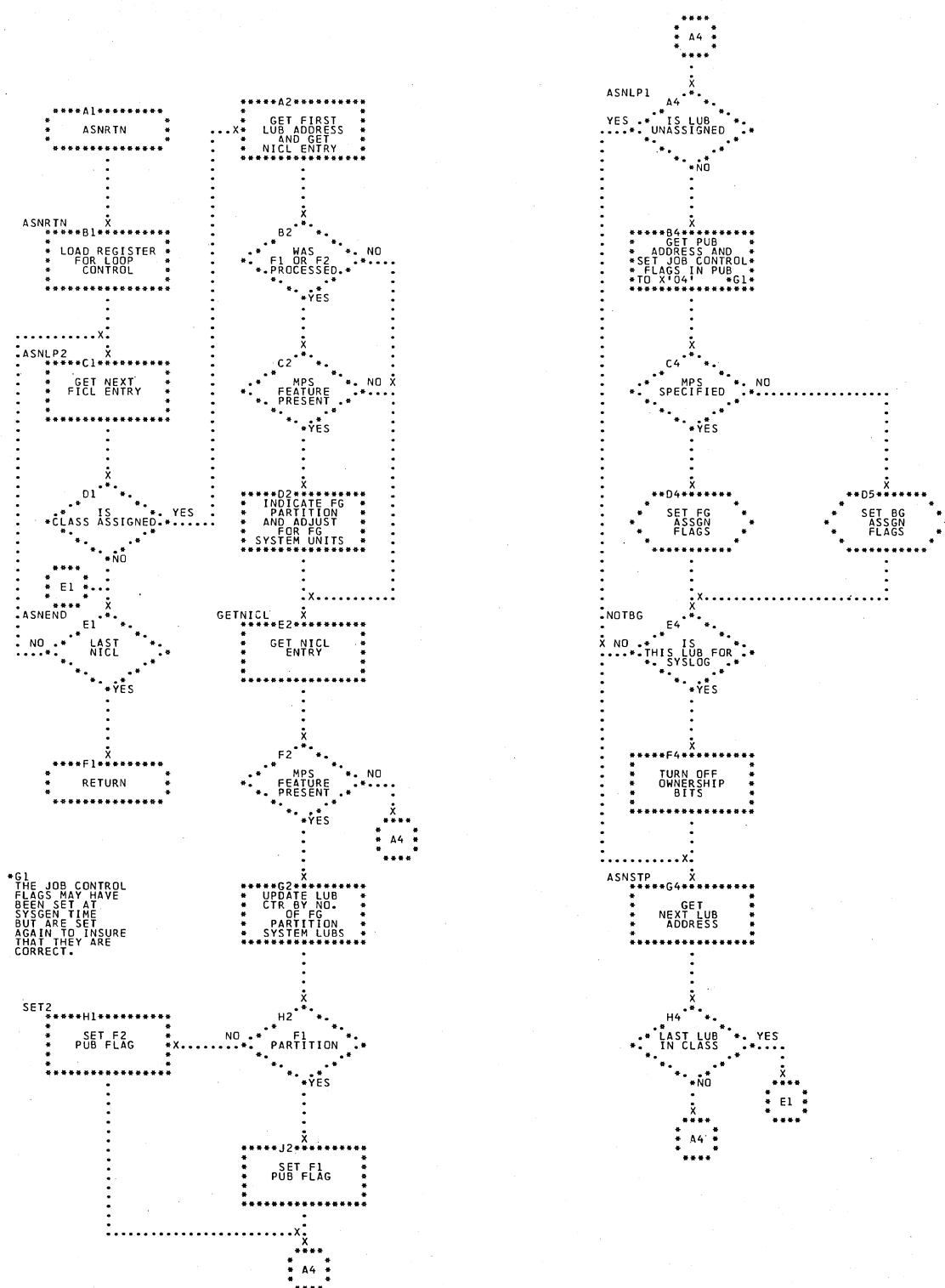
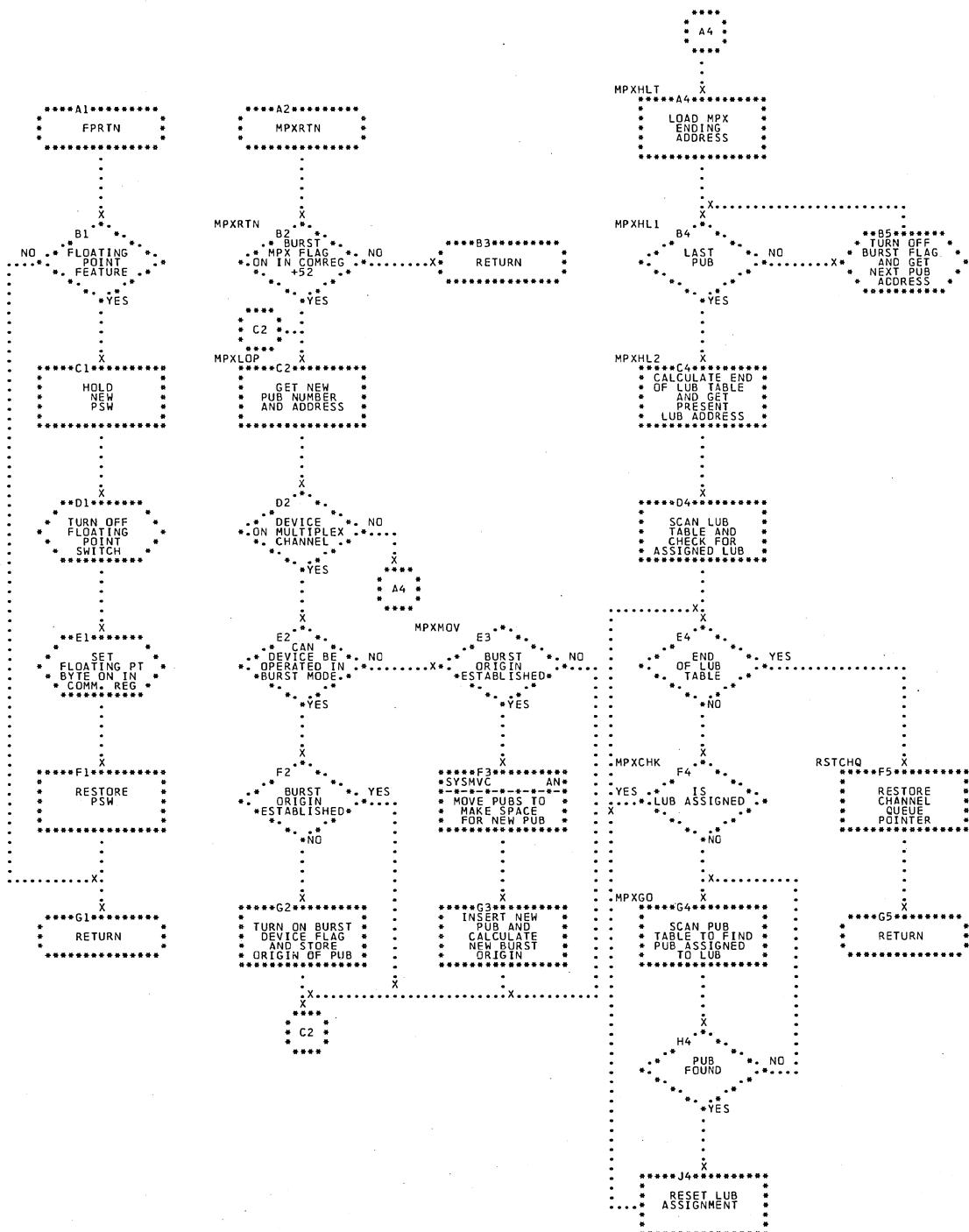


Chart AZE. \$IPLRT4 - Reorder MPX Channel LUBs and PUBs  
Refer to Chart 03.



**Chart BA. \$JOBCTLA - Initialization**  
Refer to Chart 04.

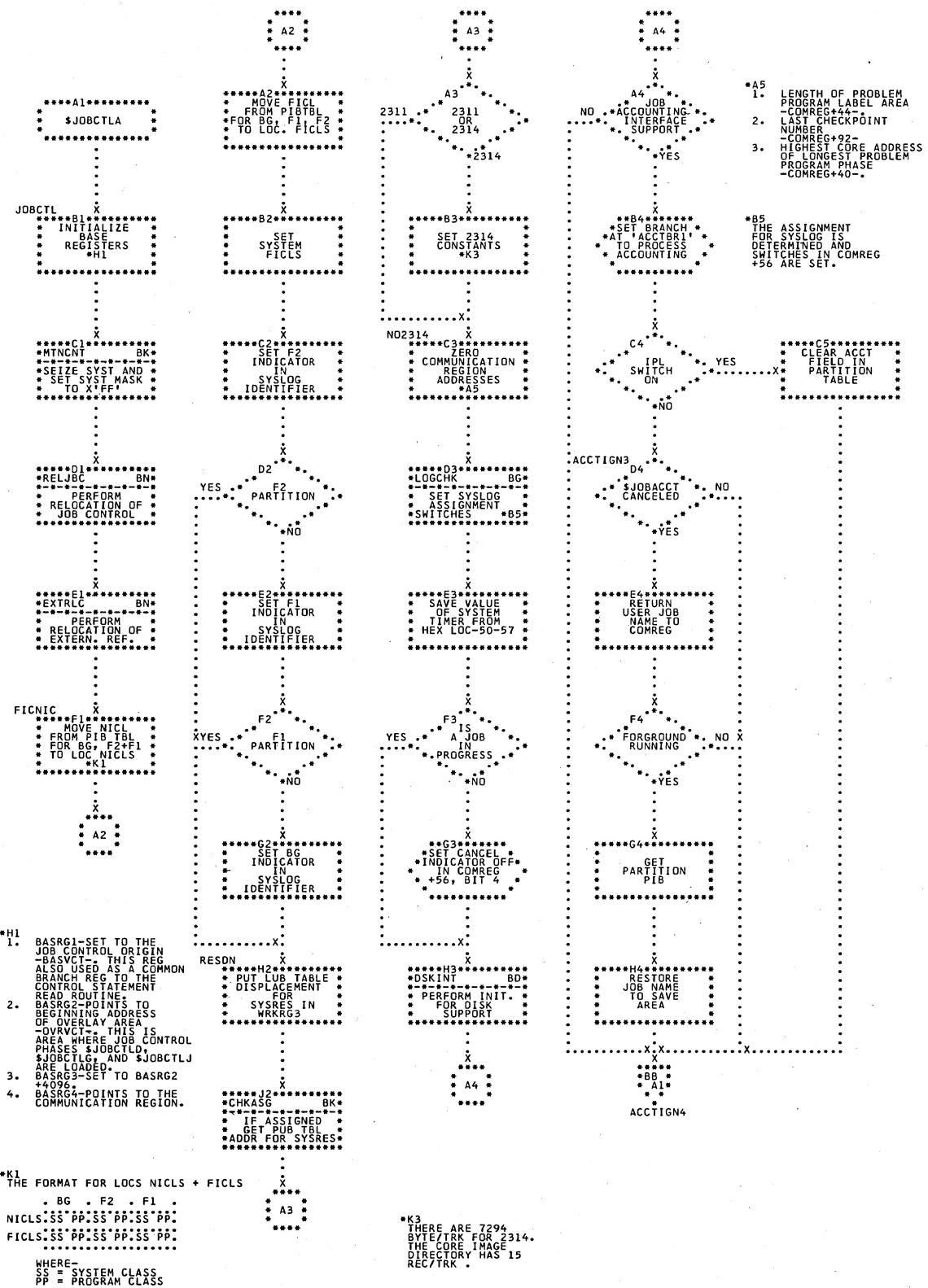


Chart BB. \$JOBCTLA - Initialization and Control Statement Read  
Refer to Chart 04.

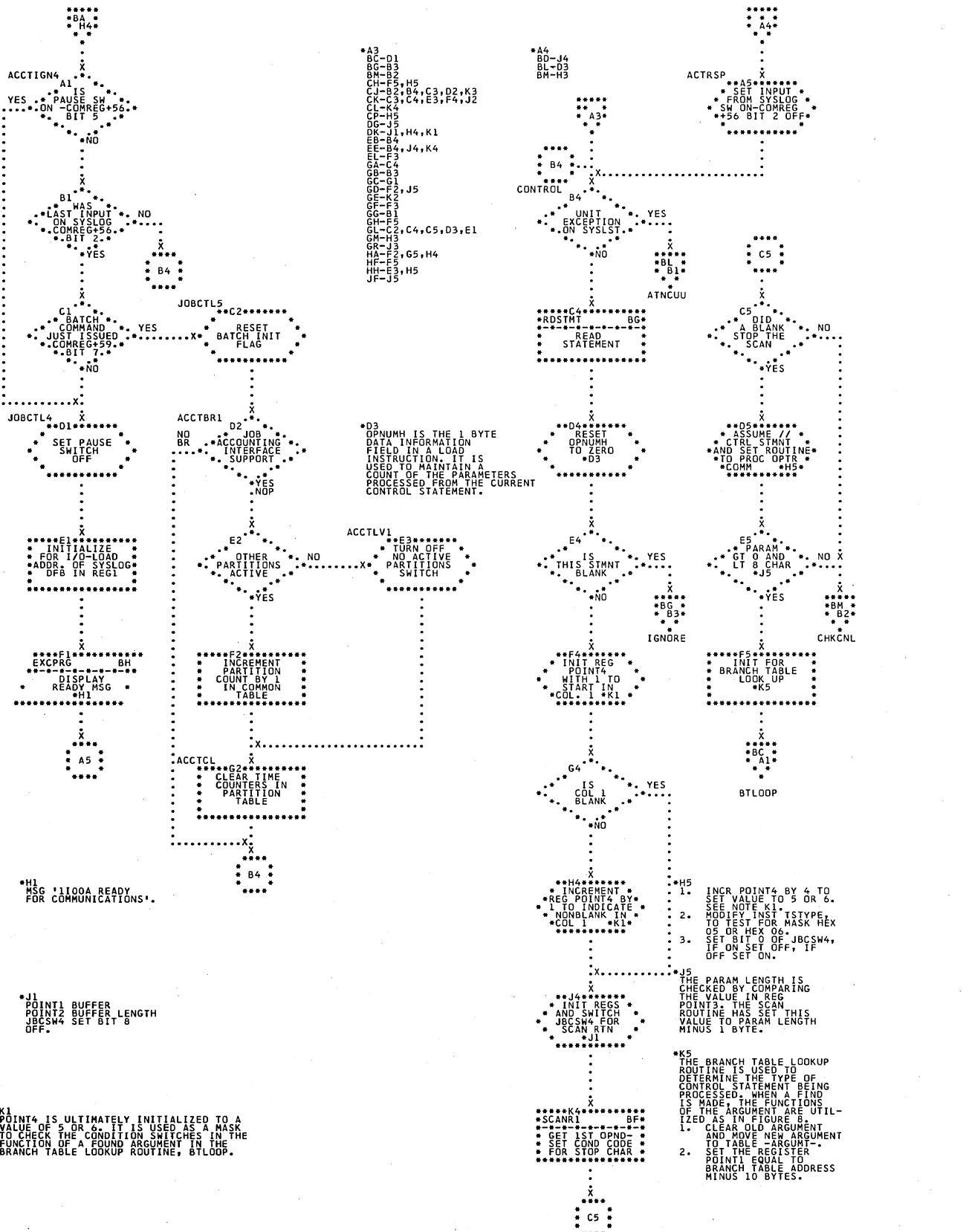


Chart BC. \$JOBCTLA - Phase Vector Table Lookup  
Refer to Chart 04.

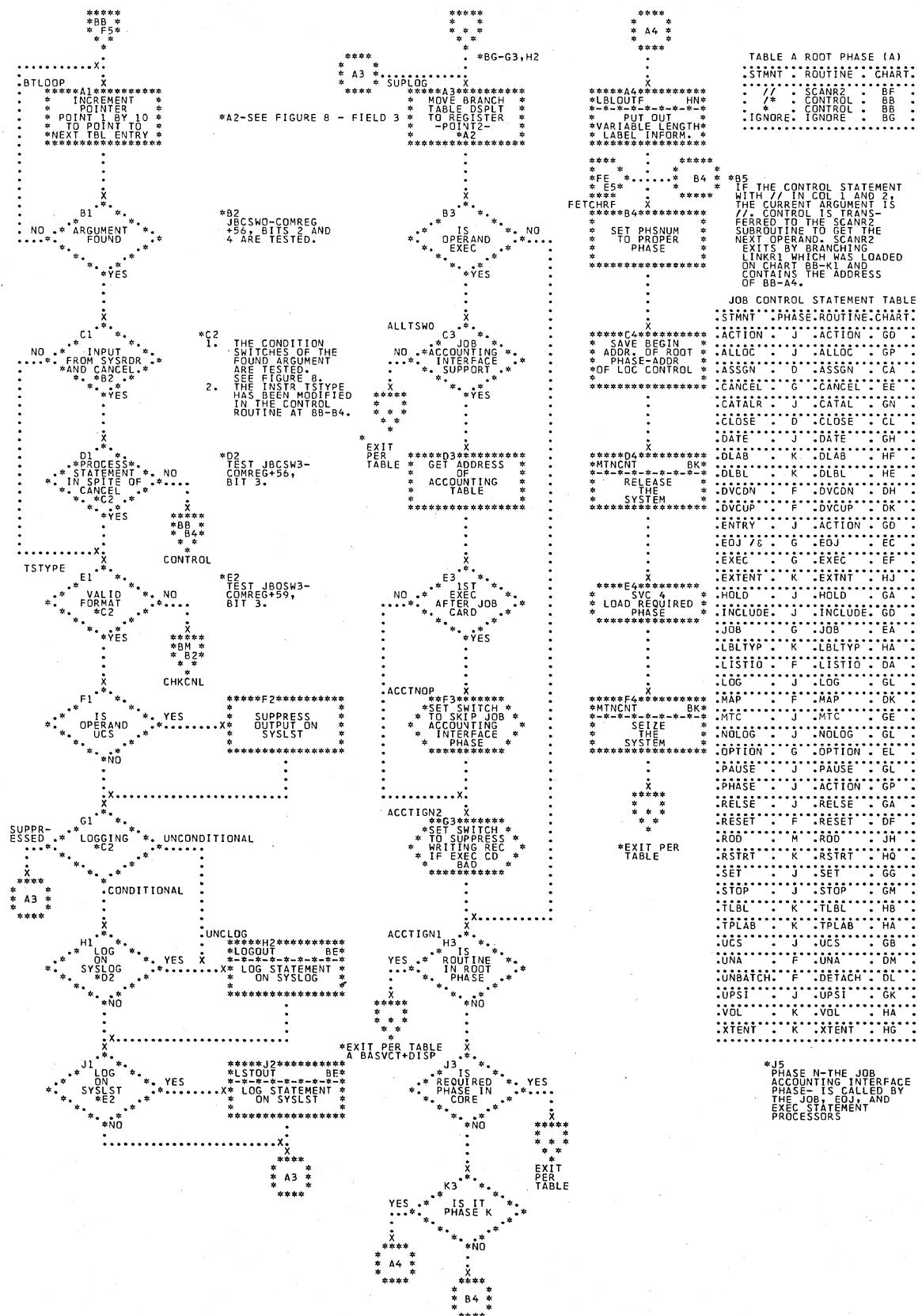
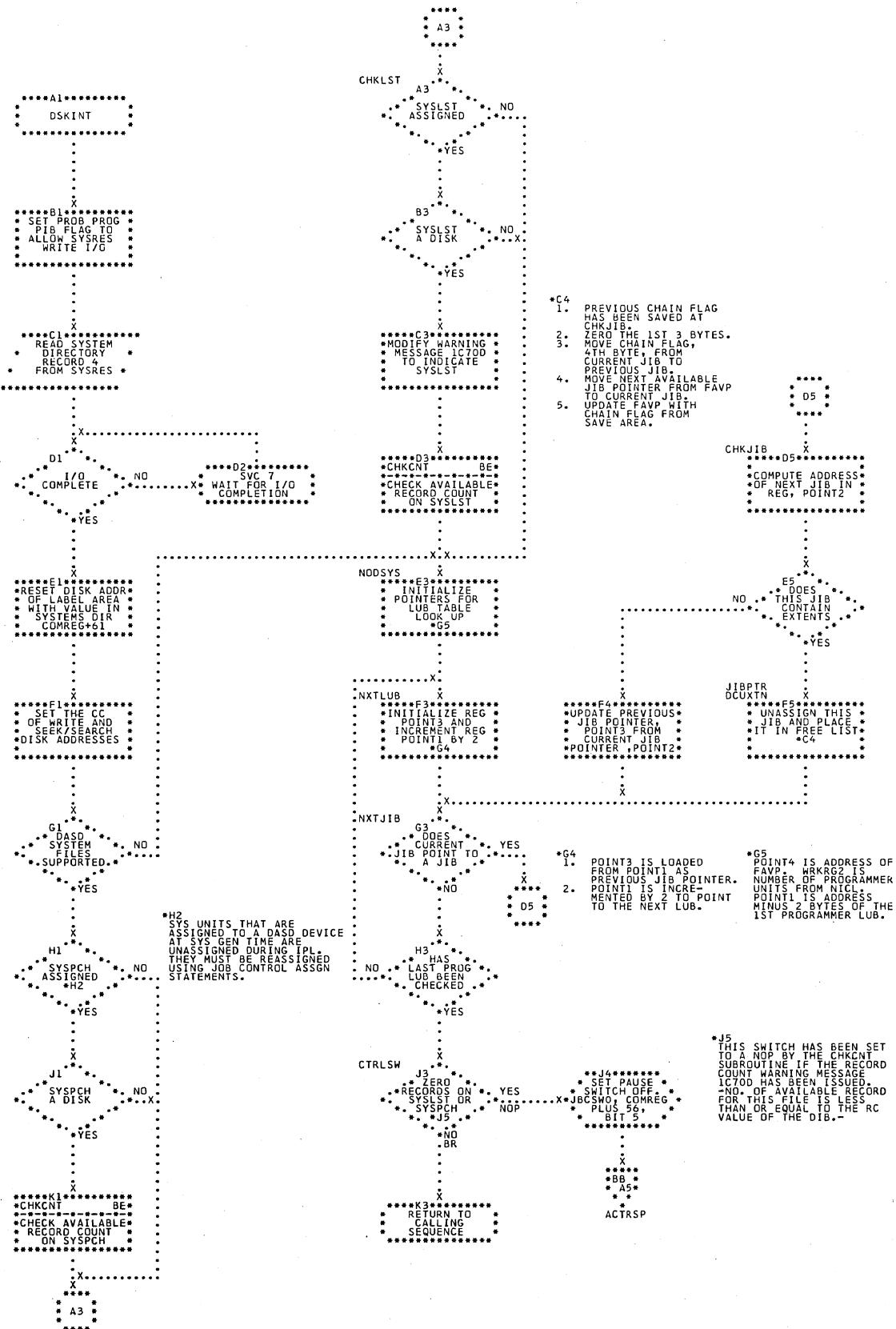


Chart BD. \$JOBCTLA - DSKINT Subroutine  
Refer to Chart 04.



**Chart BE. \$JOBCTLA - Message Subroutines**  
Refer to Chart 04.

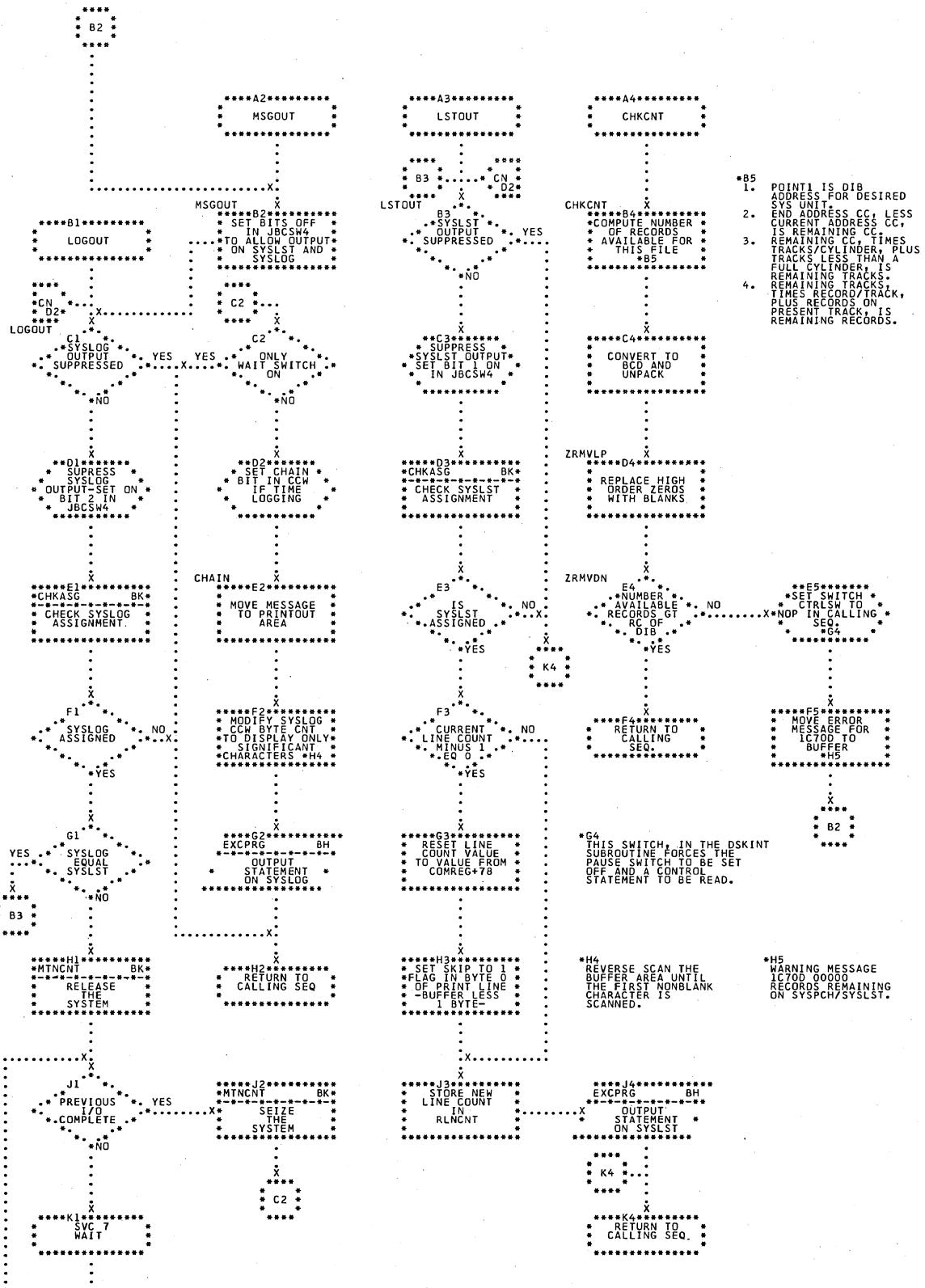
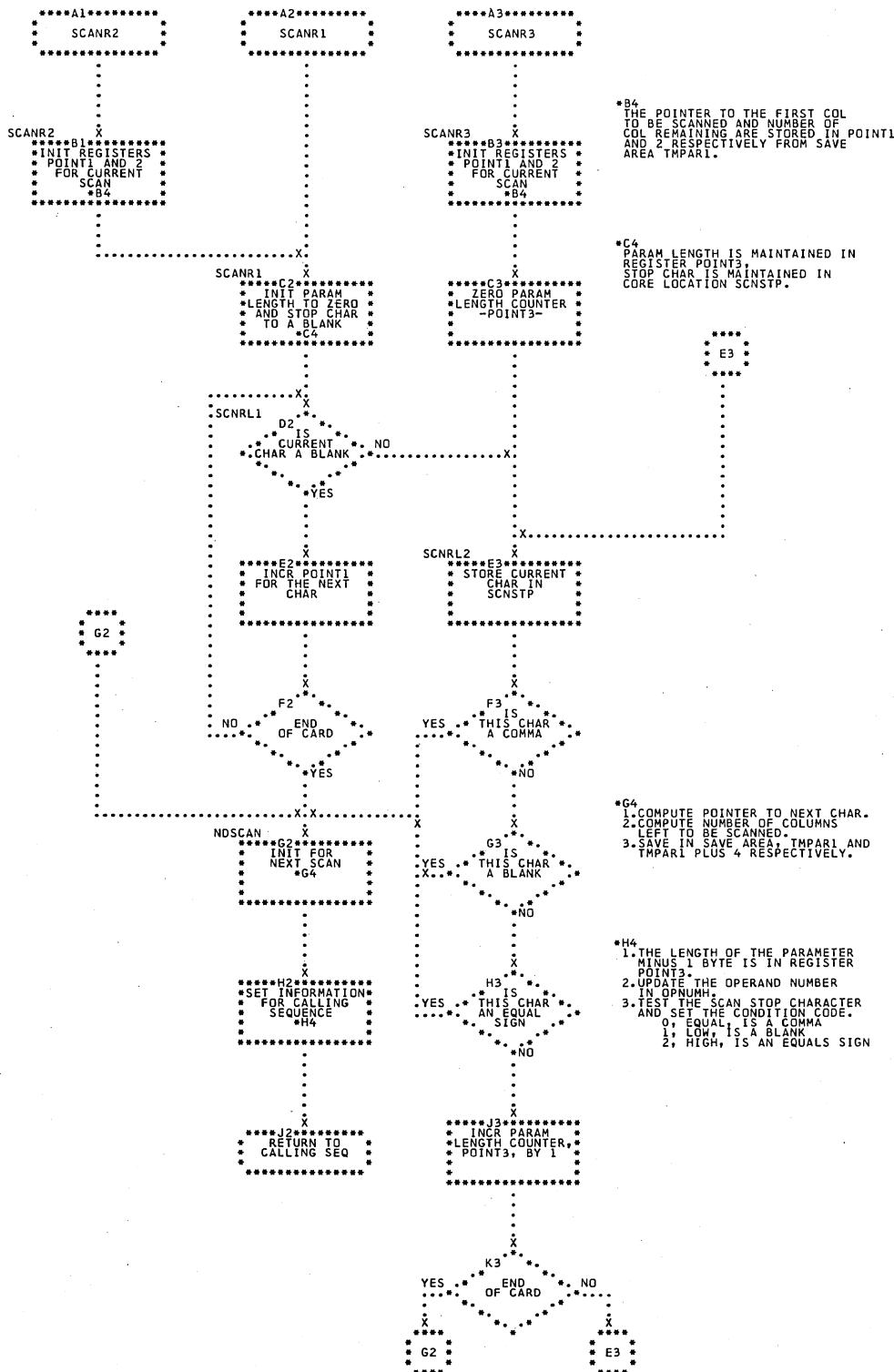


Chart BF. \$JOBCTLA - Operand Scan Subroutines  
Refer to Chart 04.



**Chart BG. \$JOBCTLA - Miscellaneous Subroutines**  
Refer to Chart 04.

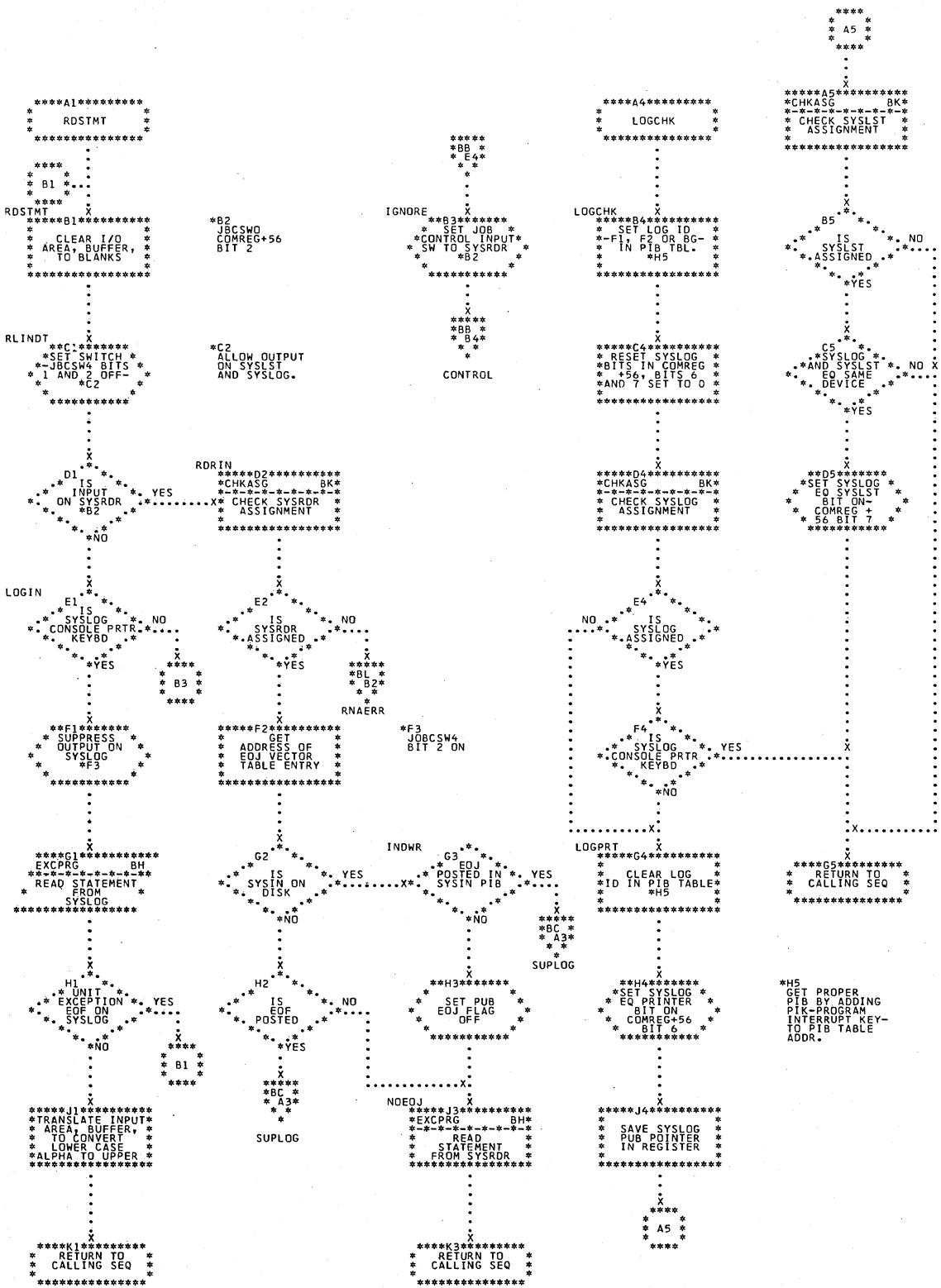
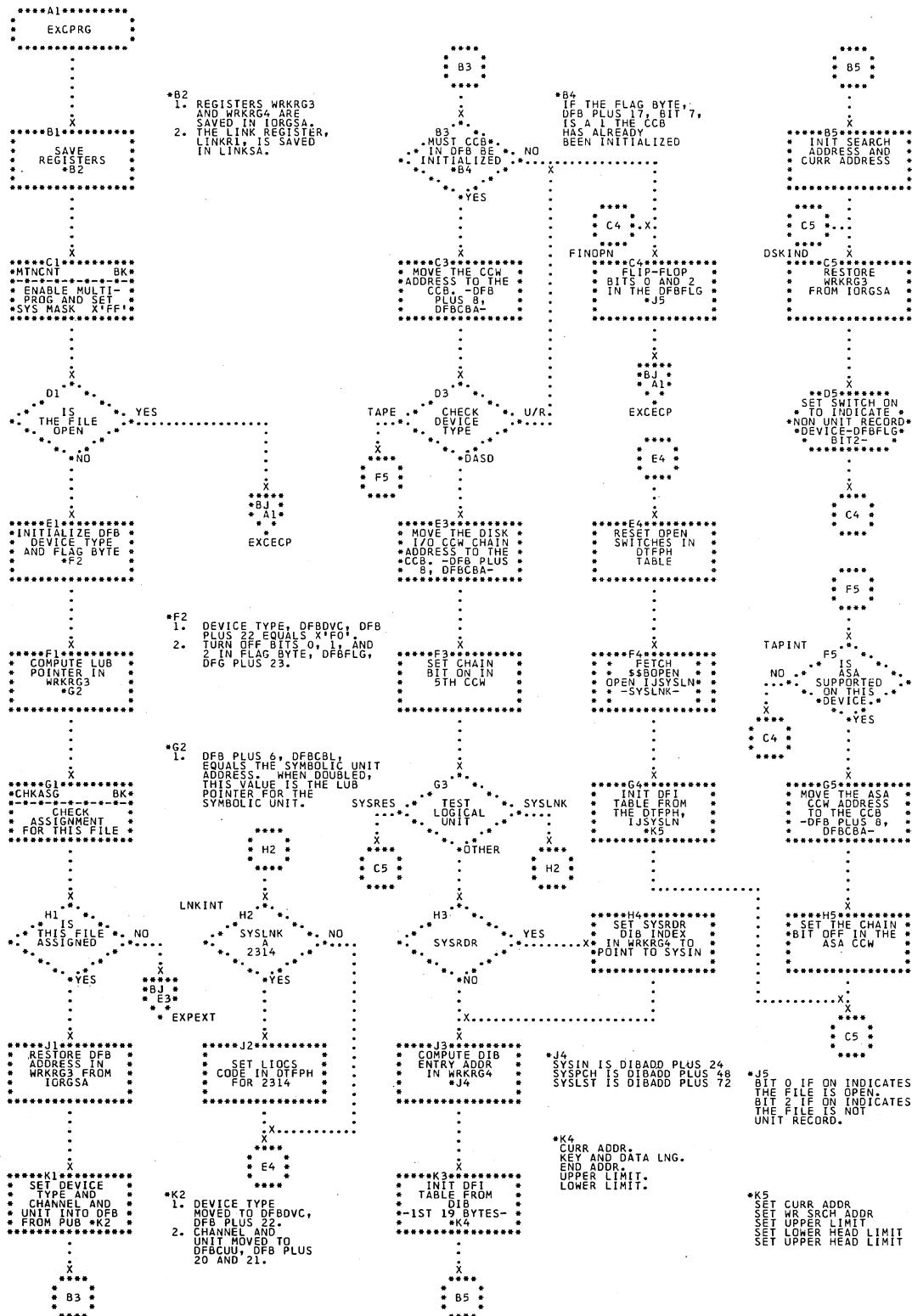


Chart BH. \$JOBCTLA - EXCP Subroutines. (Part 1 of 2)  
Refer to Chart 04.



**Chart BJ. \$JOBCTLA - EXCP Subroutines (Part 2 of 2)**  
Refer to Chart 04.

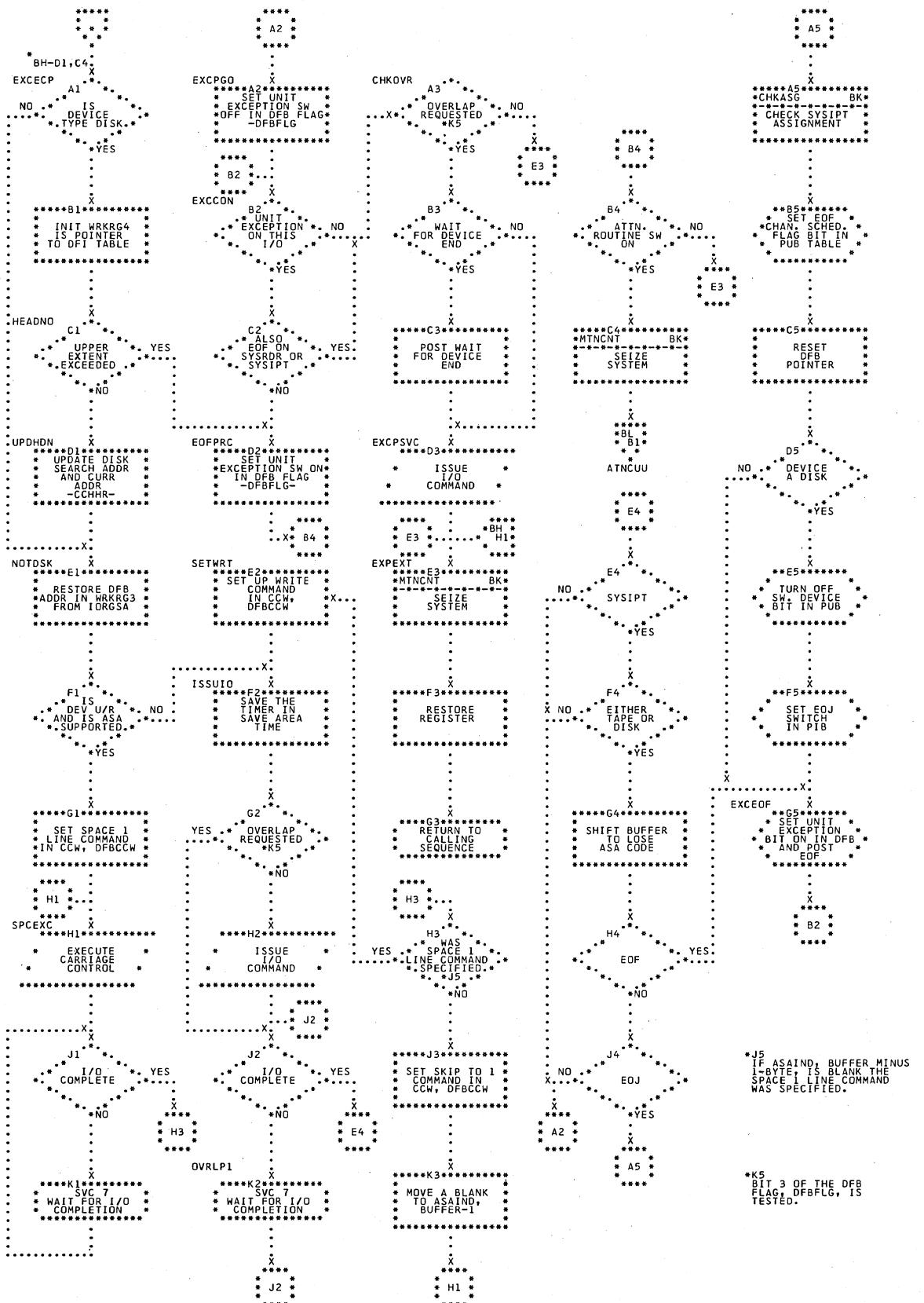


Chart BK. \$JOBCTLA - Miscellaneous Subroutines  
Refer to Chart 04.

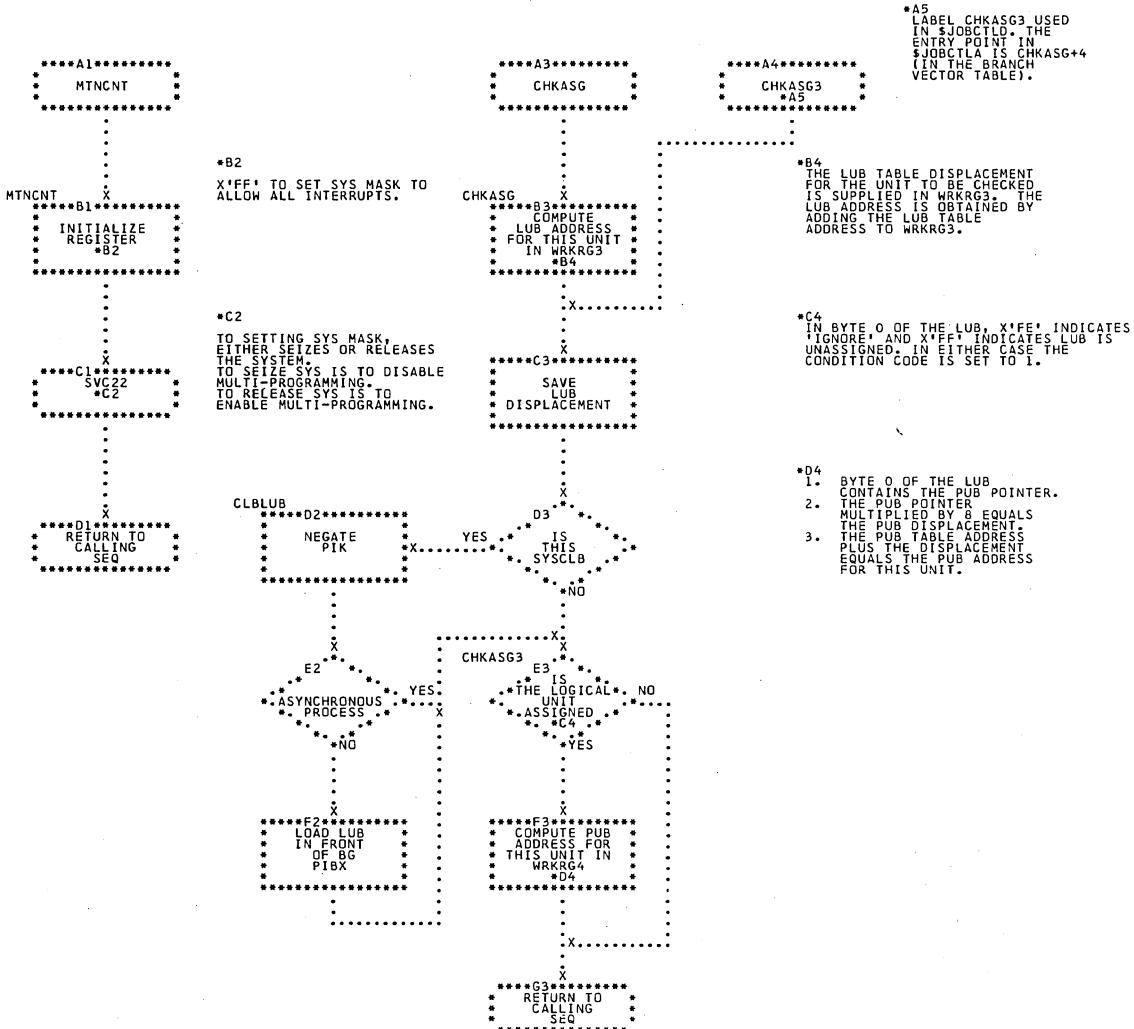


Chart BL. \$JOBCTLA - Error Subroutines (Part 1 of 2)  
Refer to Chart 04.

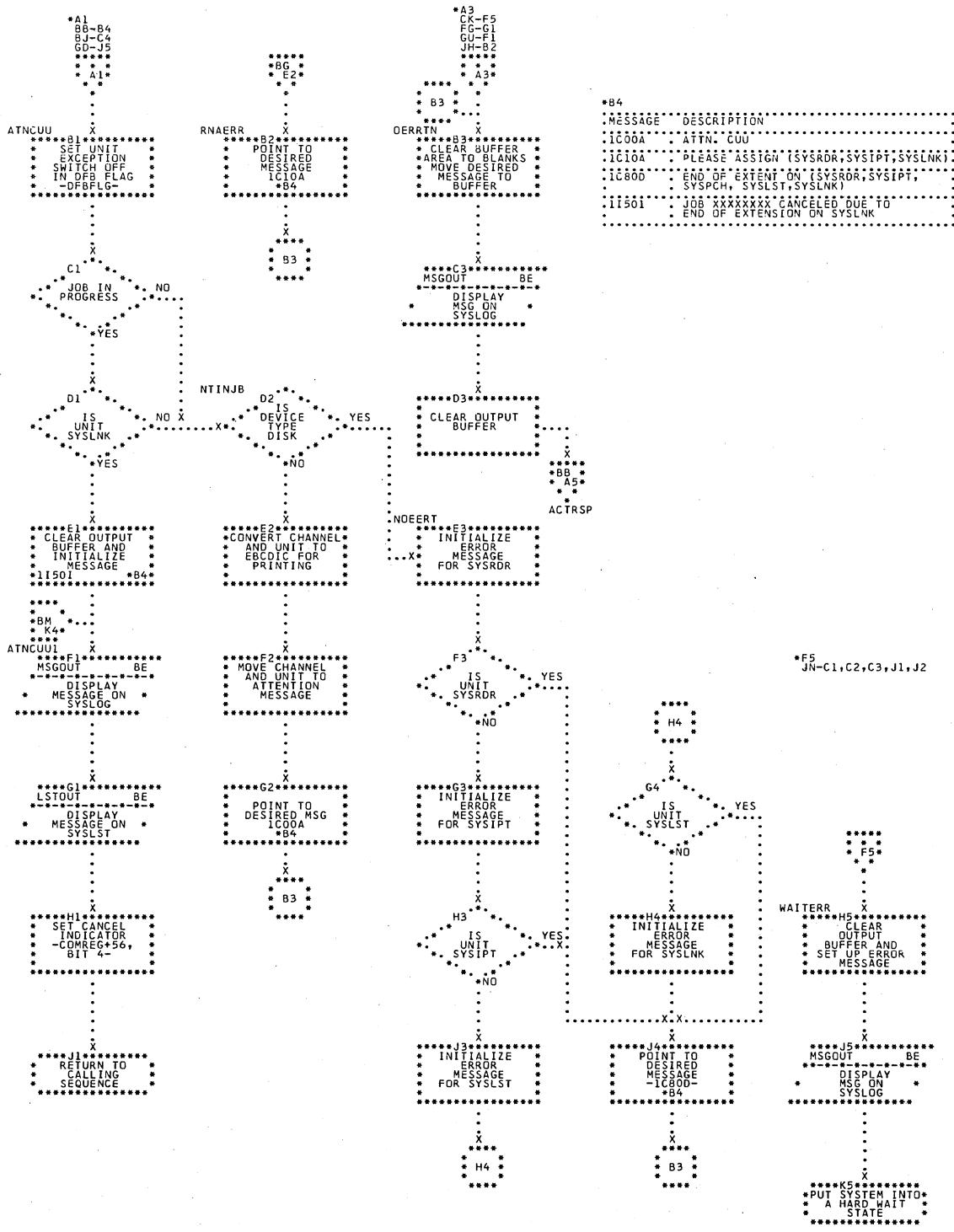


Chart BM. \$JOBCTLA - Error Subroutines (Part 2 of 2)  
Refer to Chart 04.

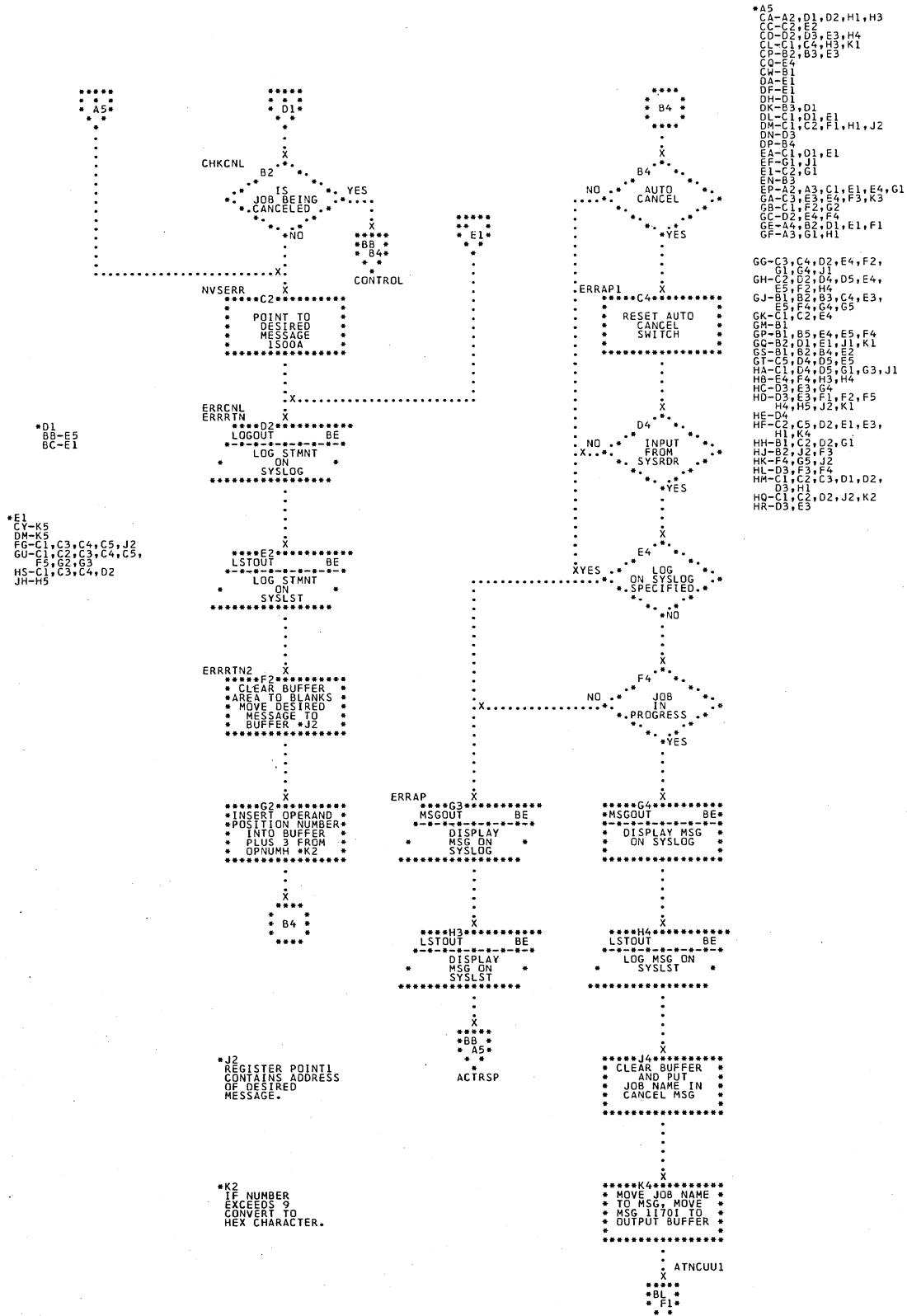
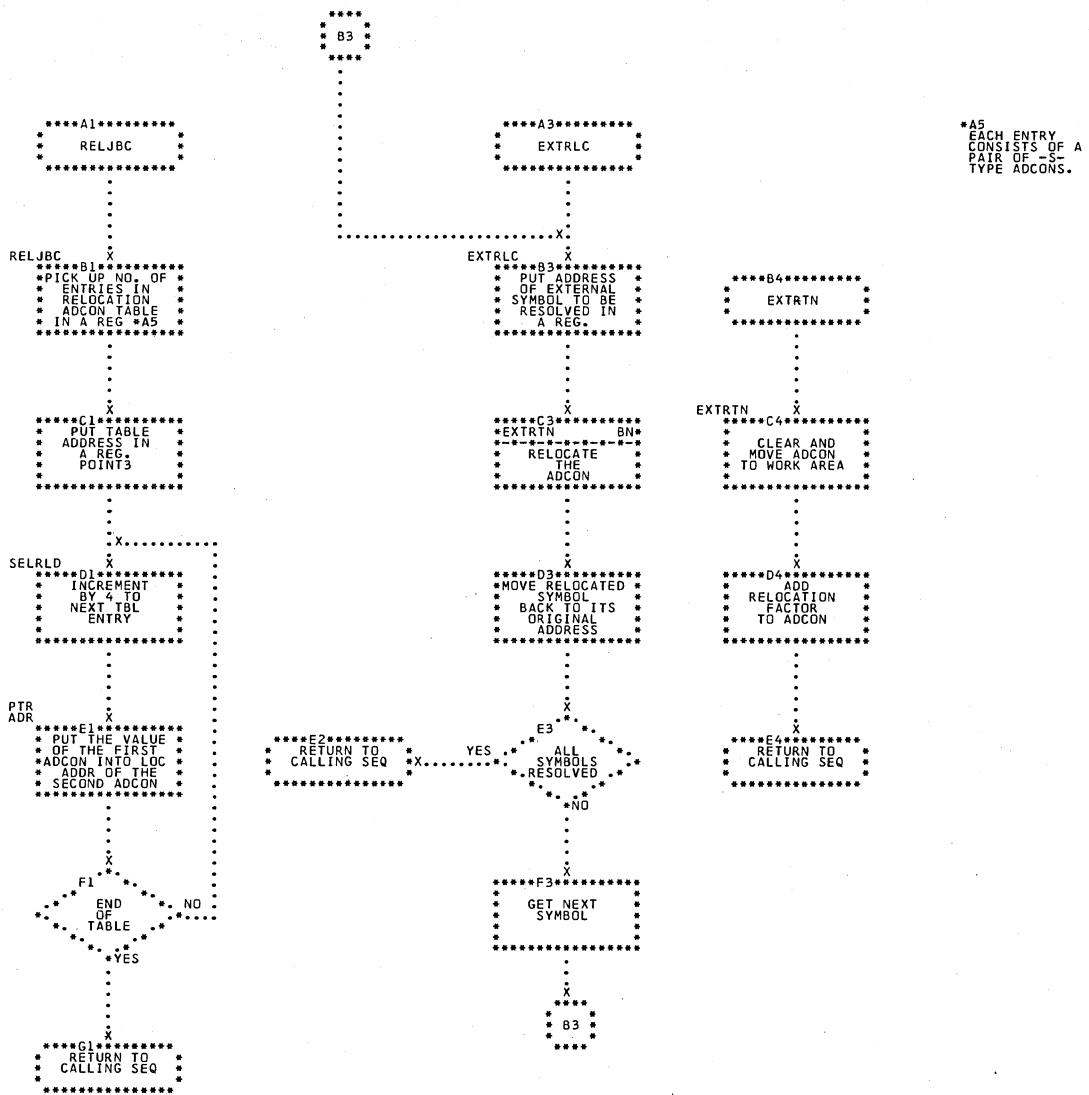


Chart BN. \$JOBCTLA - Relocation Subroutines  
Refer to Chart 04.



**Chart CA. \$JOBCTL - ASSGN Statement Processor (Part 1 of 10)**  
Refer to Chart 05.

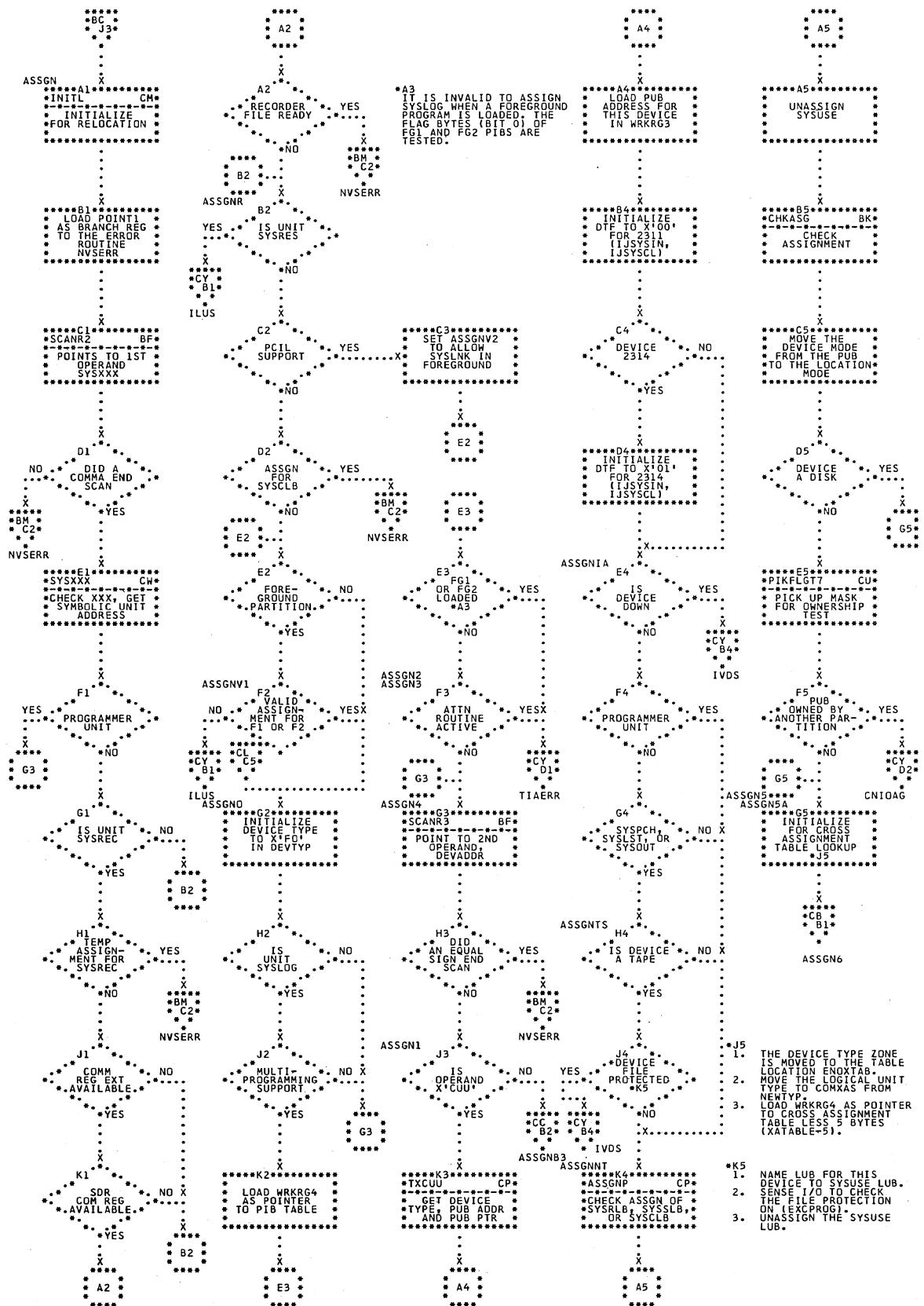
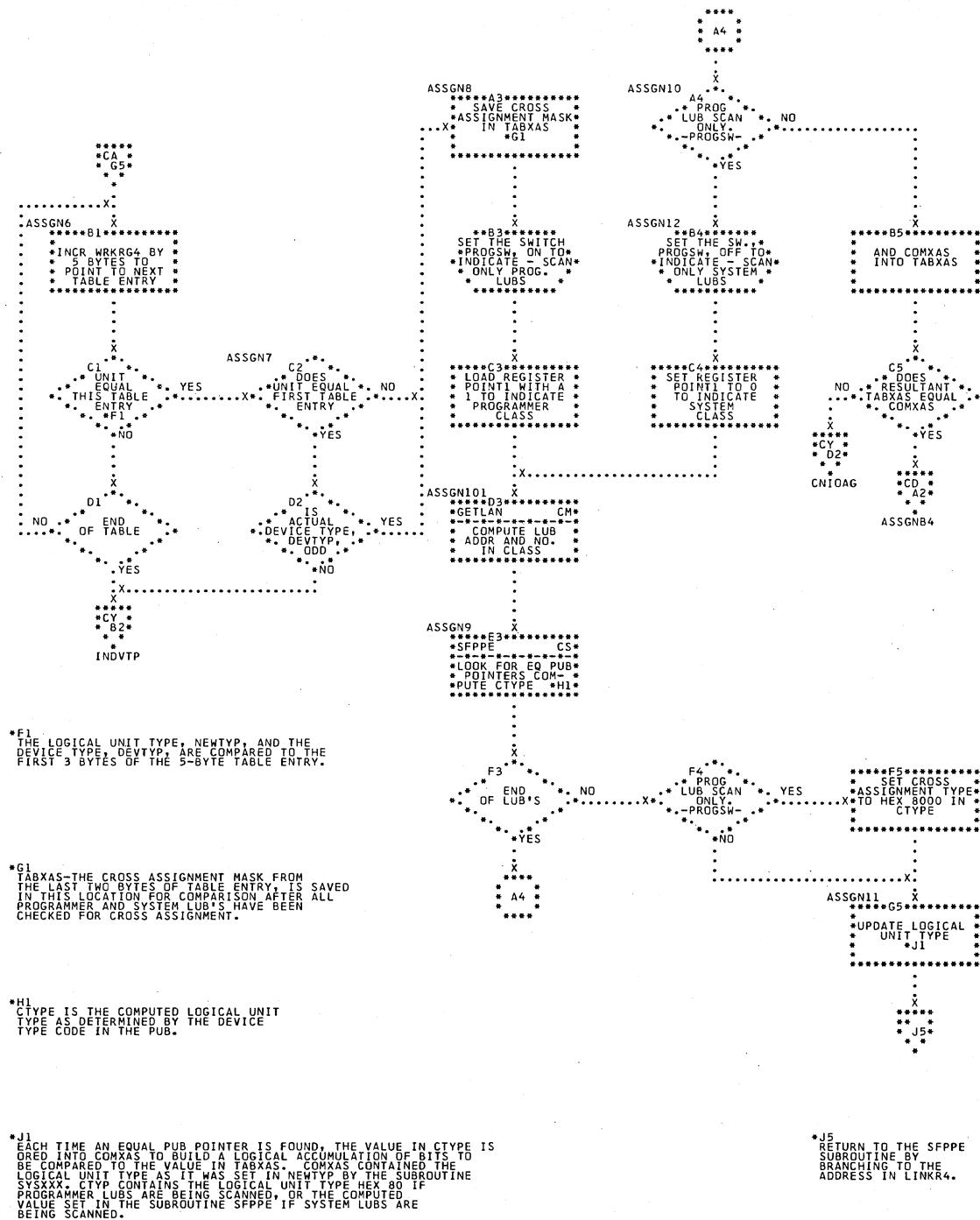


Chart CB. \$JOBCTL0 - ASSGN Statement Processor (Part 2 of 10)  
Refer to Chart 05.



**Chart CC. \$JOBCTL - ASSGN Statement Processor (Part 3 of 10)**  
Refer to Chart 05.

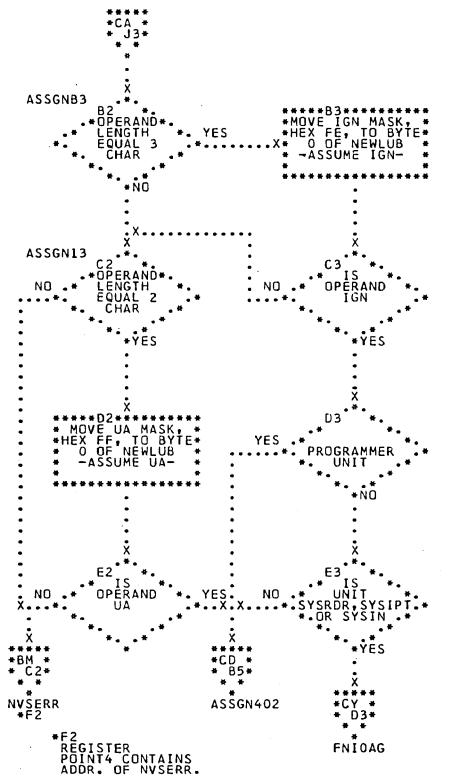
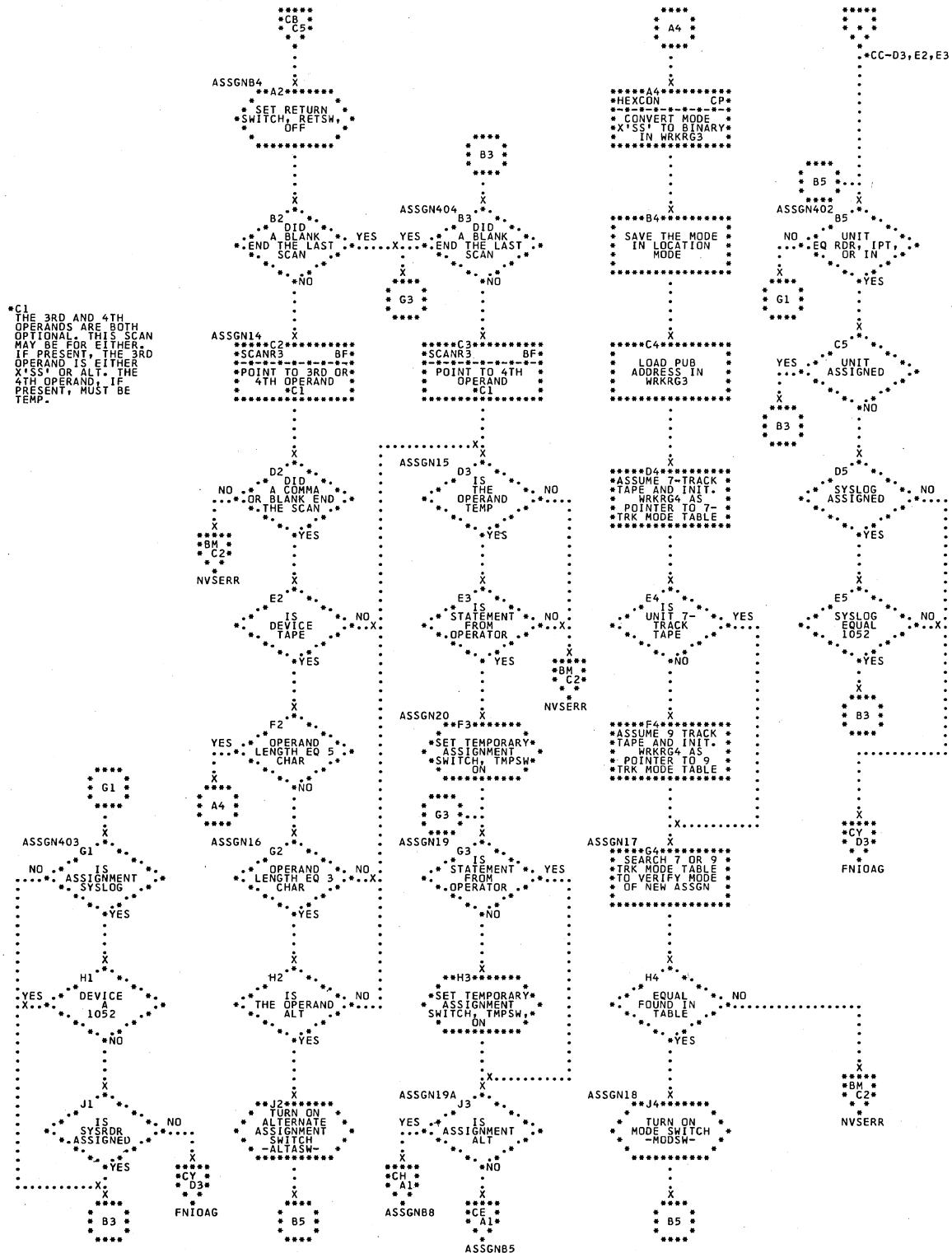


Chart CD. \$JOBCTL - ASSGN Statement Processor (Part 4 of 10)  
Refer to Chart 05.



**Chart CE. \$JOBCTL - ASSGN Statement Processor (Part 5 of 10)**  
Refer to Chart 05.

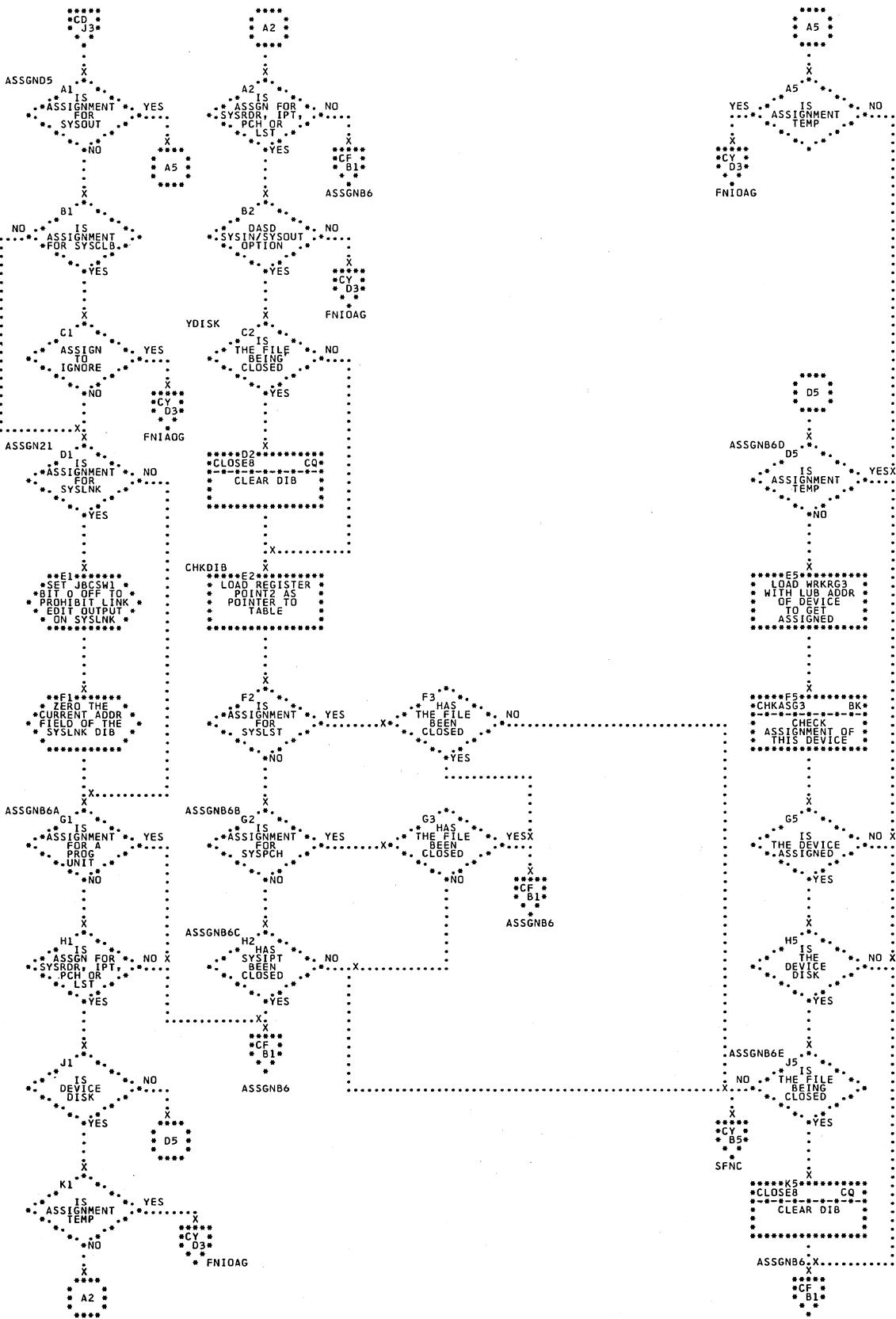


Chart CF. \$JOBCTLD - ASSGN Statement Processor (Part 6 of 10)  
Refer to Chart 05.

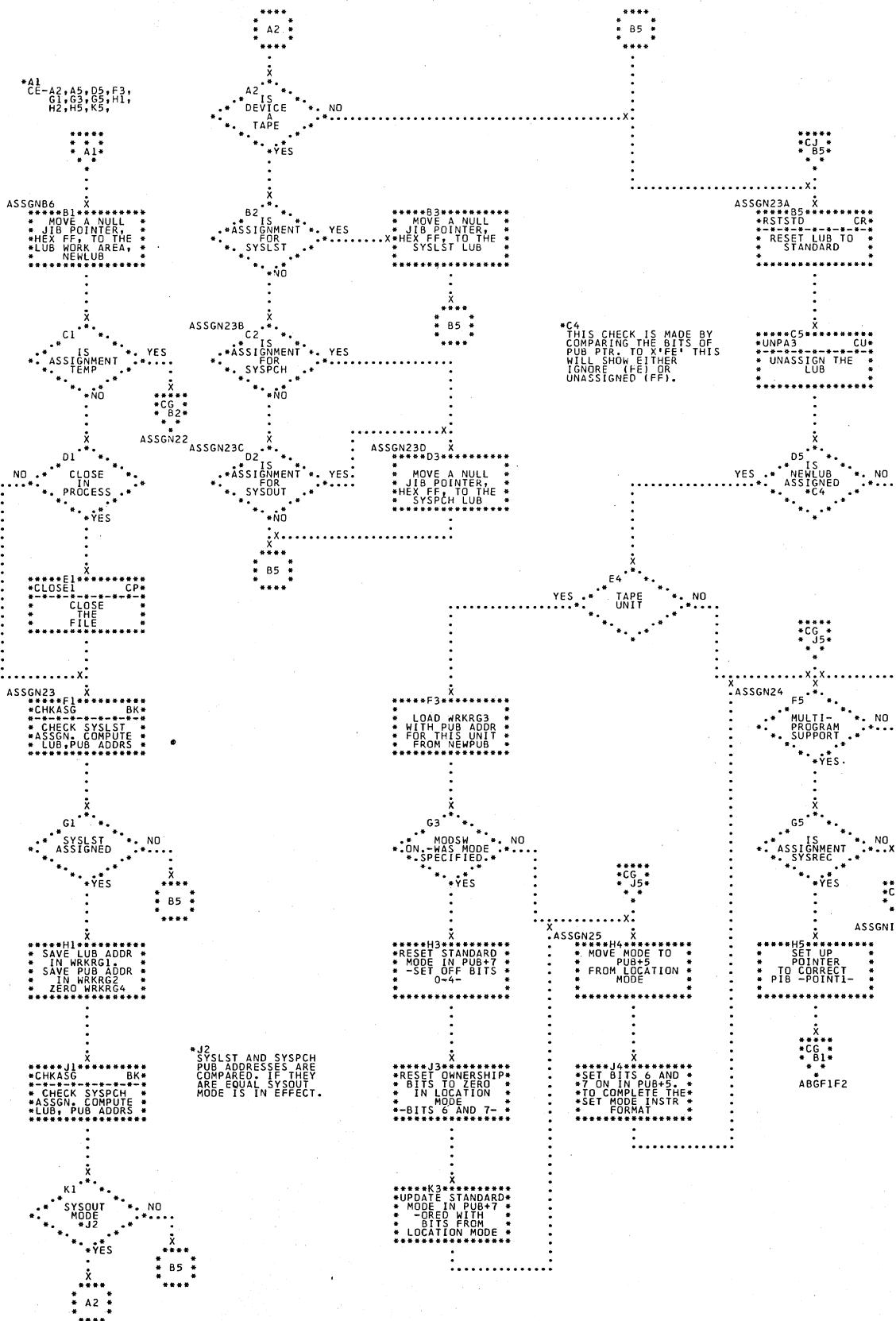


Chart CG. \$JOBCTL - ASSGN Statement Processor (Part 7 of 10)  
Refer to Chart 05.

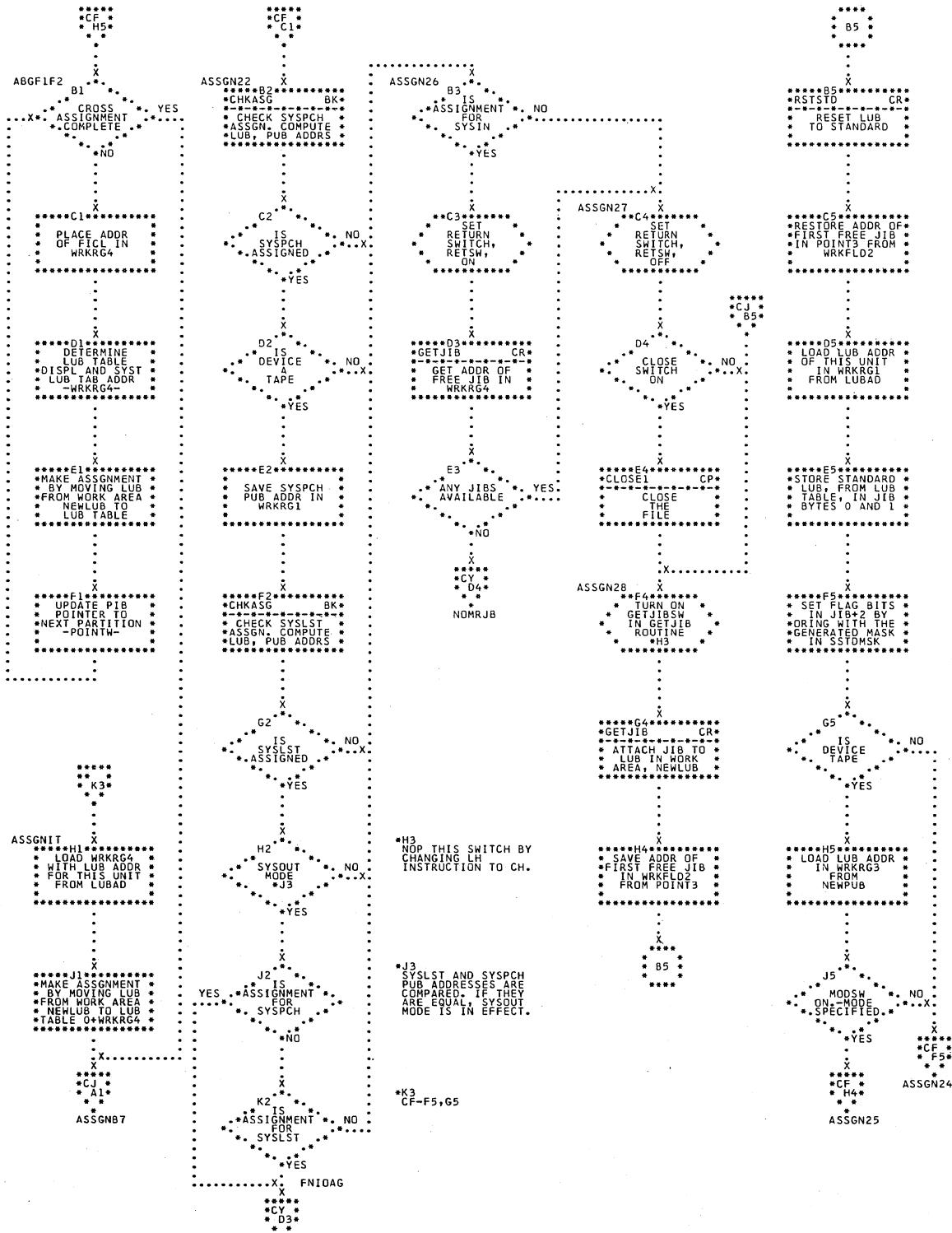


Chart CH. \$JOBCTLD - ASSGN Statement Processor (Part 8 of 10)  
Refer to Chart 05.

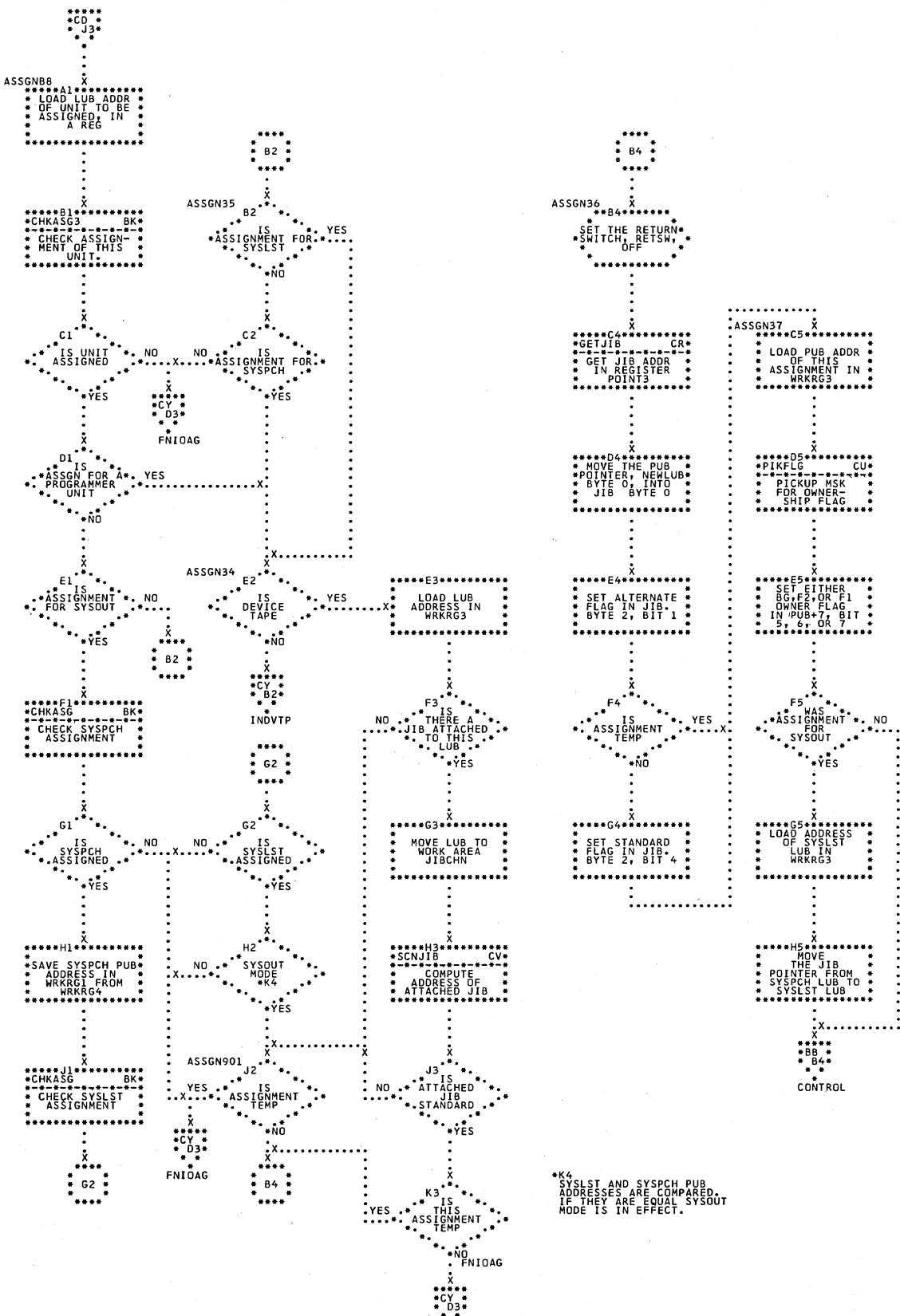


Chart CJ. \$JOBCTL - ASSGN Statement Processor (Part 9 of 10)  
Refer to Chart 05.

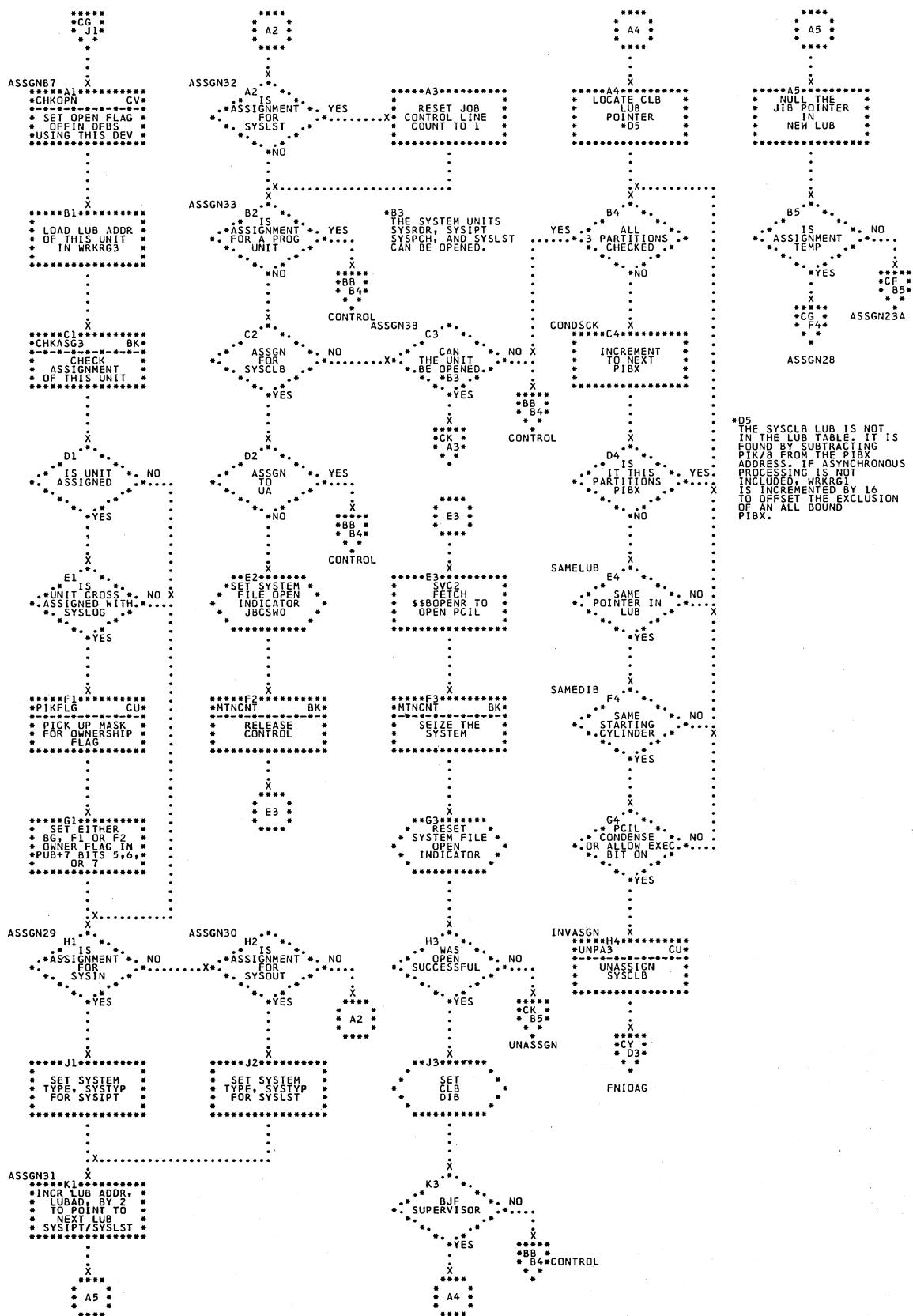
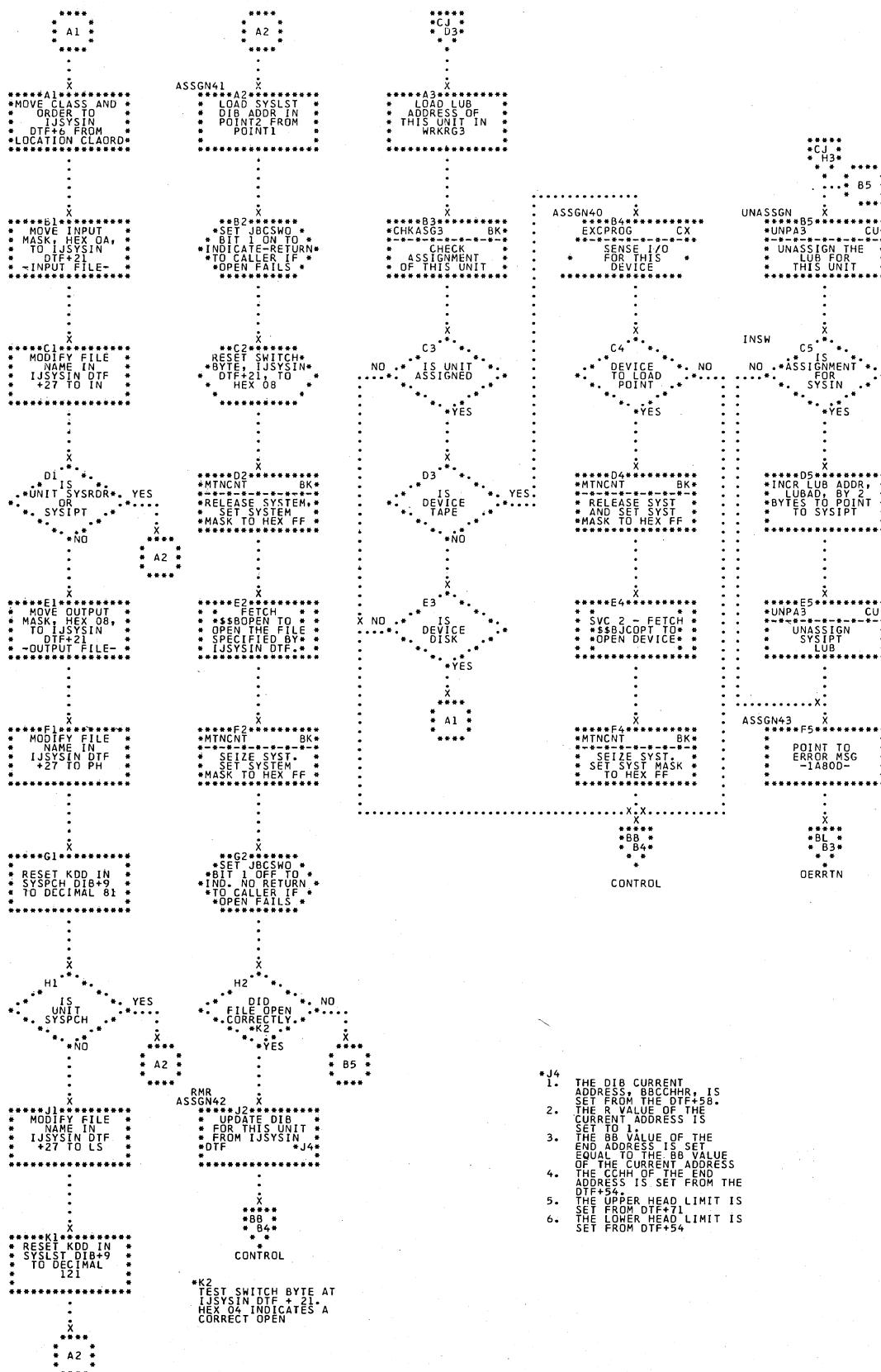


Chart CK. \$JOBCTLD - ASSGN Statement Processor (Part 10 of 10)  
Refer to Chart 05.



- \***J4**
  - 1. THE DIB CURRENT ADDRESS, BBCCHRR, IS SET FROM THE DTF+58.
  - 2. THE HIGH ORDER OF THE CURRENT ADDRESS IS SET TO 1.
  - 3. THE BE VALUE OF THE END ADDRESS IS SET EQUAL TO THE BE VALUE OF THE CURRENT ADDRESS.
  - 4. THE CCH OF THE END ADDRESS IS SET FROM THE DTF+59.
  - 5. THE UPPER HEAD LIMIT IS SET FROM DTF+71.
  - 6. THE LOWER HEAD LIMIT IS

Chart CL. \$JOBCTLD - CLOSE Statement Processor  
Refer to Chart 06.

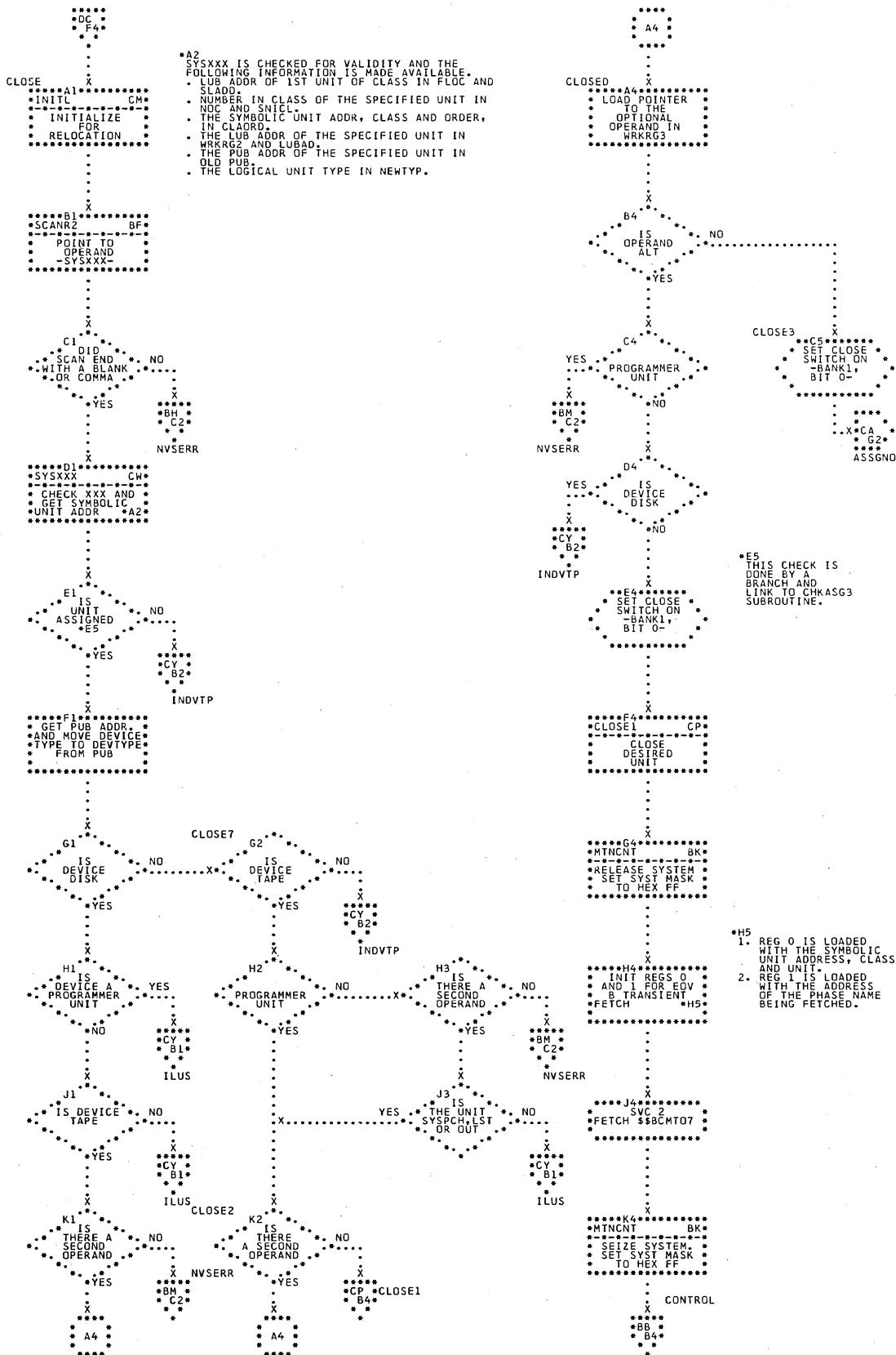


Chart CM. \$JOBCTLD - Miscellaneous Subroutines (Part 1 of 3)  
Refer to Chart 06.

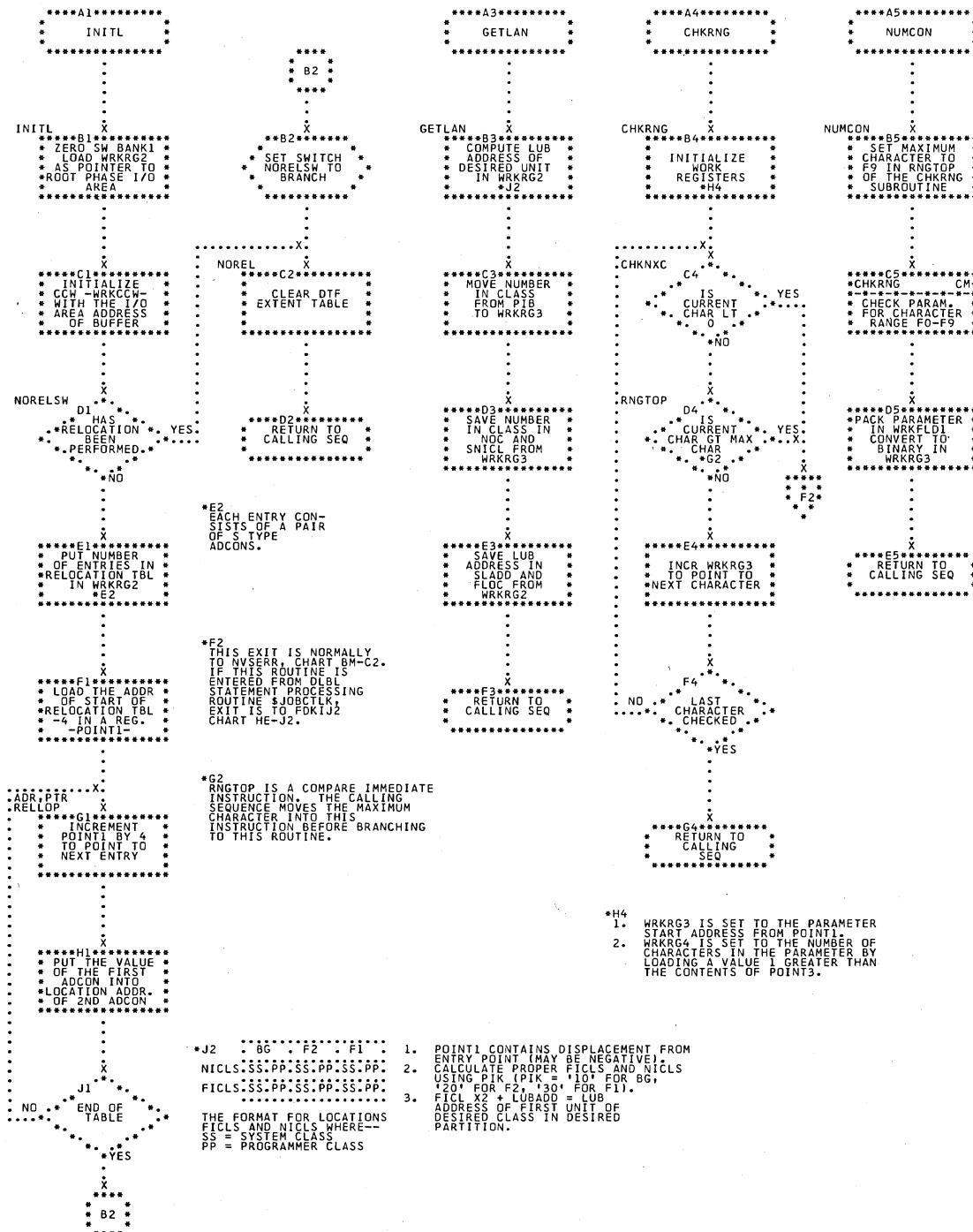


Chart CN. \$JOBCTL - Miscellaneous Subroutines (Part 2 of 3)  
 Refer to Chart 06.

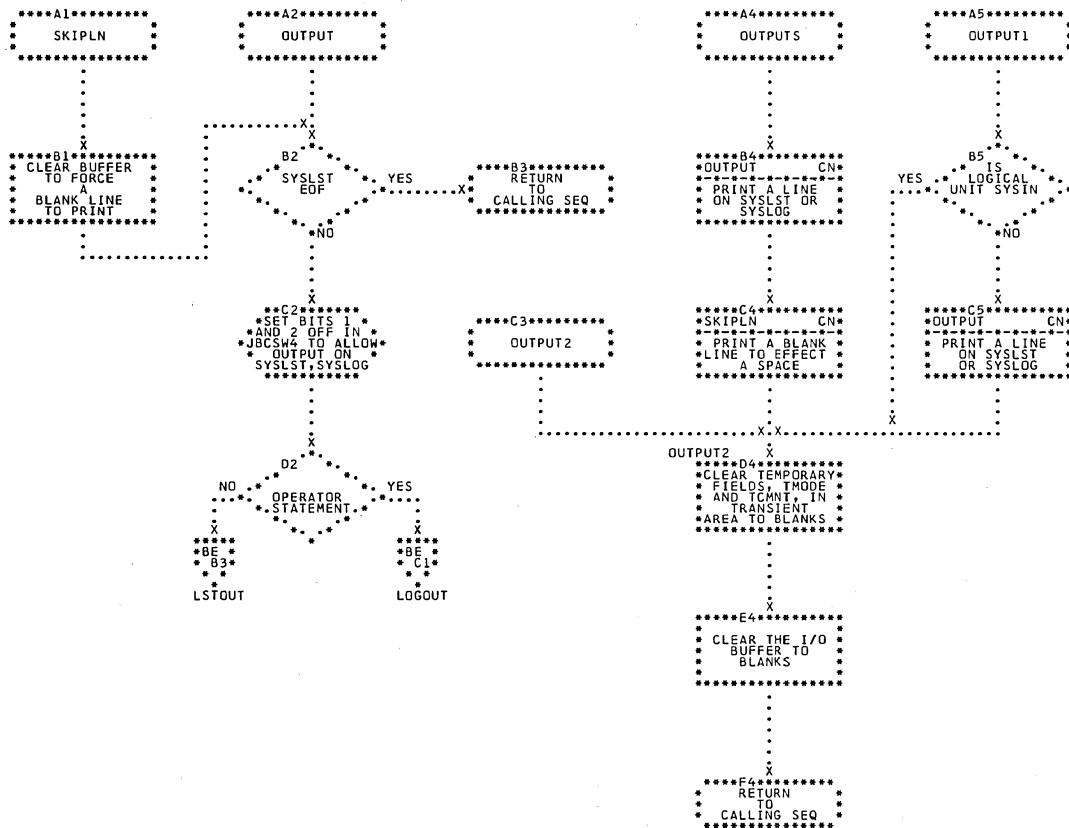


Chart CP. \$JOBCTLD - Miscellaneous Subroutines (Part 3 of 3)  
Refer to Chart 06.

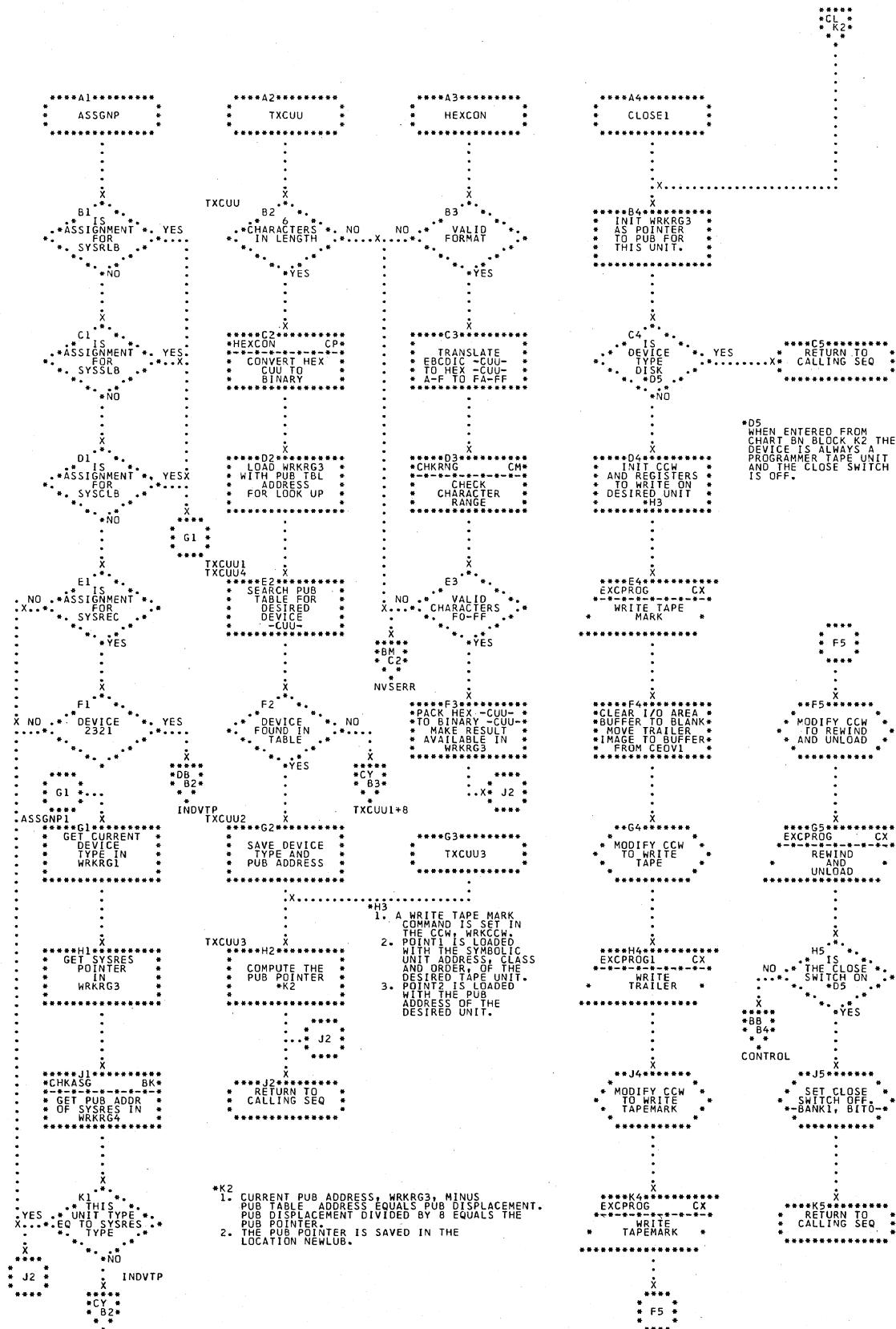


Chart CQ. \$JOBCTLID - Close Subroutine  
Refer to Chart 06.

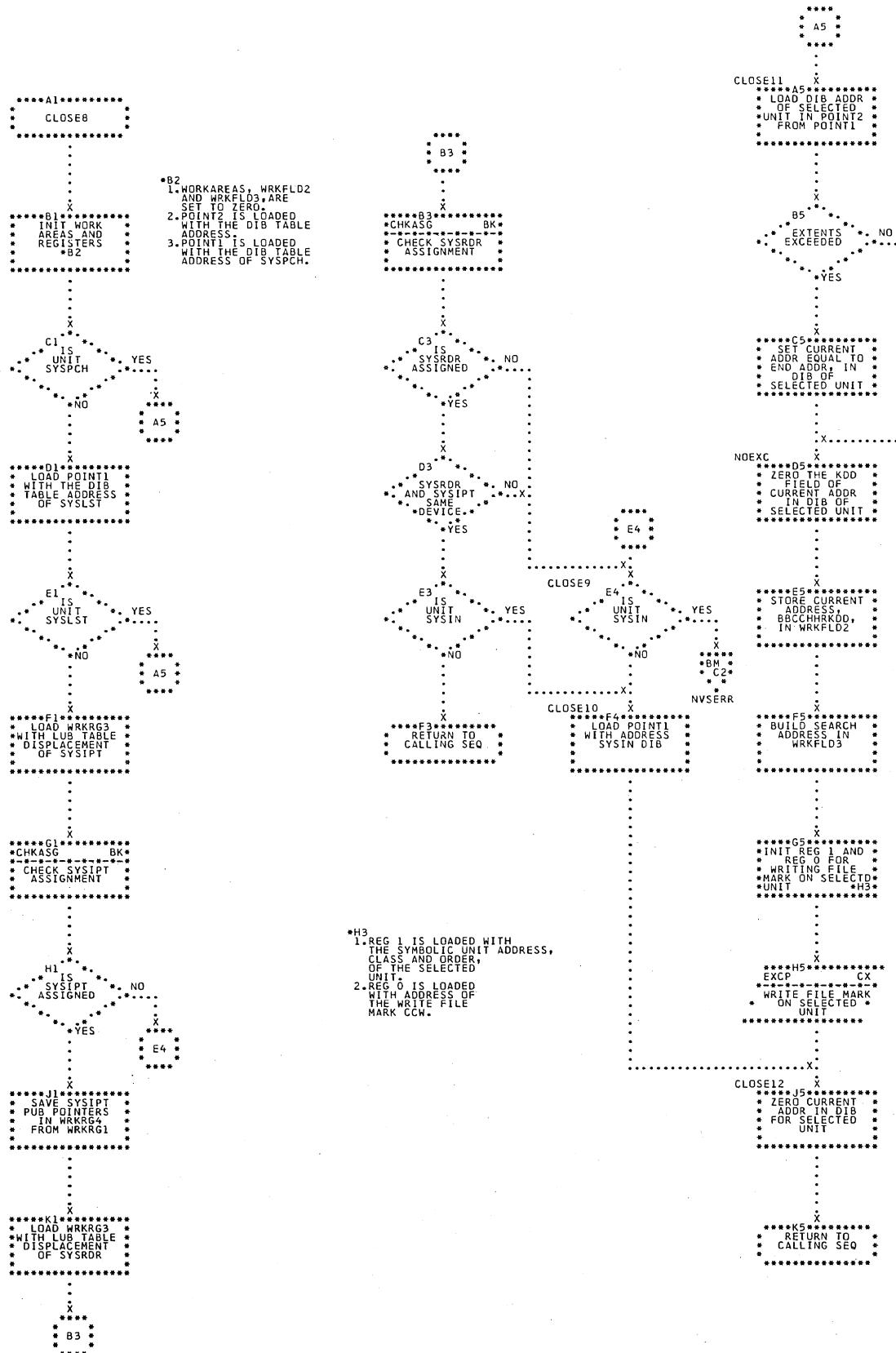


Chart CR. \$JOBCTL - Miscellaneous Subroutines (Part 1 of 5)  
Refer to Chart 06.

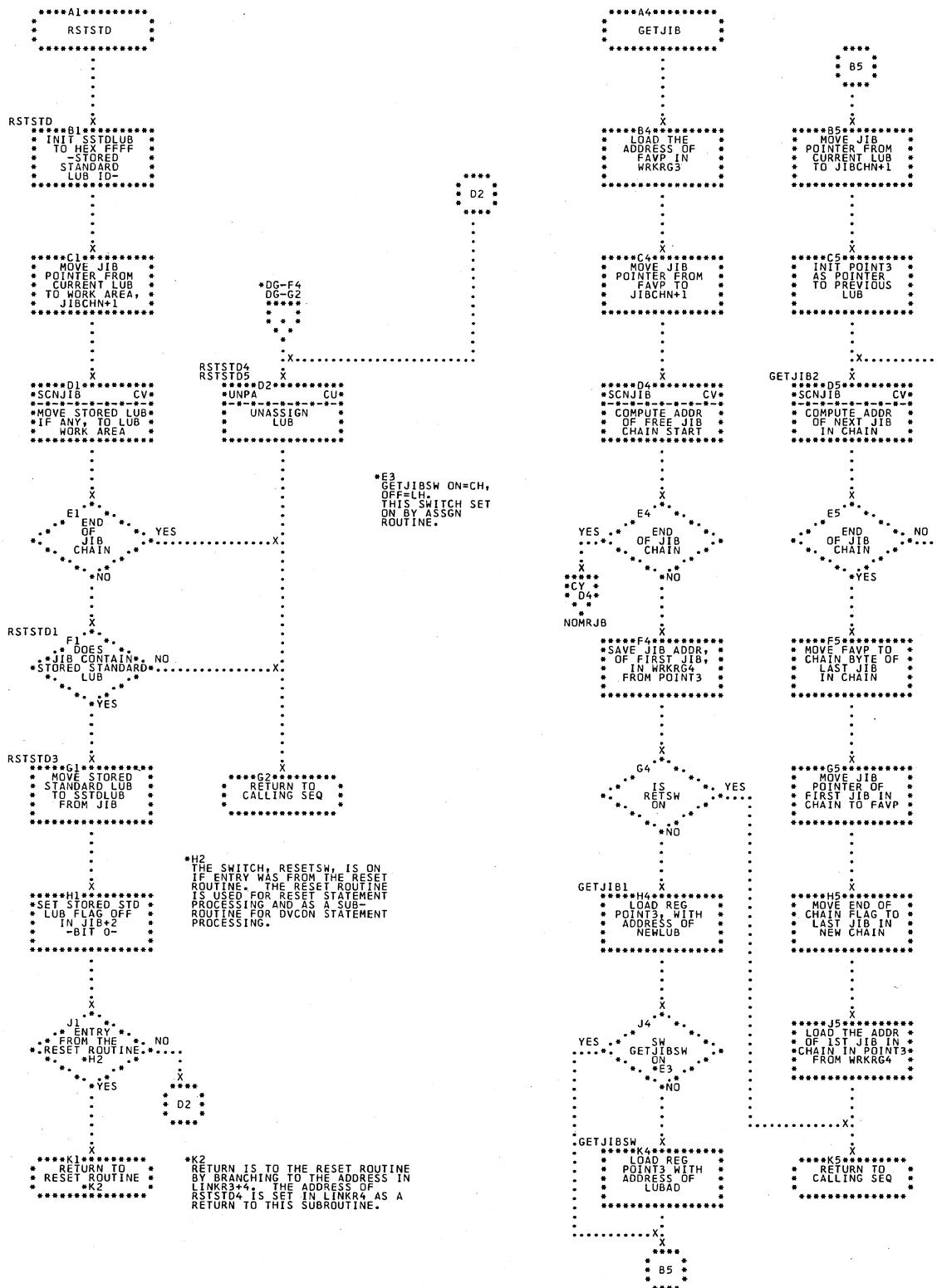


Chart CS. \$JOBCTLD - Miscellaneous Subroutines (Part 2 of 5)  
Refer to Chart 06.

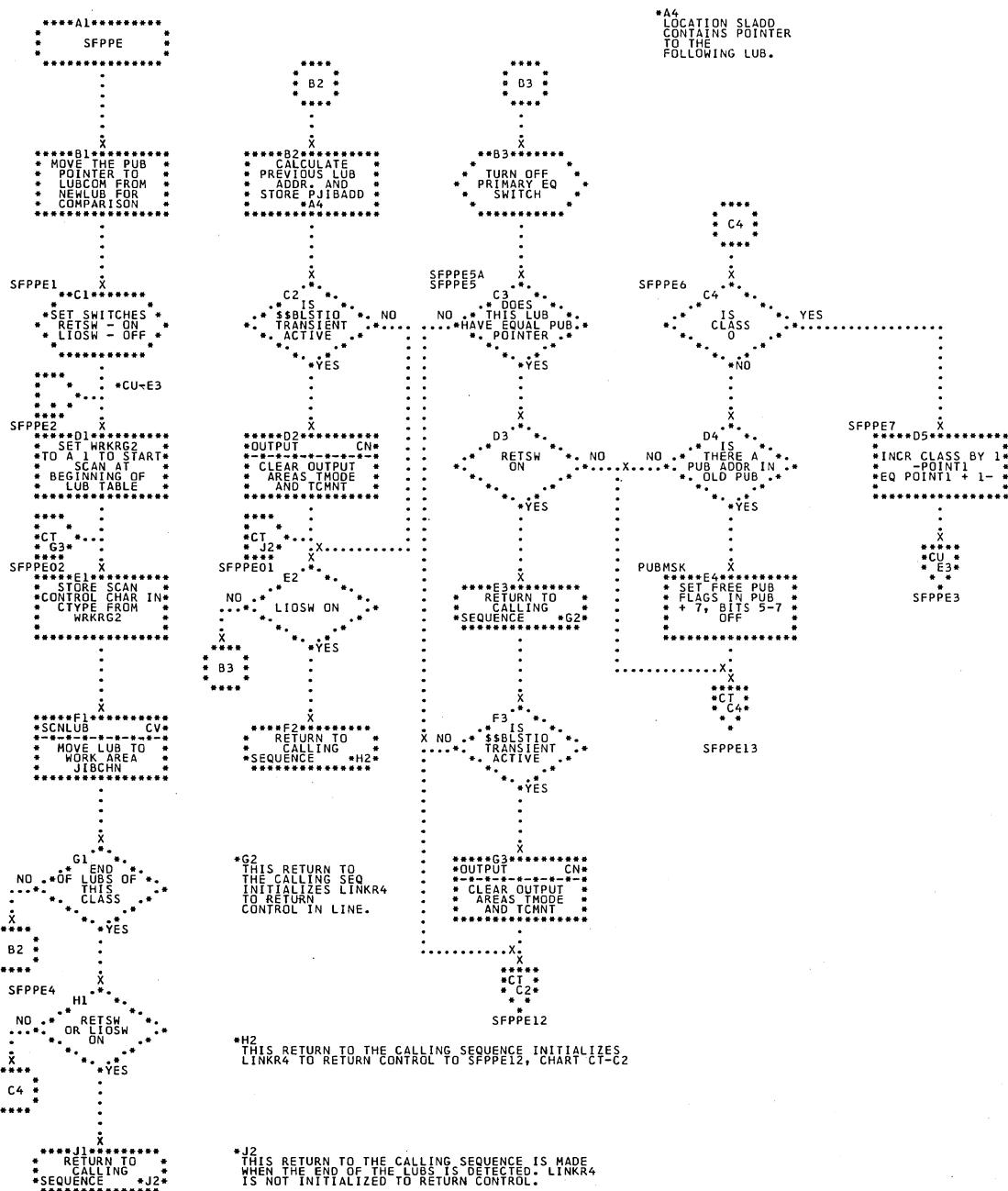


Chart CT. \$JOBCTLD - Miscellaneous Subroutines (Part 3 of 5)  
Refer to Chart 06.

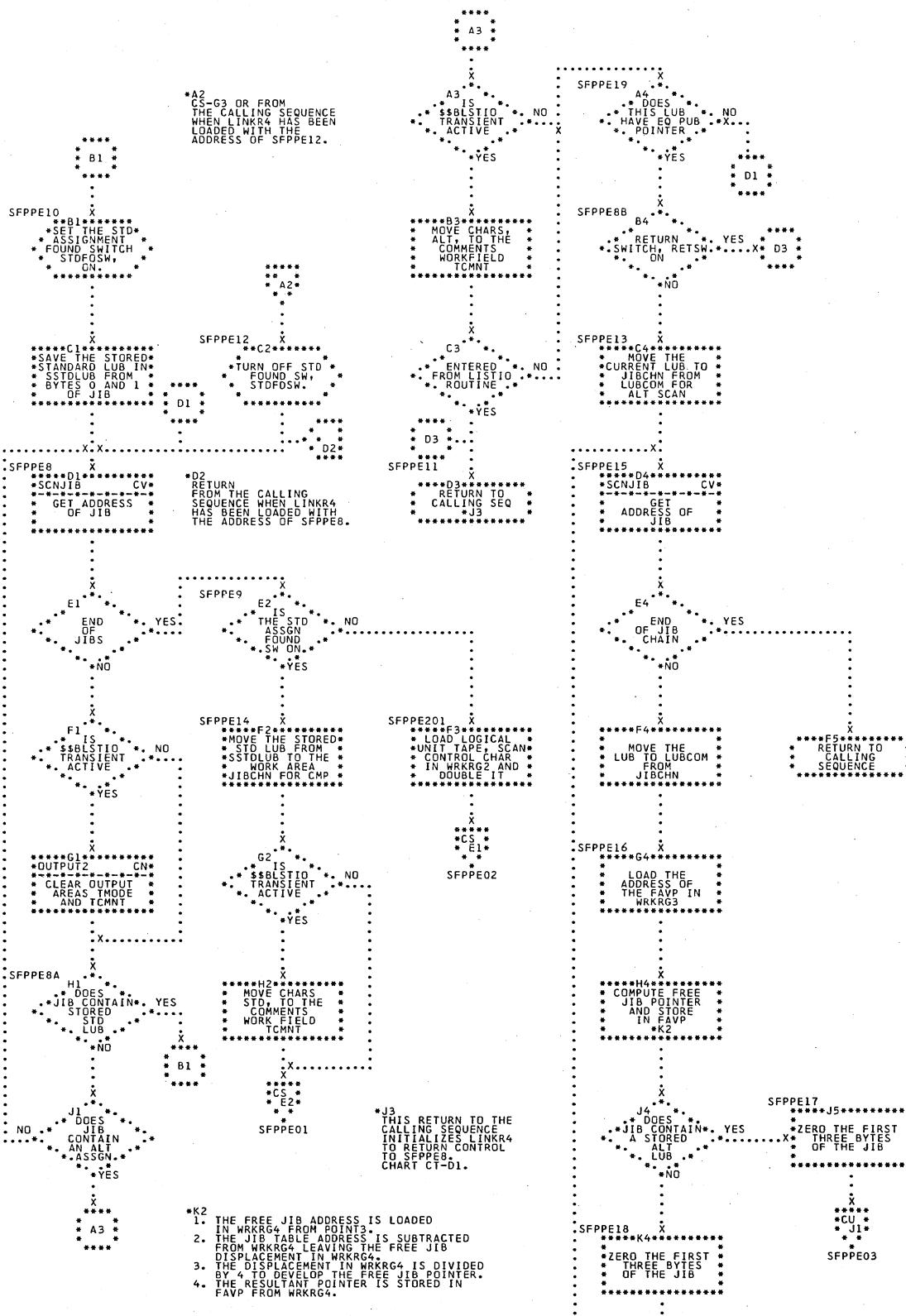


Chart CU. \$JOBCTL - Miscellaneous Subroutines (Part 4 of 5)  
Refer to Chart 06.

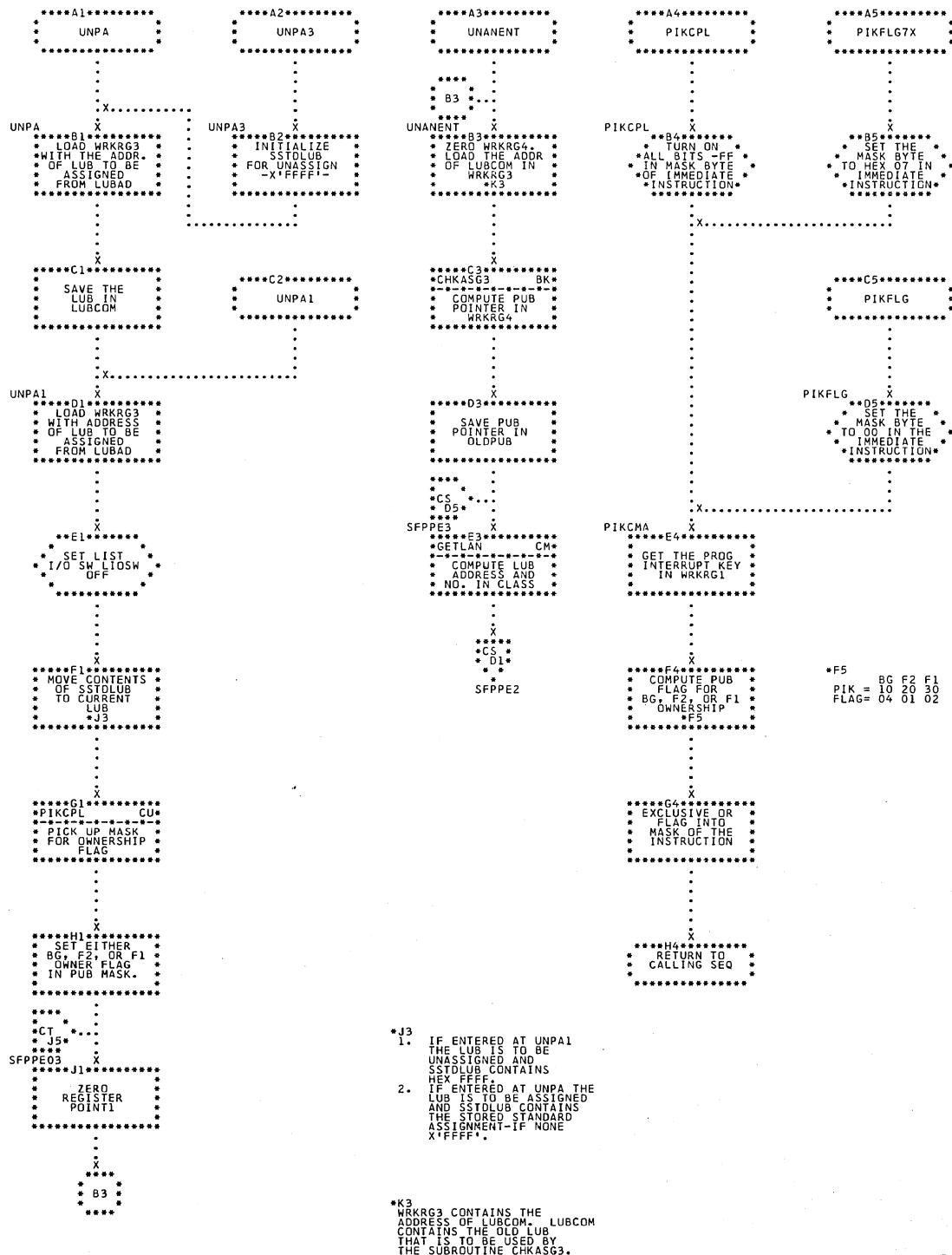


Chart CV. \$JOBCTL - Miscellaneous Subroutines (Part 5 of 5)  
 Refer to Chart 06.

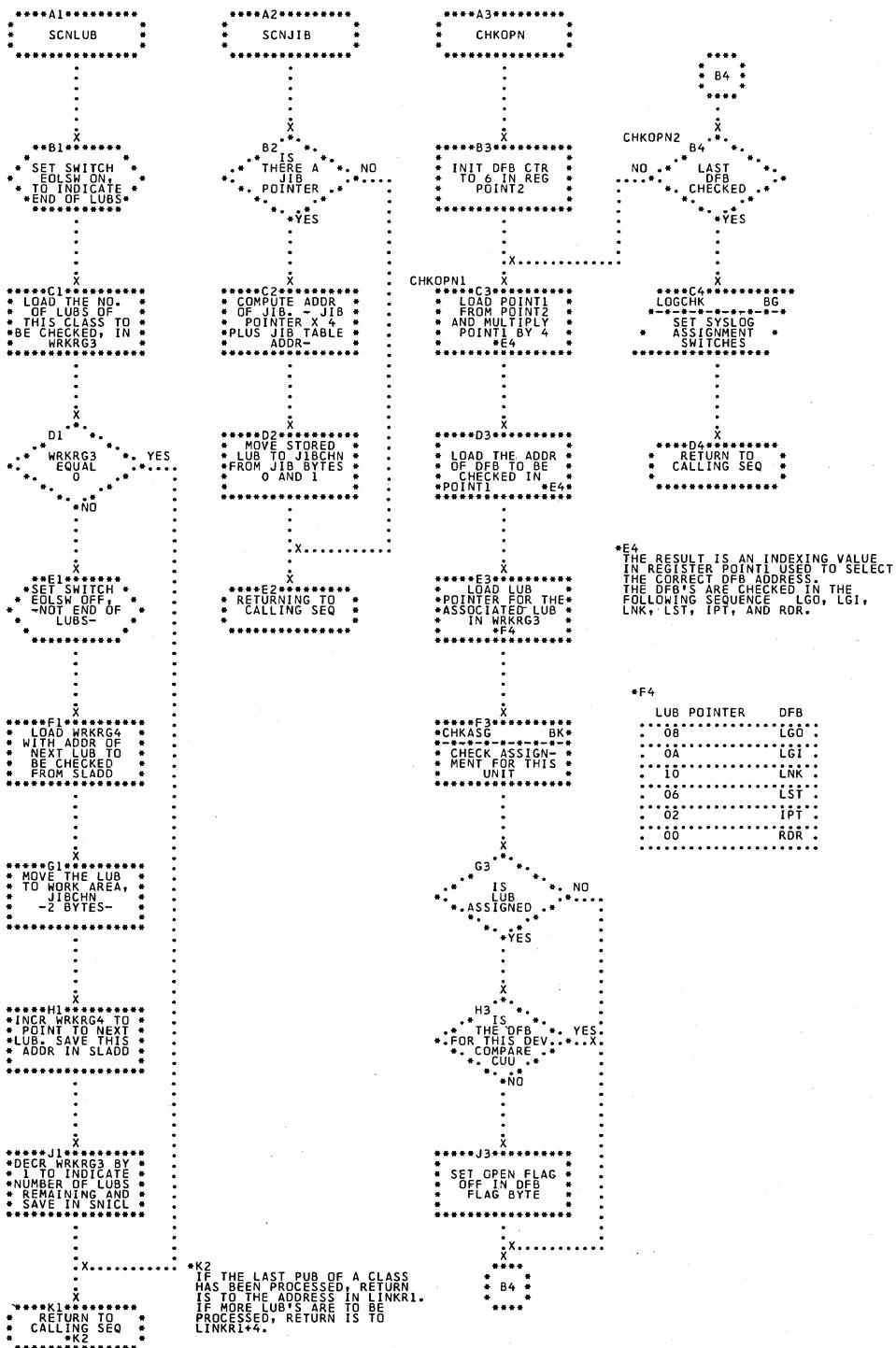
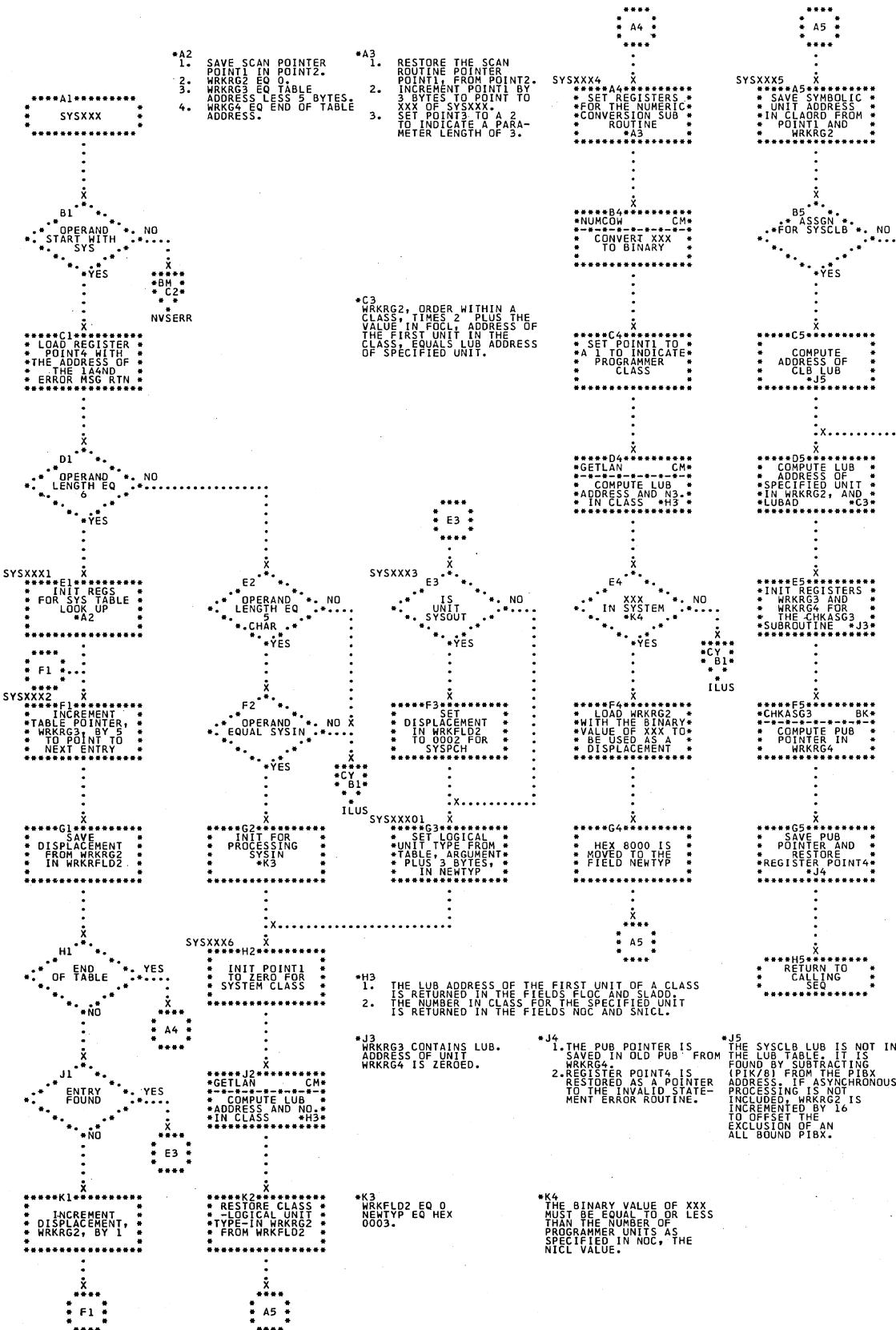
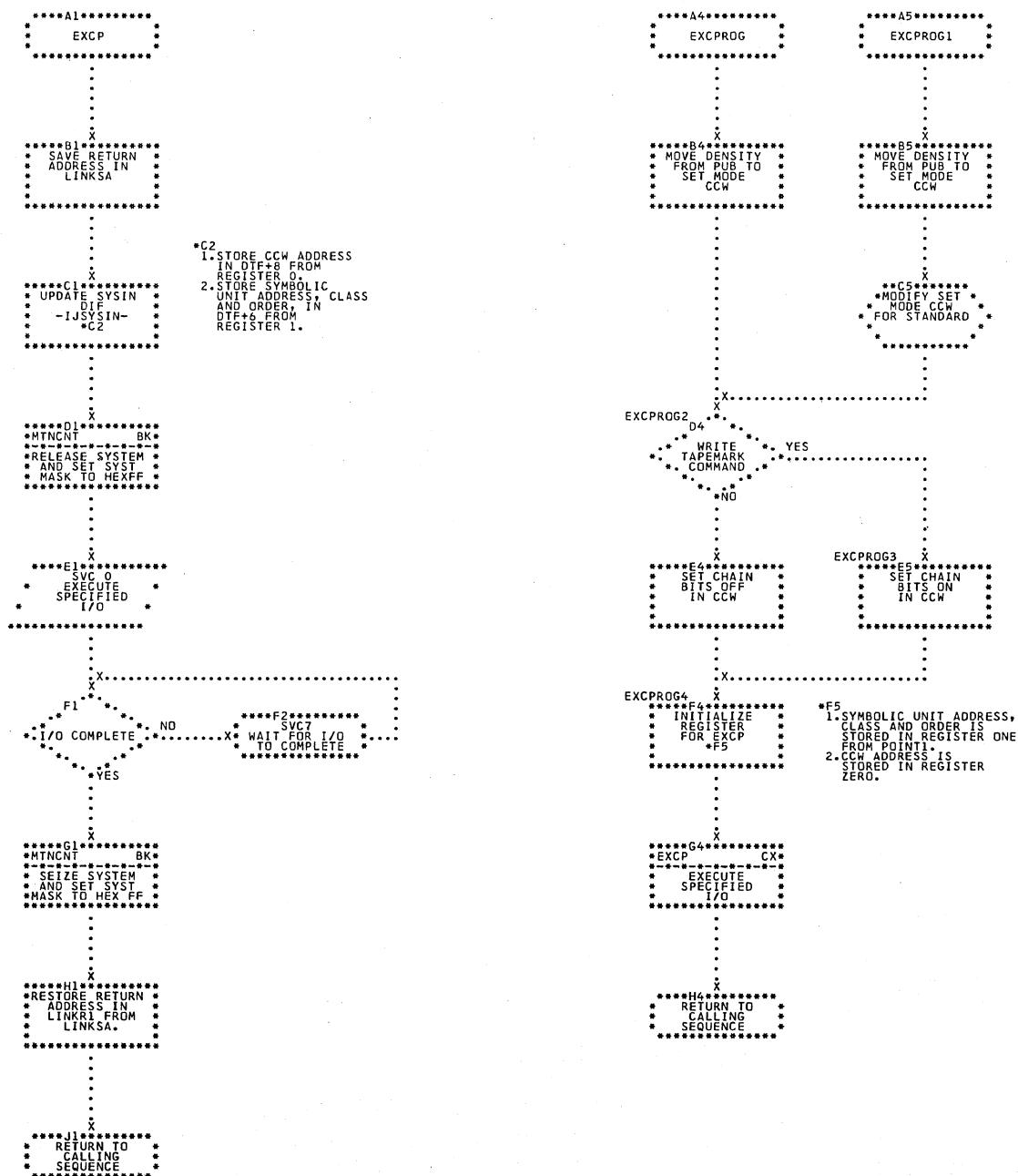


Chart CW. \$JOBCTL - SYSXXX Operand Processor  
Refer to Chart 06.



**Chart CX. \$JOBCTL - EXCP Subroutines**  
 Refer to Chart 06.



**Chart CY. \$JOBCTL - Error Subroutines**  
Refer to Chart 06.

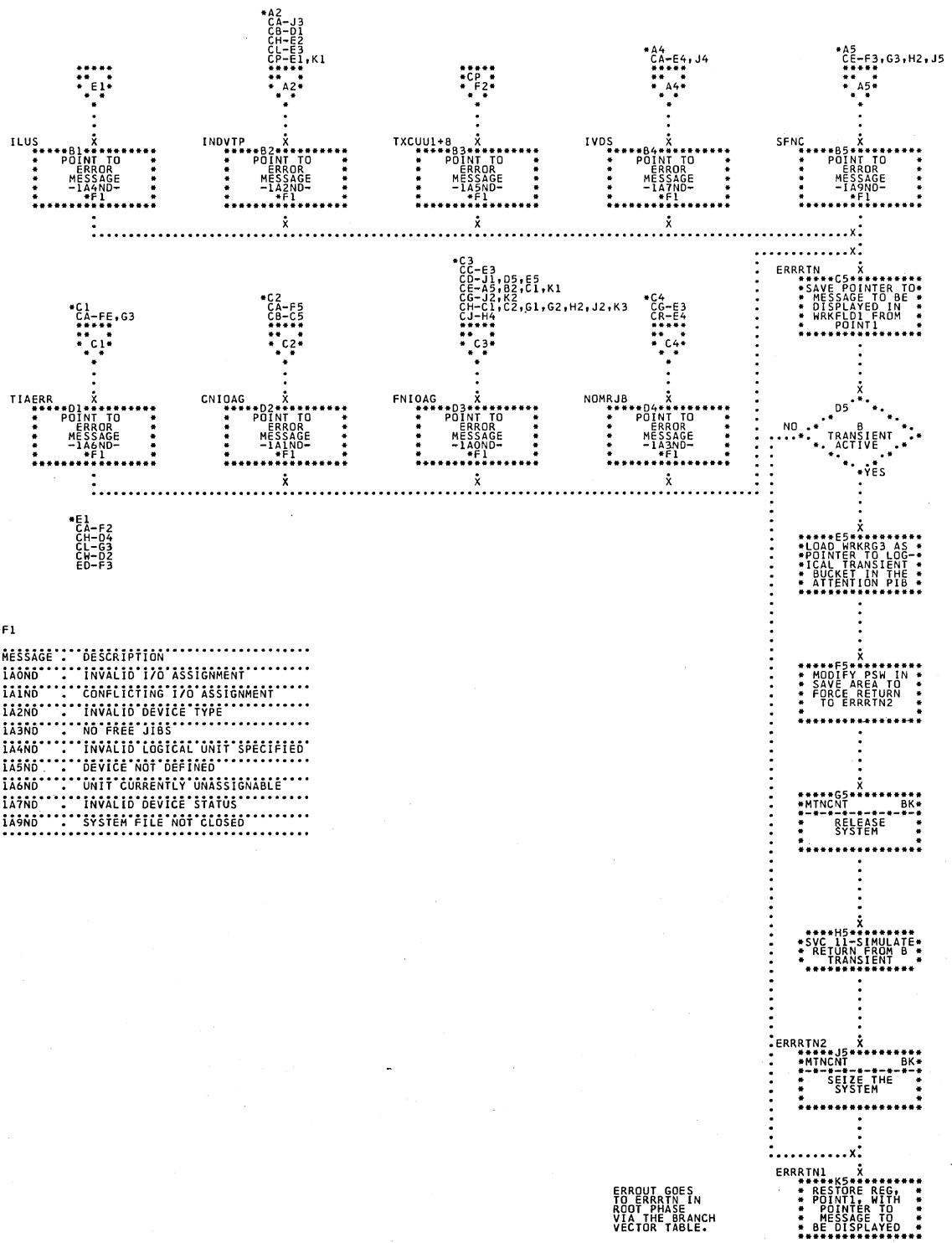


Chart DA. \$JOBCTLF - LISTIO Statement Processor (Part 1 of 5)  
Refer to Chart 07.

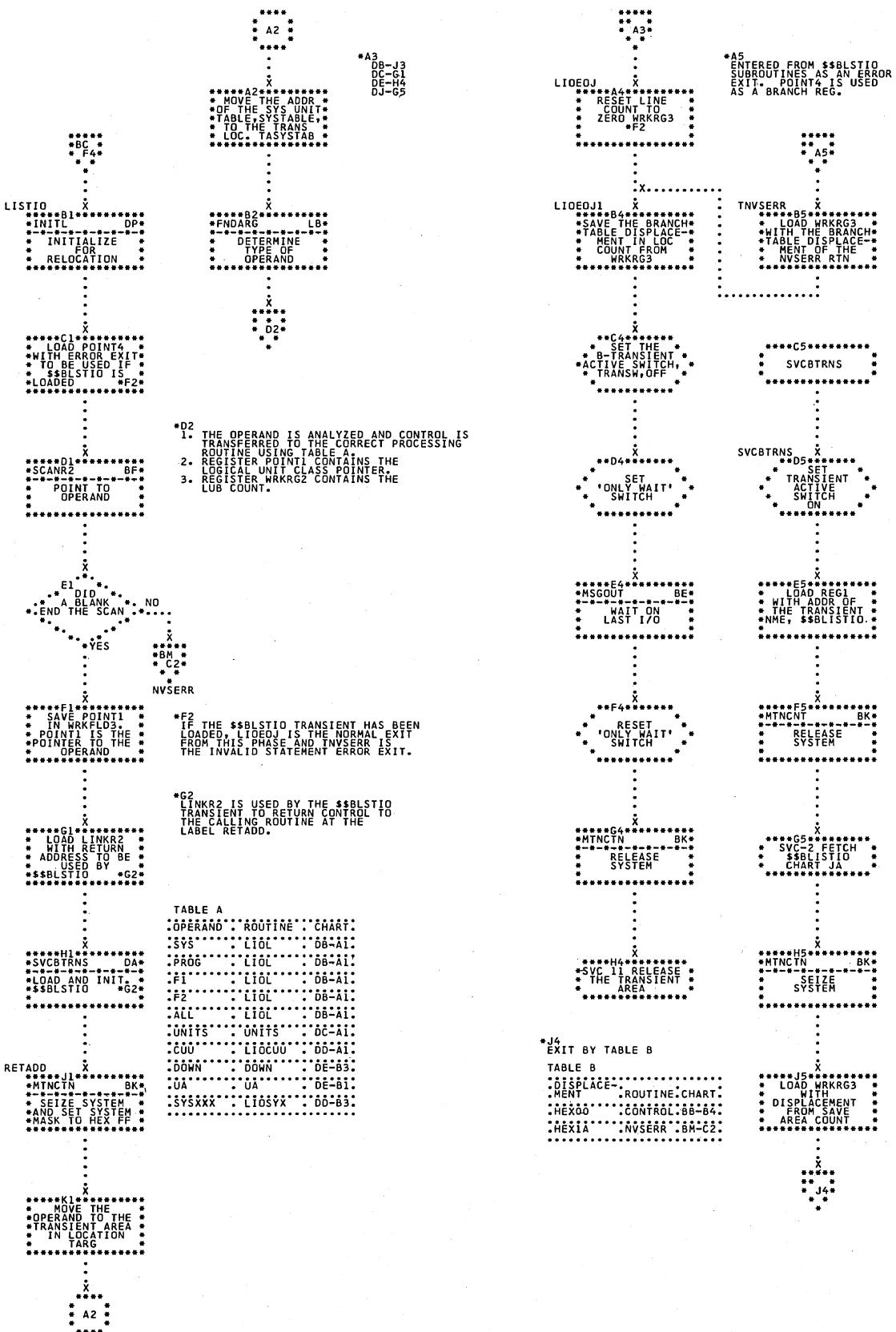
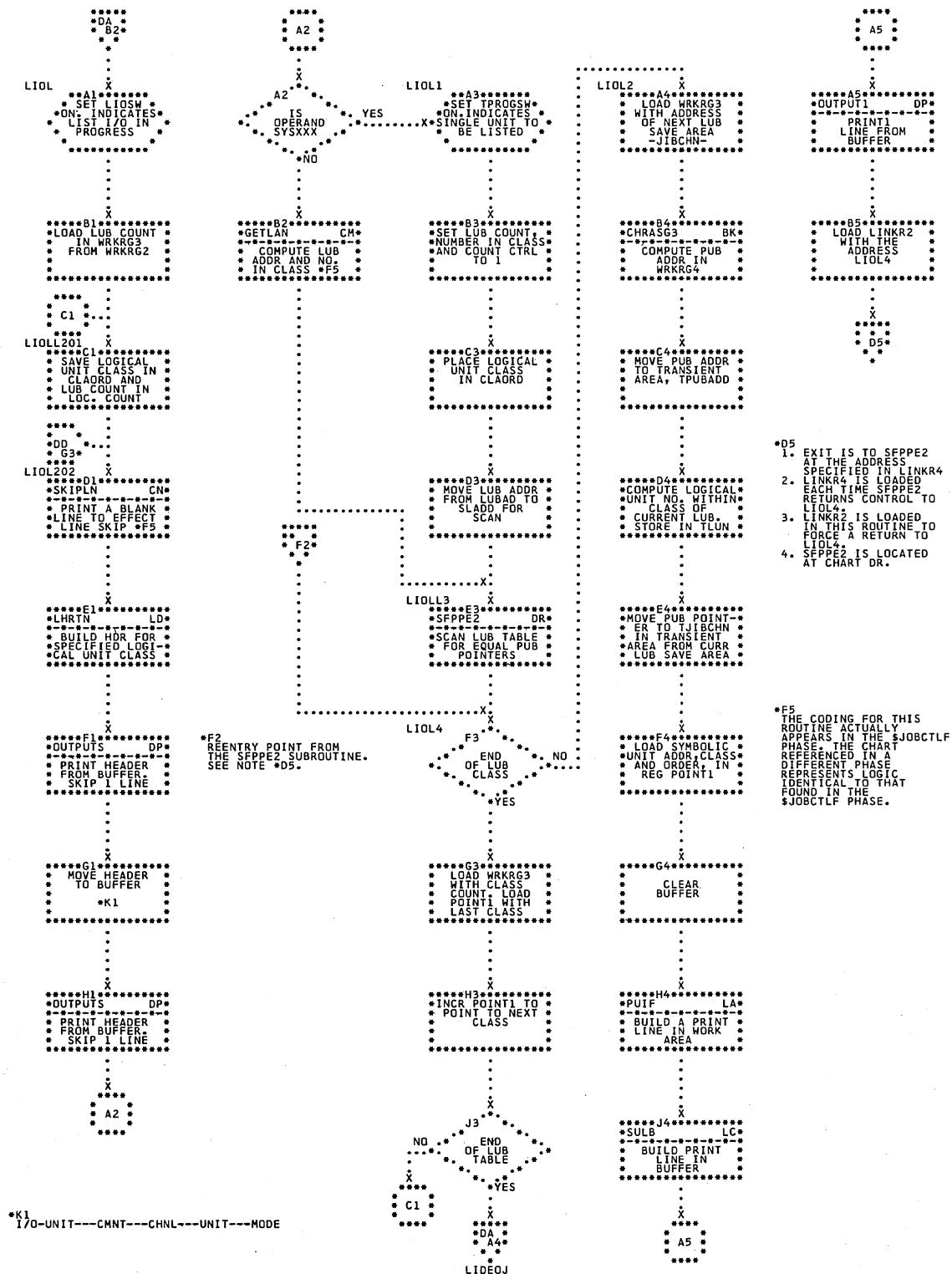


Chart DB. \$JOBCTLF - LISTIO Statement Processor (Part 2 of 5)  
Refer to Chart 07.



**Chart DC. \$JOBCTLF - LISTIO Statement Processor (Part 3 of 5)**  
Refer to Chart 07.

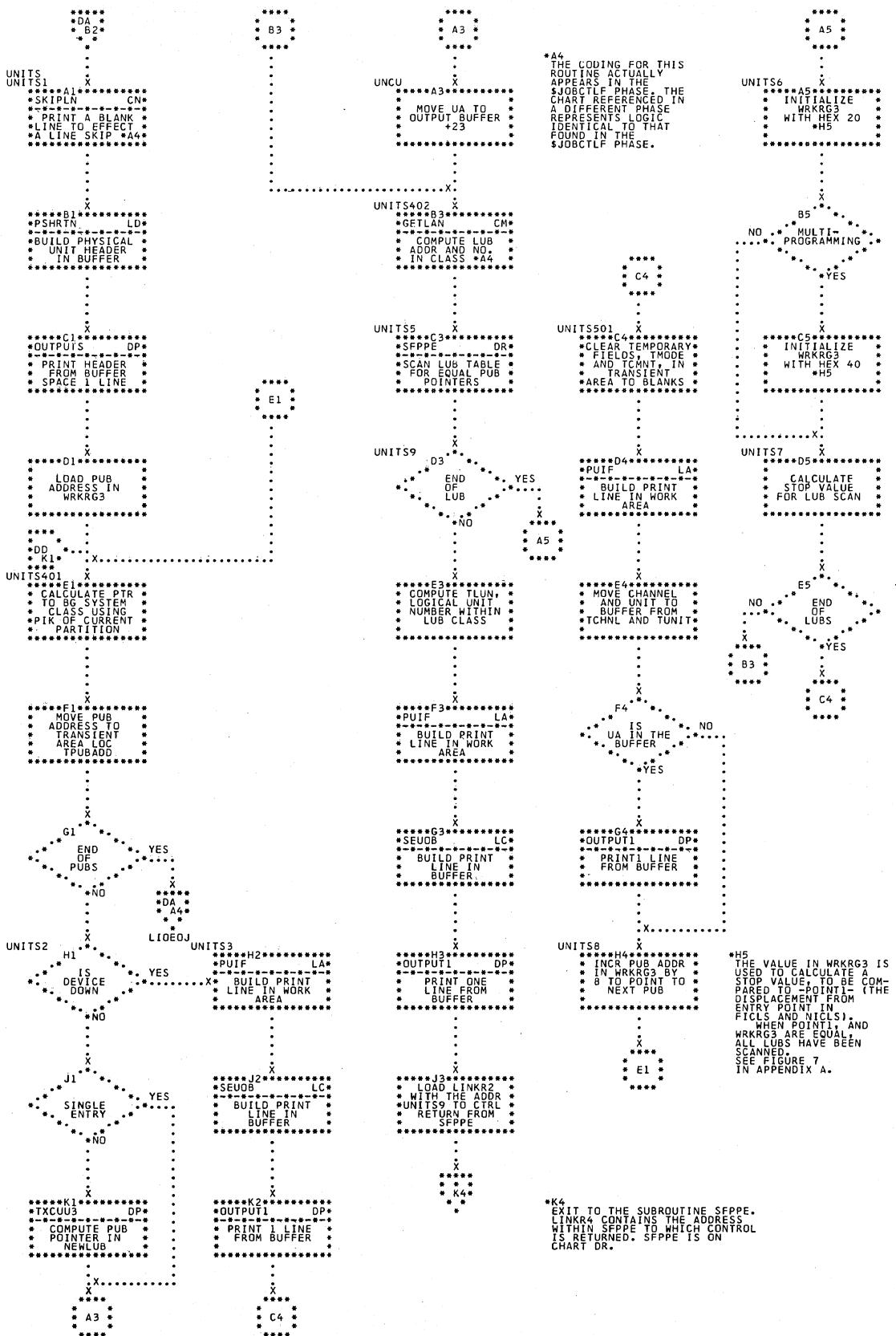


Chart DD. \$JOBCTLF - LISTIO Statement Processor (Part 4 of 5)  
Refer to Chart 07.

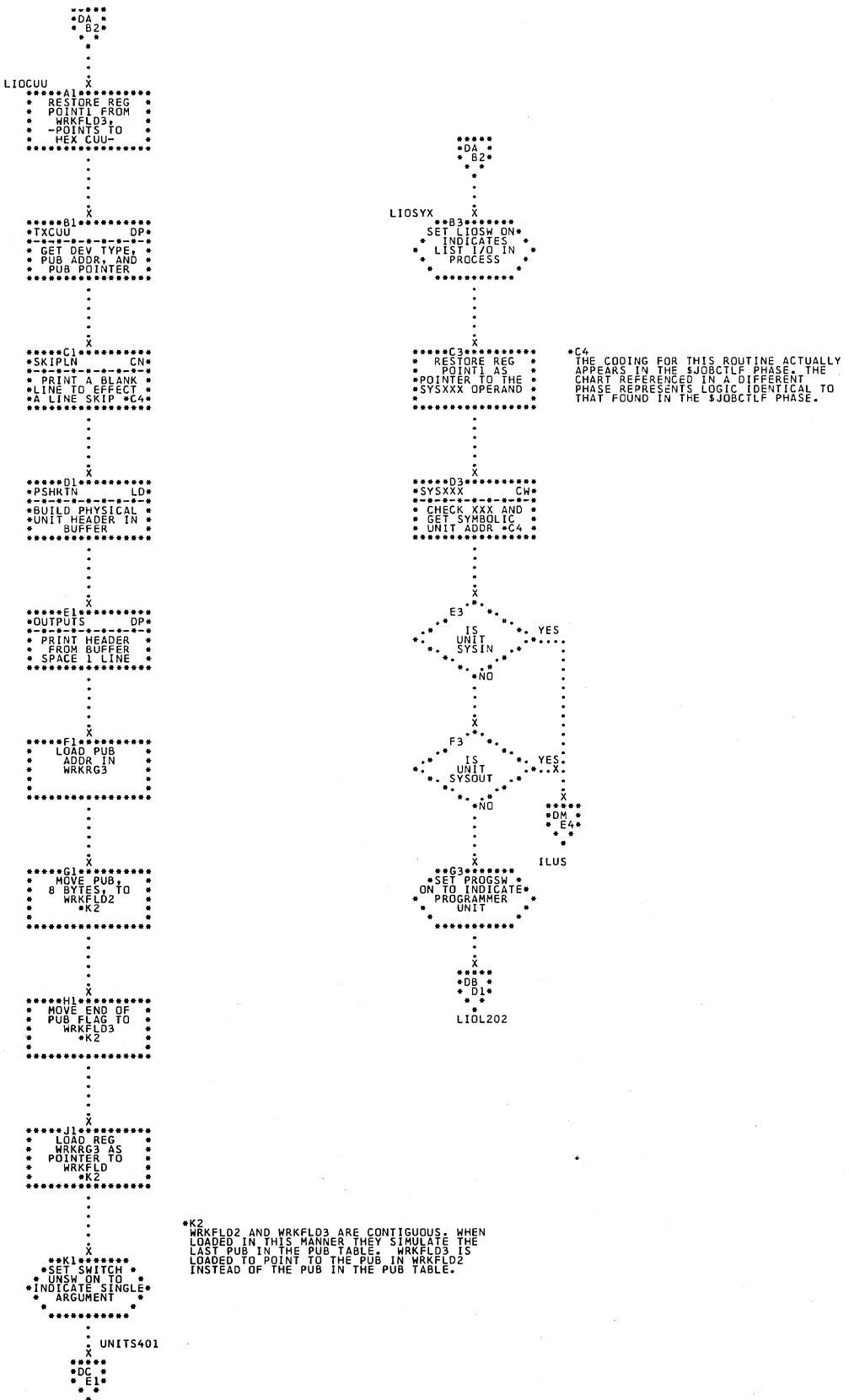


Chart DE. \$JOBCTLF - LISTIO Statement Processor (Part 5 of 5)  
Refer to Chart 07.

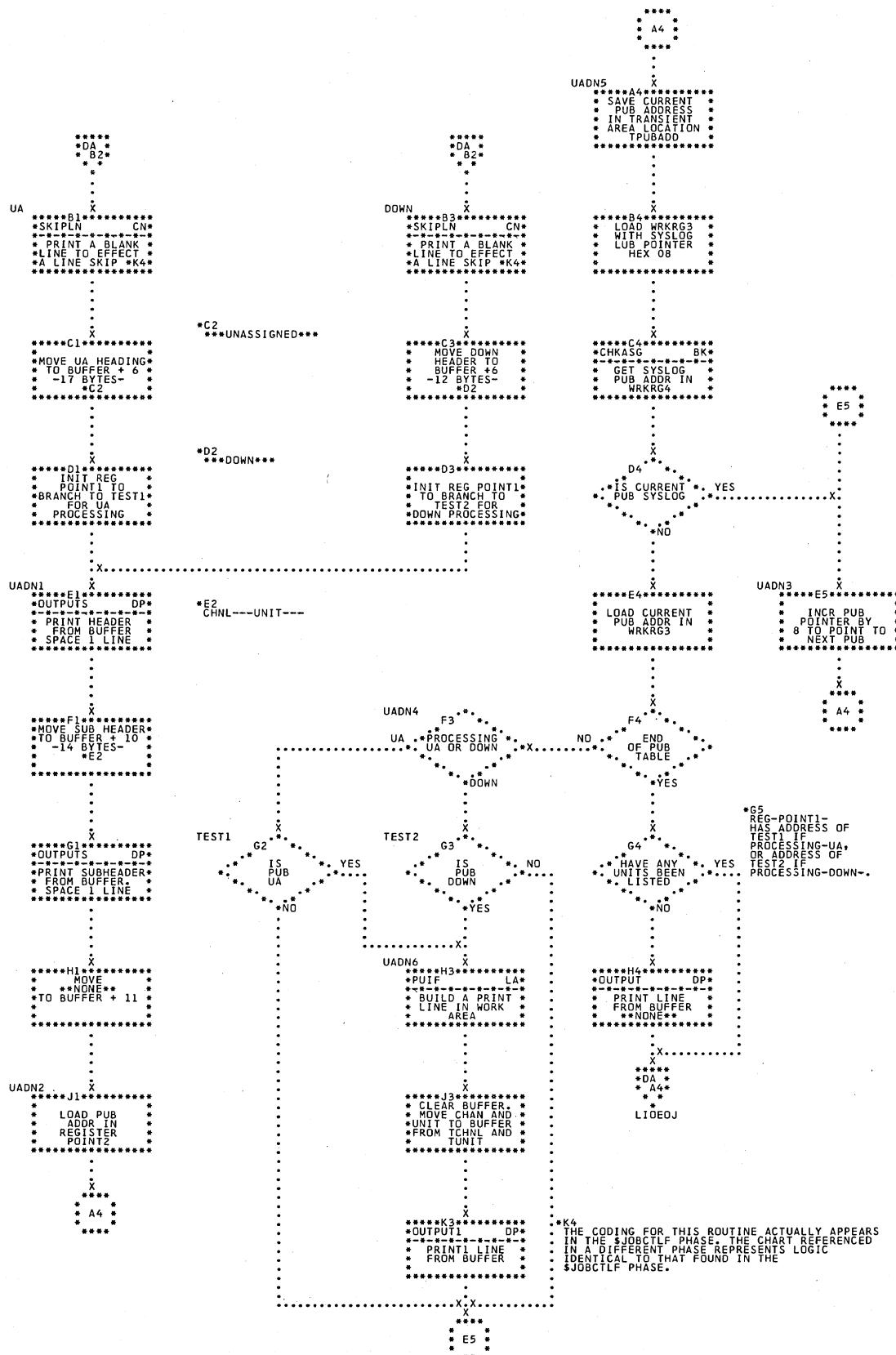


Chart DF. \$JOBCTLF - RESET Statement Processor (Part 1 of 2)  
Refer to Chart 07.

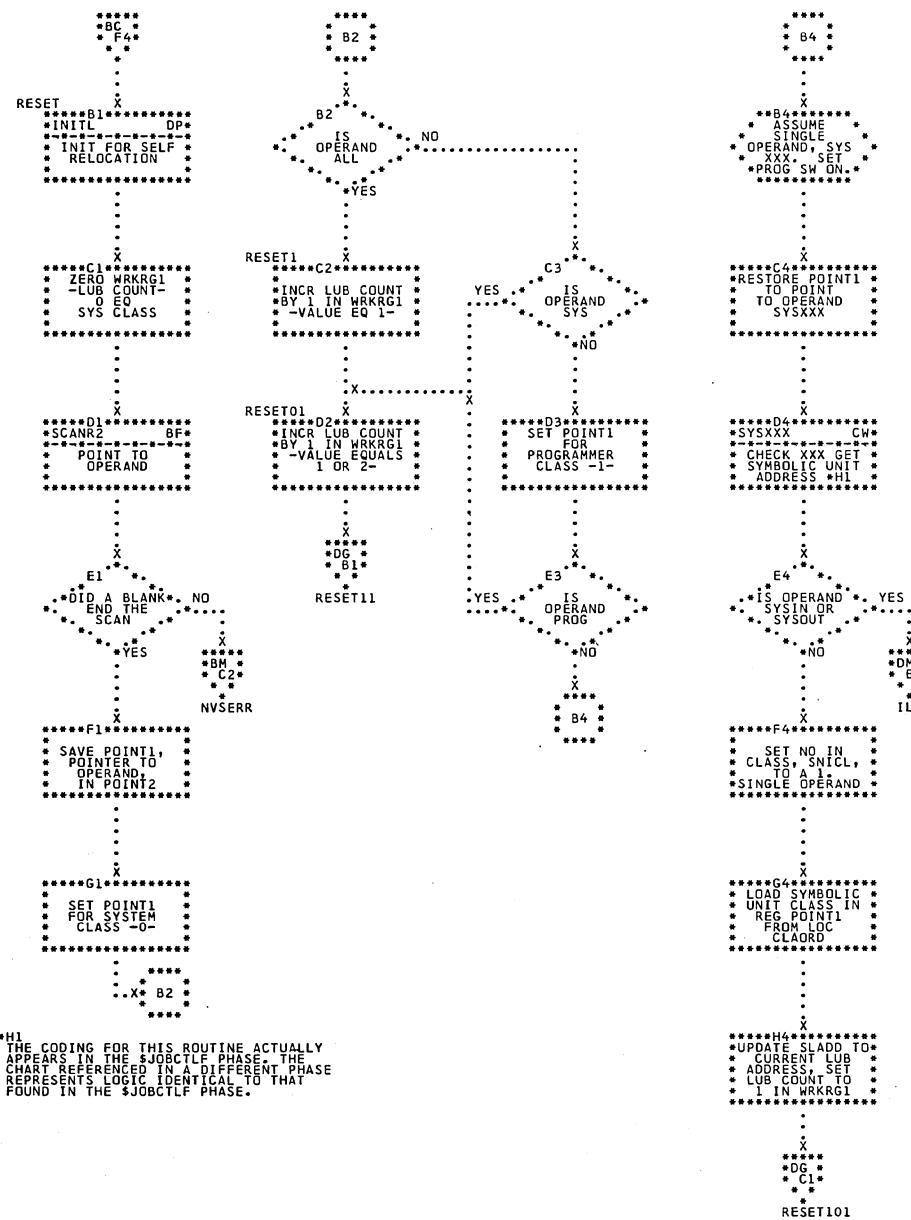


Chart DG. \$JOBCTLF - RESET Statement Processor (Part 2 of 2)  
Refer to Chart 07.

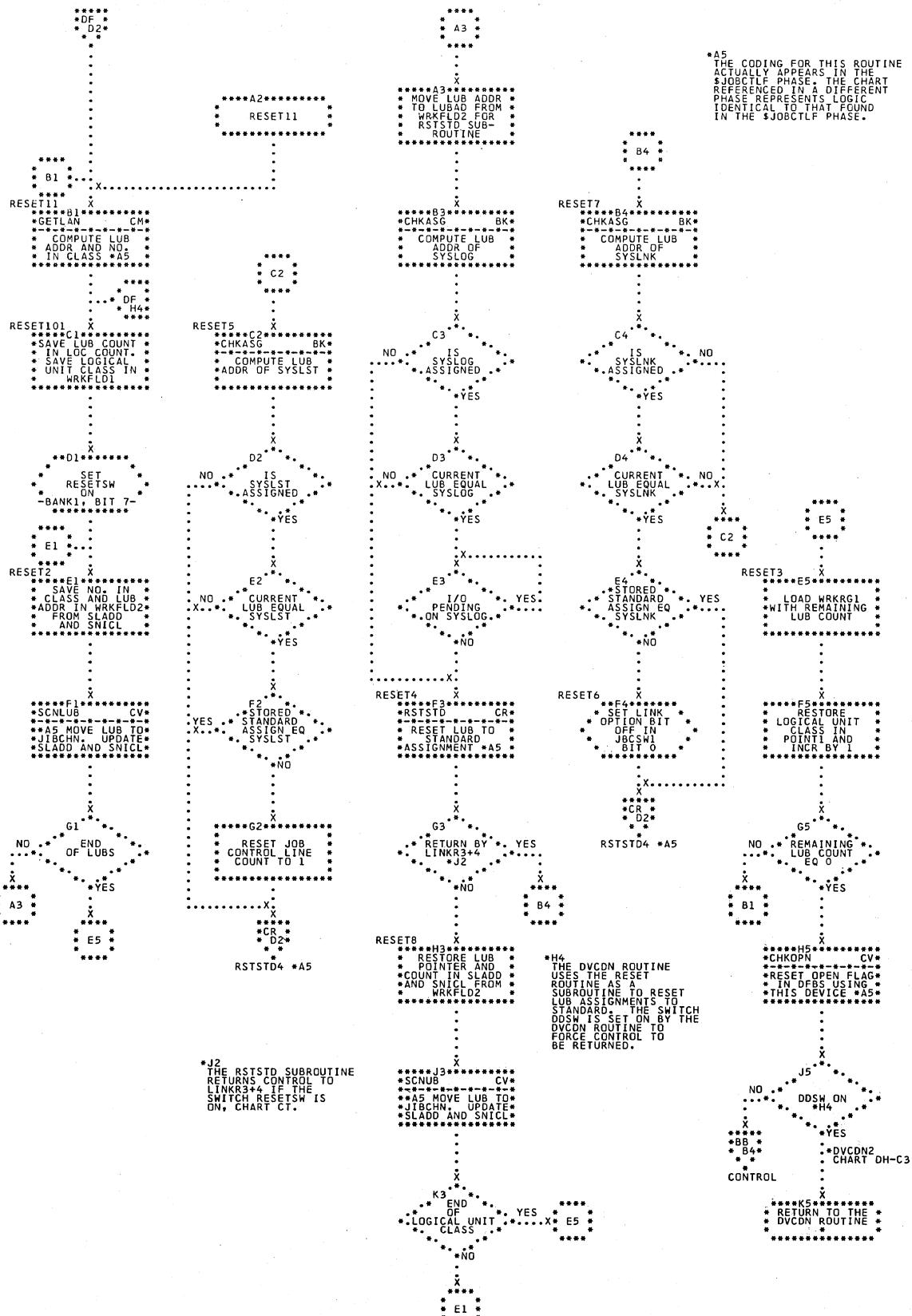
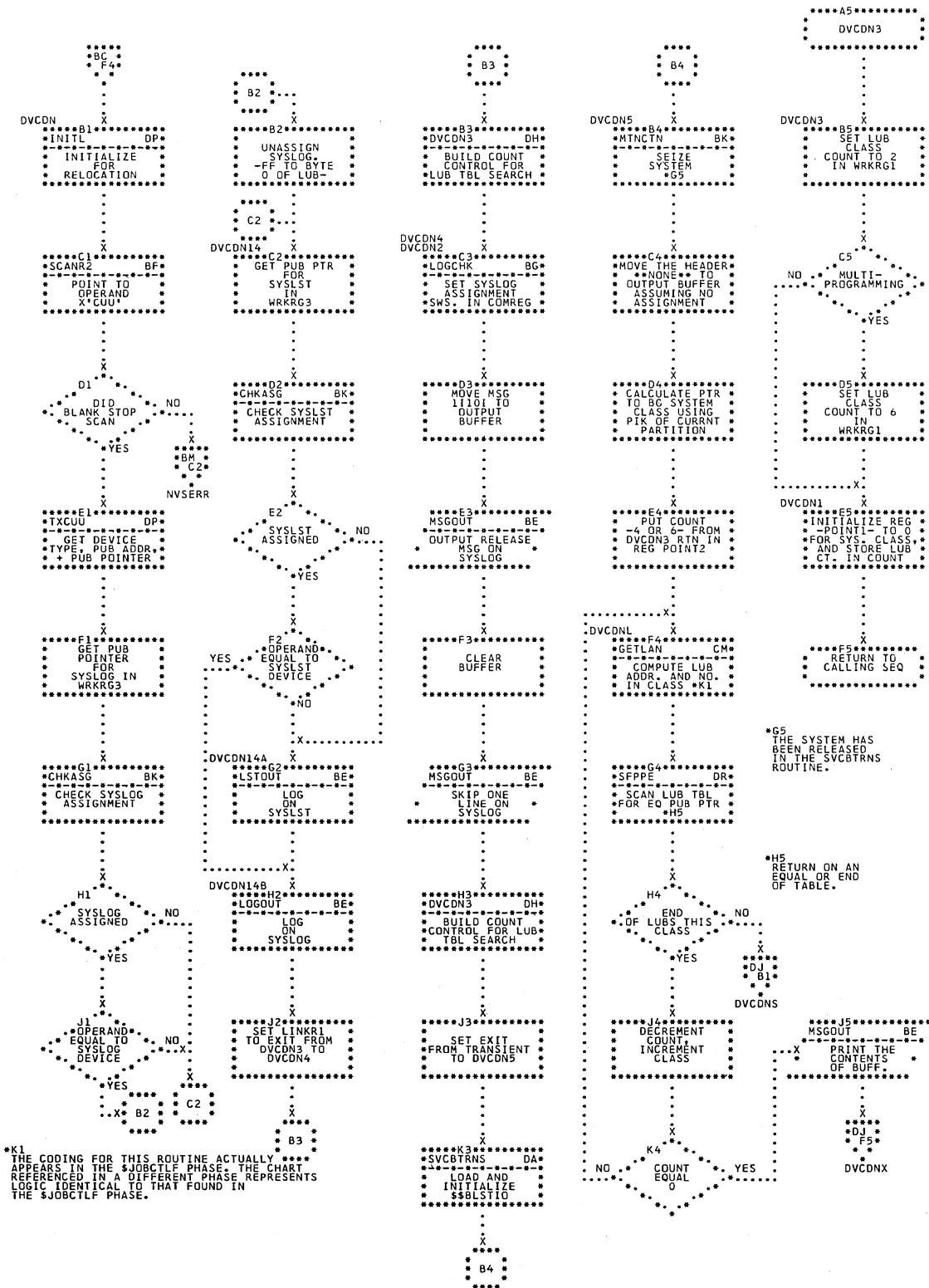


Chart DH. \$JOBCTLF - DVCDN Statement Processor (Part 1 of 2)  
Refer to Chart 07.



**Chart DJ. \$JOBCTLF - DVCDN Statement Processor (Part 2 of 2).**  
Refer to Chart 07.

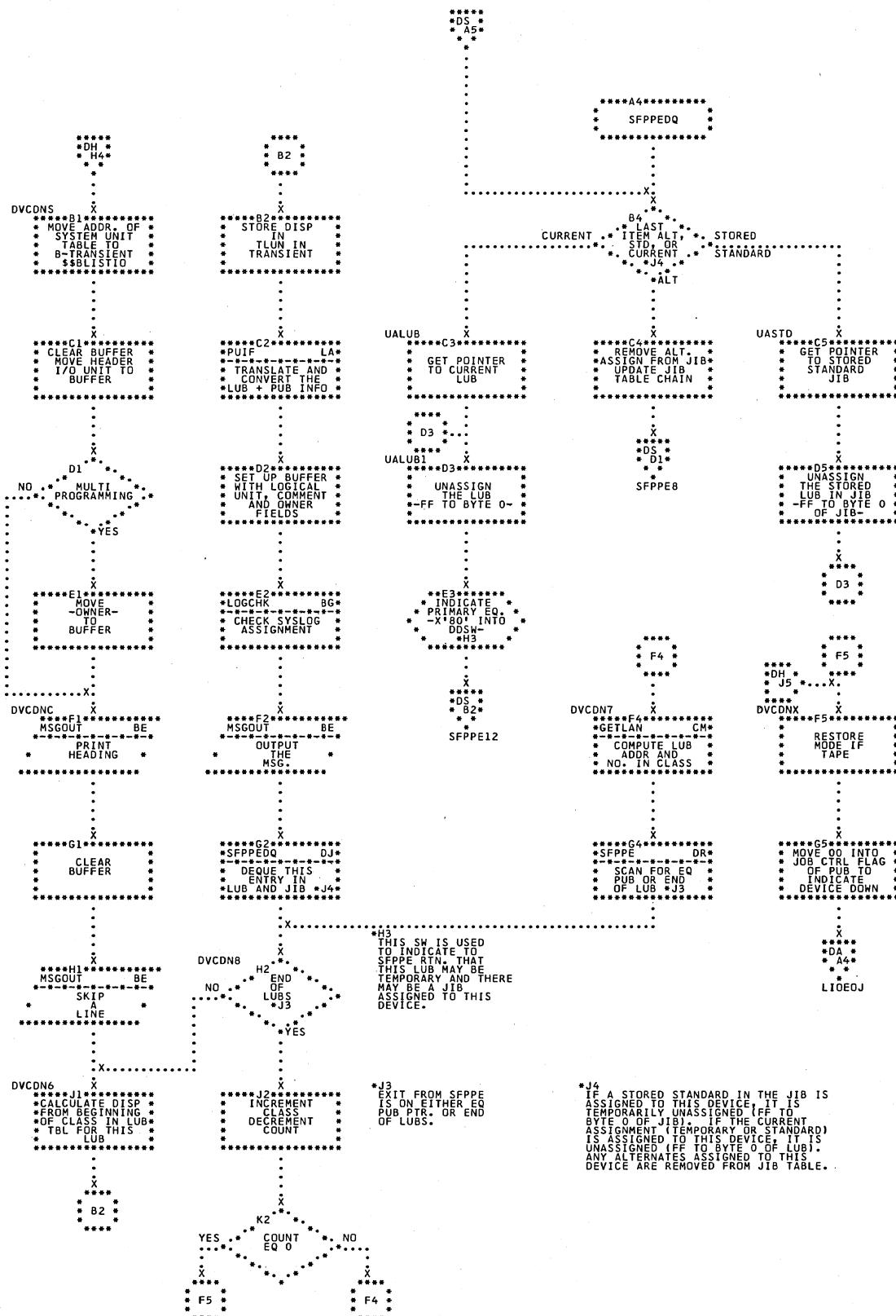


Chart DK. \$JOBCTLF - DVCUP and MAP Statement Processors  
Refer to Chart 07.

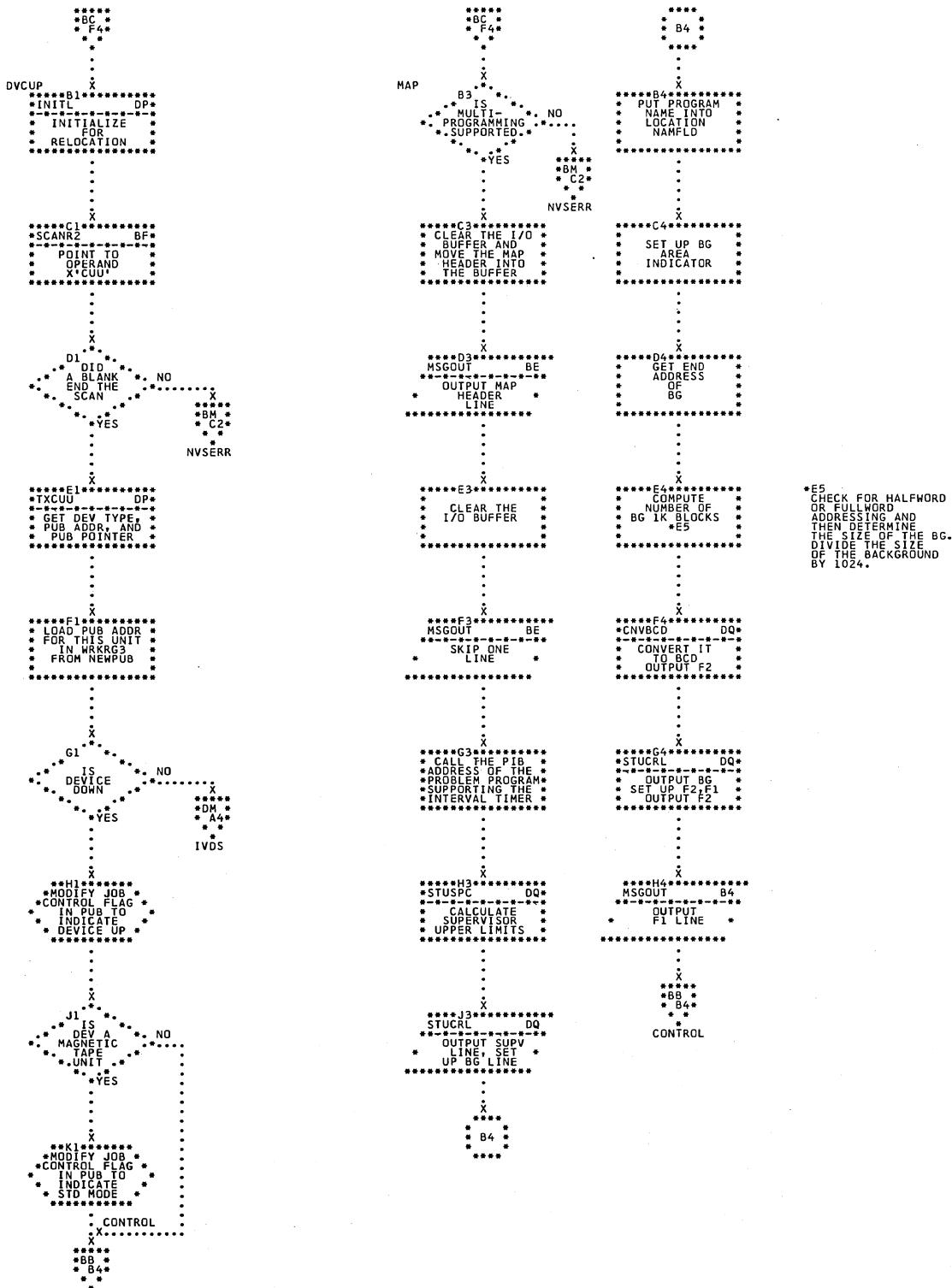


Chart DL. \$JOBCTLF - UNBATCH Statement Processor  
Refer to Chart 07.

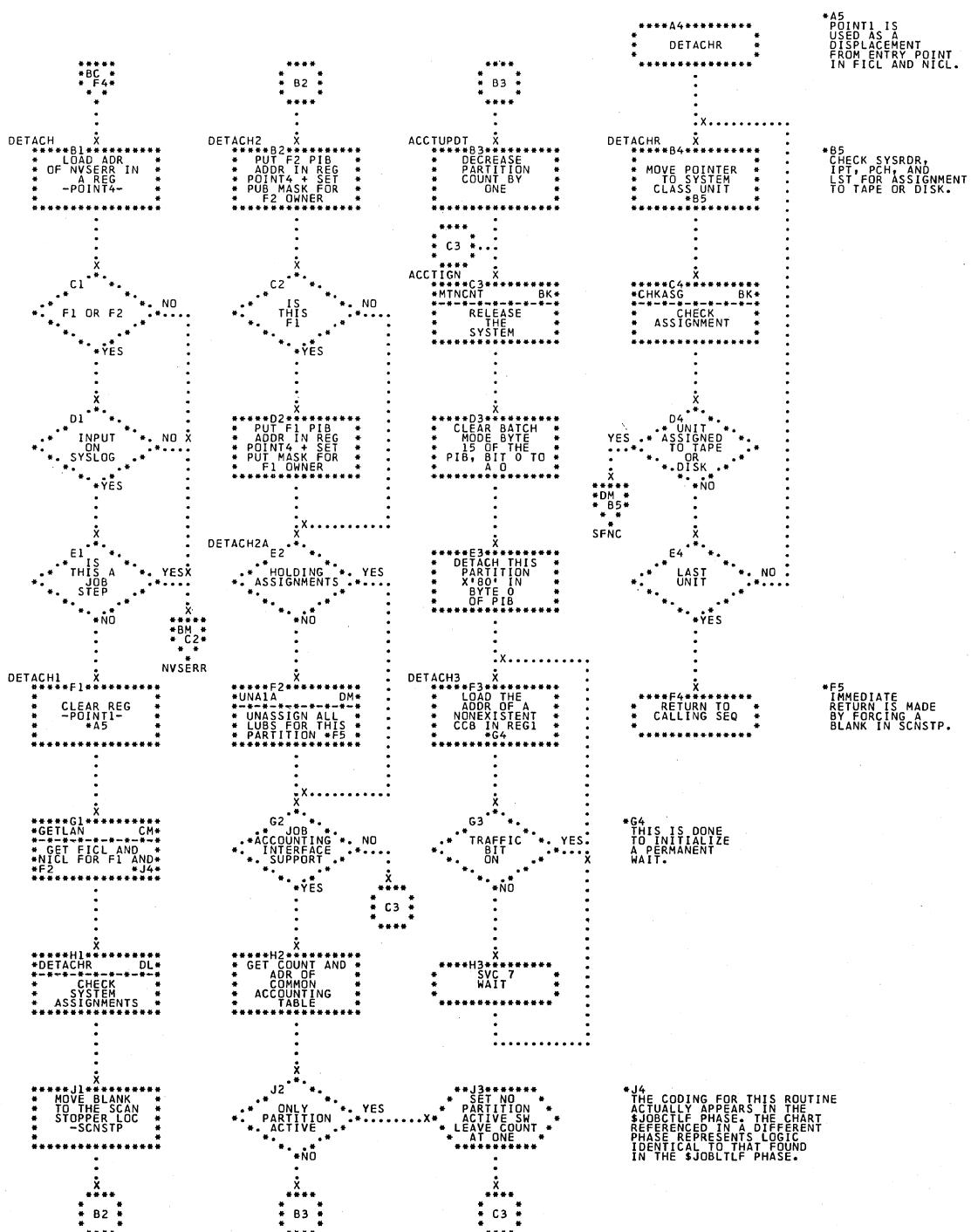


Chart DM. \$JOBCTLF - Error Subroutines and UNA Statement Processor (Part 1 of 2)  
Refer to Chart 07.

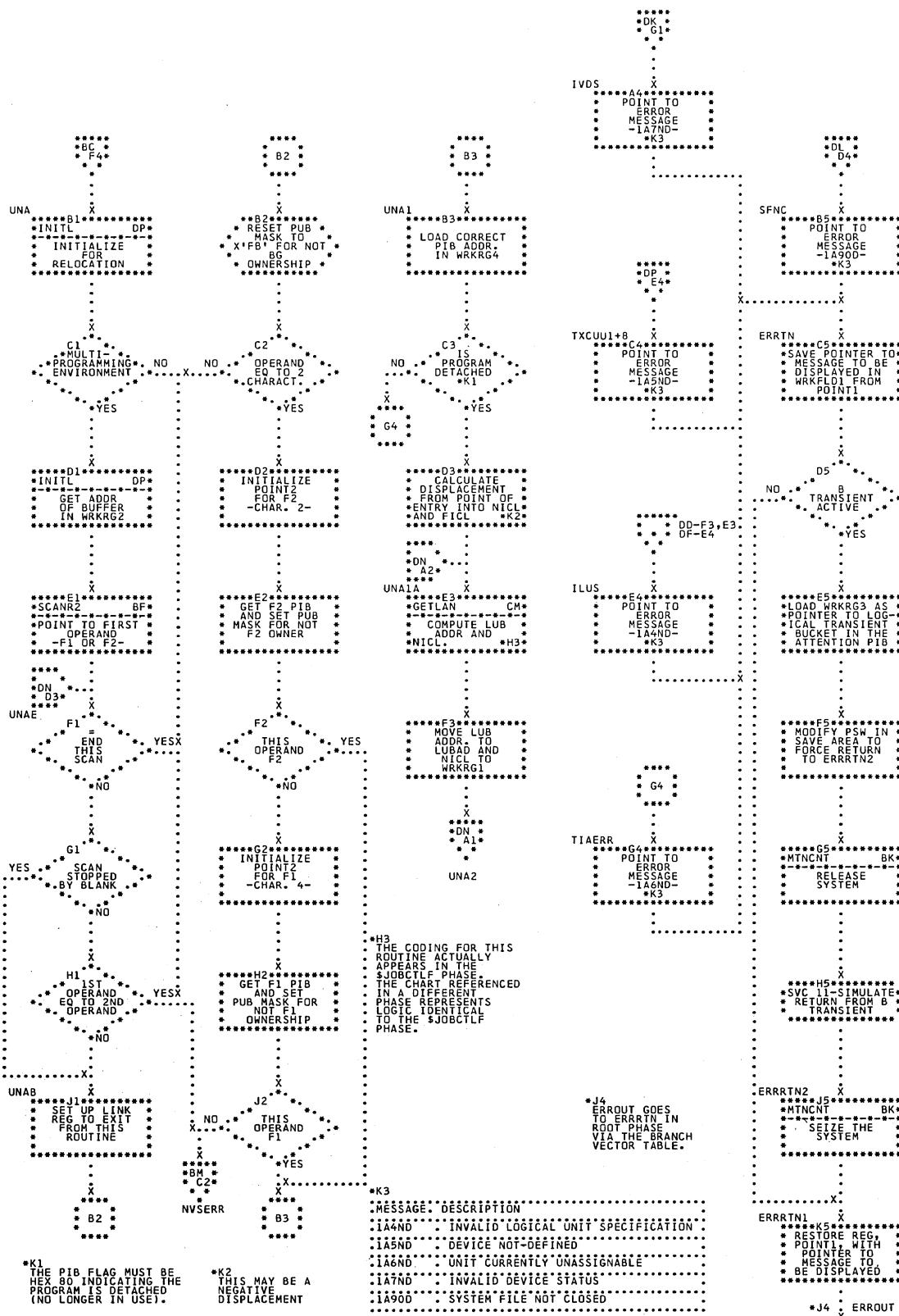


Chart DN. \$JOBCTLF - UNA Statement Processor (Part 2 of 2)  
Refer to Chart 07.

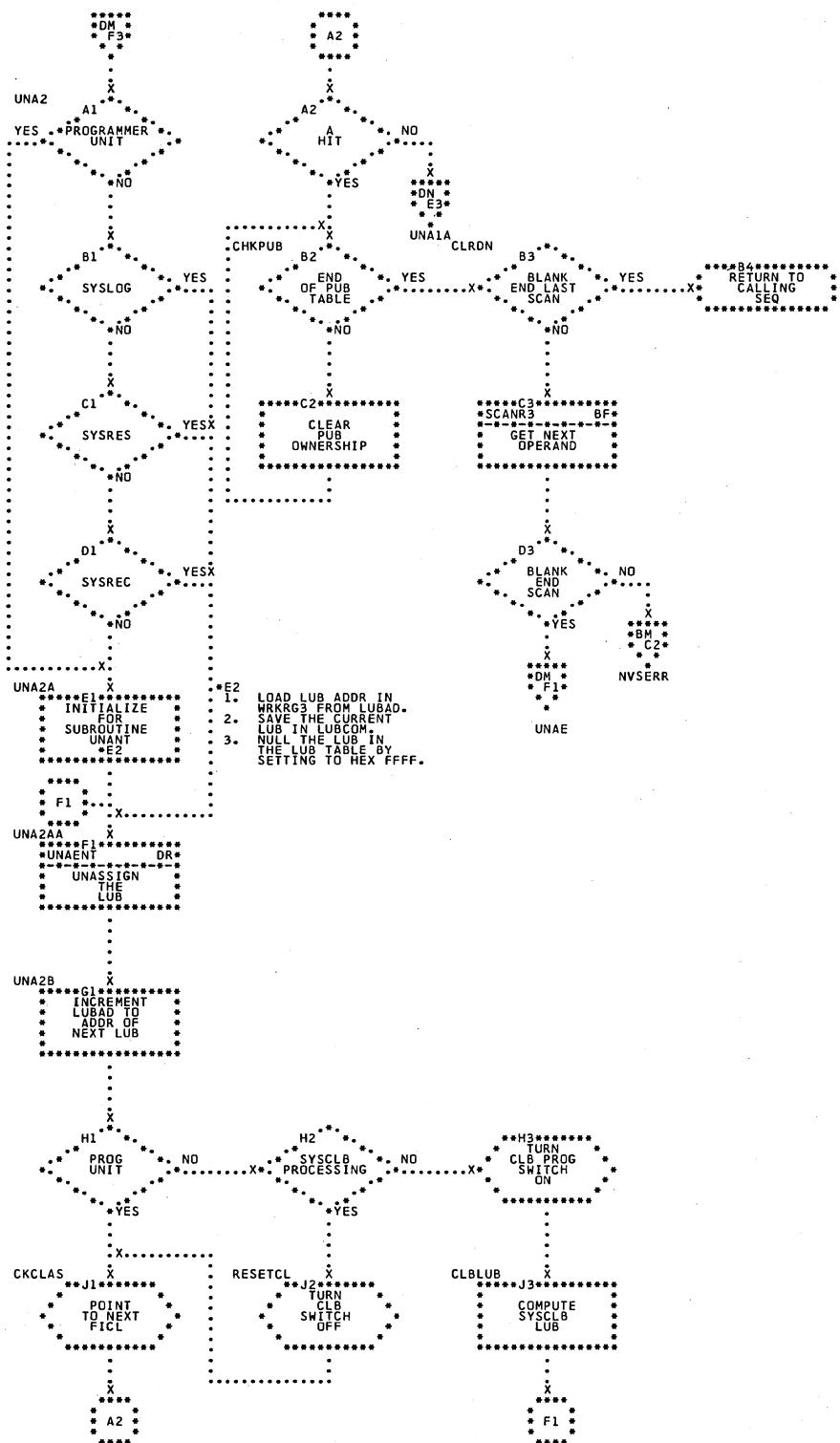
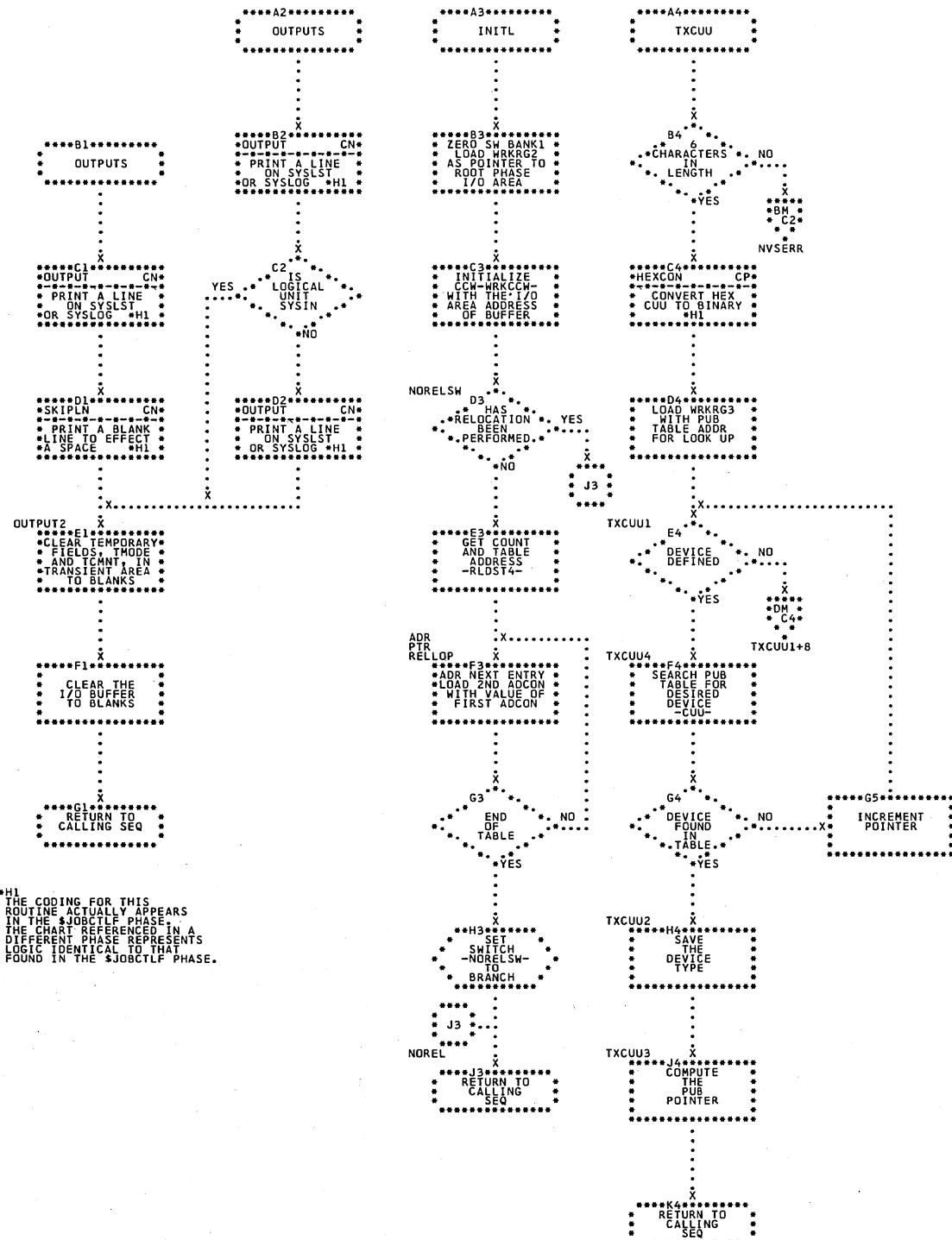
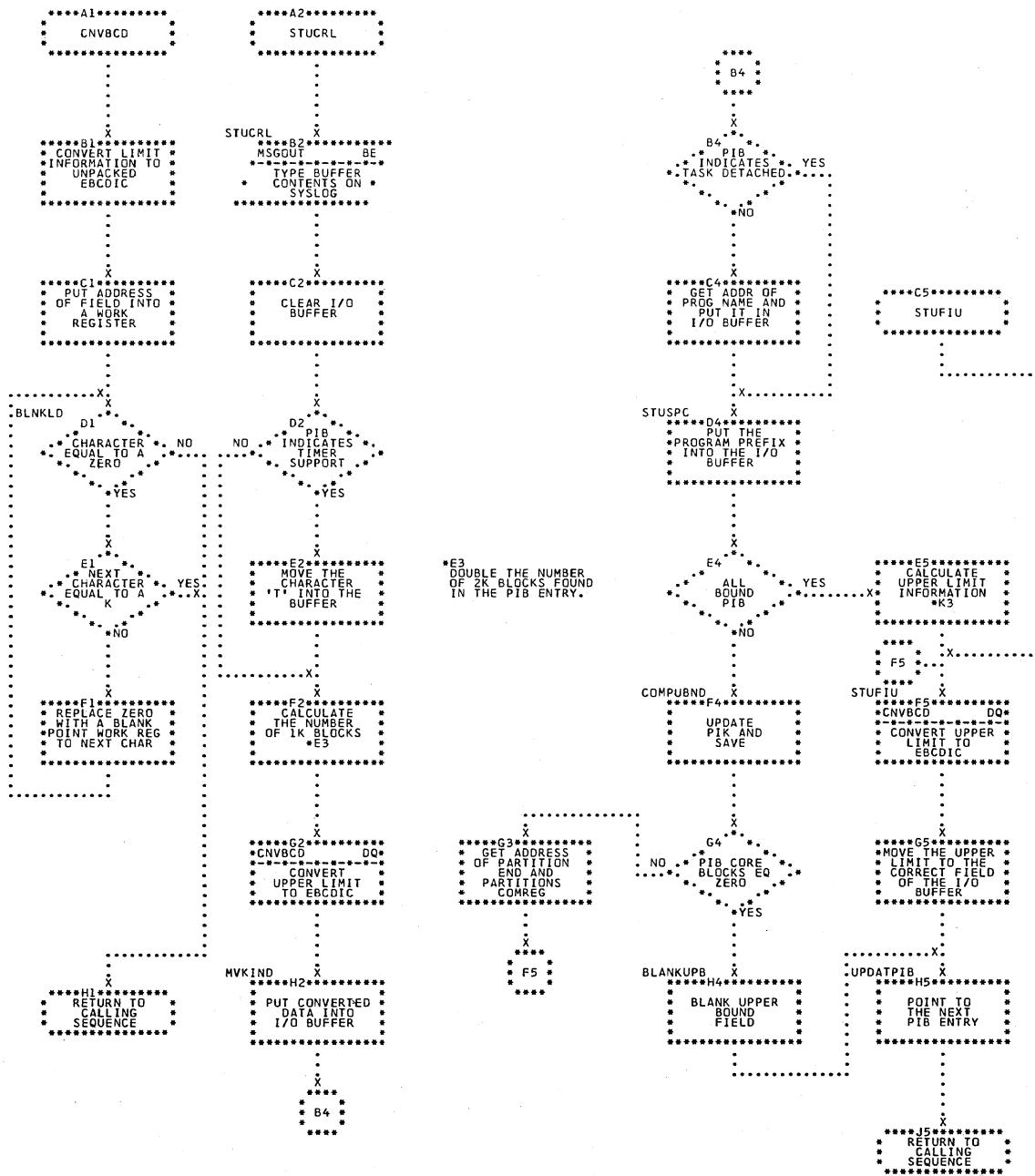


Chart DP. \$JOBCTLF - Miscellaneous Subroutines (Part 1 of 4)  
 Refer to Chart 07.



**Chart DQ. \$JOBCTLF - Miscellaneous Subroutines (Part 2 of 4)**  
 Refer to Chart 07.



\*K3  
THE SUPERVISOR AND BG PROGRAM UPPER  
LIMITS ARE CALCULATED BY GETTING THE  
BG AND F2 ORIGIN ADDRESSES  
RESPECTIVELY AND DECREASING THEIR  
VALUE BY ONE.

Chart DR. \$JOBCTLF - Miscellaneous Subroutines (Part 3 of 4)  
Refer to Chart 07.

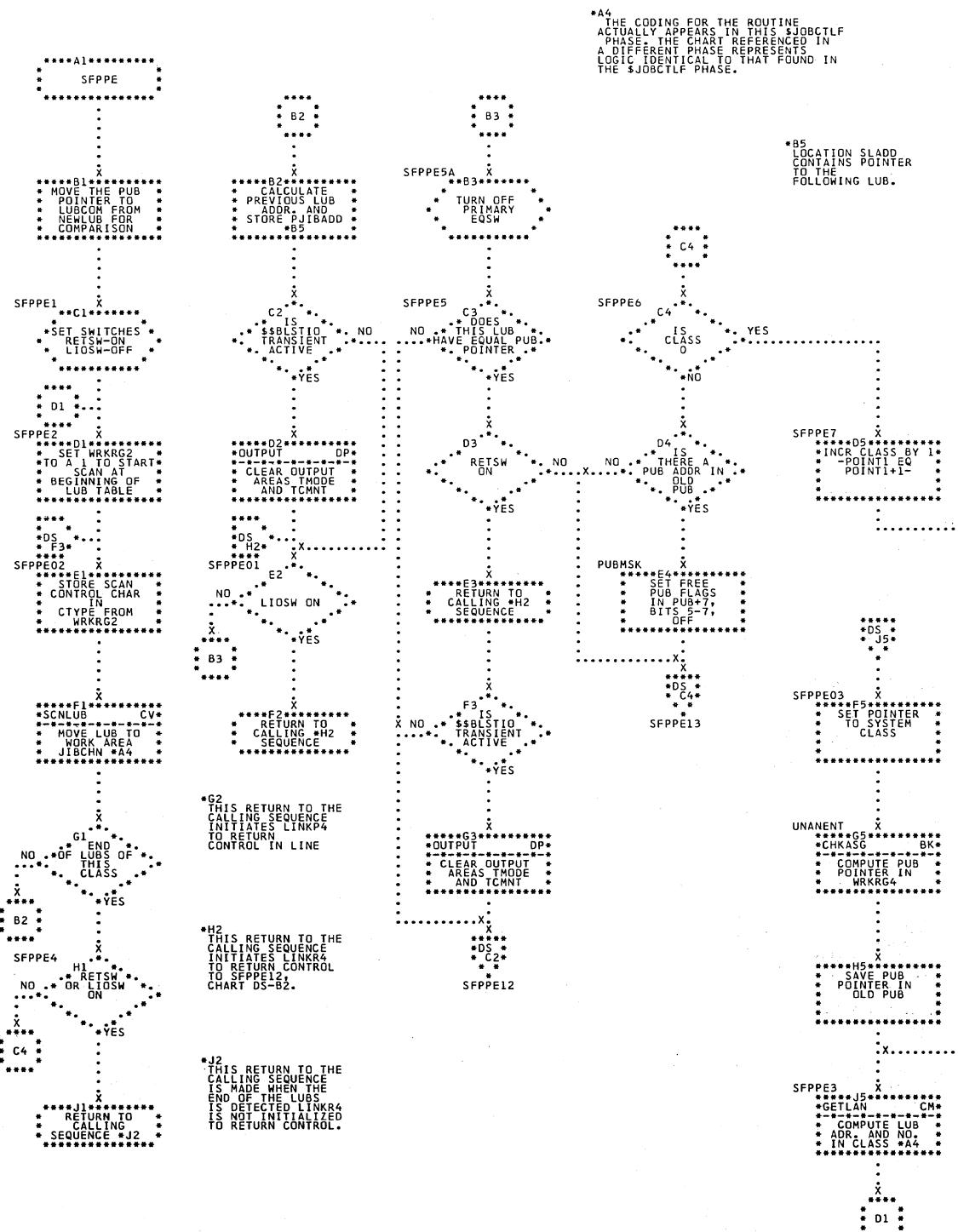


Chart DS. \$JOBCLTF - Miscellaneous Subroutines (Part 4 of 4)  
Refer to Chart 07.

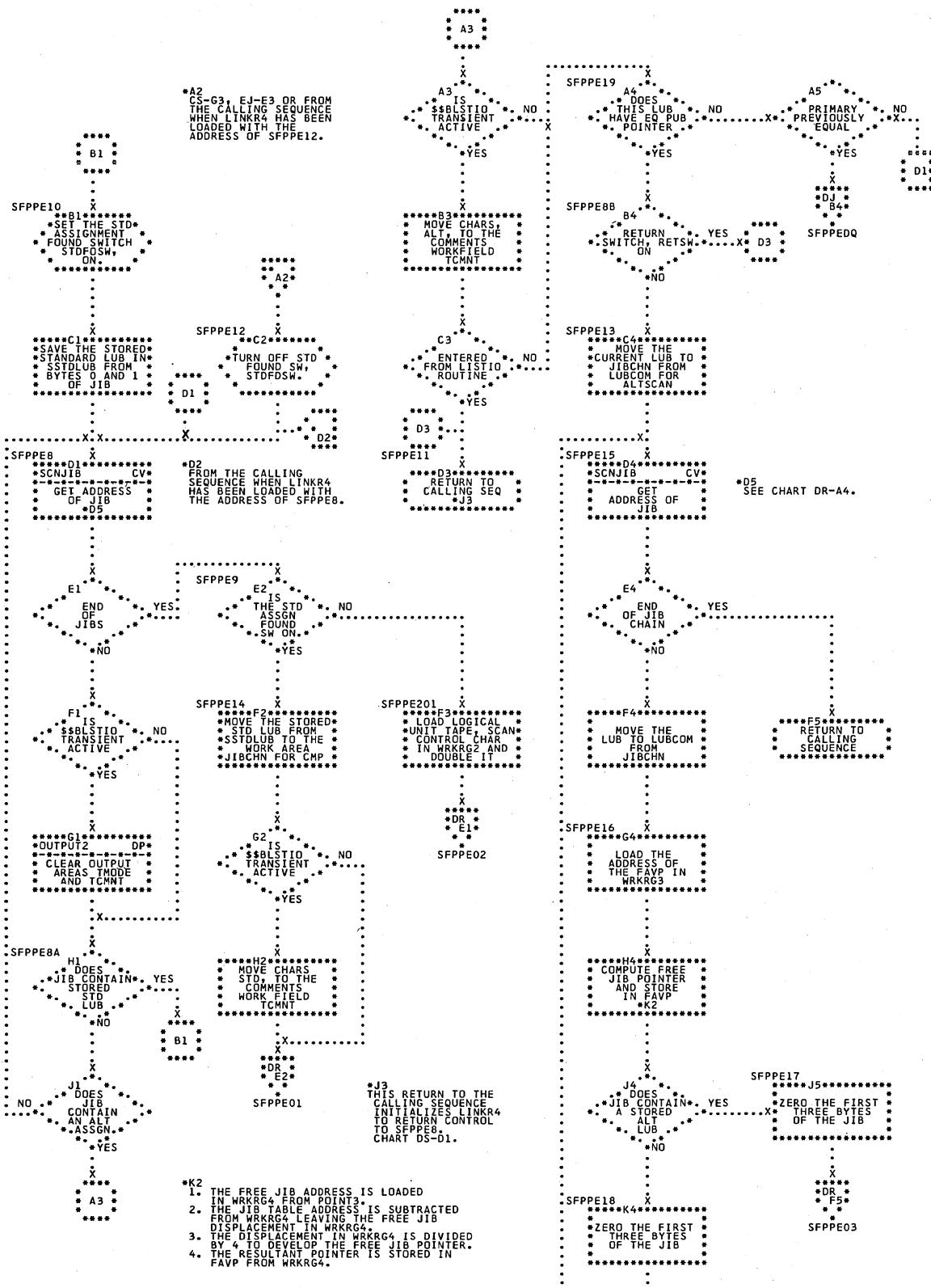


Chart EA. \$JOBCTLG - JOB Statement Processor (Part 1 of 2)  
Refer to Chart 09.

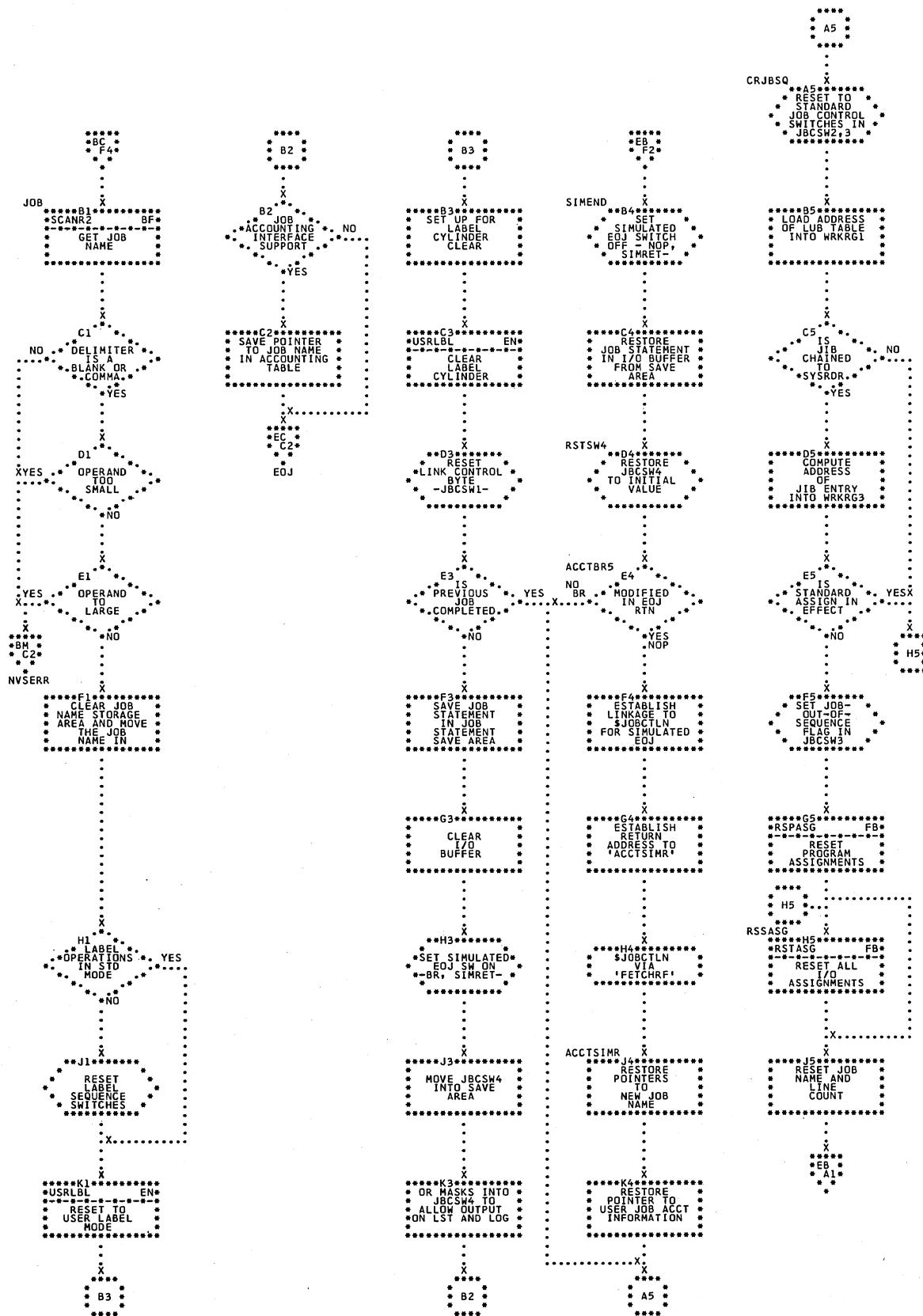


Chart EB. \$JOBCTLG - JOB Statement Processor (Part 2 of 2)  
Refer to Chart 09.

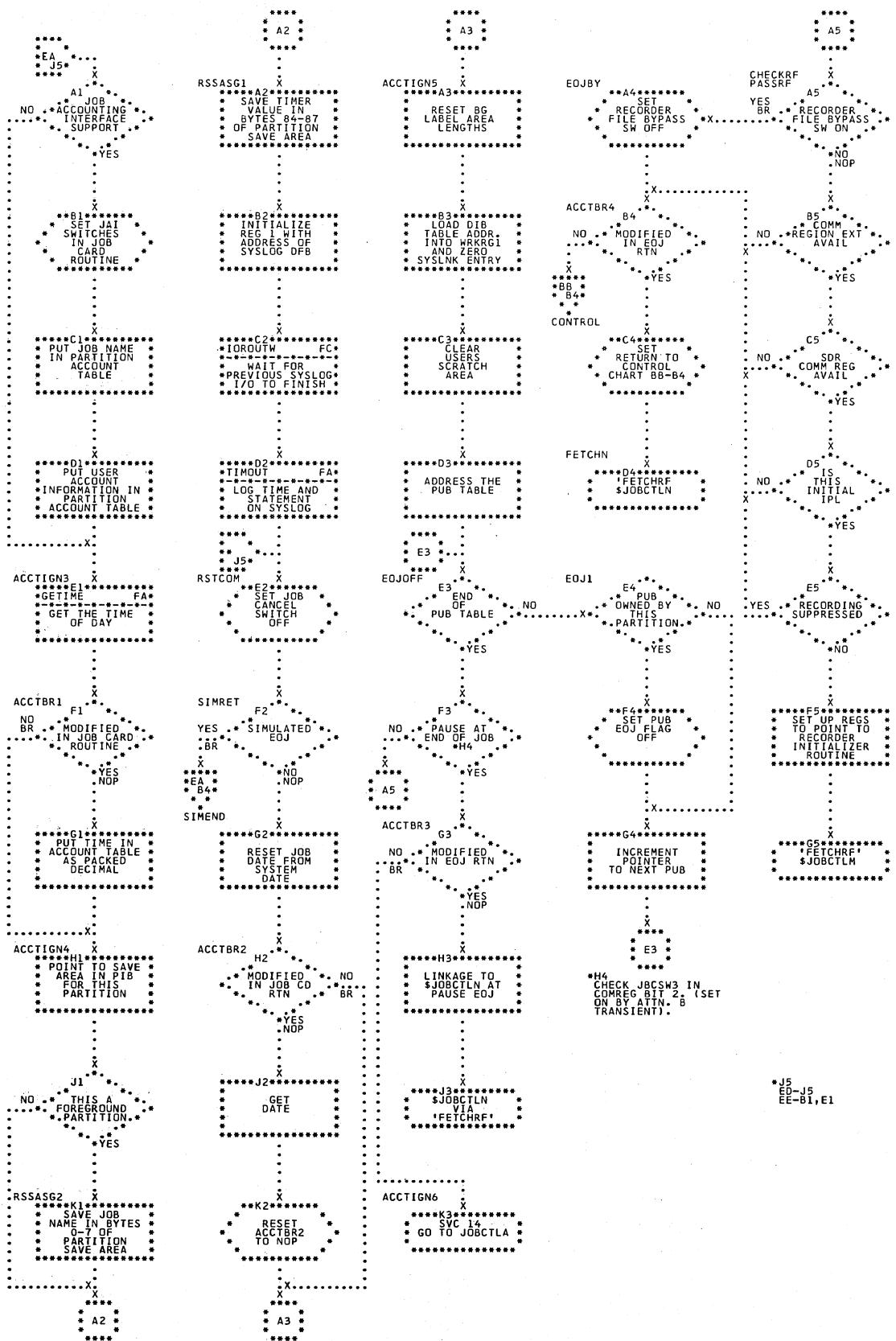


Chart EC. \$JOBCTLG - /& Statement Processor (Part 1 of 3)  
Refer to Chart 09.

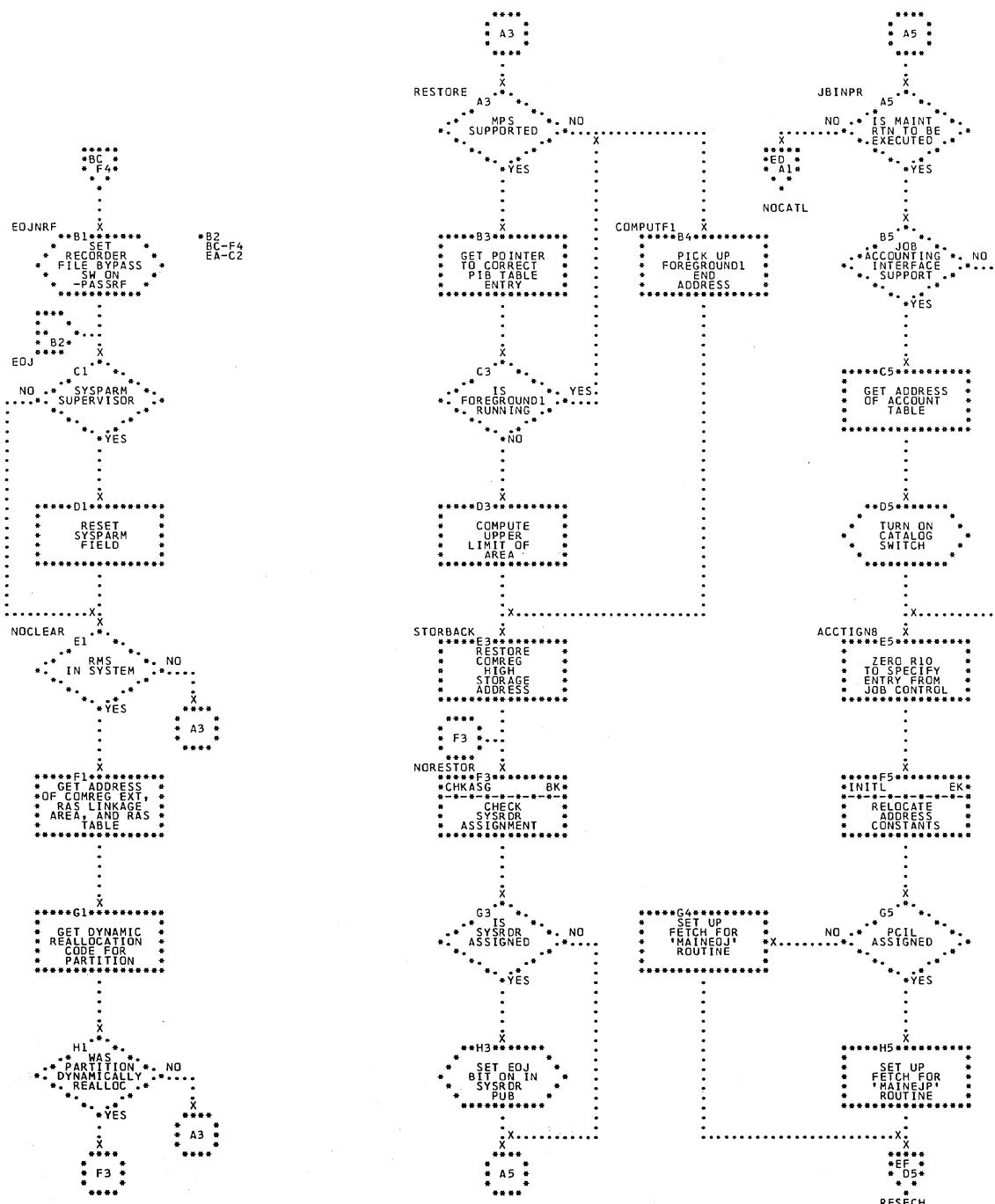


Chart ED. \$JOBCTLG - /& Statement Processor (Part 2 of 3)  
Refer to Chart 09.

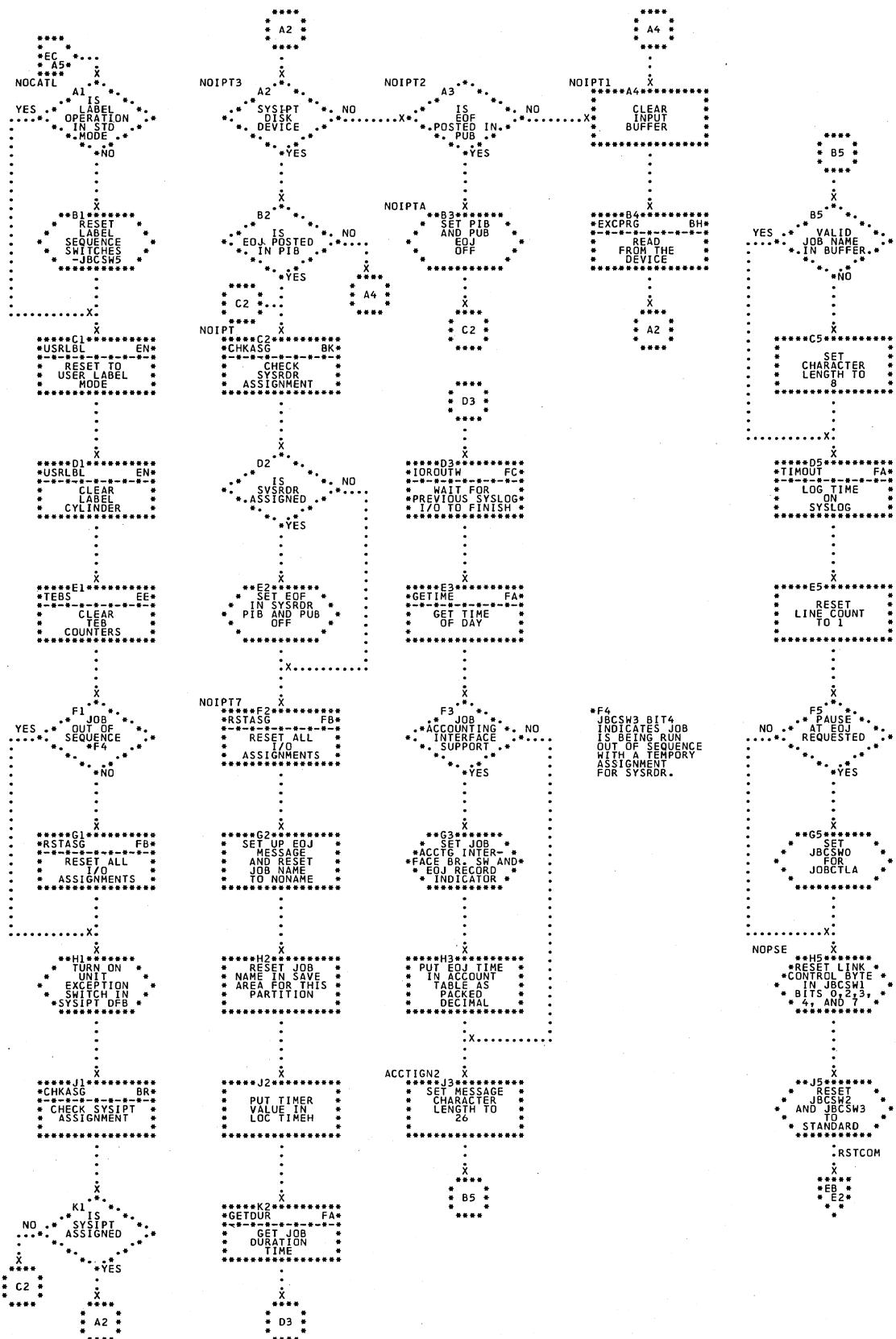


Chart EE. \$JOBCTLG - /6 Statement Processor (Part 3 of 3) and CANCEL Statement Processor  
Refer to Chart 09.

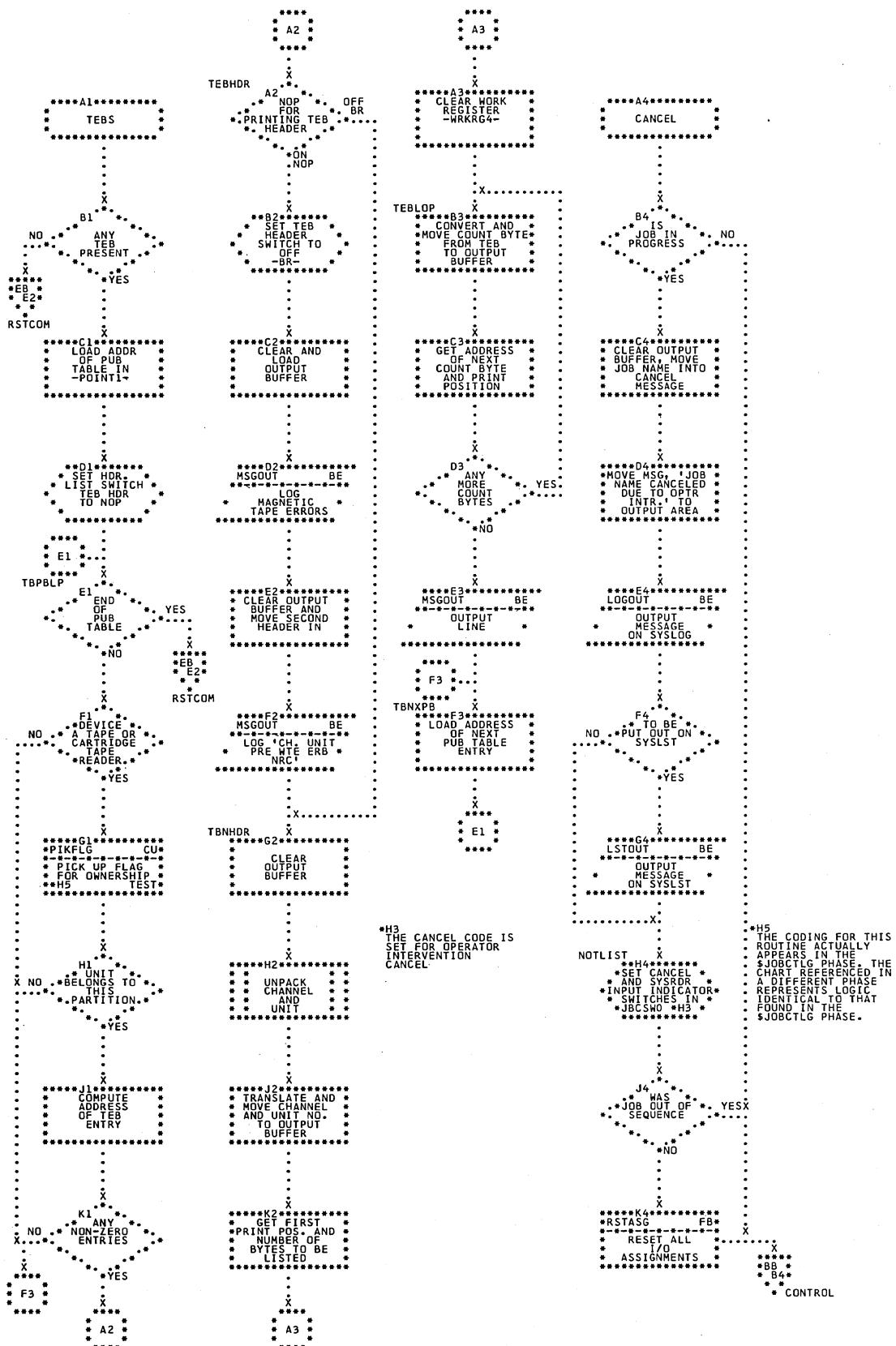


Chart EF. \$JOBCTLG - EXEC Statement Processor (Part 1 of 5)  
Refer to Chart 09.

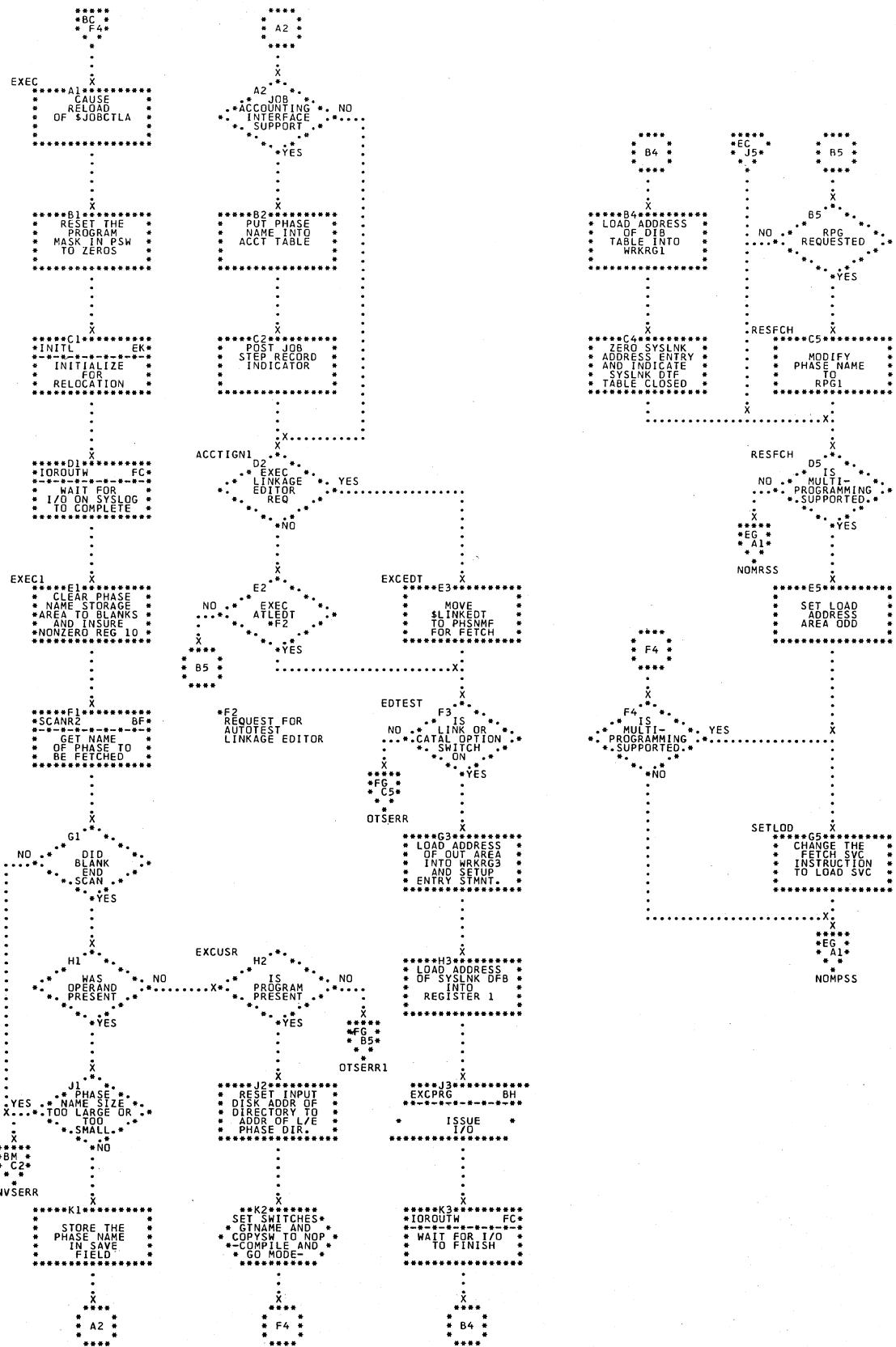
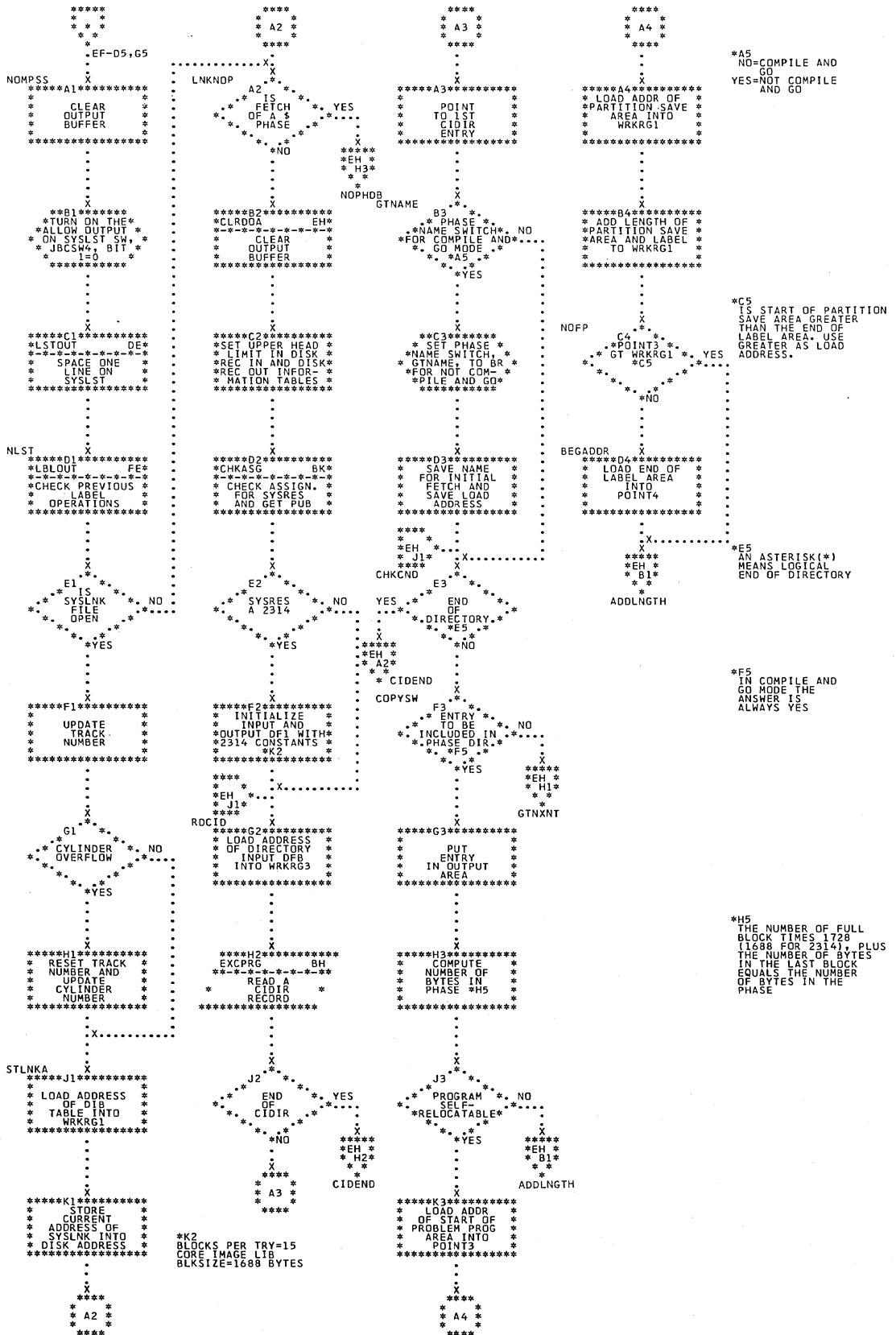


Chart EG. \$JOBCTLG - EXEC Statement Processor (Part 2 of 5)  
Refer to Chart 09.



**Chart EH. \$JOBCTLG - EXEC Statement Processor (Part 3 of 5)**  
Refer to Chart 09.

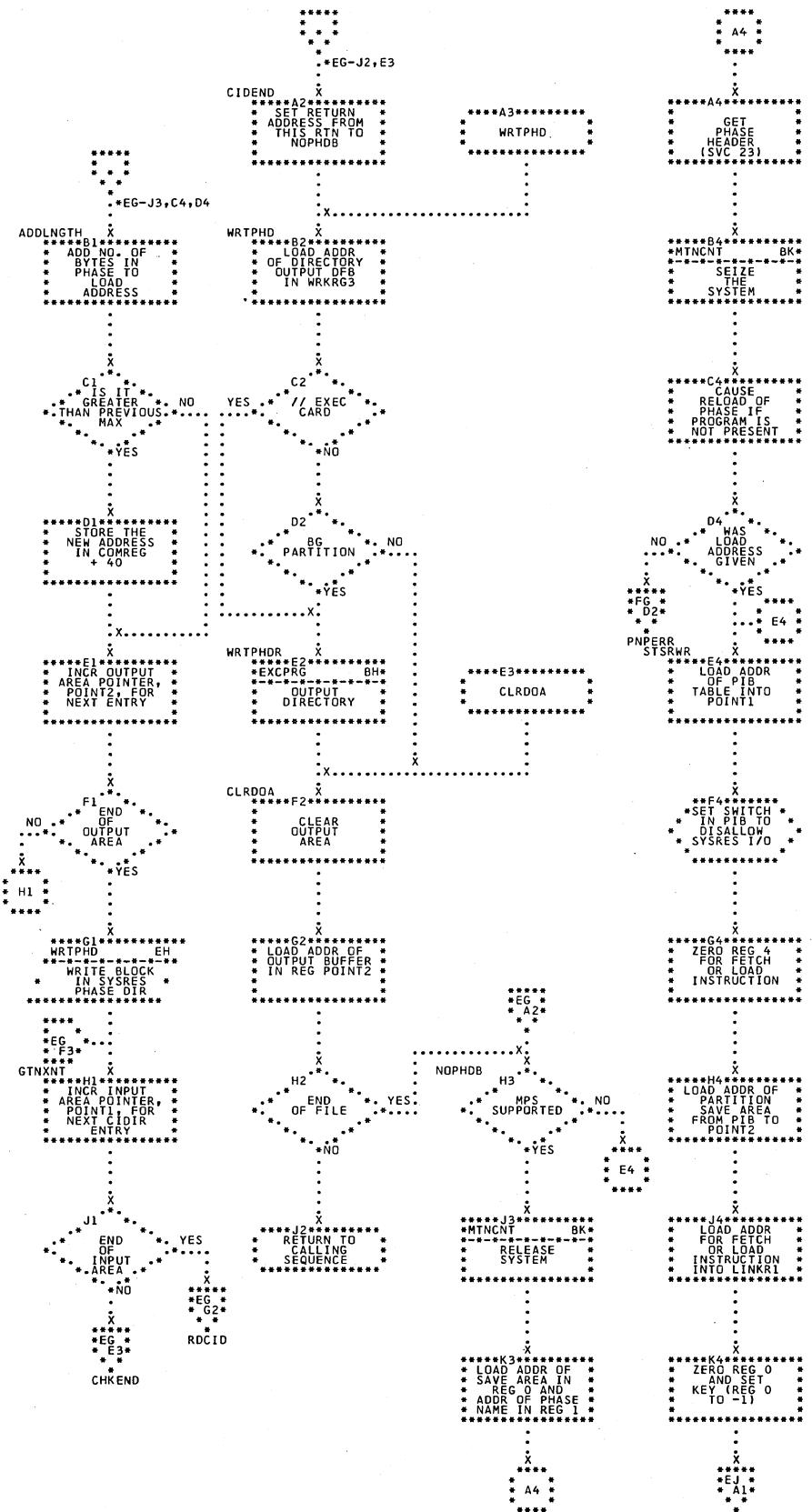


Chart EJ. \$JOBCTLG - EXEC Statement Processor (Part 4 of 5)  
Refer to Chart 09.

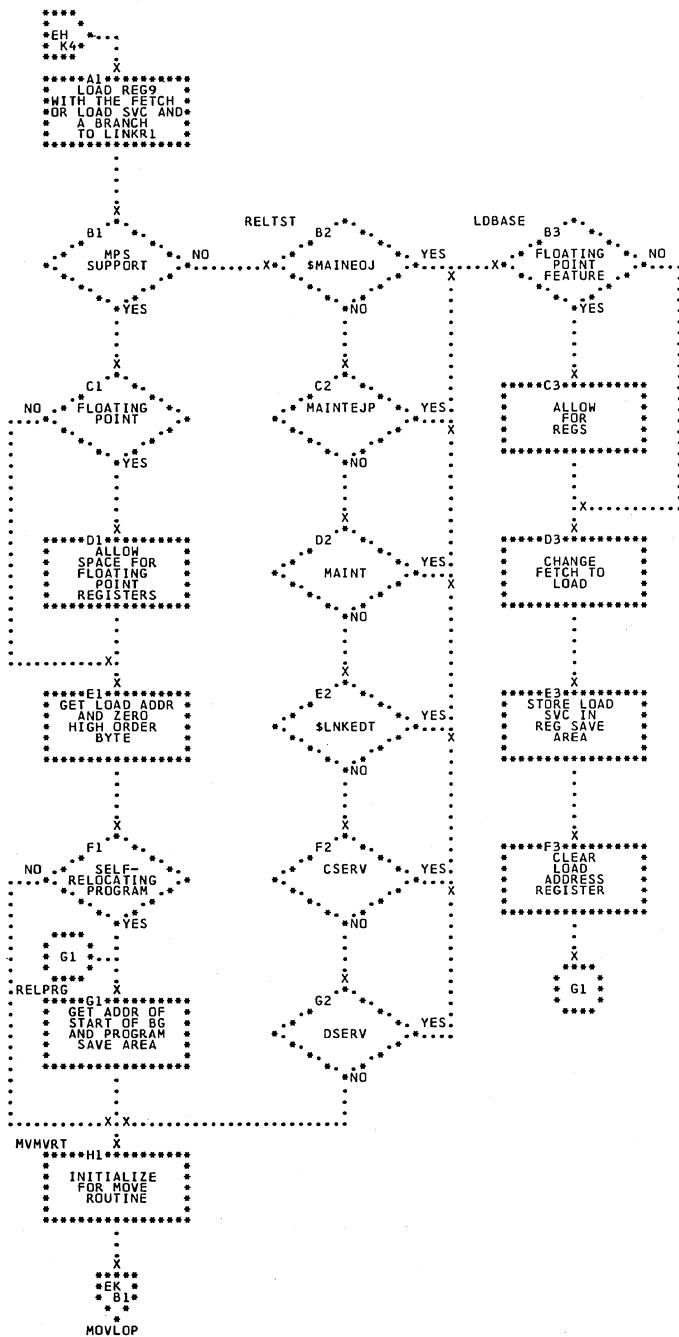


Chart EK. \$JOBCTLG - EXEC Statement Processor (Part 5 of 5)  
Refer to Chart 09.

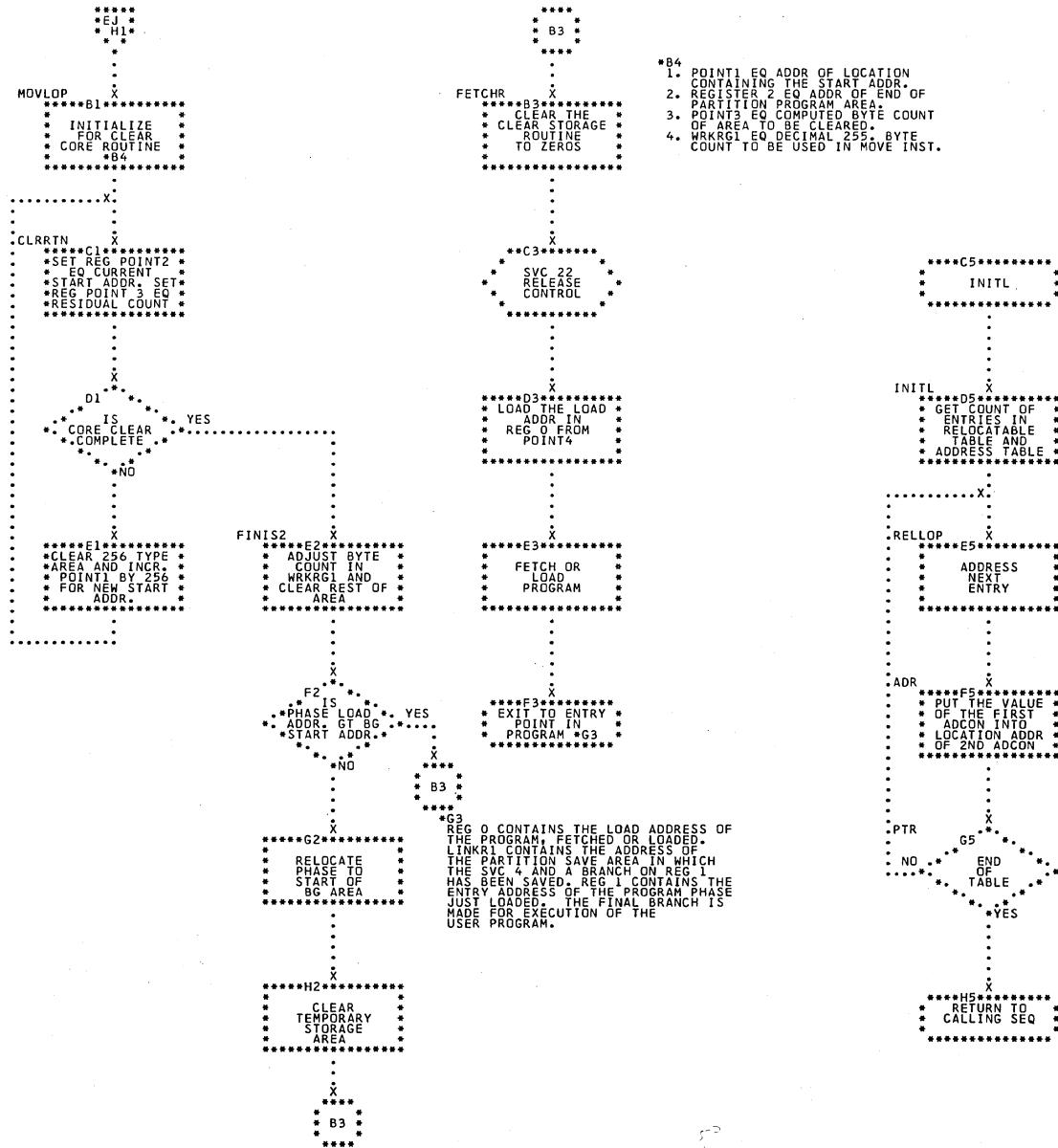


Chart EL. \$JOBCTLG - OPTION Statement Processor (Part 1 of 4)  
Refer to Chart 08.

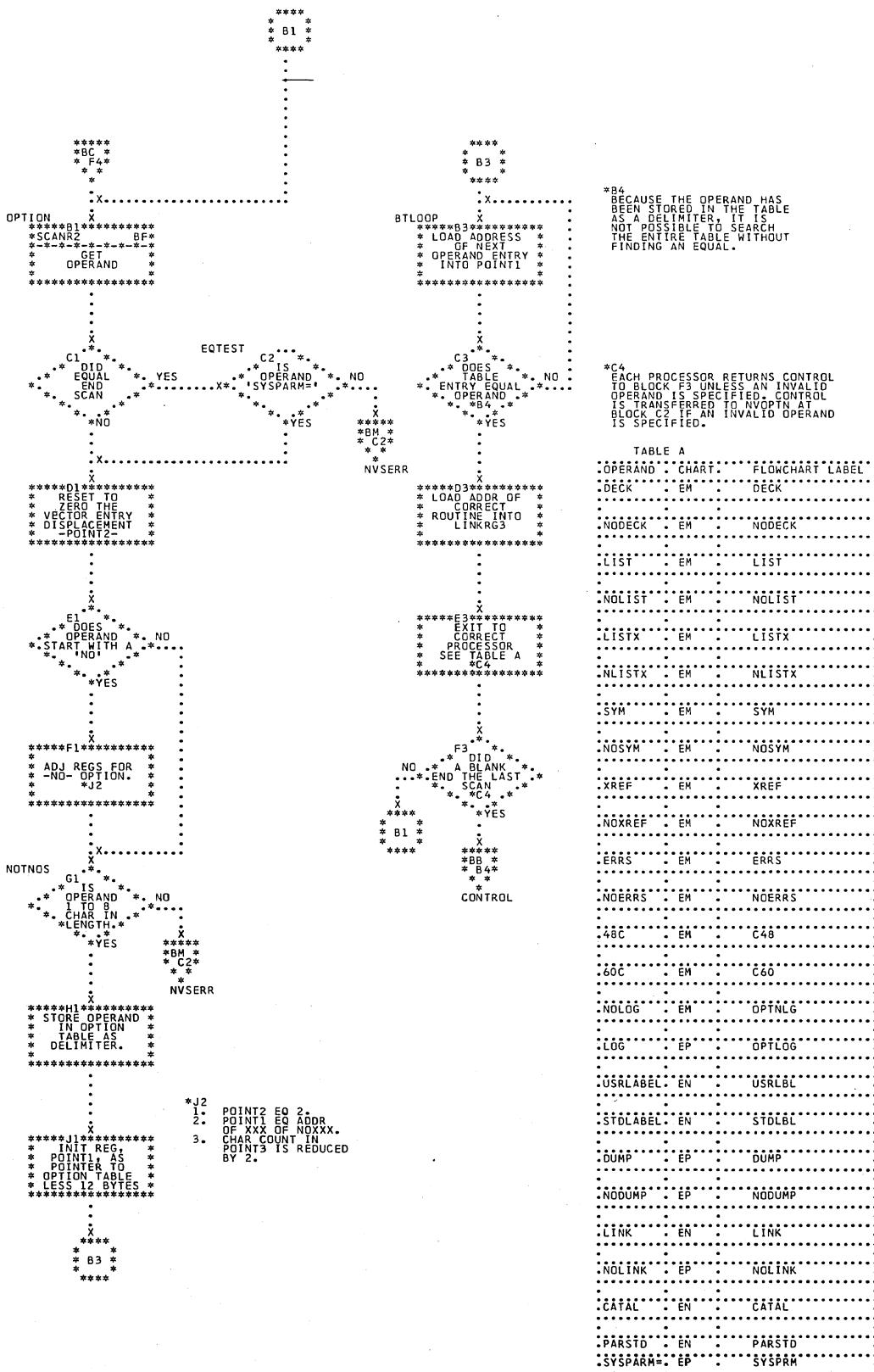


Chart EM. \$JOBCTLG - OPTION Statement Processor (Part 2 of 4)  
 Refer to Chart 08.

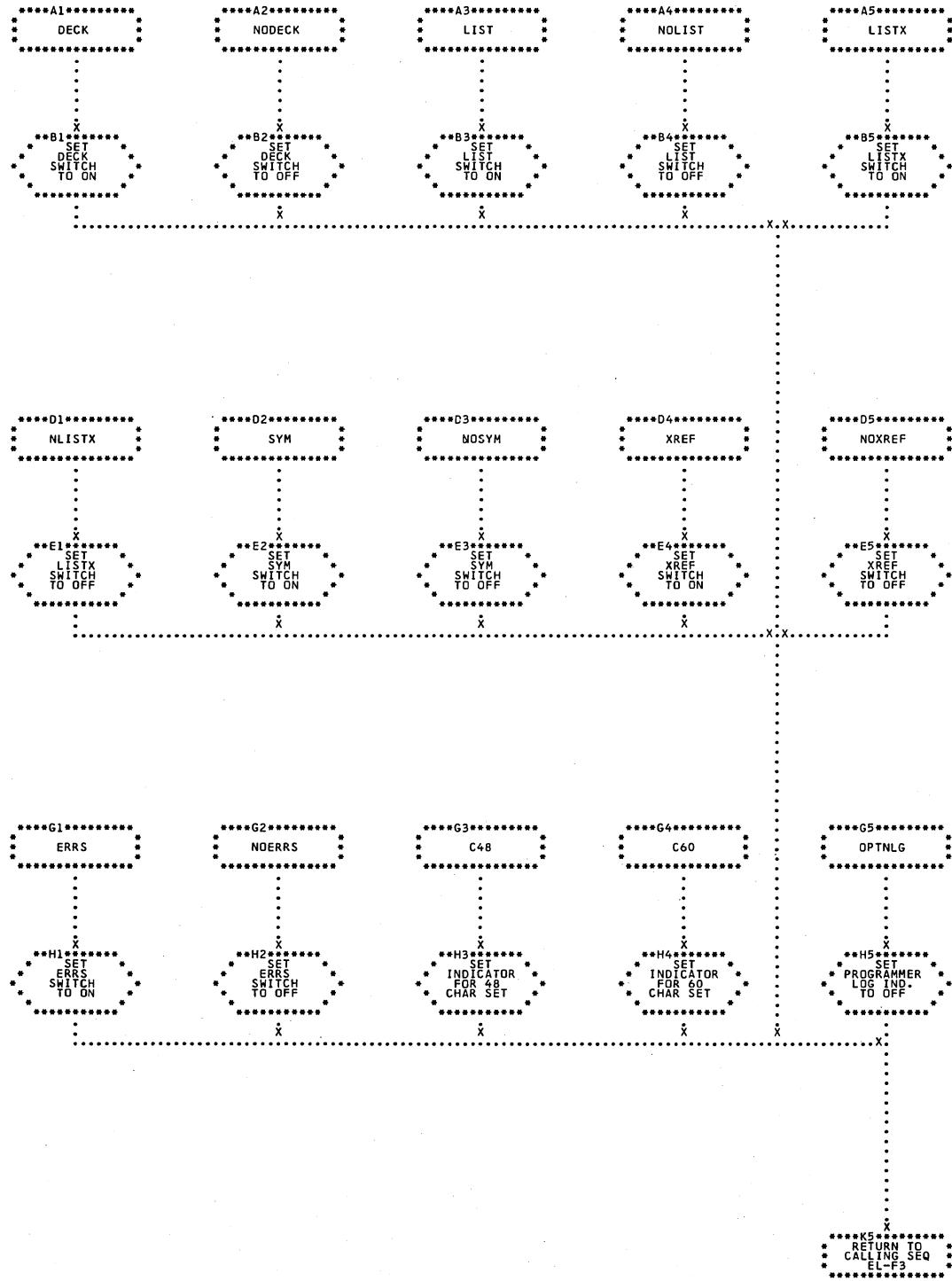
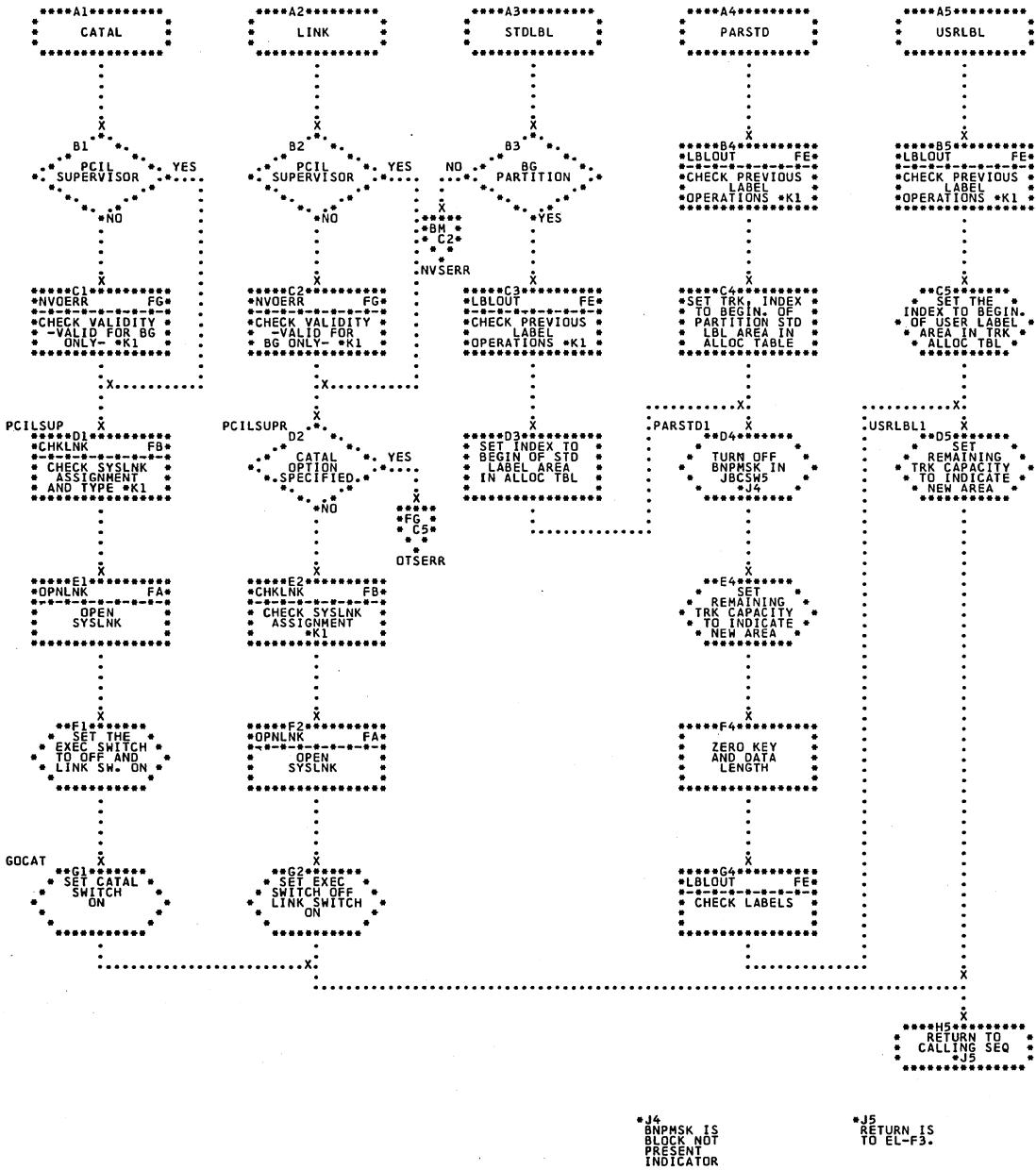


Chart EN. \$JOBCTLG - OPTION Statement Processor (Part 3 of 4)  
Refer to Chart 08.



\*K1  
IF AN ERROR IS  
FOUND RETURN  
IS NOT TO THIS  
ROUTINE.

\*J4  
BNPMISK IS  
BLOCK NOT  
PRESENT  
INDICATOR

\*J5  
RETURN IS  
TO EL-F3.

Chart EP. \$JOBCTLG - OPTION Statement Processor (Part 4 of 4)  
Refer to Chart 08.

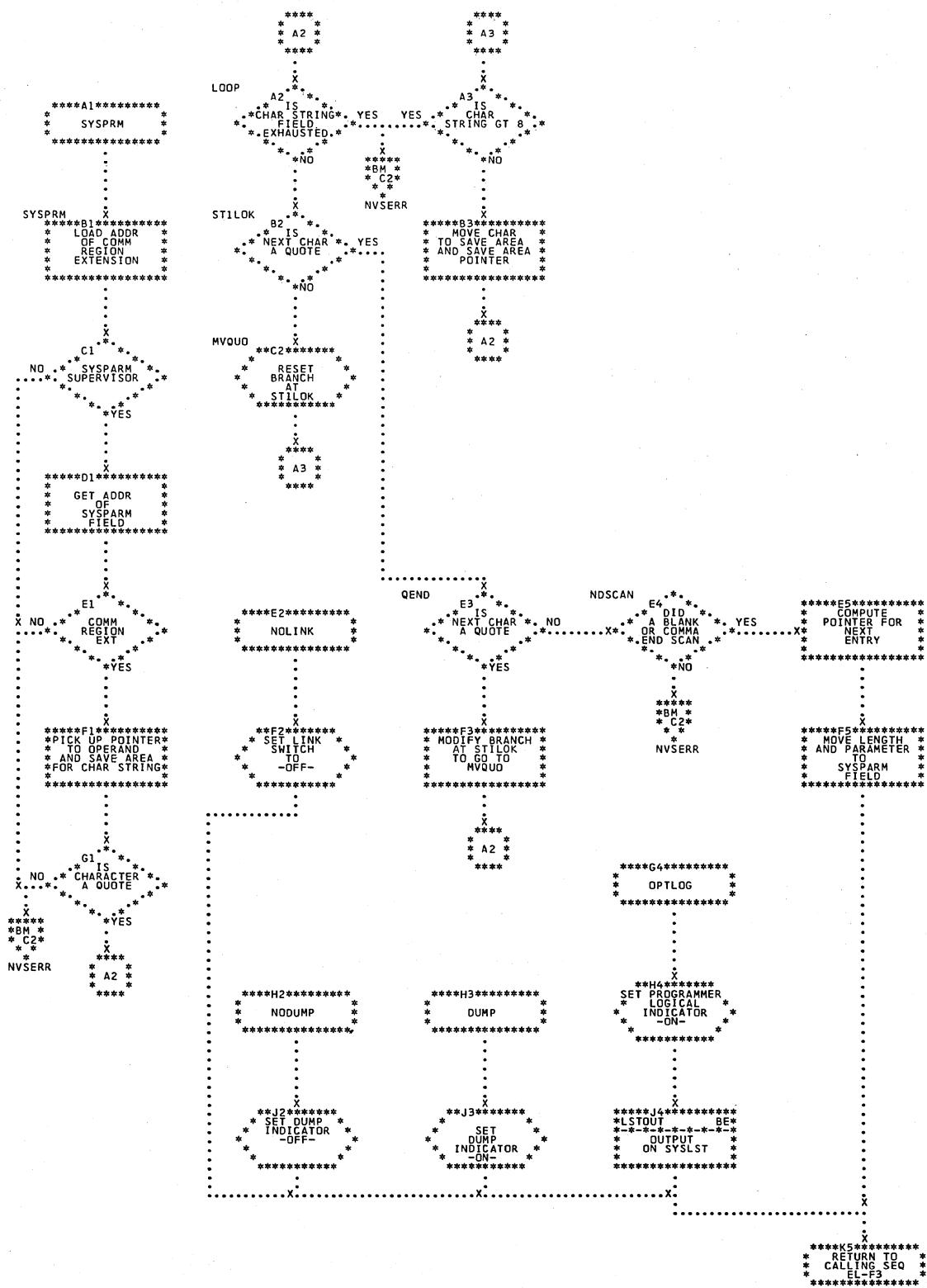
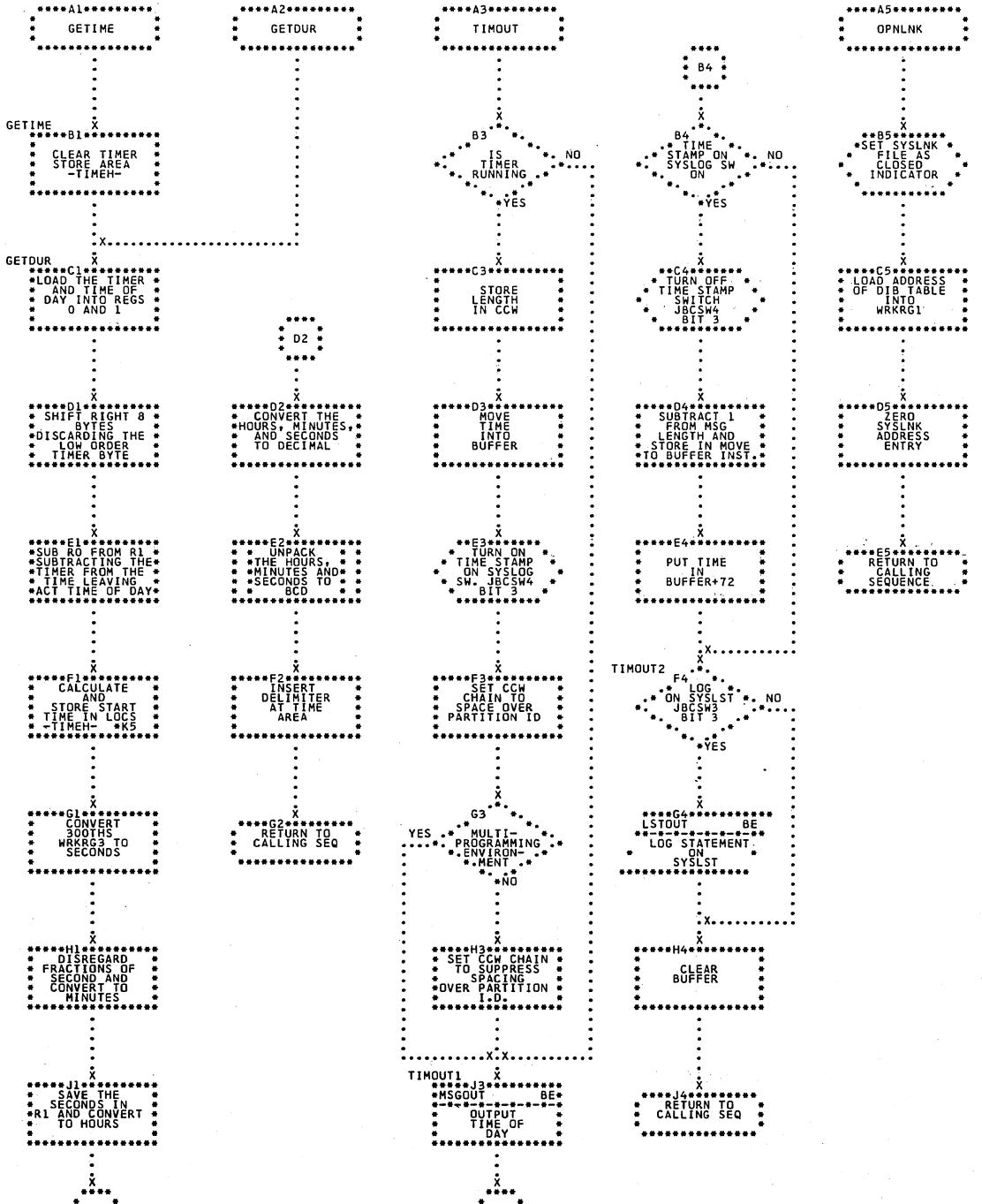


Chart FA. \$JOBCTLG - Time Stamping Subroutines  
Refer to Chart 09.



K5  
IF THIS ROUTINE IS  
ENTERED AT GETDUR,  
TIMER HAS TO BE RESET.  
IF ENTERED AT  
GETIME, TIMEH IS  
RESET TO CURRENT TIME.

Chart FB. \$JOBCTLG - Miscellaneous Subroutines (Part 1 of 3)  
Refer to Chart 09.

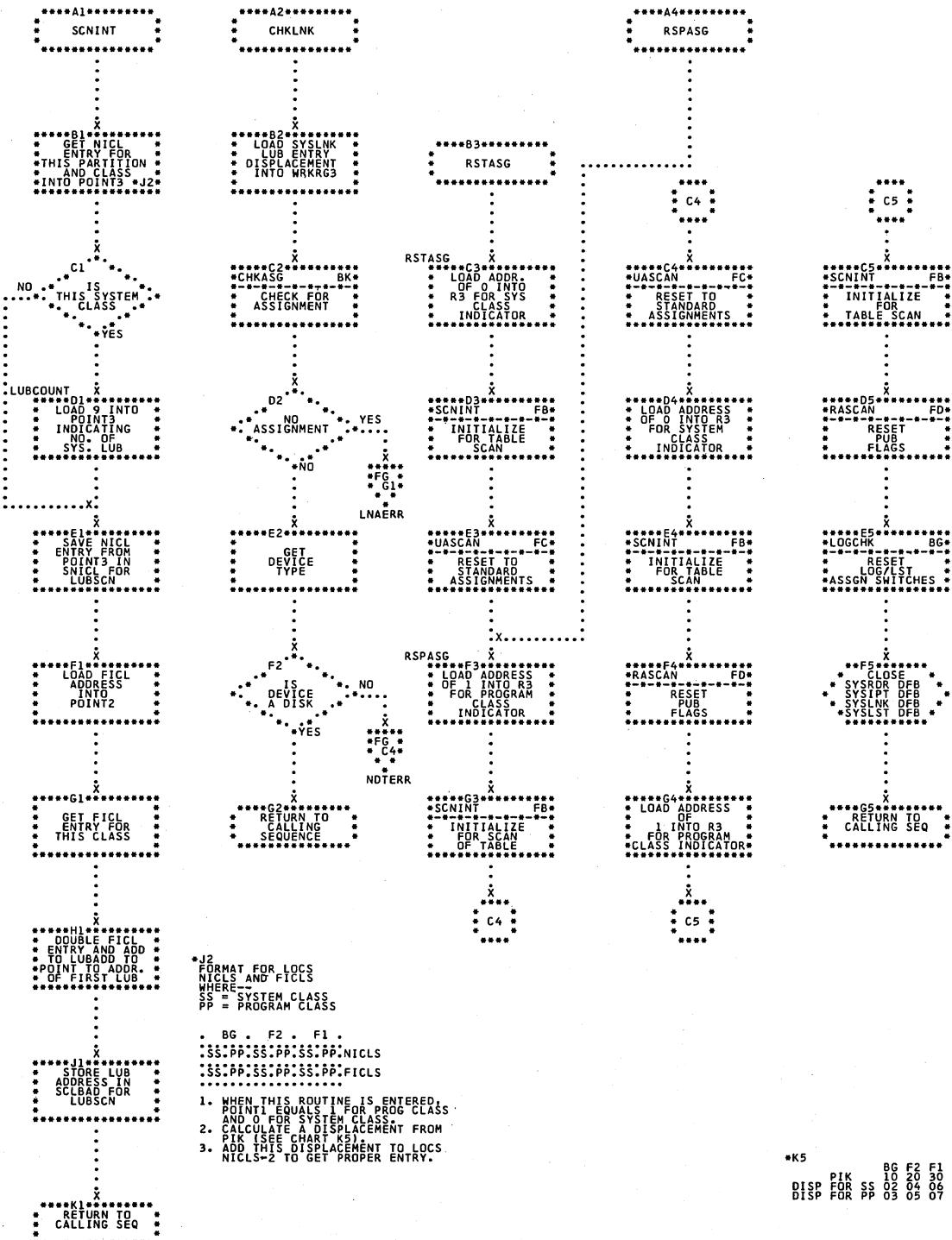
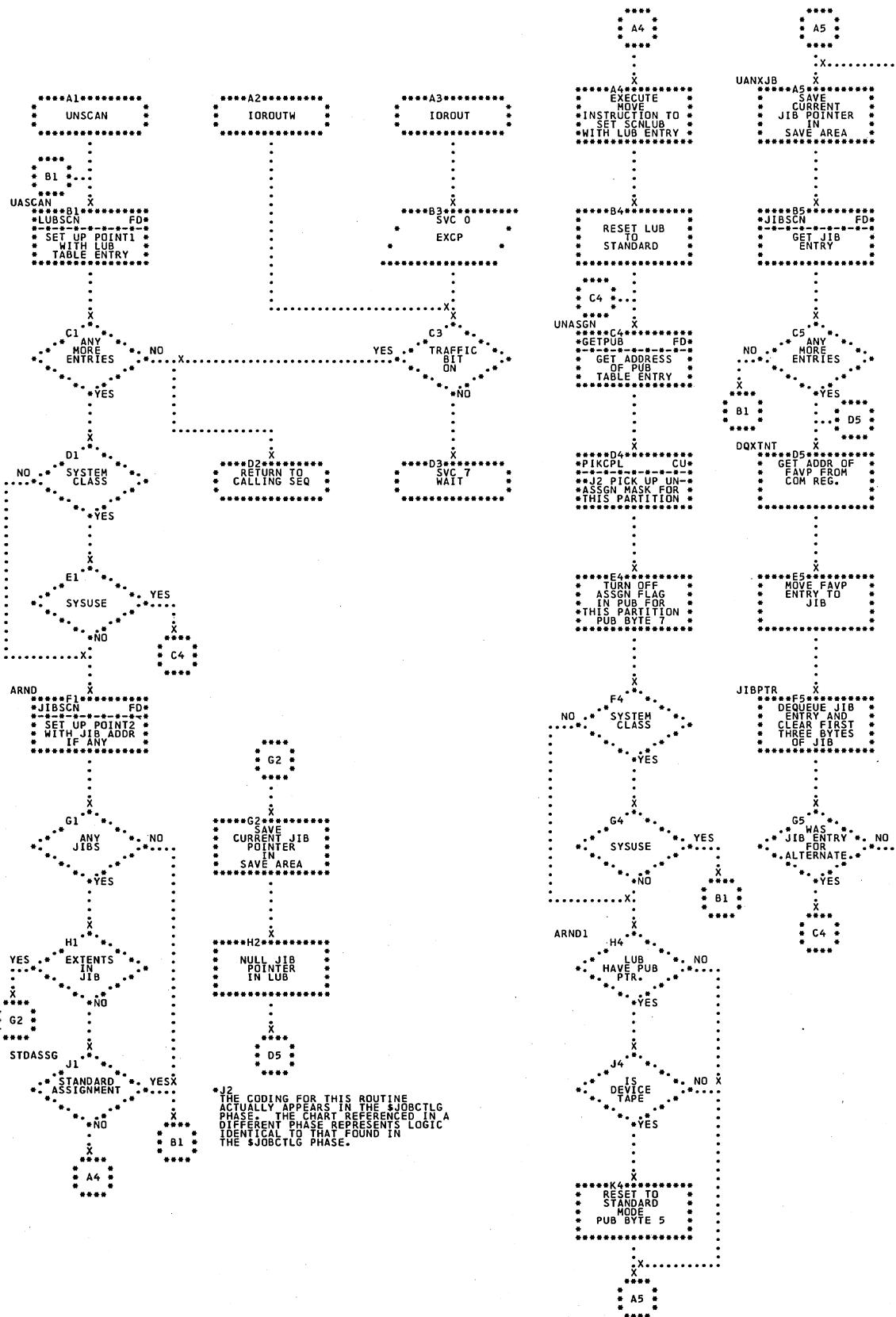


Chart FC. \$JOBCTLG - Miscellaneous Subroutines (Part 2 of 3)  
Refer to Chart 09.



**Chart FD. \$JOBCTLG - Miscellaneous Subroutines (Part 3 of 3)**  
 Refer to Chart 09.

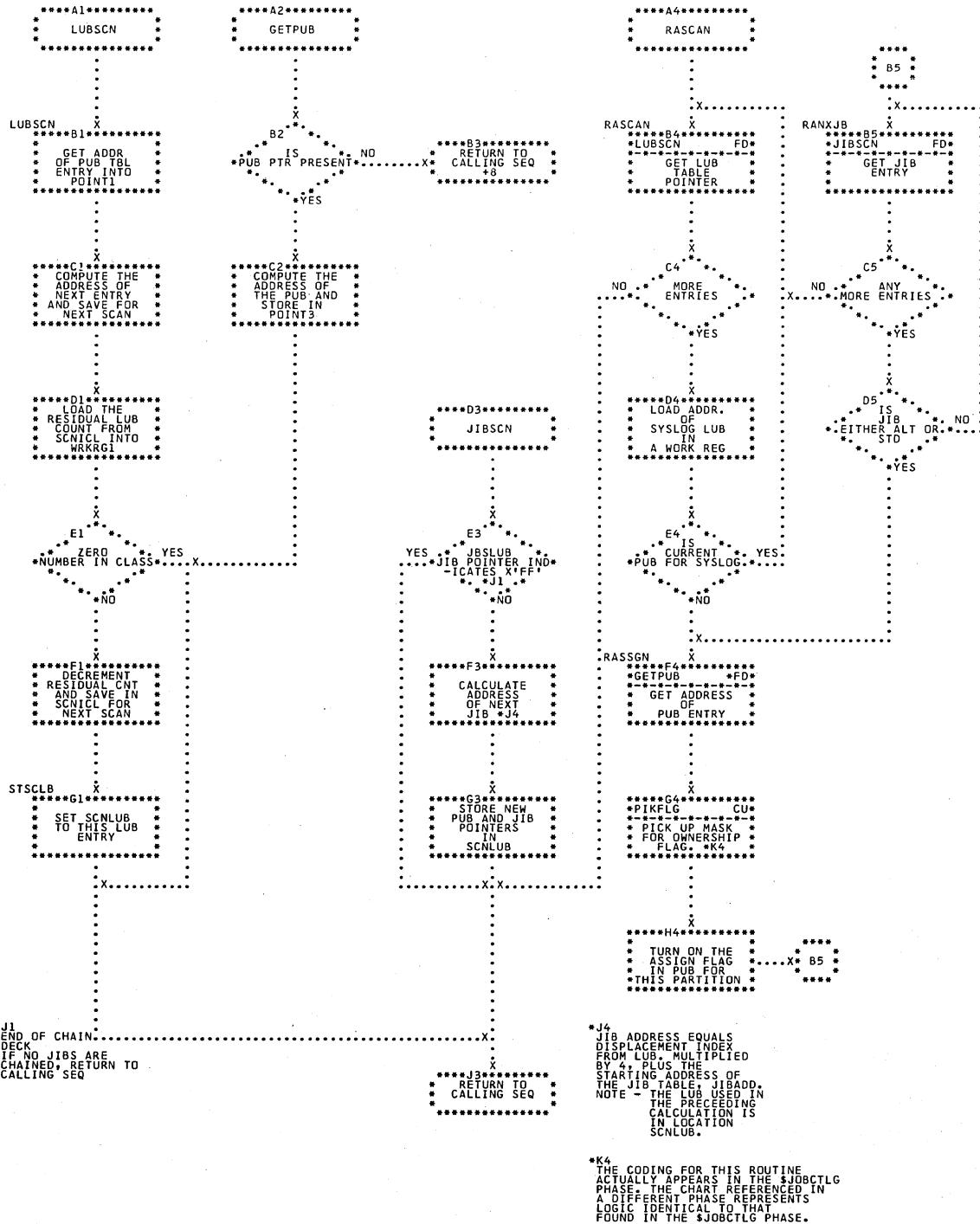


Chart FE. \$JOBCTLG - Label Processing Subroutines (Part 1 of 2)  
Refer to Charts 08 and 09.

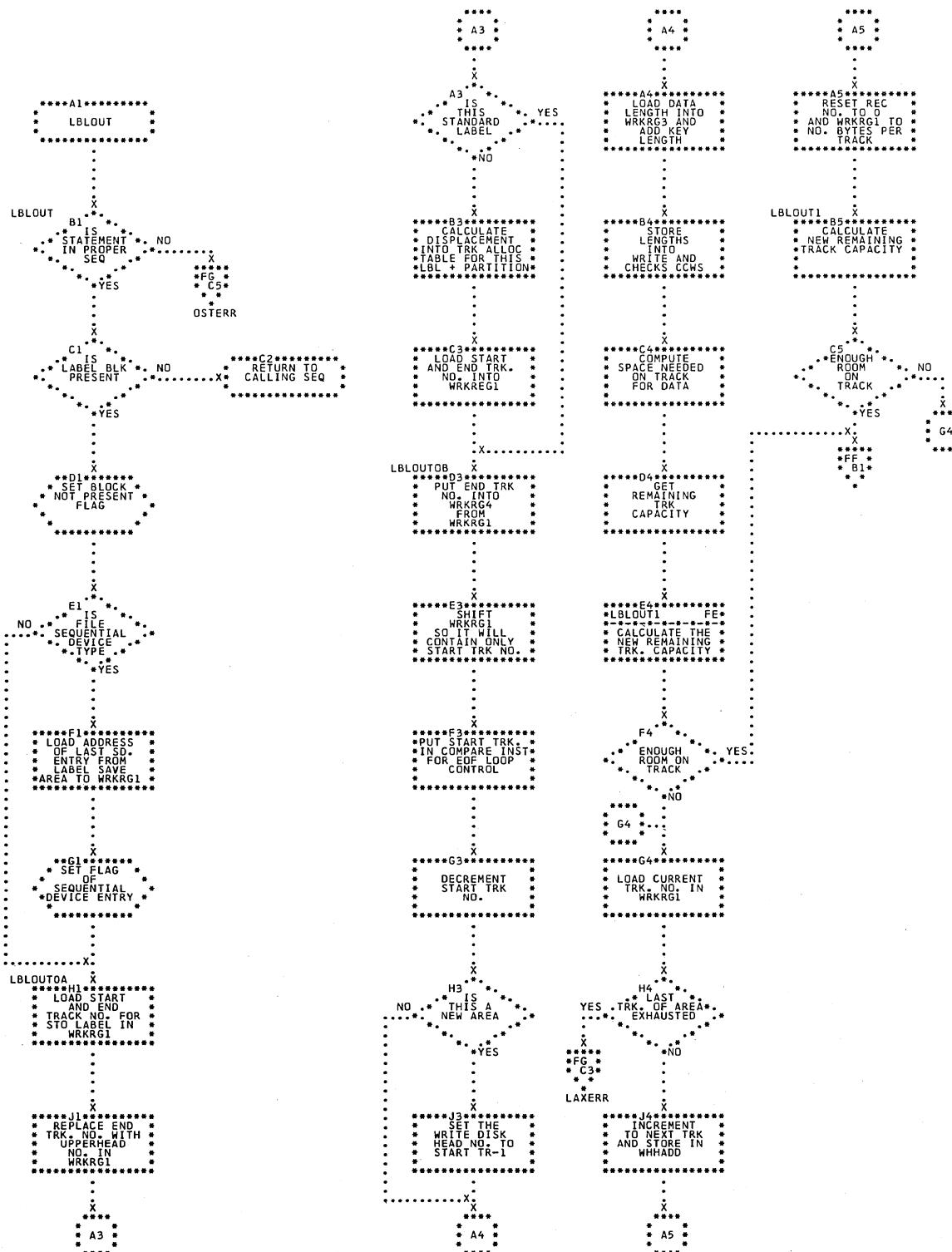


Chart FF. \$JOBCTLG - Label Processing Subroutines (Part 2 of 2)  
Refer to Charts 08 and 09.

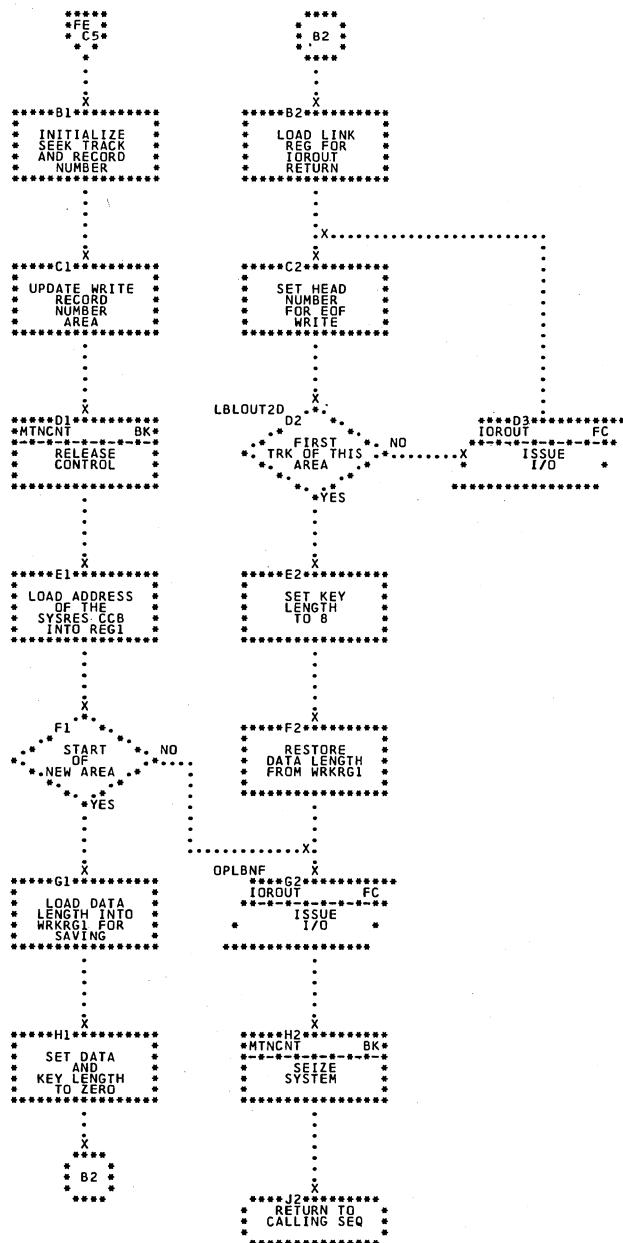
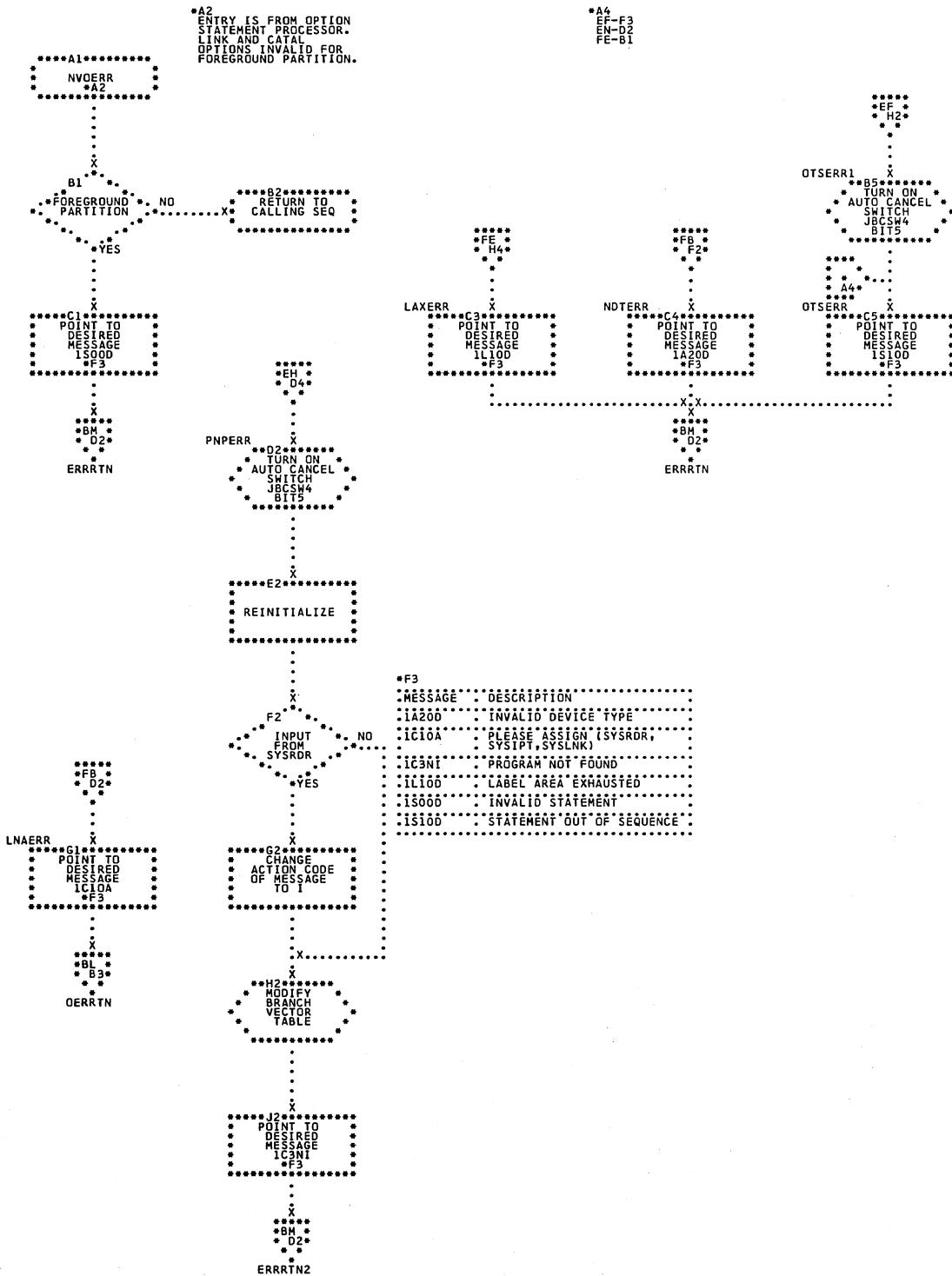


Chart FG. \$JOBCTLG - Error Subroutines  
Refer to Charts 08 and 09.



**Chart GA. \$JOBCTLJ - RELSE and HOLD Statement Processors**  
 Refer to Chart 11.

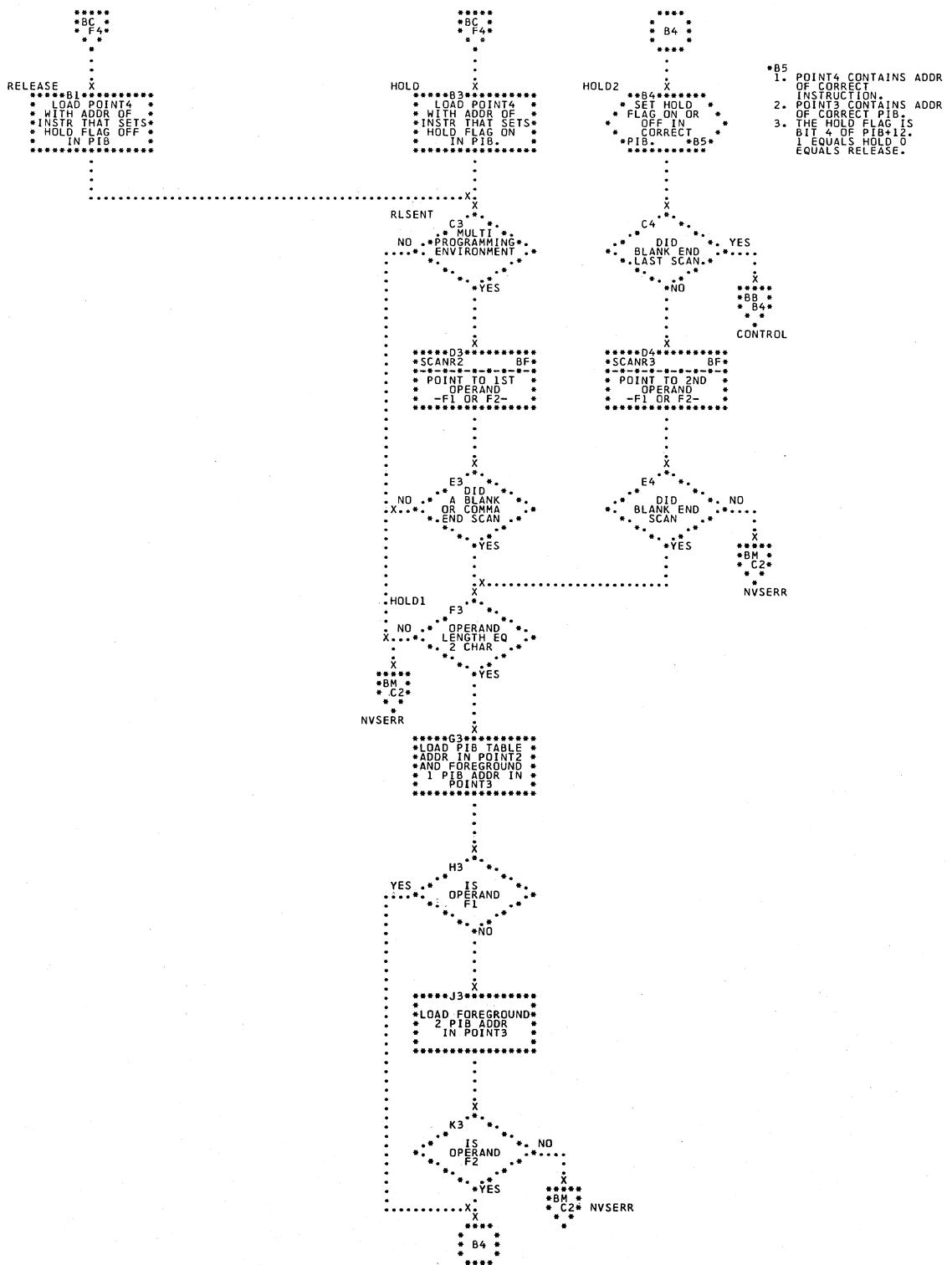


Chart GB. \$JOBCTLJ - UCS Statement Processor (Part 1 of 2)  
Refer to Chart 11.

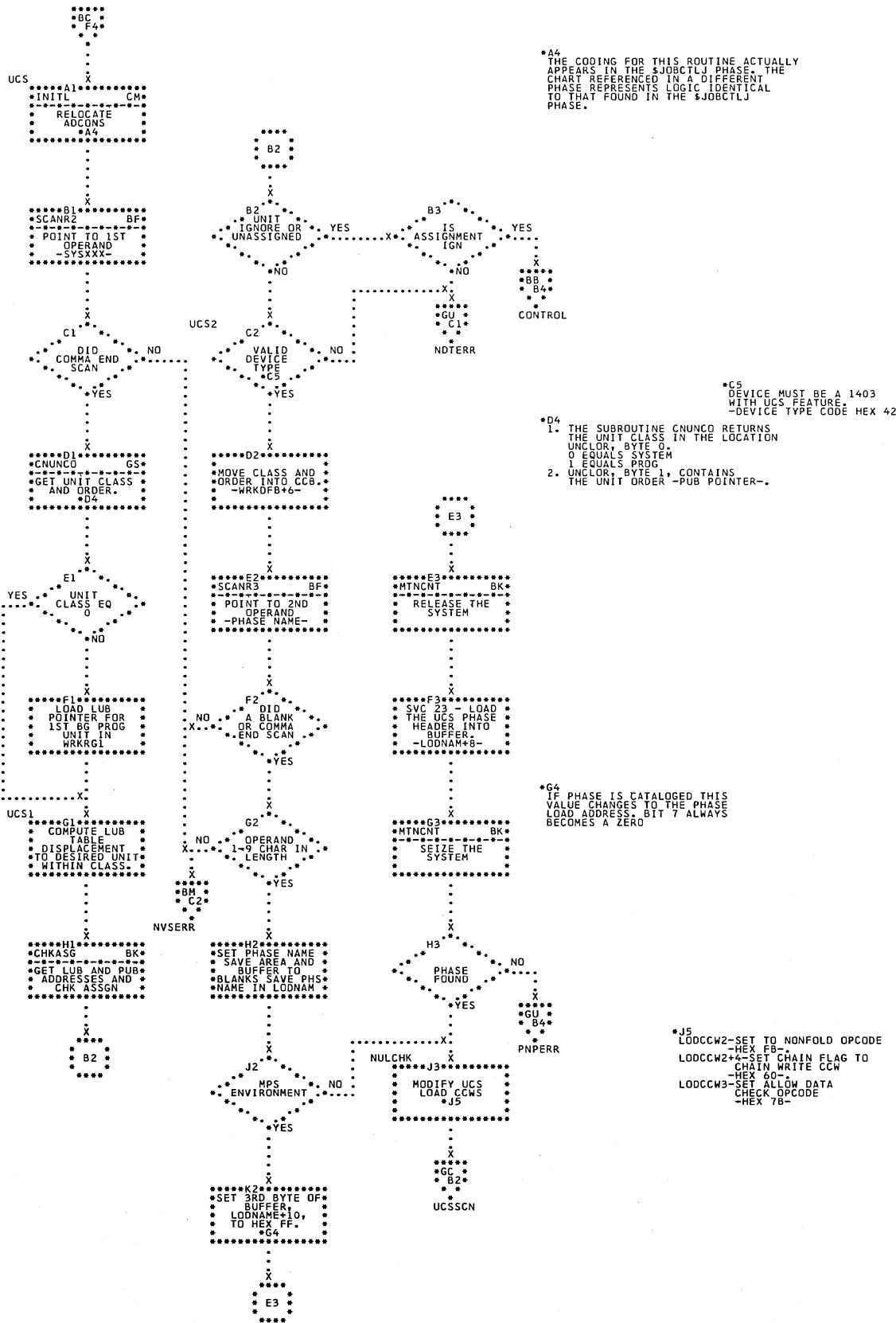


Chart GC. \$JOBCTLJ - UCS Statement Processor (Part 2 of 2)  
 Refer to Chart 11.

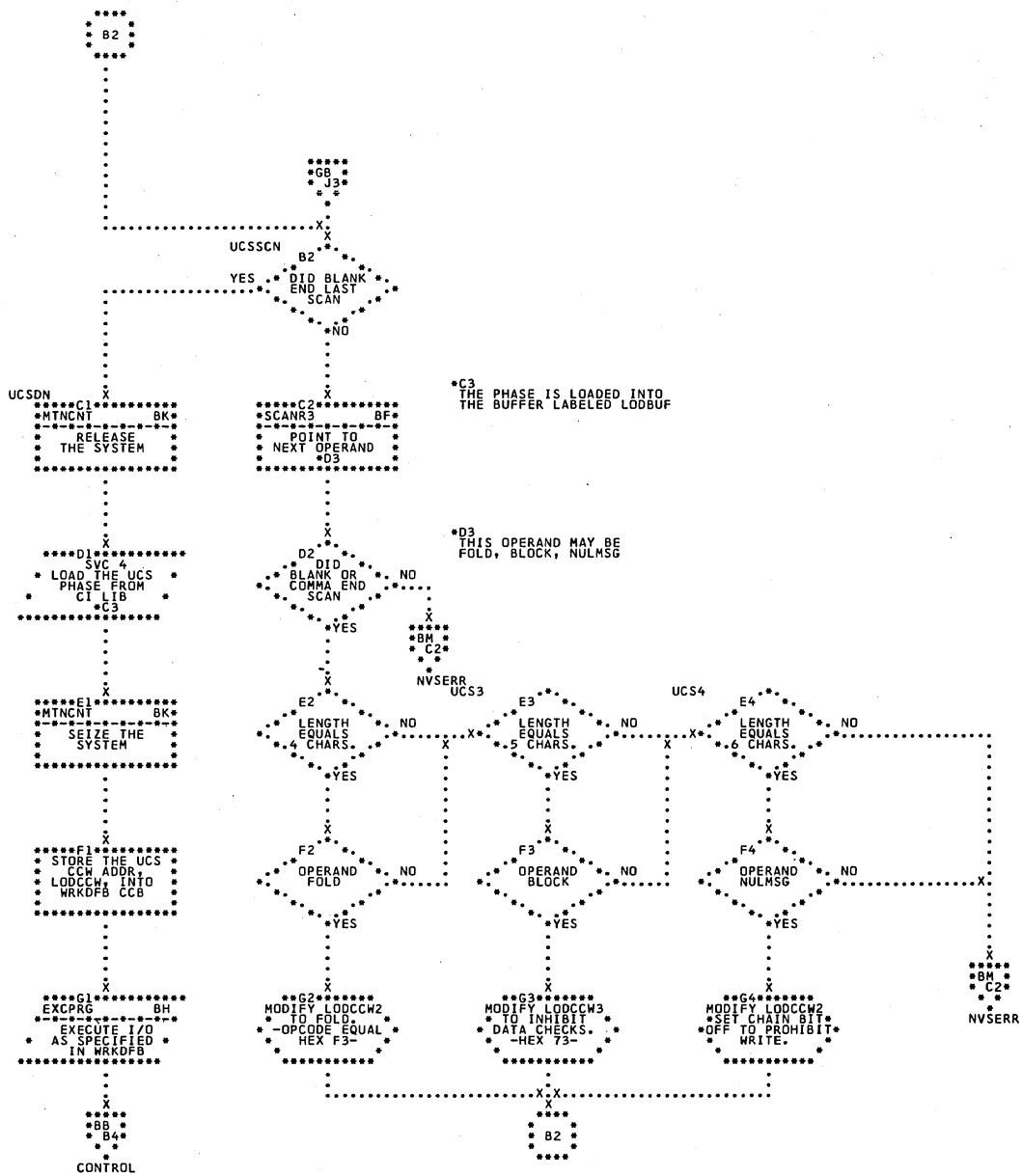


Chart GD. \$JOBCTLJ - ACTION and INCLUDE Statement Processors  
Refer to Charts 11 and 12.

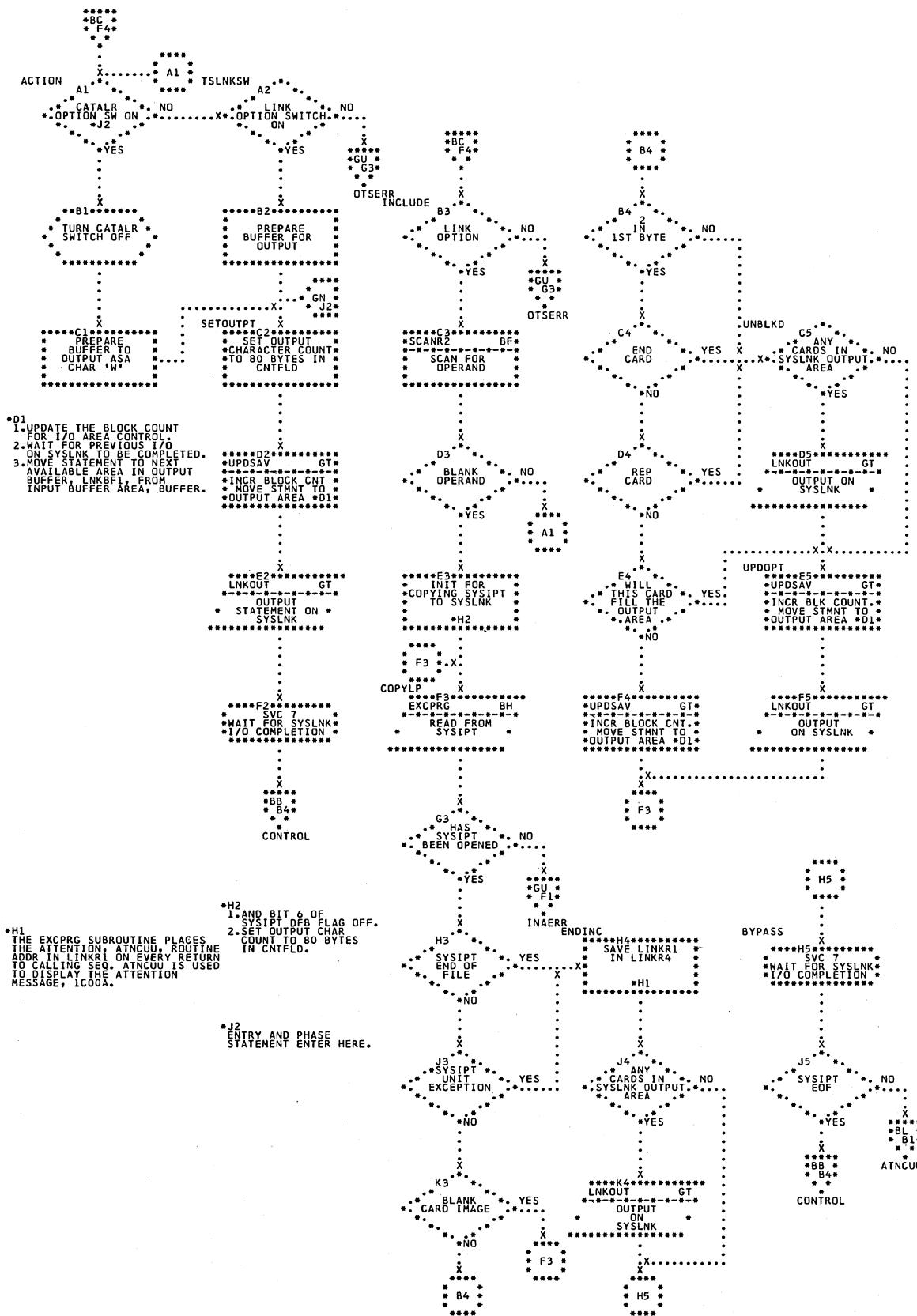


Chart GE. \$JOBCTLJ - MTC Statement Processor (Part 1 of 2)  
Refer to Chart 12.

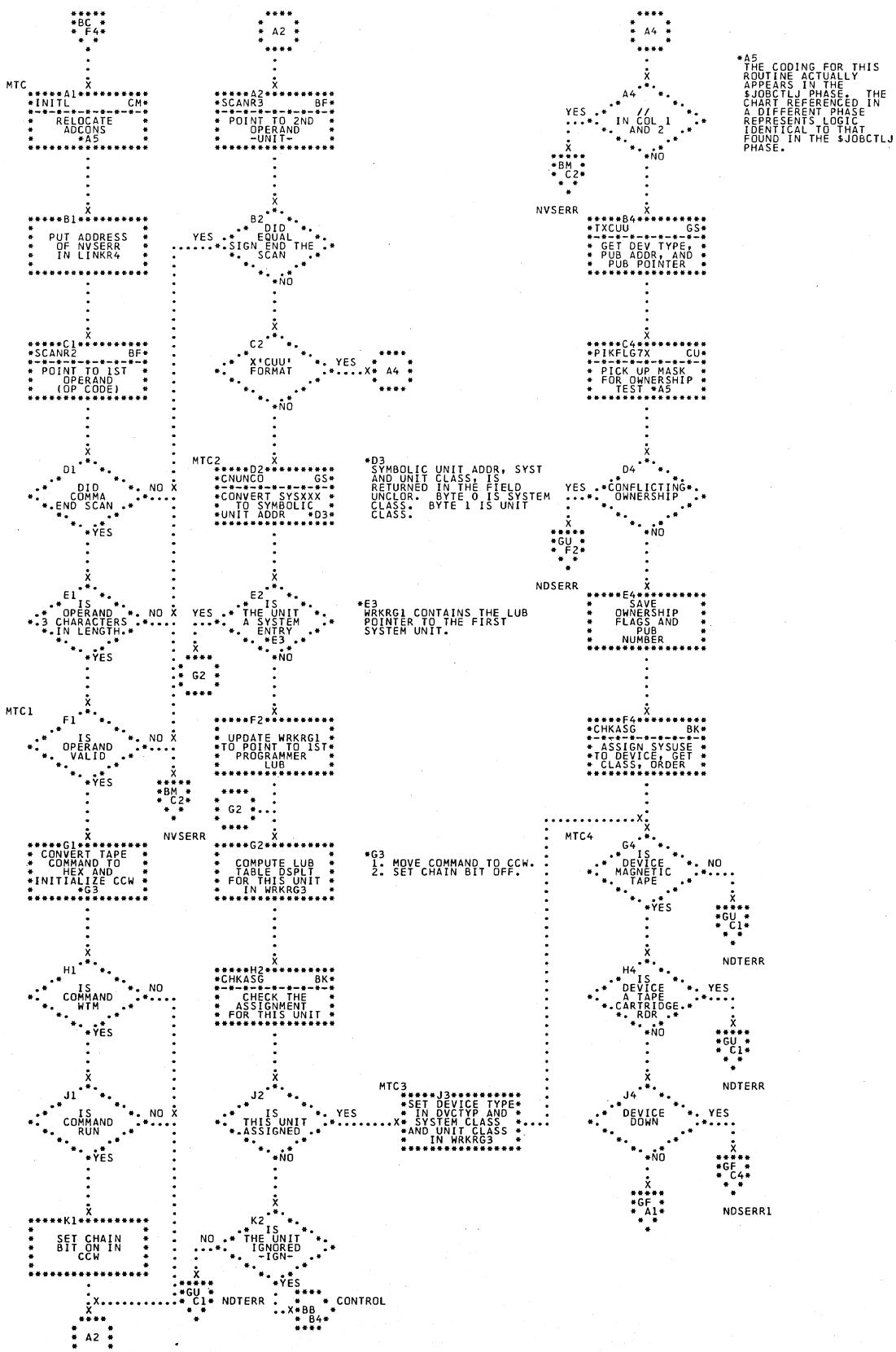


Chart GF. \$JOBCTLJ - MTC Statement Processor (Part 2 of 2)  
Refer to Chart 12.

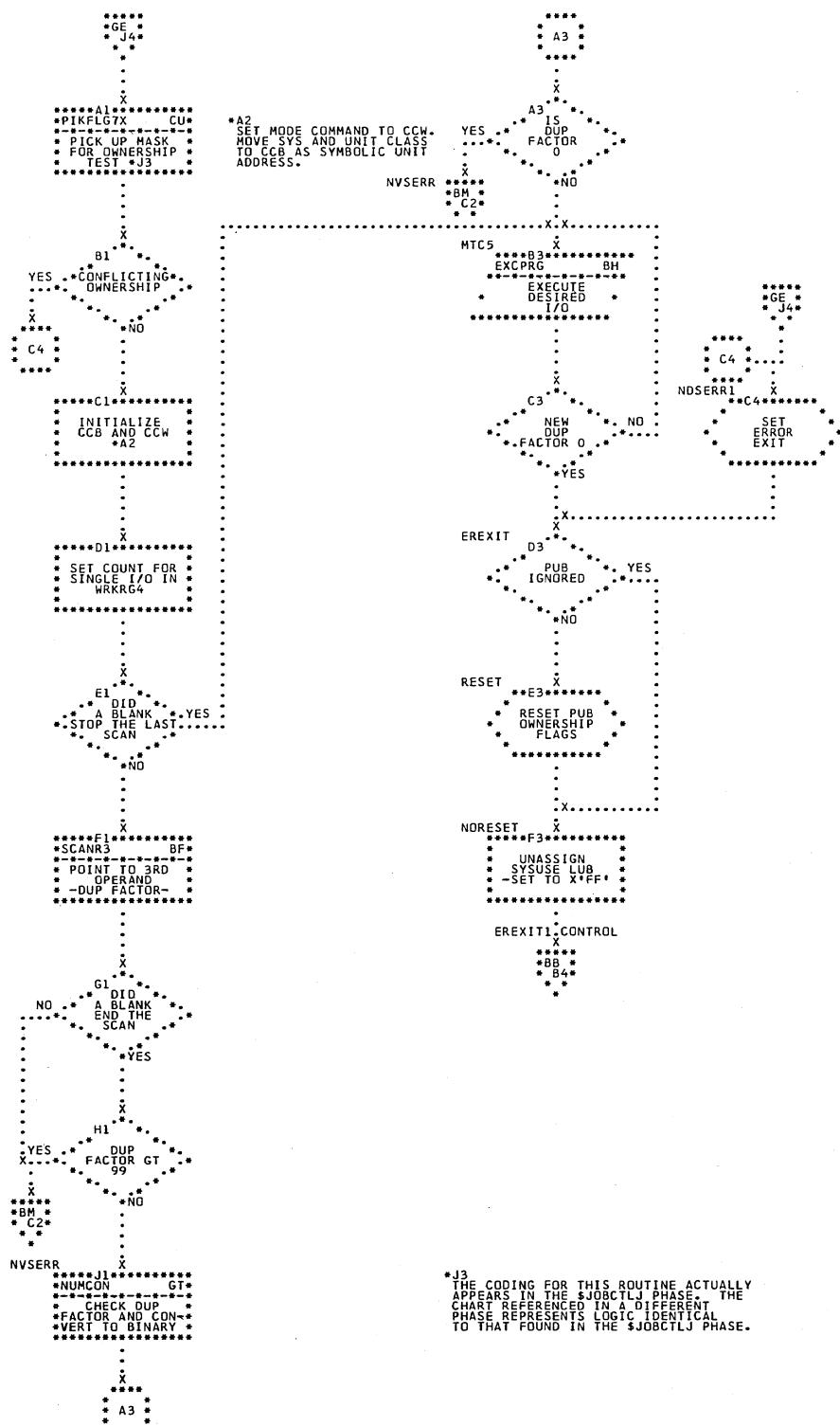


Chart GG. \$JOBCTLJ - SET Statement Processor (Part 1 of 3)  
Refer to Chart 12.

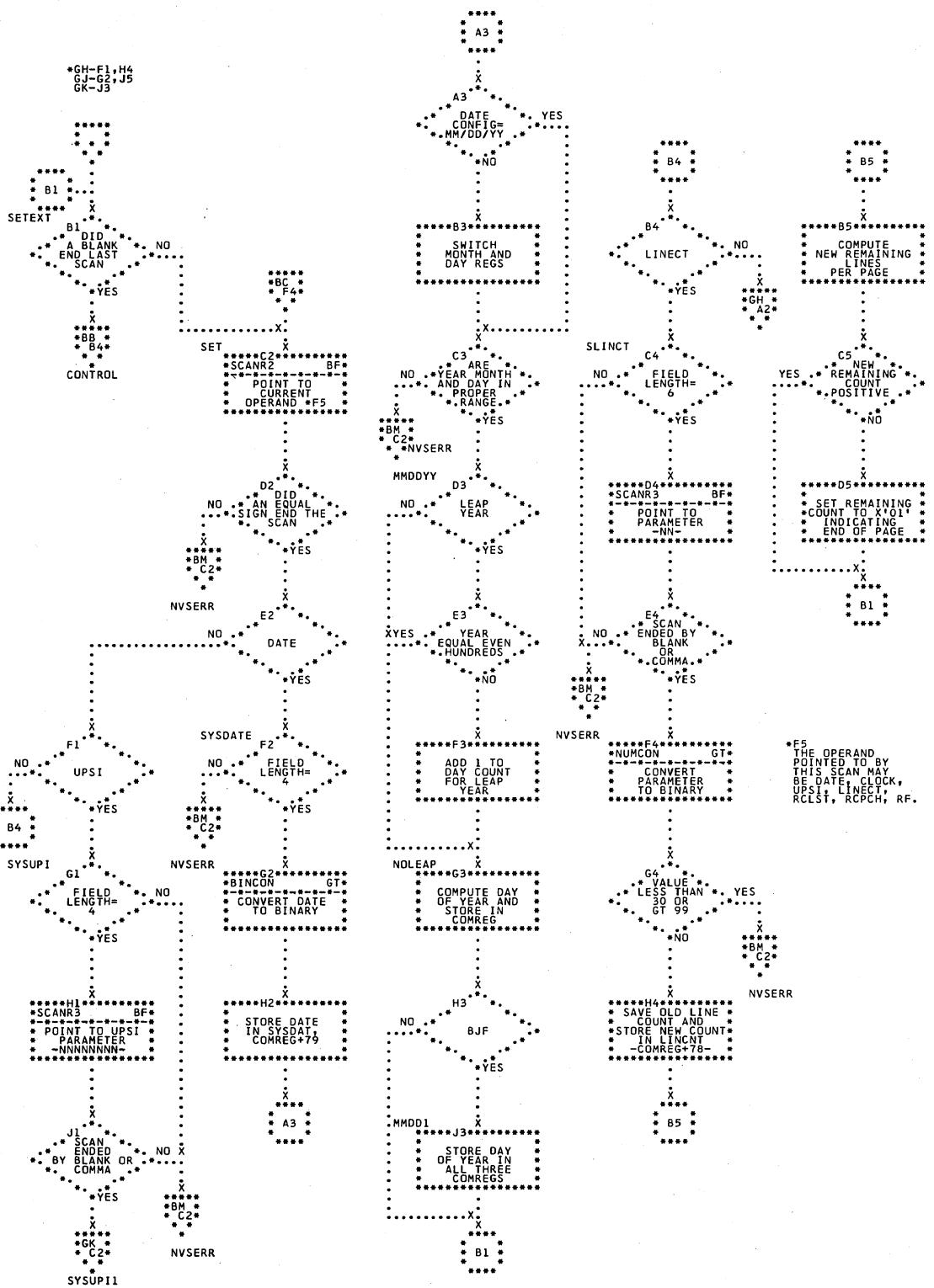
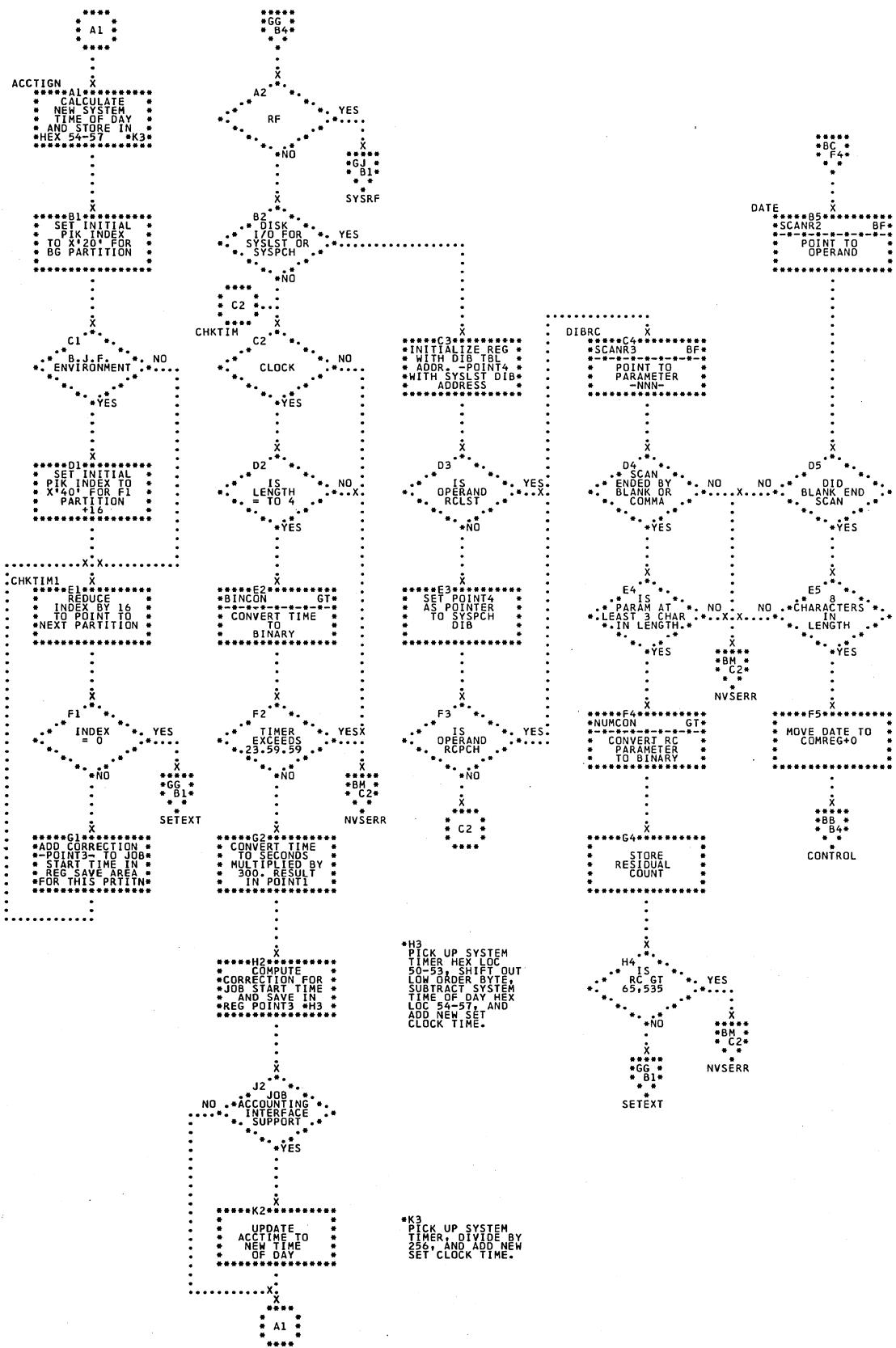


Chart GH. \$JOBCTLJ - SET Statement Processor (Part 2 of 3)  
Refer to Chart 12.



**Chart GJ. \$JOBCTLJ - SET Statement Processor (Part 3 of 3)**  
 Refer to Chart 12.

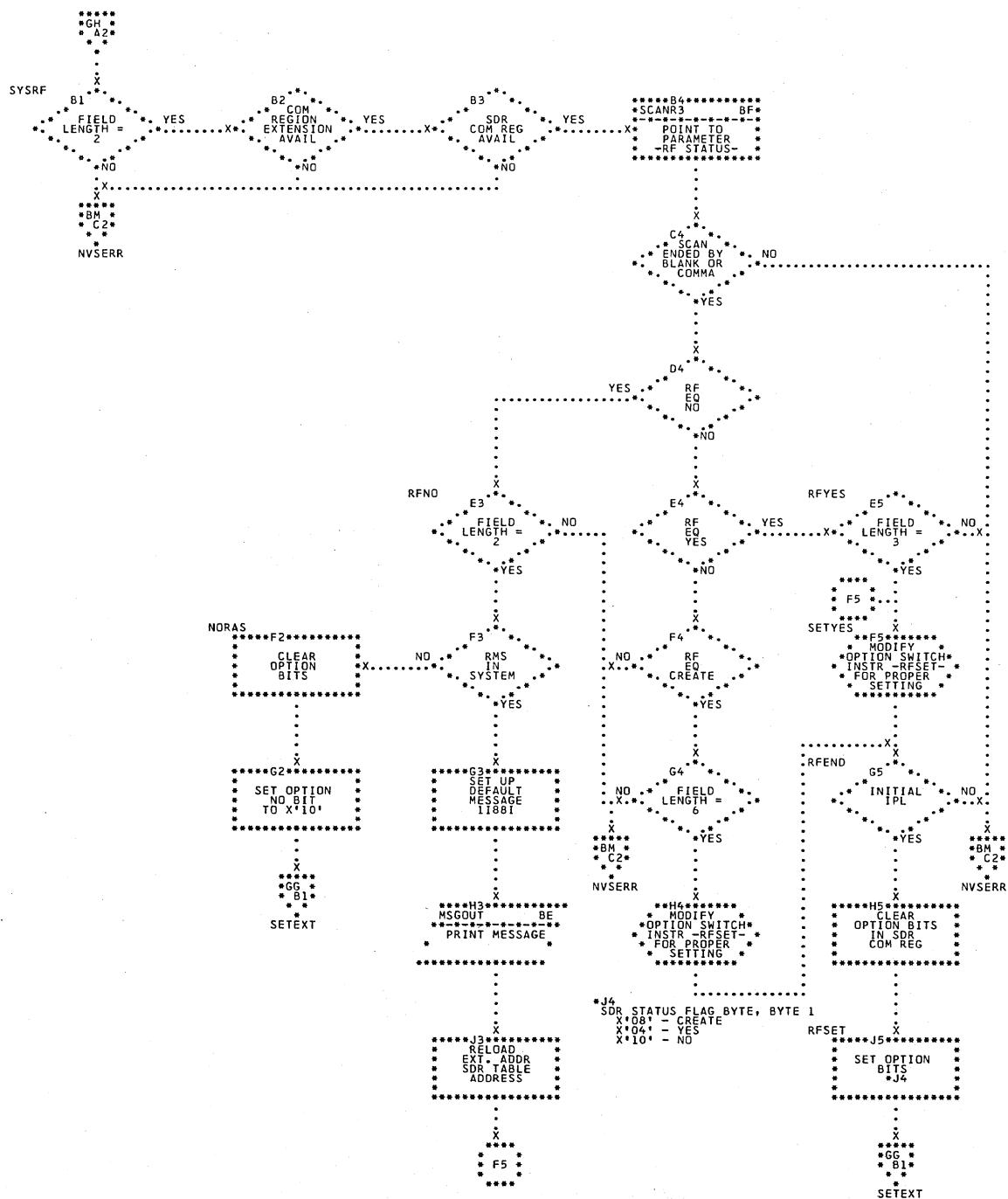


Chart GK. \$JOBCTLJ - UPSI Statement Processor  
Refer to Chart 12.

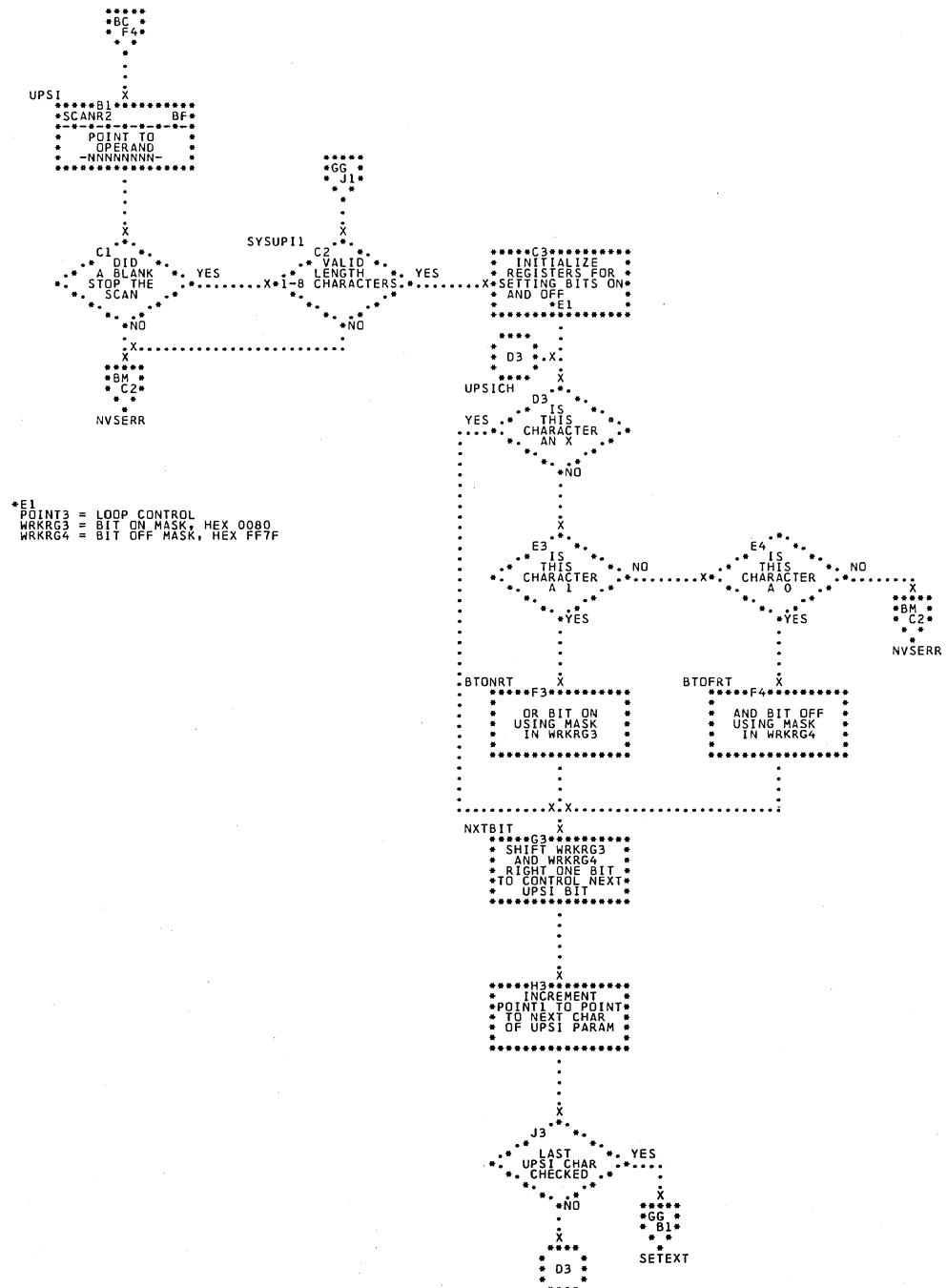


Chart GL. \$JOBCTLJ - PAUSE, LOG, and NOLOG Statement Processors  
Refer to Chart 10.

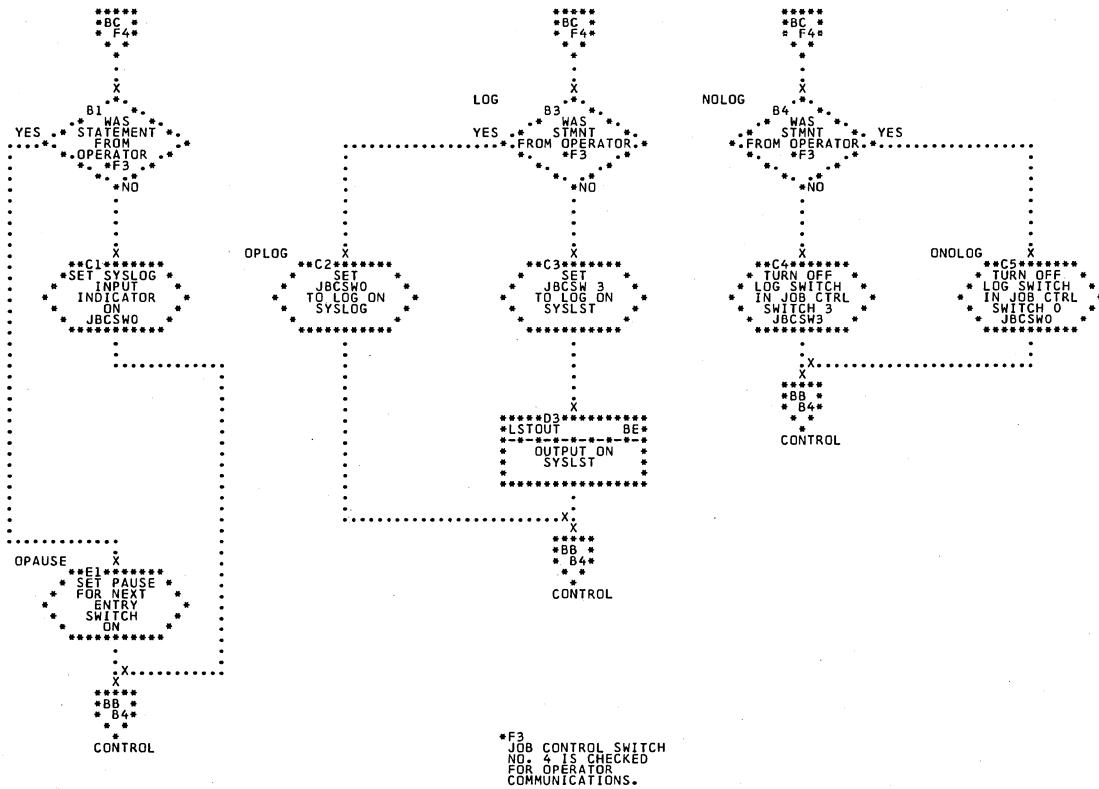


Chart GM. \$JOBCTLJ - STOP Statement Processor  
Refer to Chart 10.

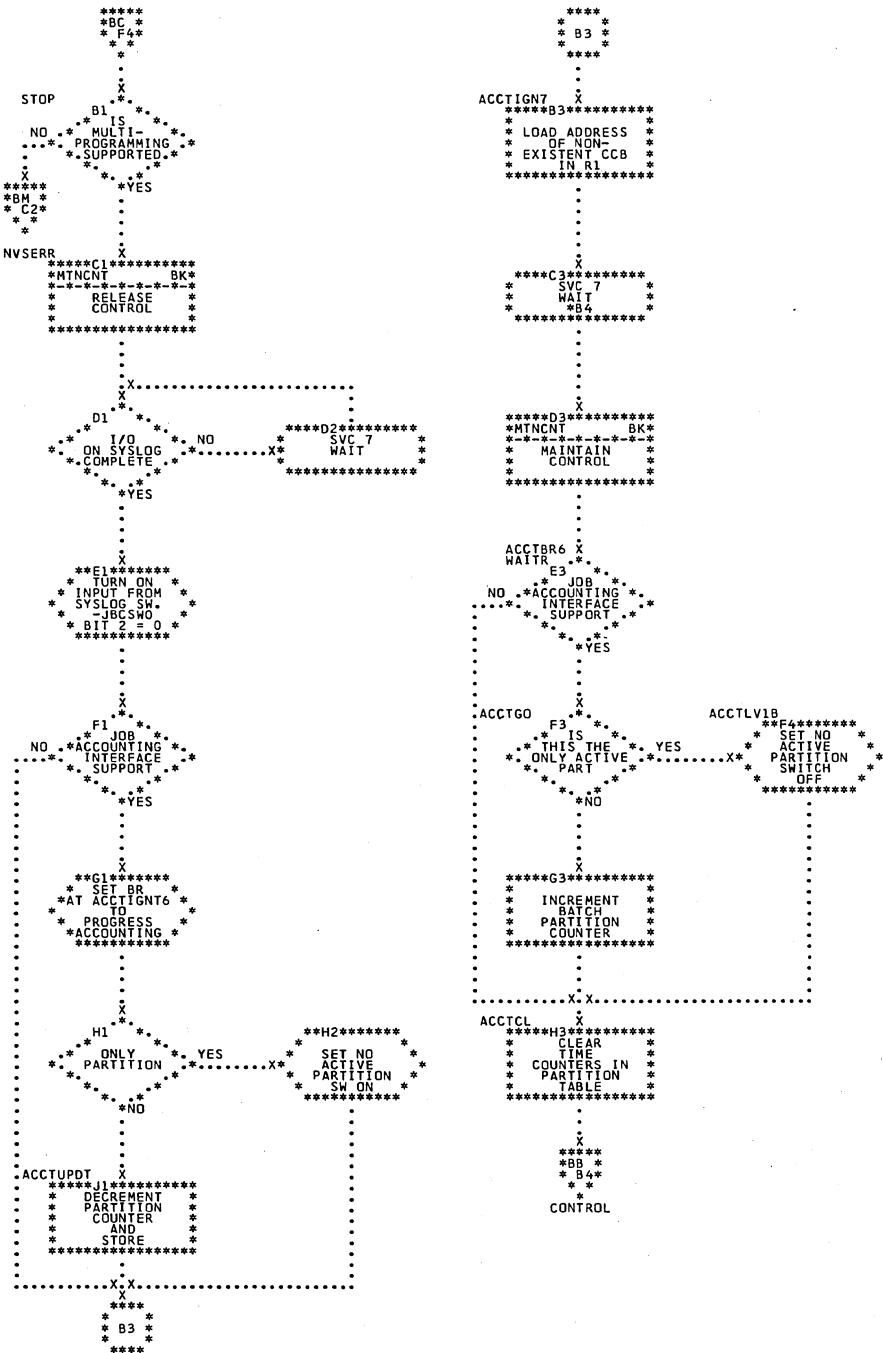


Chart GN. \$JOBCTLJ - CATALR Card Processor  
Refer to Chart 12.

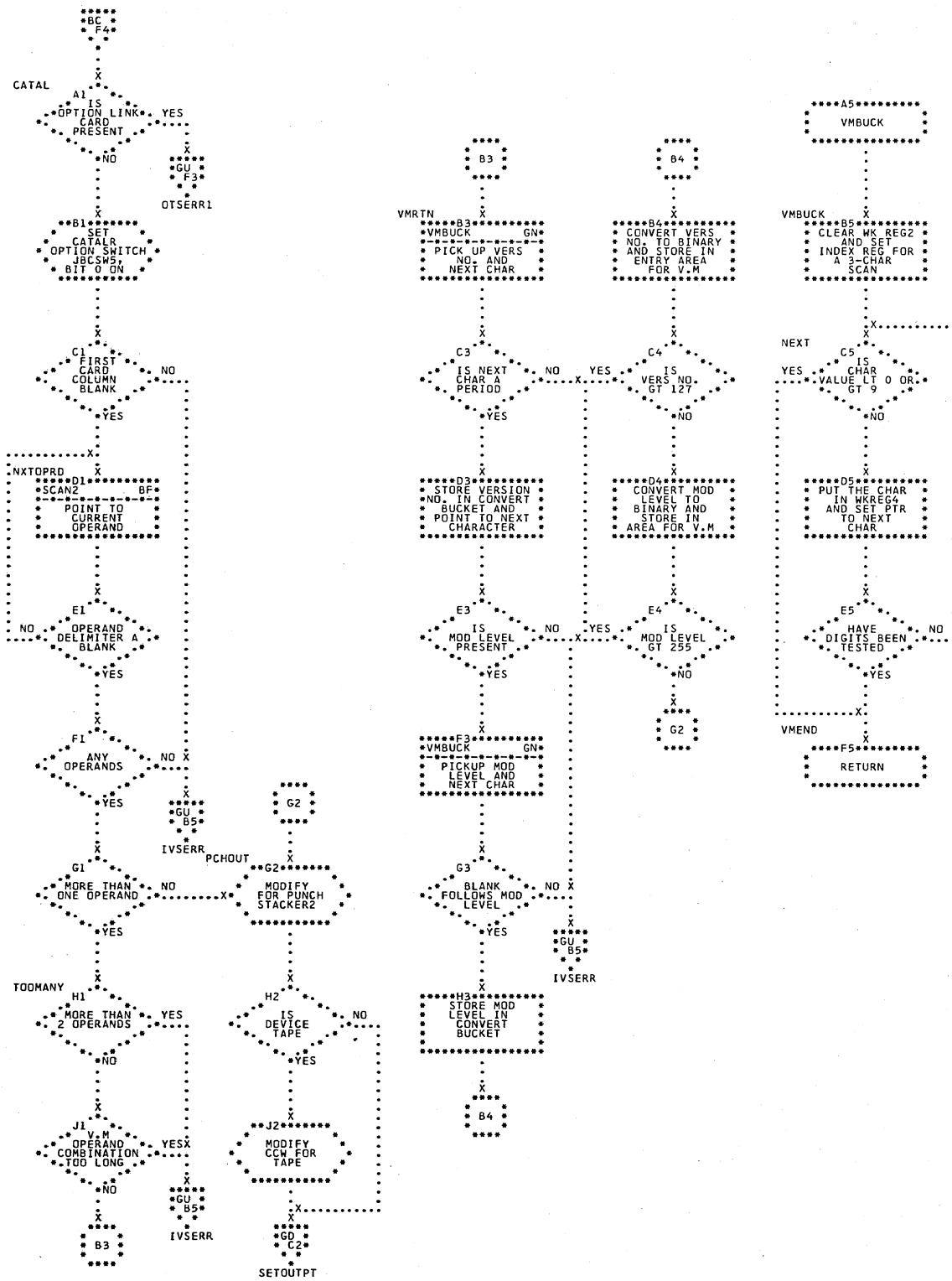


Chart GP. \$JOBCTLJ - ALLOC Statement Processor (Part 1 of 3)  
Refer to Chart 10.

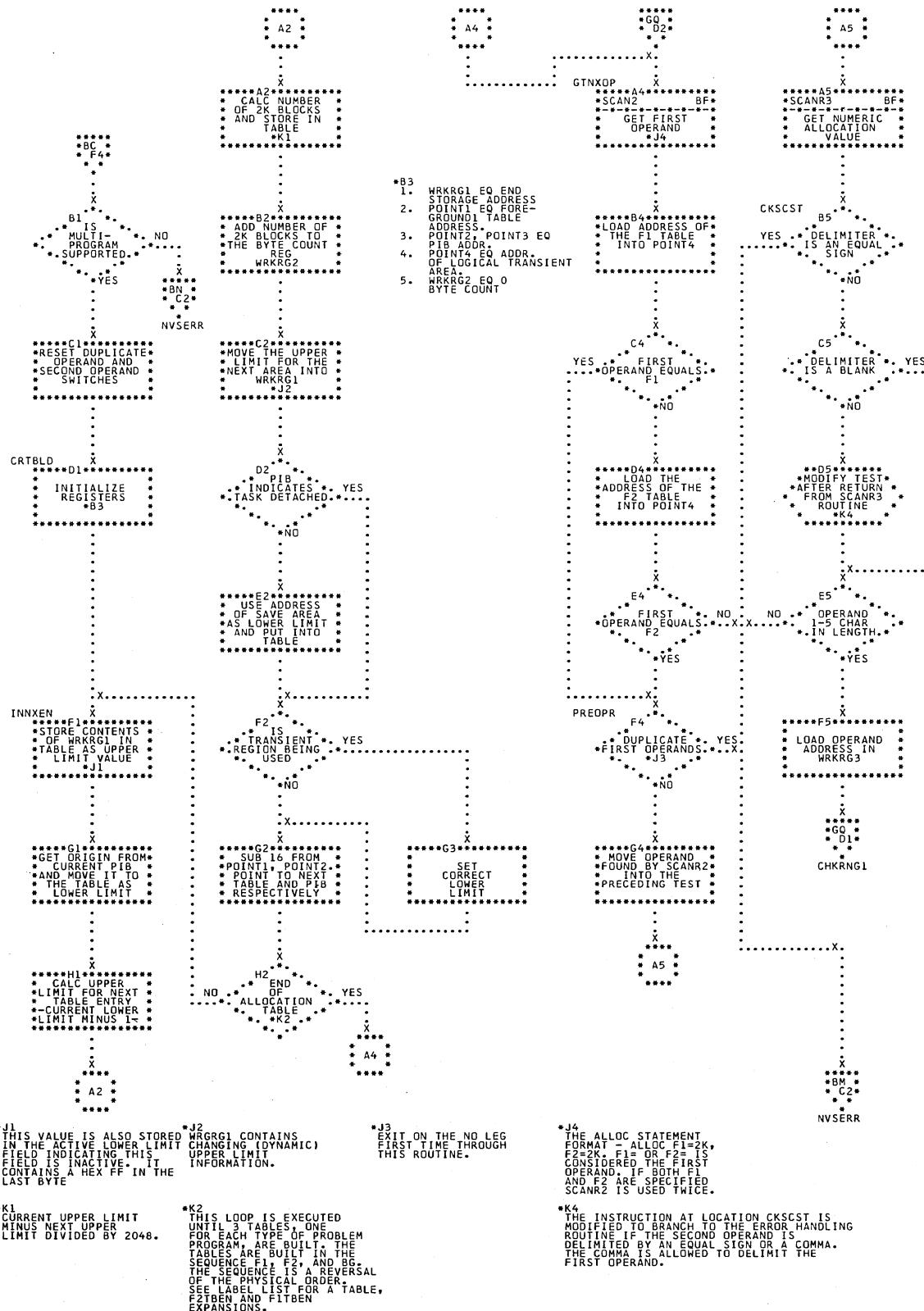


Chart GQ. \$JOBCTLJ - ALLOC Statement Processor (Part 2 of 3)  
Refer to Chart 10.

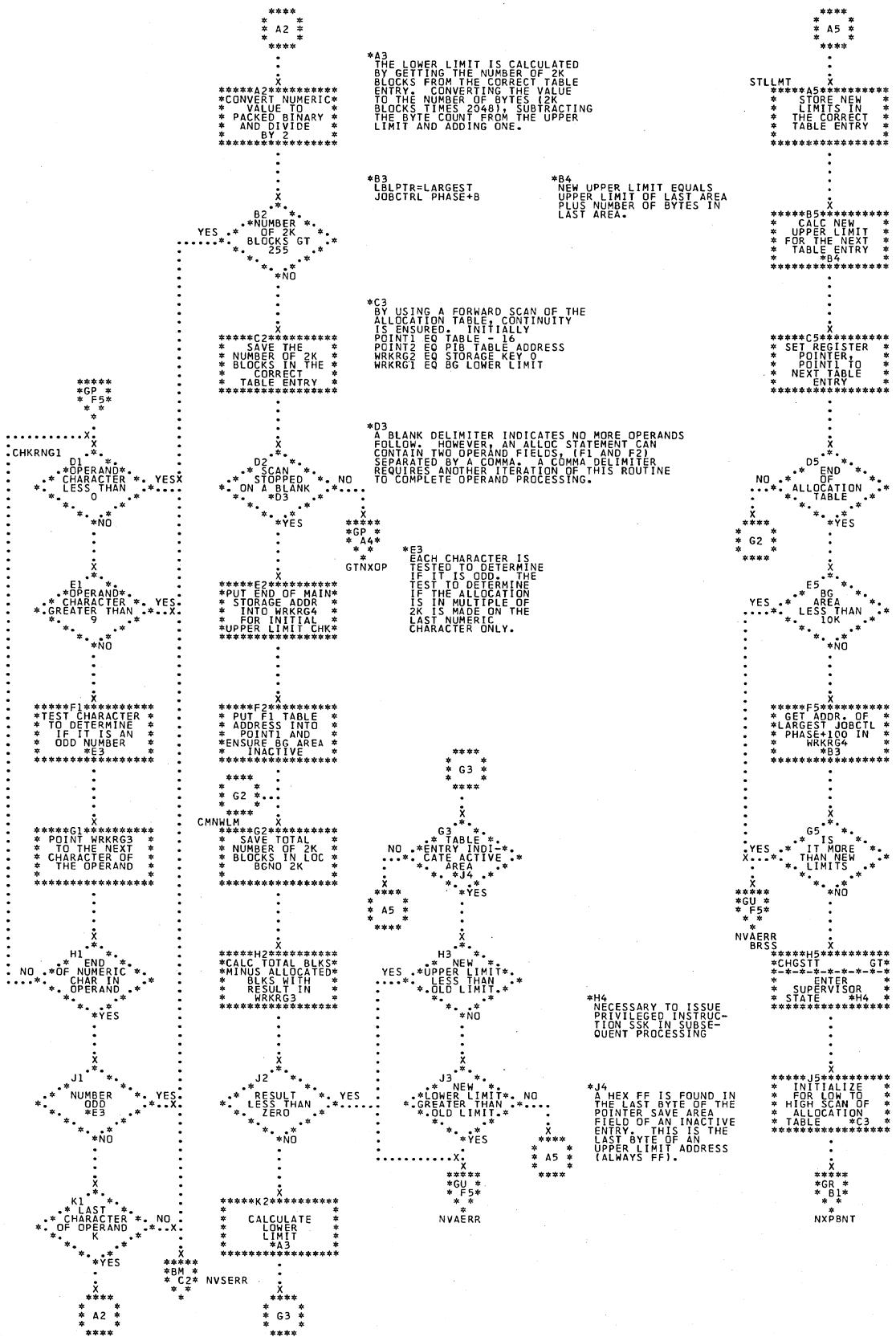


Chart GR. \$JOBCTLJ - ALLOC Statement Processor (Part 3 of 3)  
 Refer to Chart 10.

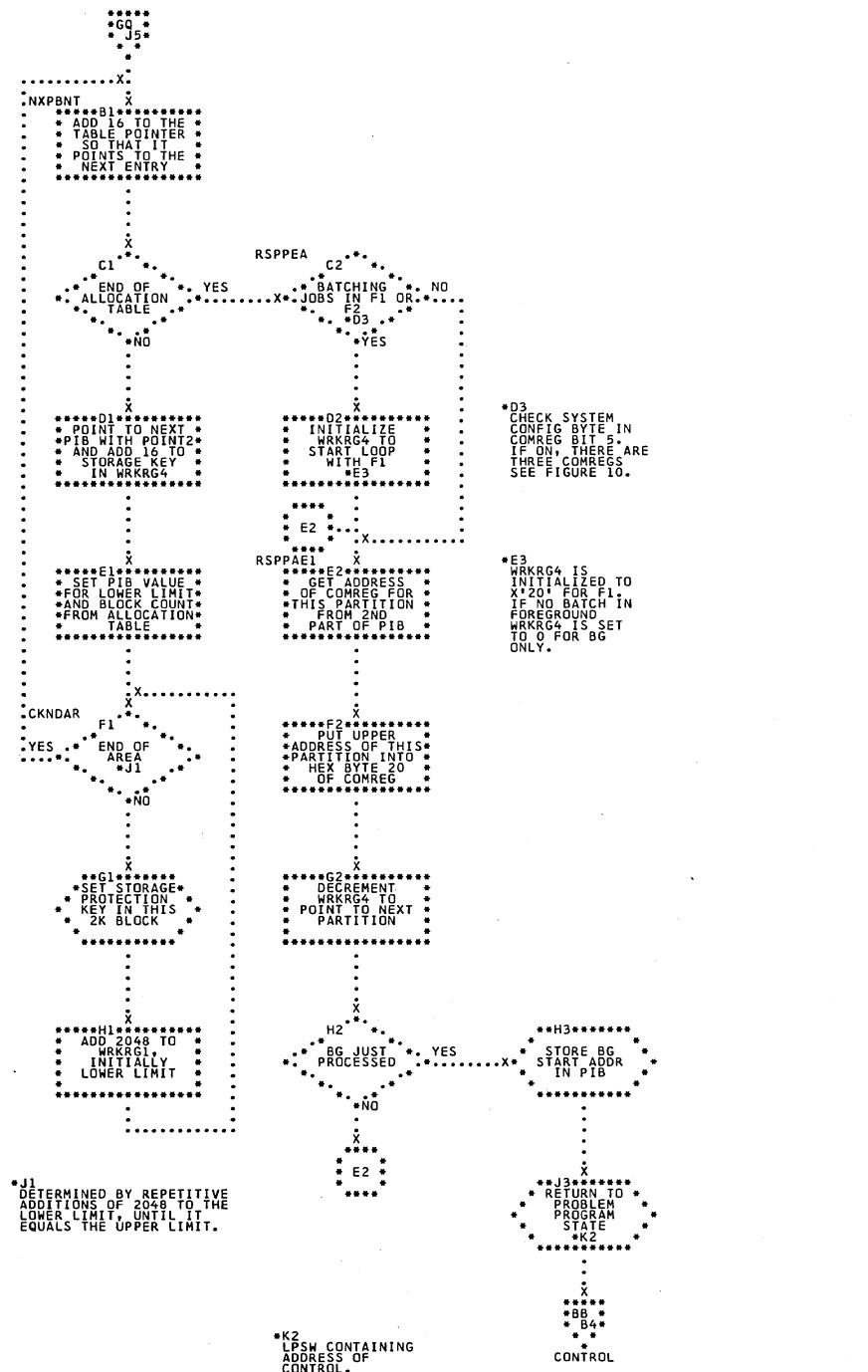
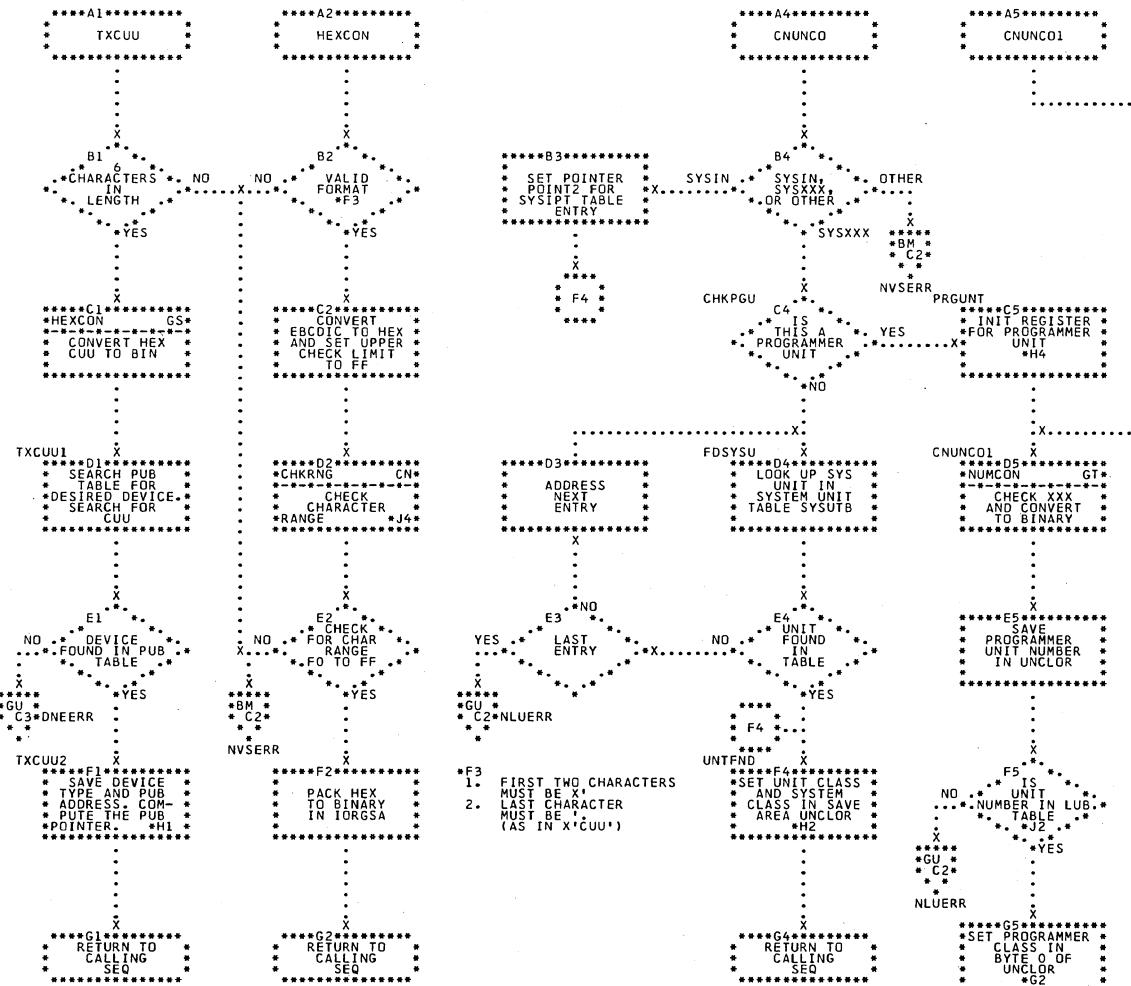


Chart GS. \$JOBCTLJ - Miscellaneous Subroutines (Part 1 of 2)  
Refer to Charts 11 and 12.



\*H1  
1. THE DEVICE TYPE IS SAVED IN DVCTYP FROM THE PUB.  
2. THE PUB ADDRESS IS SAVED IN REGISTER PNT1.  
3. THE PUB POINTER IS COMPUTED IN WRKRG3.

\*H2  
THE SYSTEM CLASS, NICL AND FICL  
POINTER, IS SET TO A1 IN UNCLOR BYTE 0.

\*H3  
1. POINT1 IS INCREMENTED BY 3 TO POINT TO THE FIRST CHARACTER OF XXX.  
2. POINT3 IS INITIALIZED TO A2 AS A CHARACTER COUNT REGISTER.

\*H4  
\* RETURN TO CALLING SEQ

\*J2  
1. UNCLOR BYTE 1 IS THE UNIT CLASS,  
LUB POINTER IS THE FUNCTION OF THE  
SYS ARGUMENT IN THE TABLE SYSUT,  
UNCLOR BYTE 0, THE SYSTEM CLASS,  
NICL AND FICL POINTER, IS SET TO ZERO  
BECAUSE THE ROUTINE IS WORKING WITH  
A SYSTEM UNIT.

\*J4  
CODING FOR THIS ROUTINE ACTUALLY  
APPEARS IN THE \$JOBCTLJ PHASE. THE  
CHART REFERENCED IN A DIFFERENT  
PHASE REPRESENTS LOGIC IDENTICAL  
TO THAT FOUND IN THE \$JOBCTLJ PHASE.

\*K2  
CHECK ONLY LUBS  
FOR THIS PARTITION

Chart GT. \$JOBCTLJ - Miscellaneous Subroutines (Part 2 of 2)  
Refer to Charts 11 and 12.

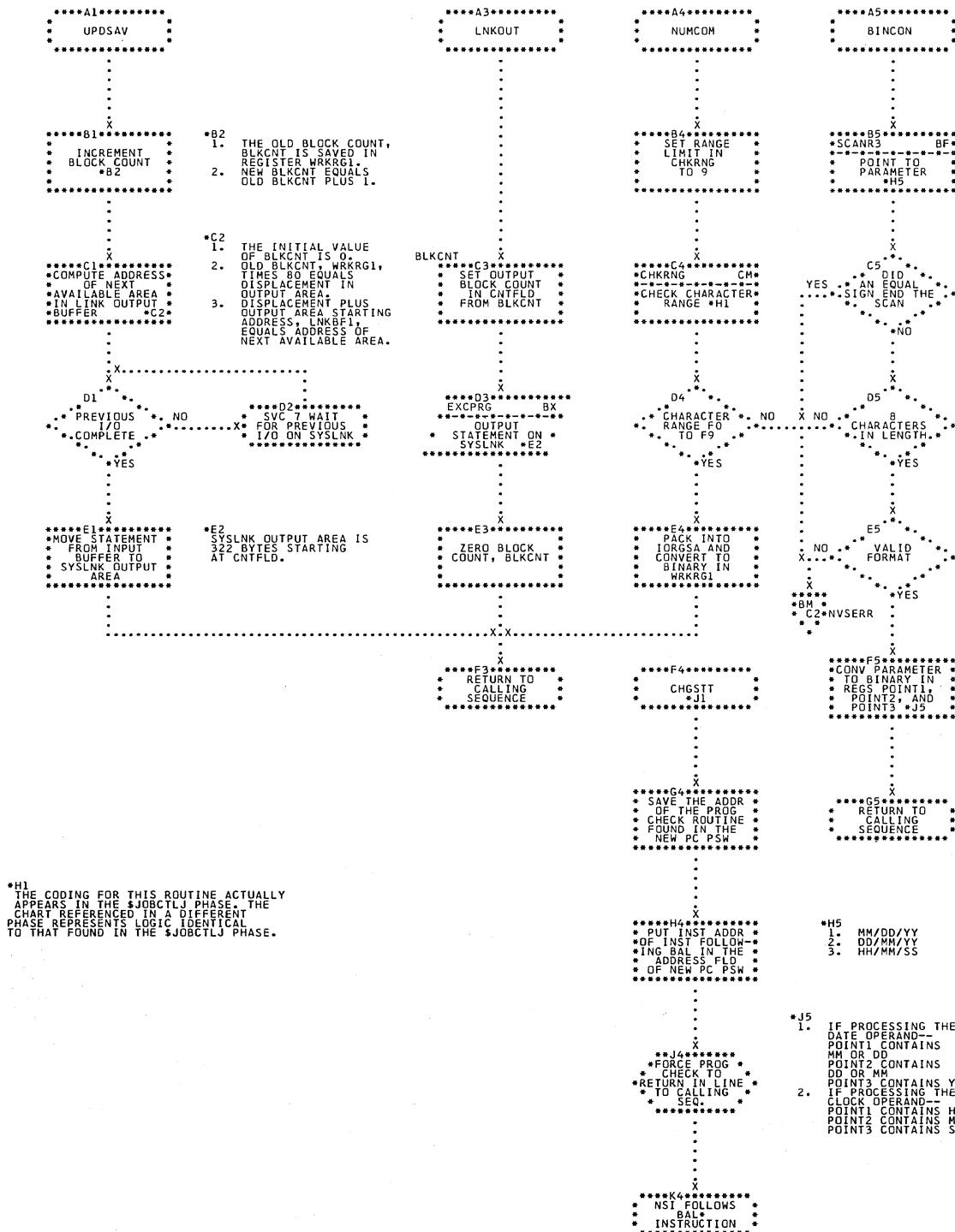
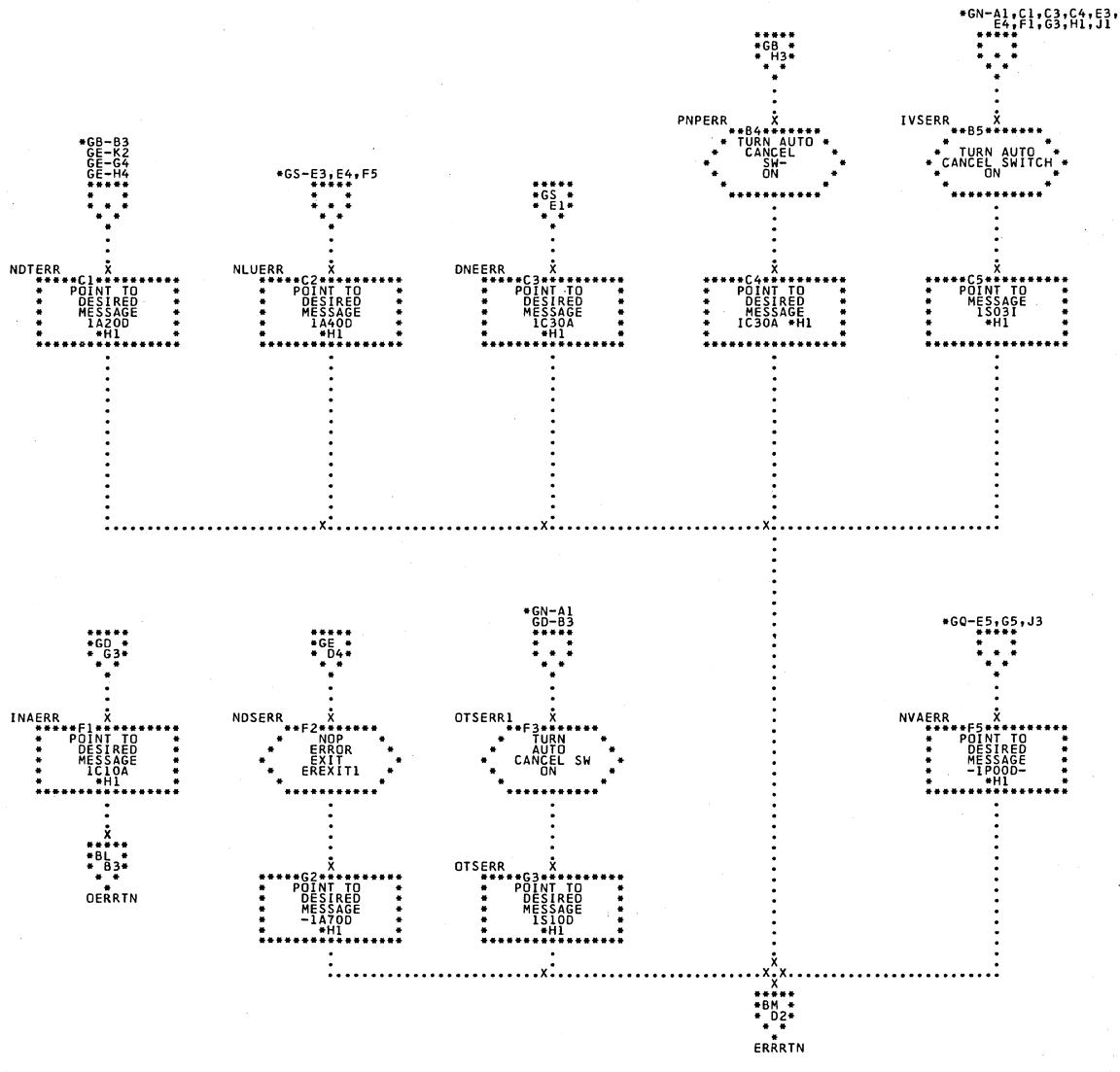


Chart GU. \$JOBCTLJ - Error Subroutines  
Refer to Charts 11 and 12.



```

*H1
MESSAGE: DESCRIPTION
1A200: INVALID DEVICE TYPE
1A40D: INVALID LOGICAL UNIT SPECIFICATION
1A50D: DEVICE NOT DEFINED
1A70D: INVALID DEVICE STATUS
1C10A: PLEASE ASSIGN-SYSRDR;SYSPTT;SYSLNK-
1C30A: PROGRAM NOT FOUND
1P00D: INVALID ALLOCATION
1S03I: INVALID STATEMENT
1S10D: STATEMENT OUT OF SEQUENCE

```

Chart HA. \$JOBCTLK - LBLTYP, VOL, & TPLAB Statement Processors  
Refer to Chart 13.

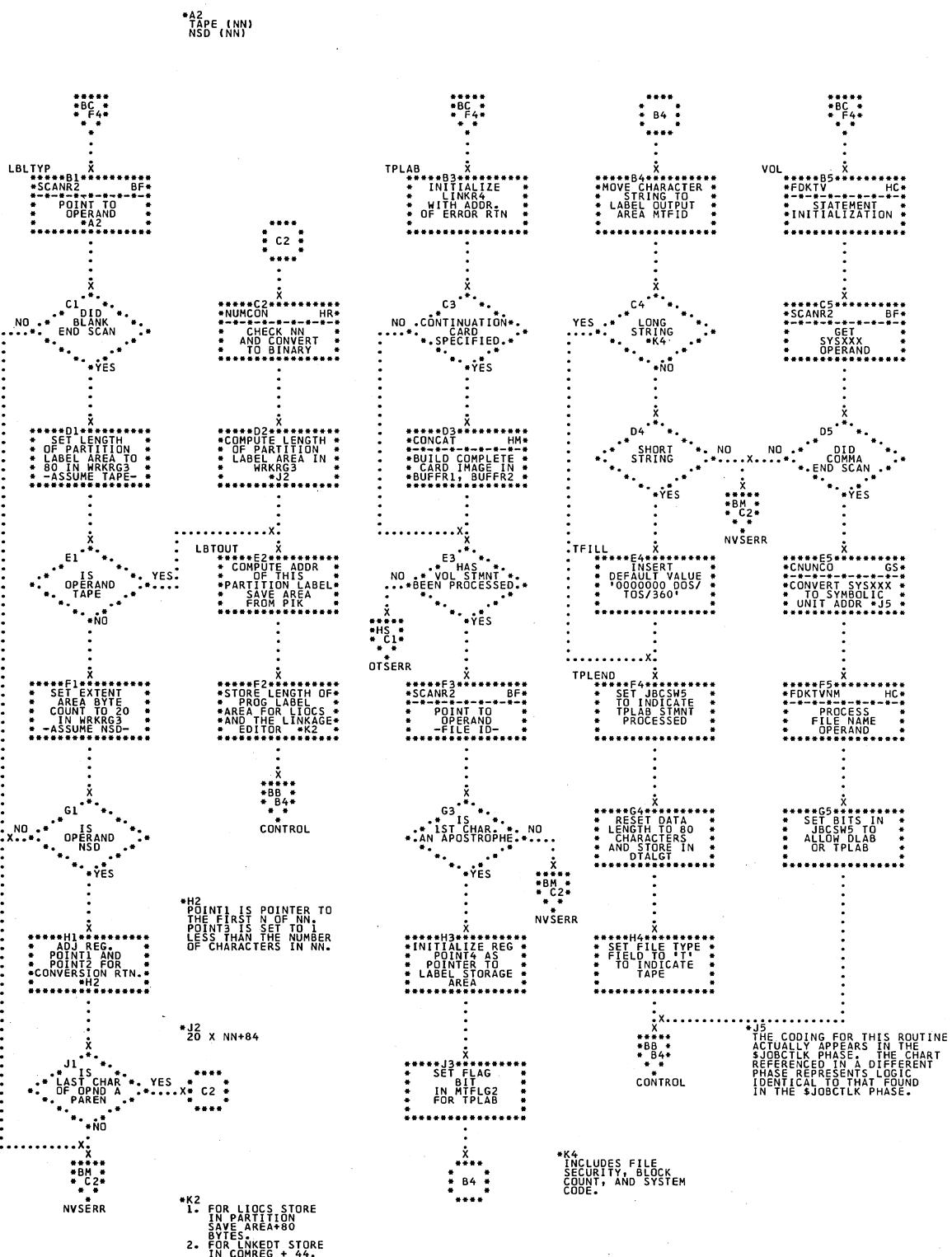
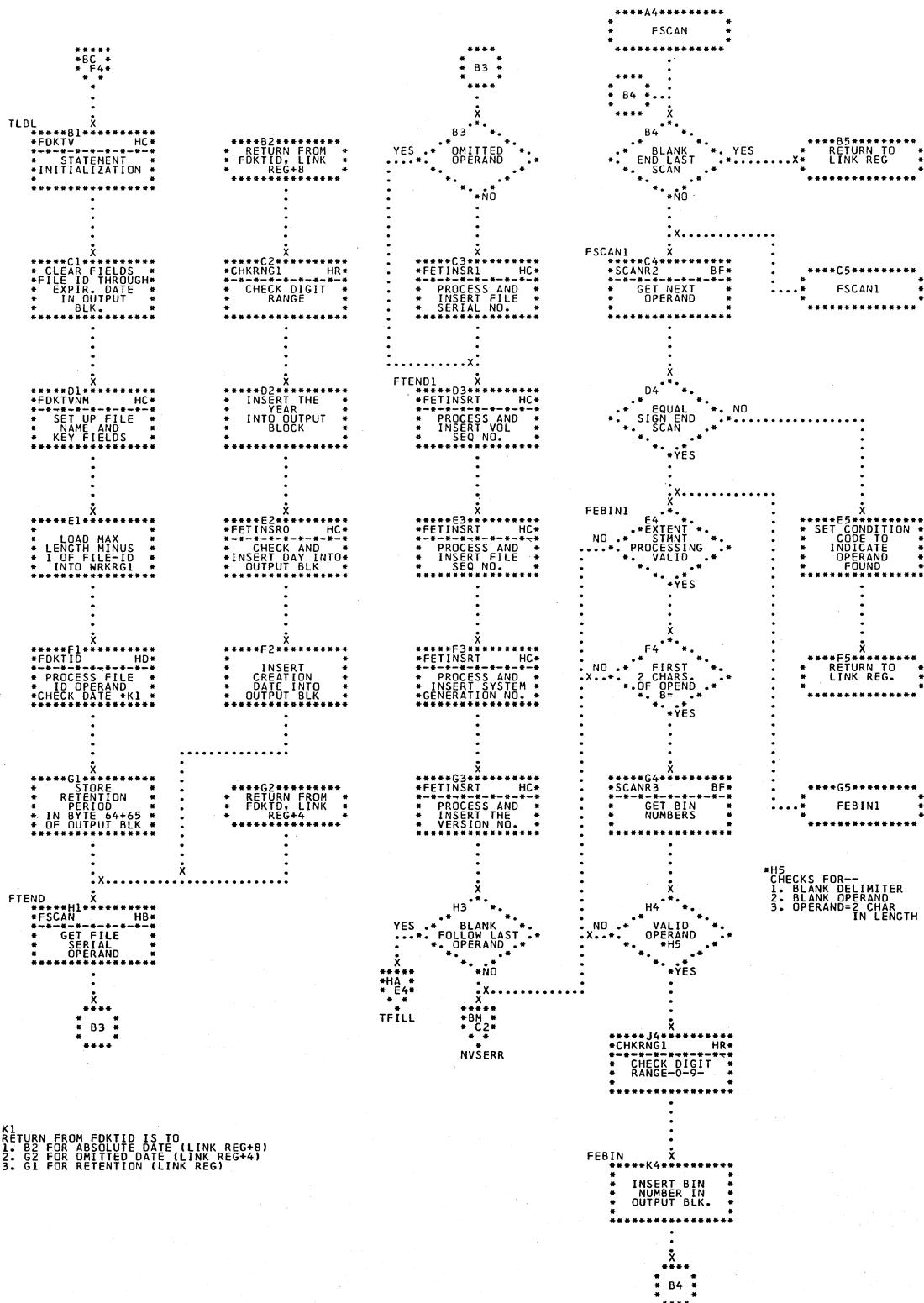


Chart HB. \$JOBCTLK - TLBL Statement Processor  
Refer to Chart 14.



**Chart HC. \$JOBCTLK - Label Processing Subroutines (Part 1 of 2)**  
 Refer to Charts 13 and 14.

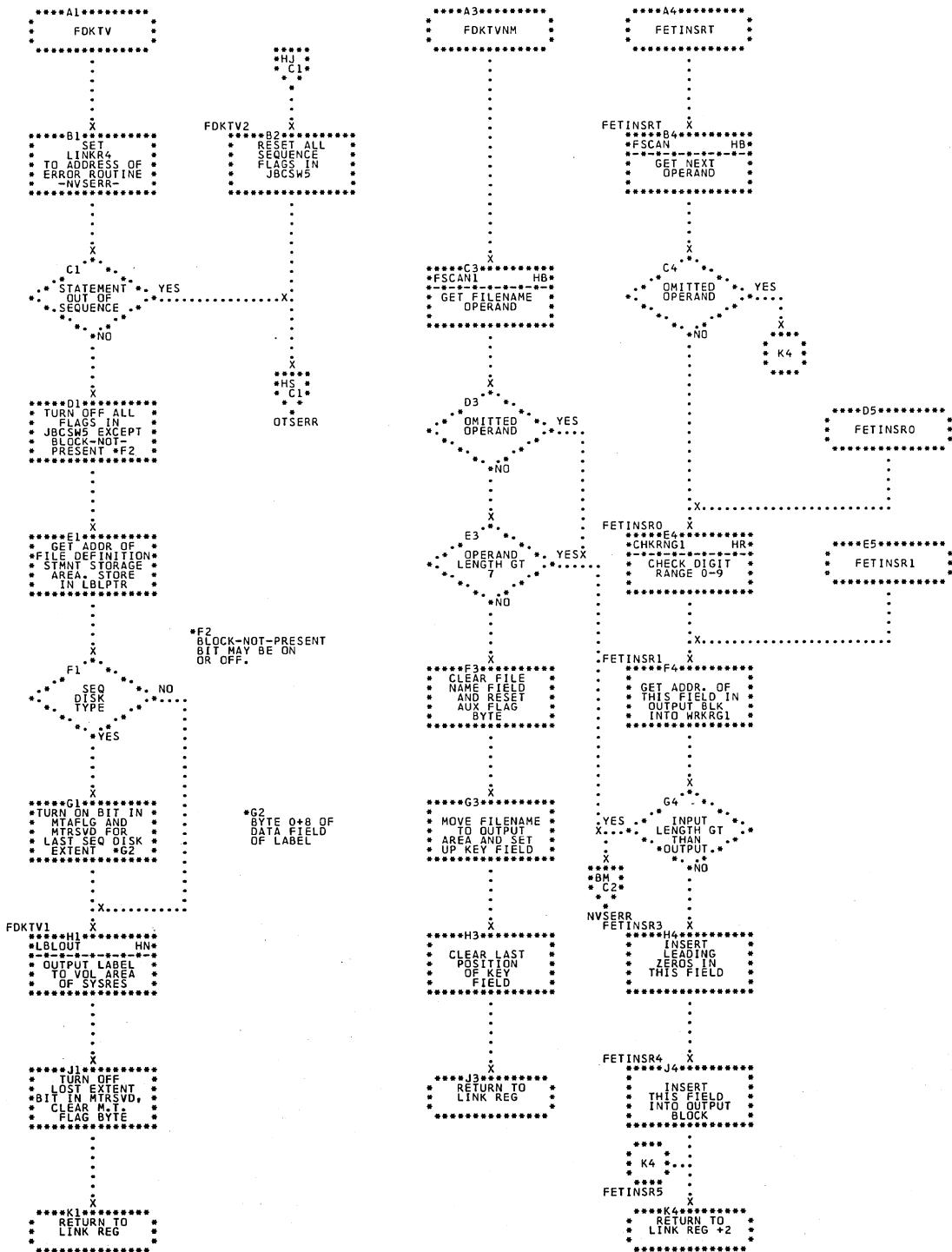


Chart HD. \$JOBCTLK - Label Processing Subroutines (Part 2 of 2)  
 Refer to Charts 13 and 14.

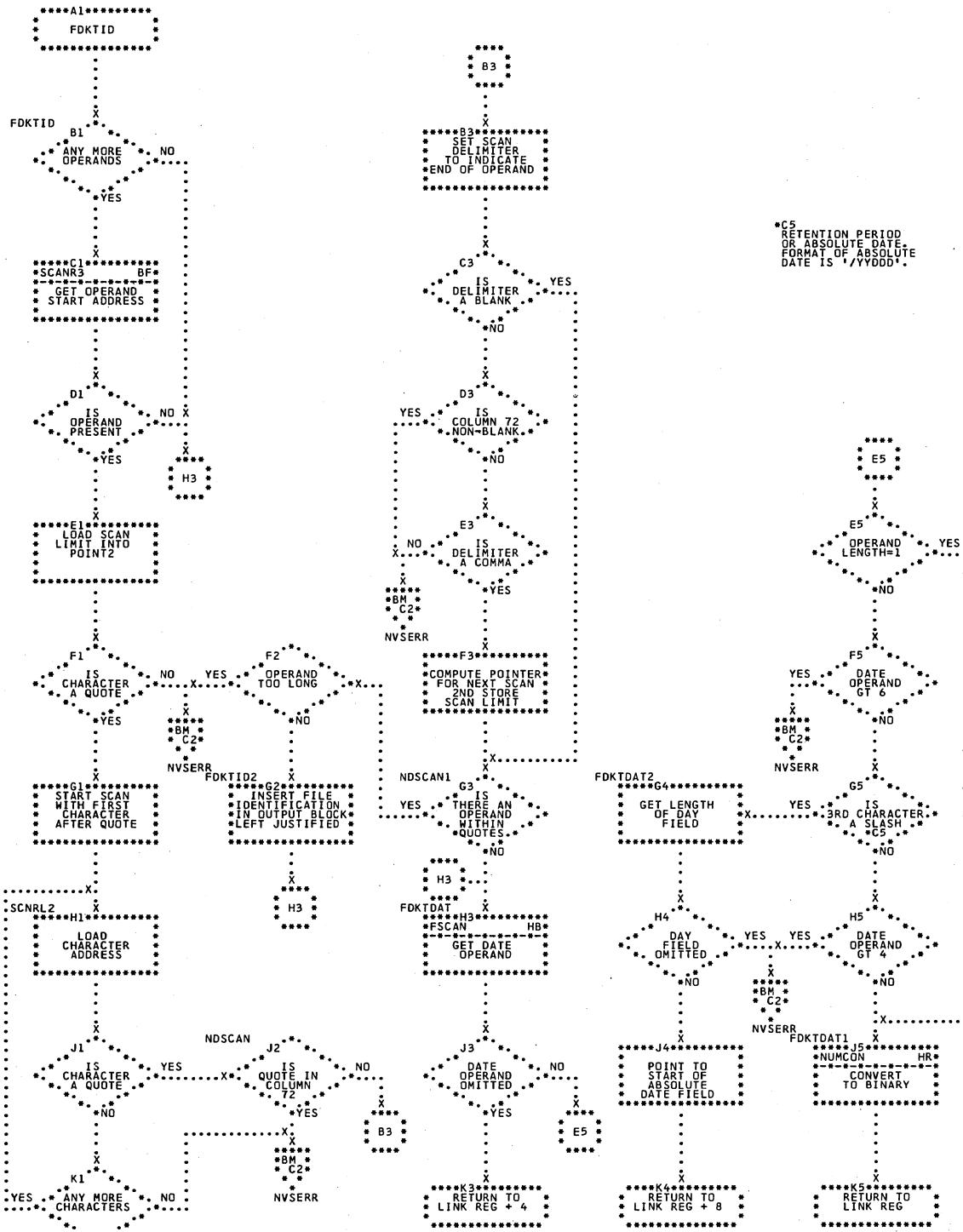


Chart HE. \$JOBCTLK - DLBL Statement Processor  
Refer to Chart 14.

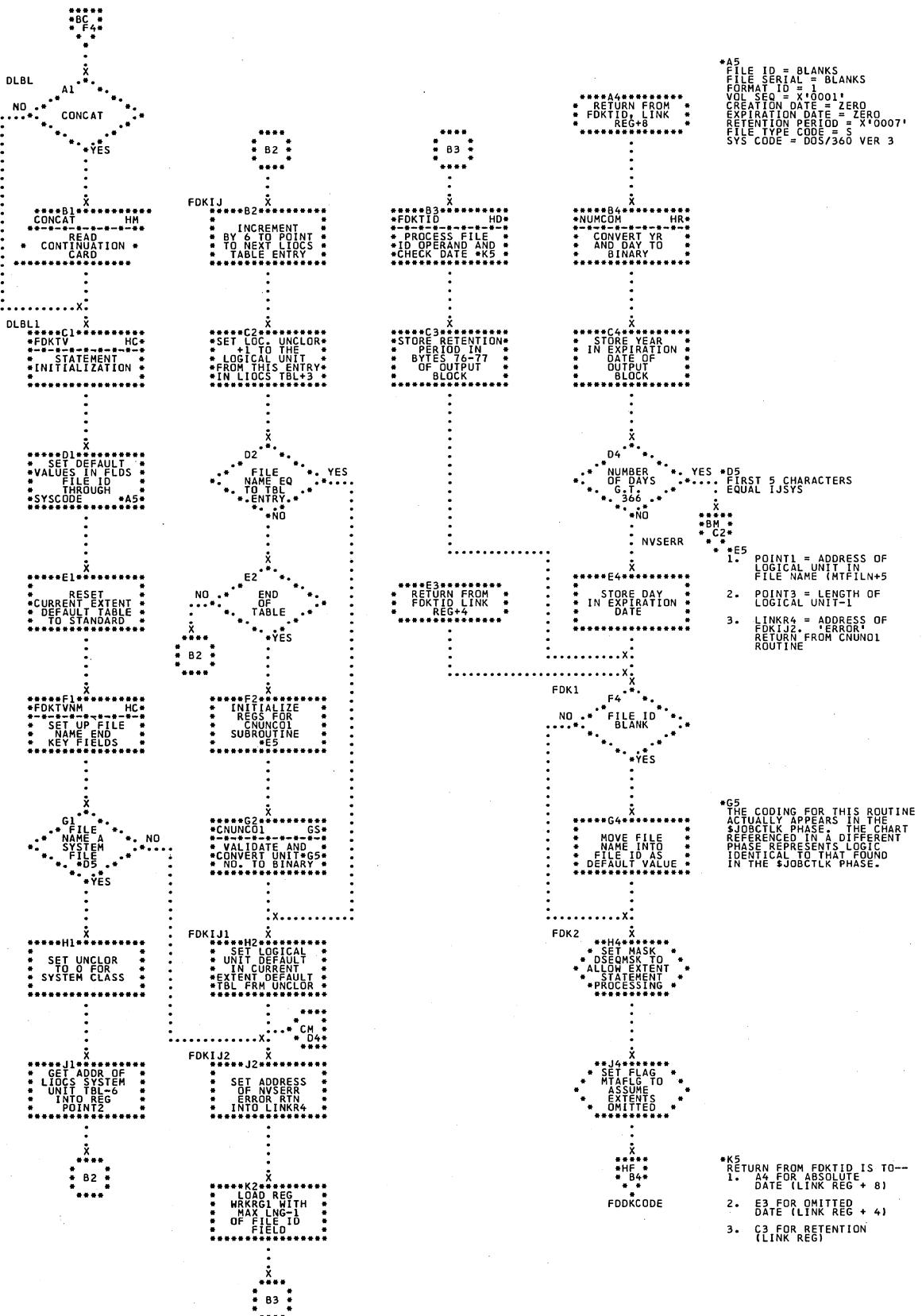


Chart HF. \$JOBCTLK - DLAB Statement Processor  
Refer to Chart 13.

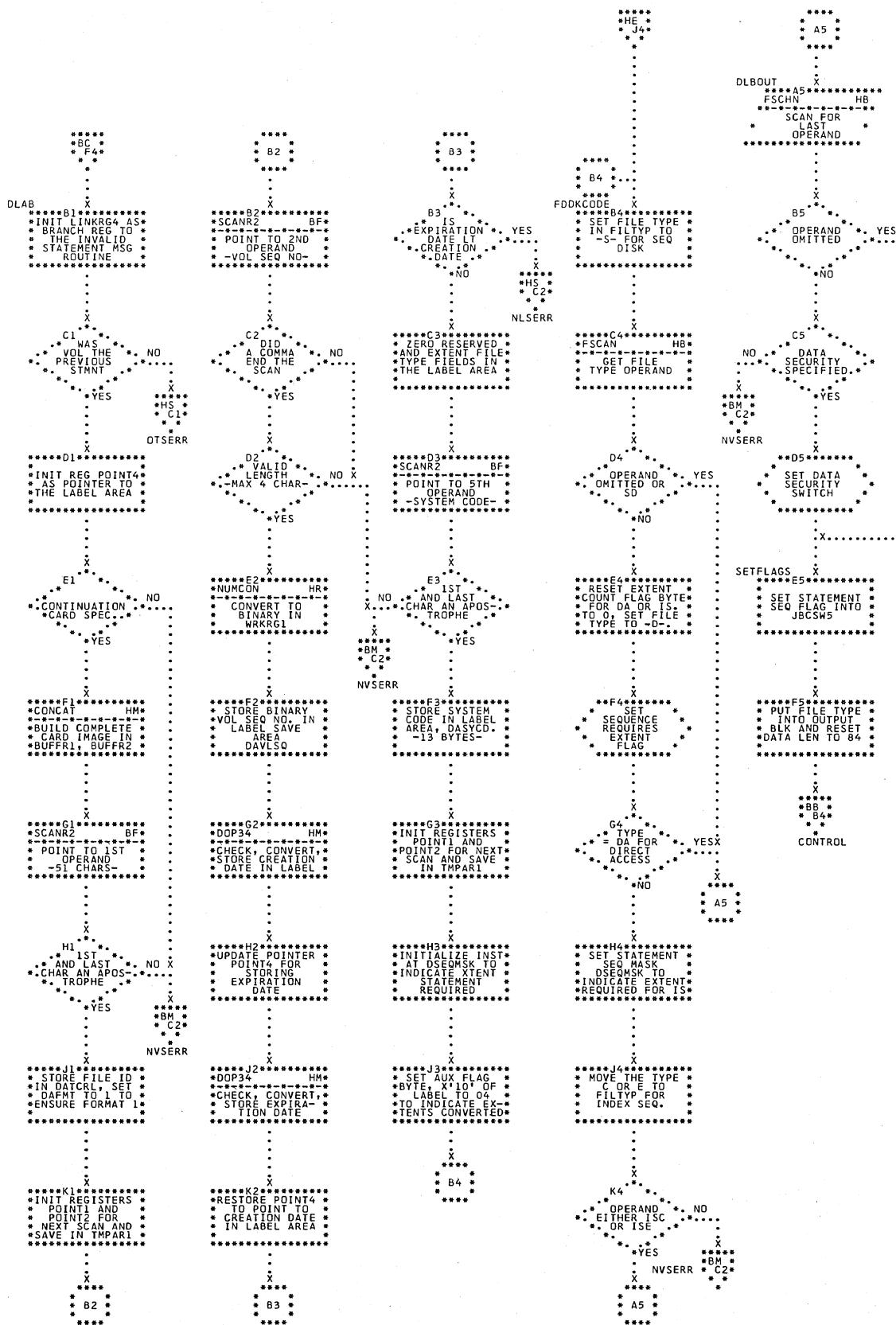


Chart HG. \$JOBCTLK - XTENT Statement Processor (Part 1 of 2)  
Refer to Chart 13.

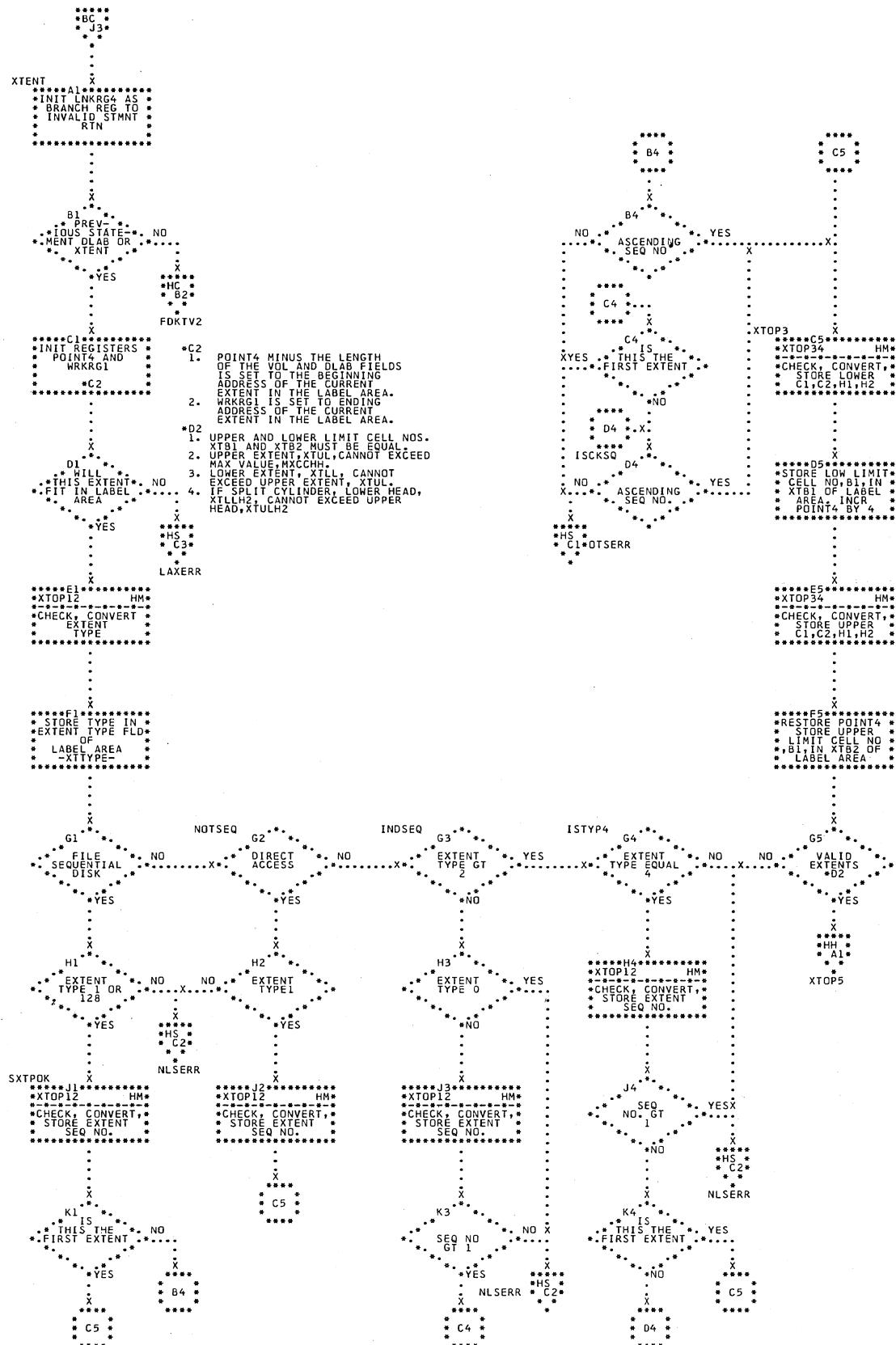


Chart HH. \$JOBCTLK - XTENT Statement Processor (Part 2 of 2)  
Refer to Chart 13.

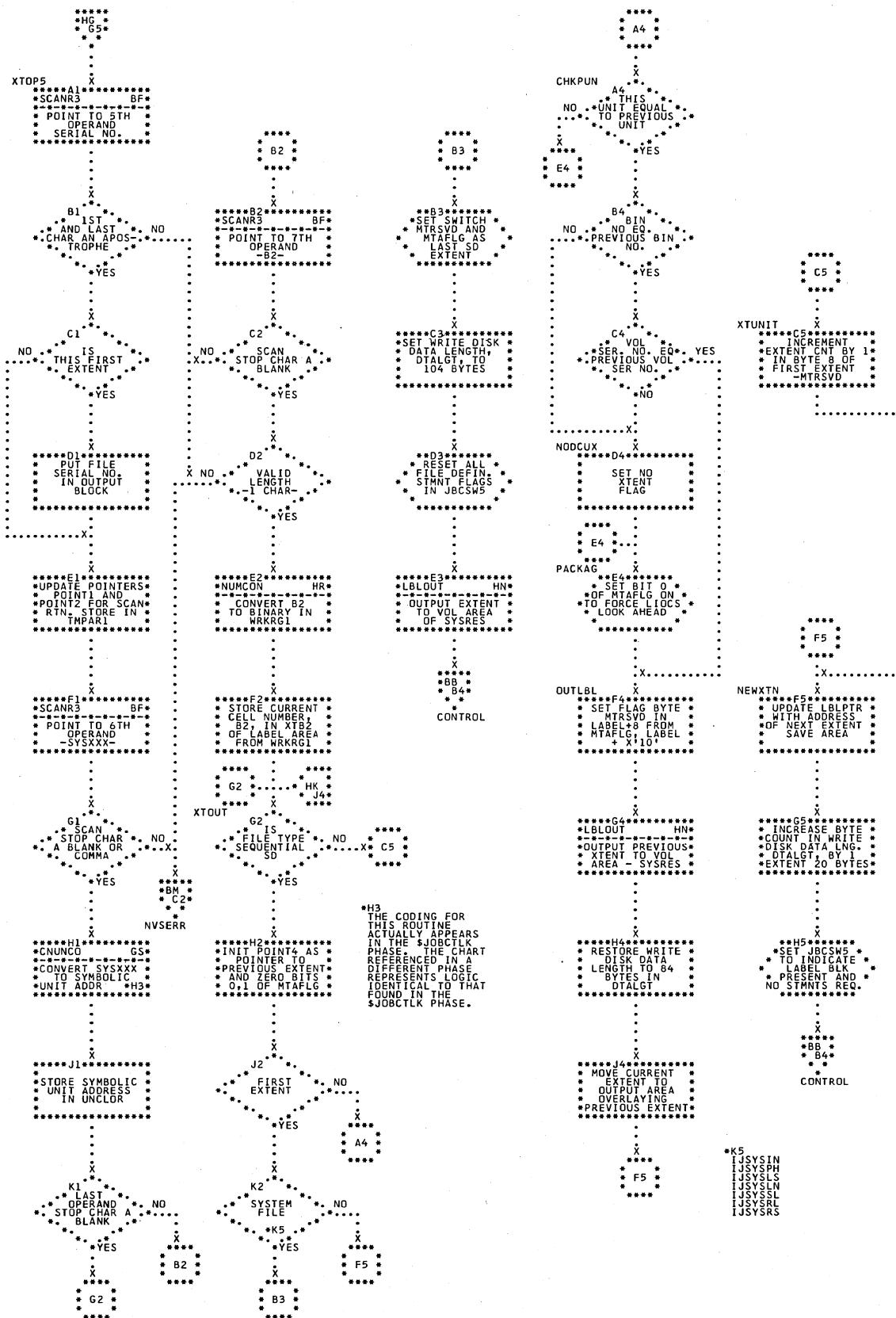


Chart HJ. \$JOBCTLK - EXTENT Statement Processor (Part 1 of 3)  
Refer to Chart 14.

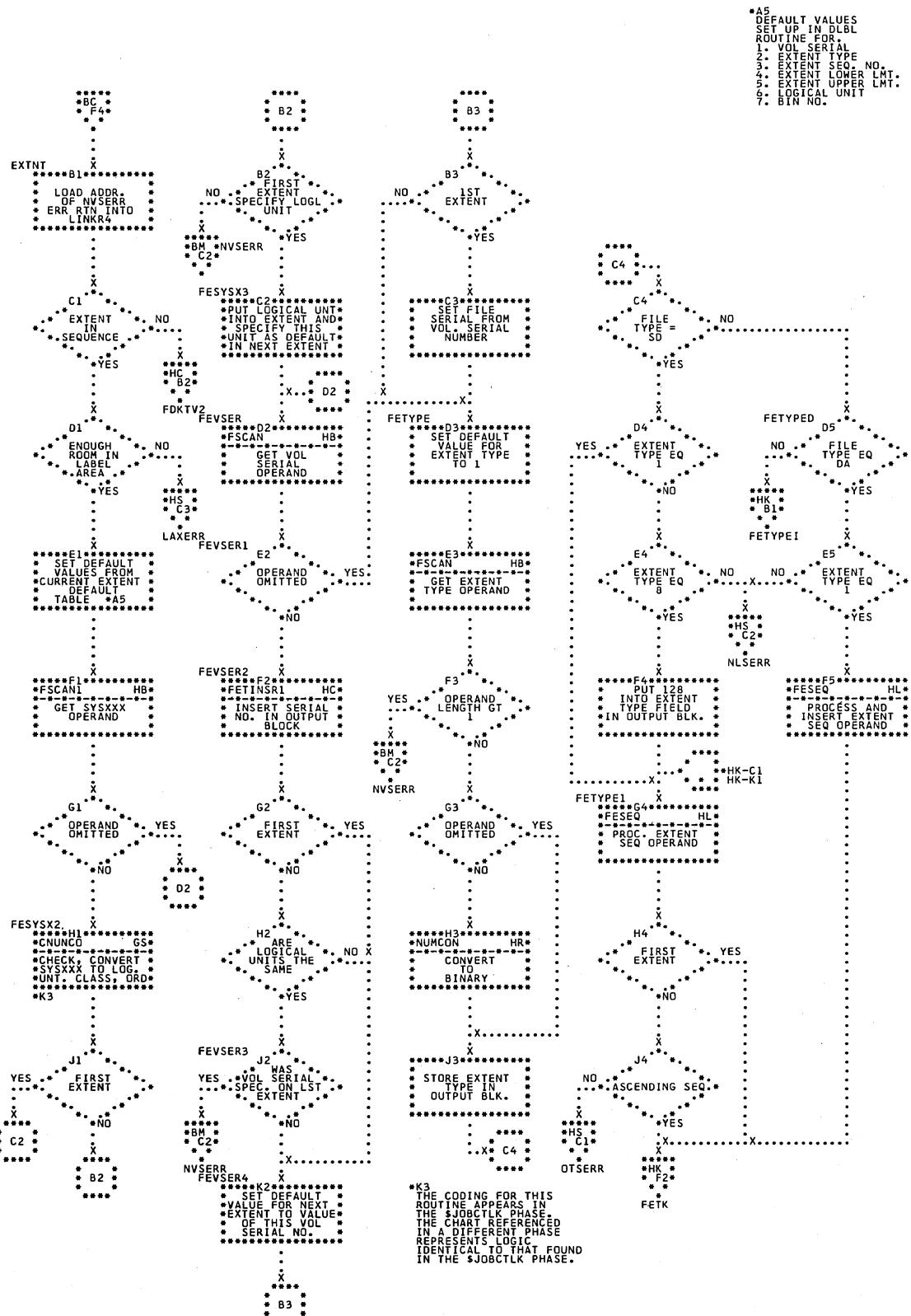


Chart HK. \$JOBCTLK - EXTENT Statement Processor (Part 2 of 3)  
Refer to Chart 14.

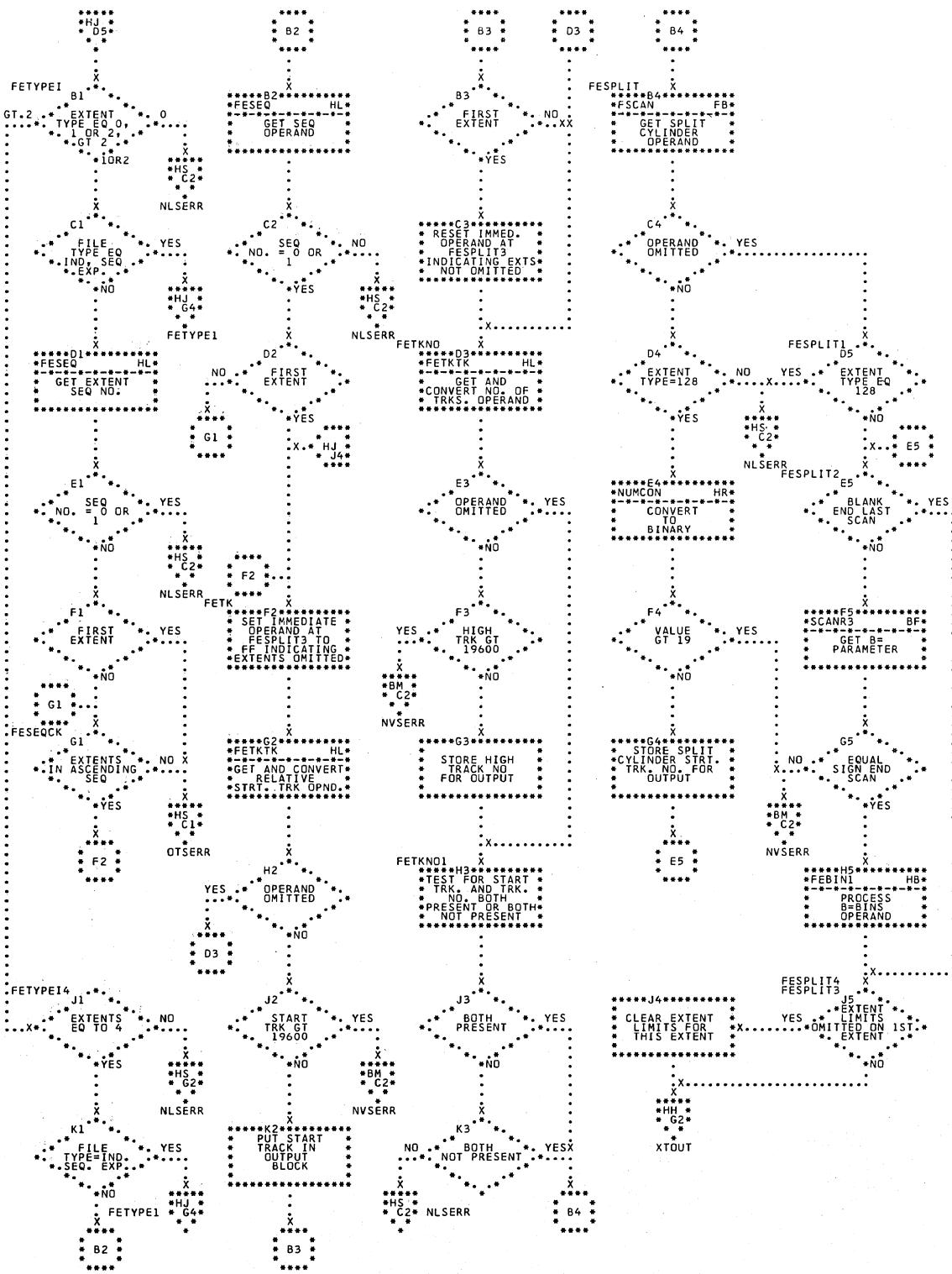


Chart HL. \$JOBCTLK - EXTENT Statement Processor (Part 3 of 3)  
Refer to Chart 14.

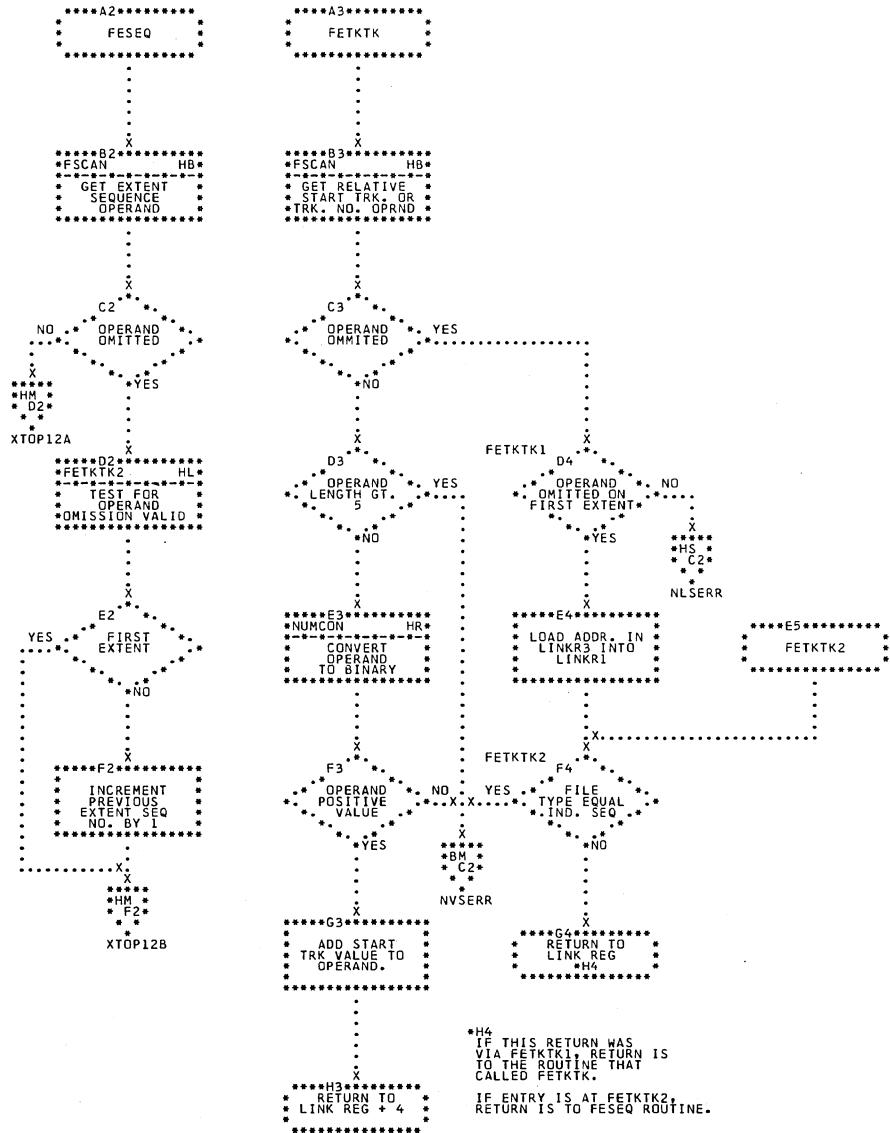


Chart HM. \$JOBCTLK - Label Processing Subroutines (Part 1 of 3)  
Refer to Charts 13 and 14.

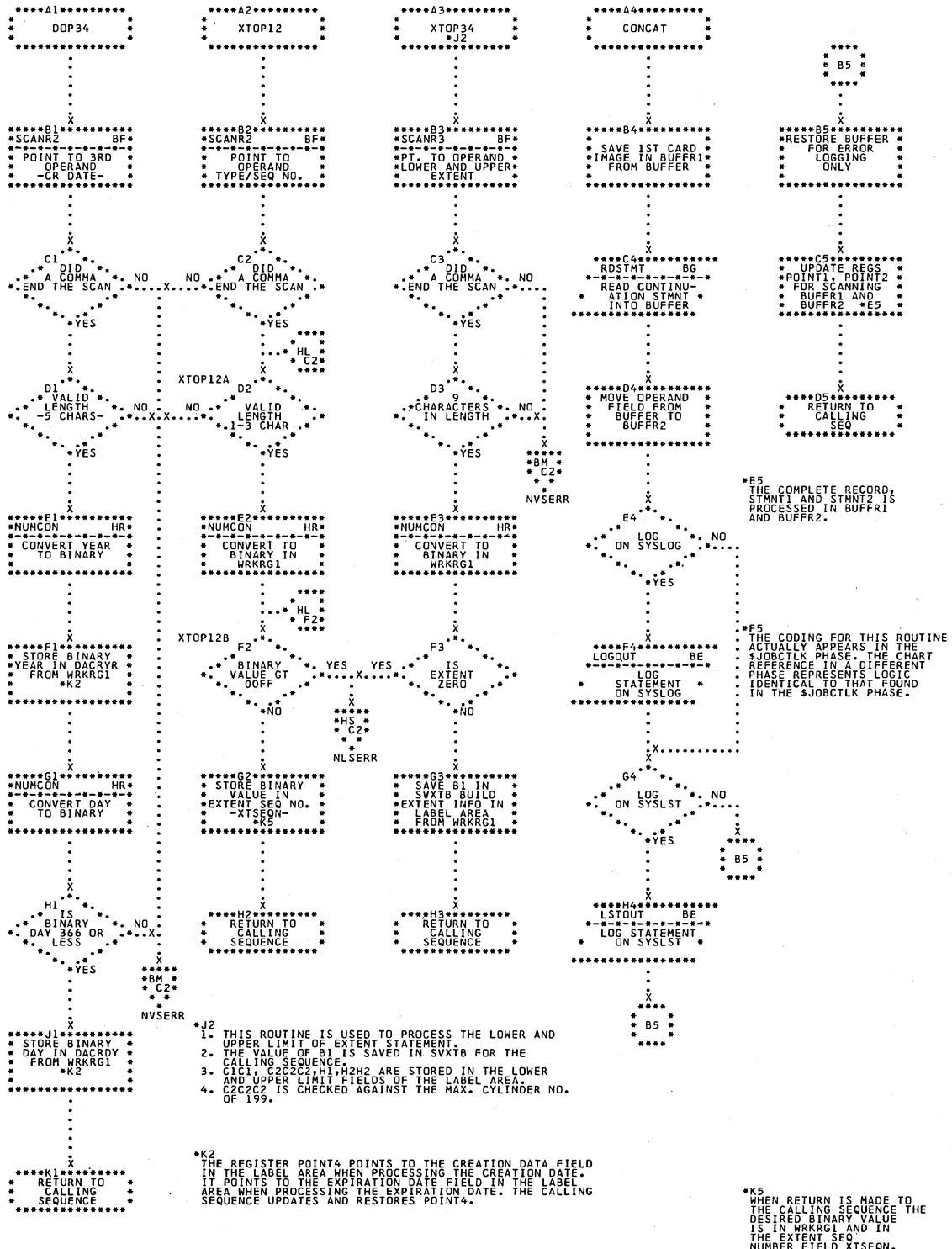


Chart HN. \$JOBCTLK - Label Processing Subroutines (Part 2 of 3)  
Refer to Charts 13 and 14.

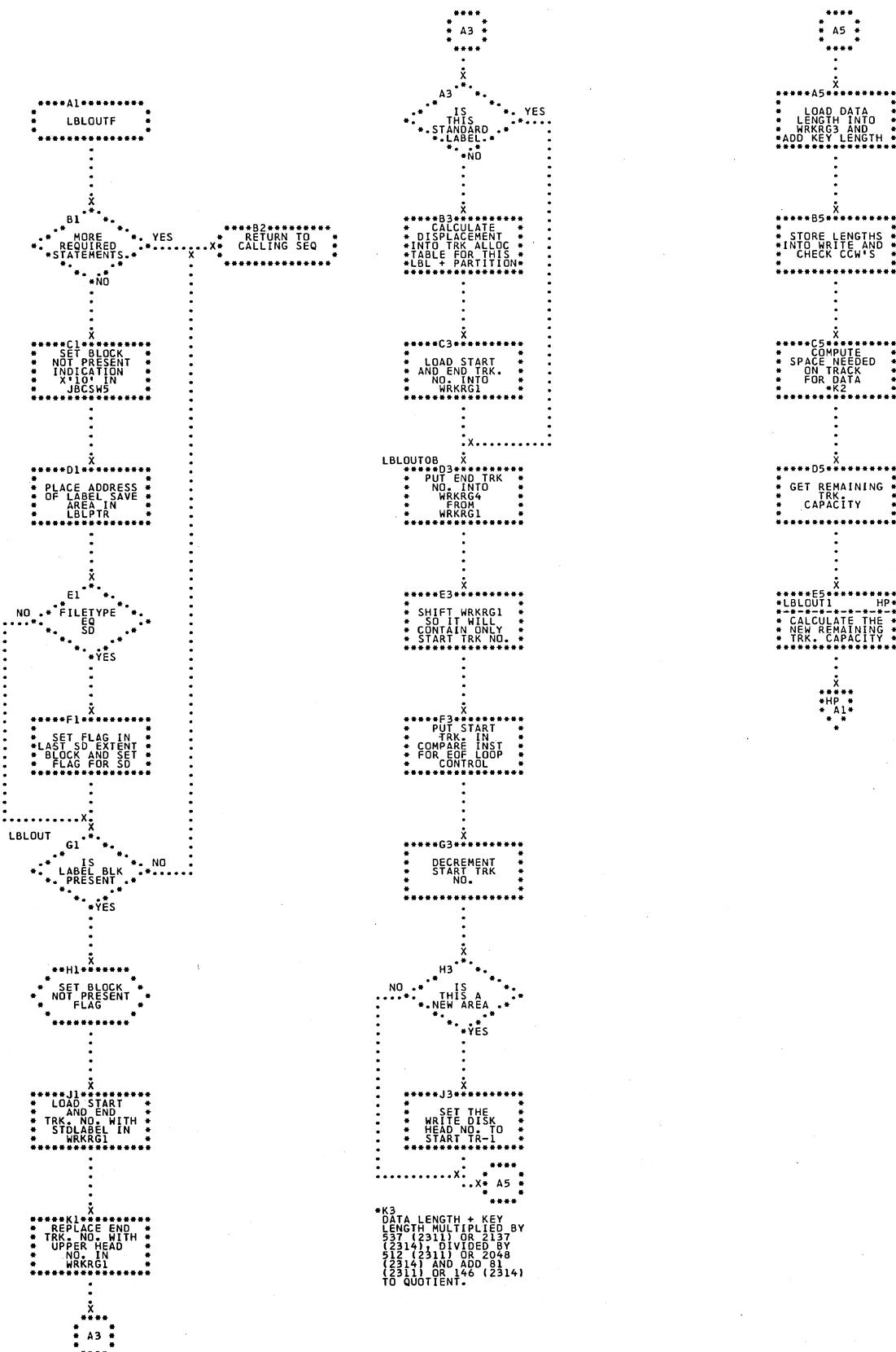


Chart HP. \$JOBCTLK - Label Processing Subroutines (Part 3 of 3)  
Refer to Charts 13 and 14.

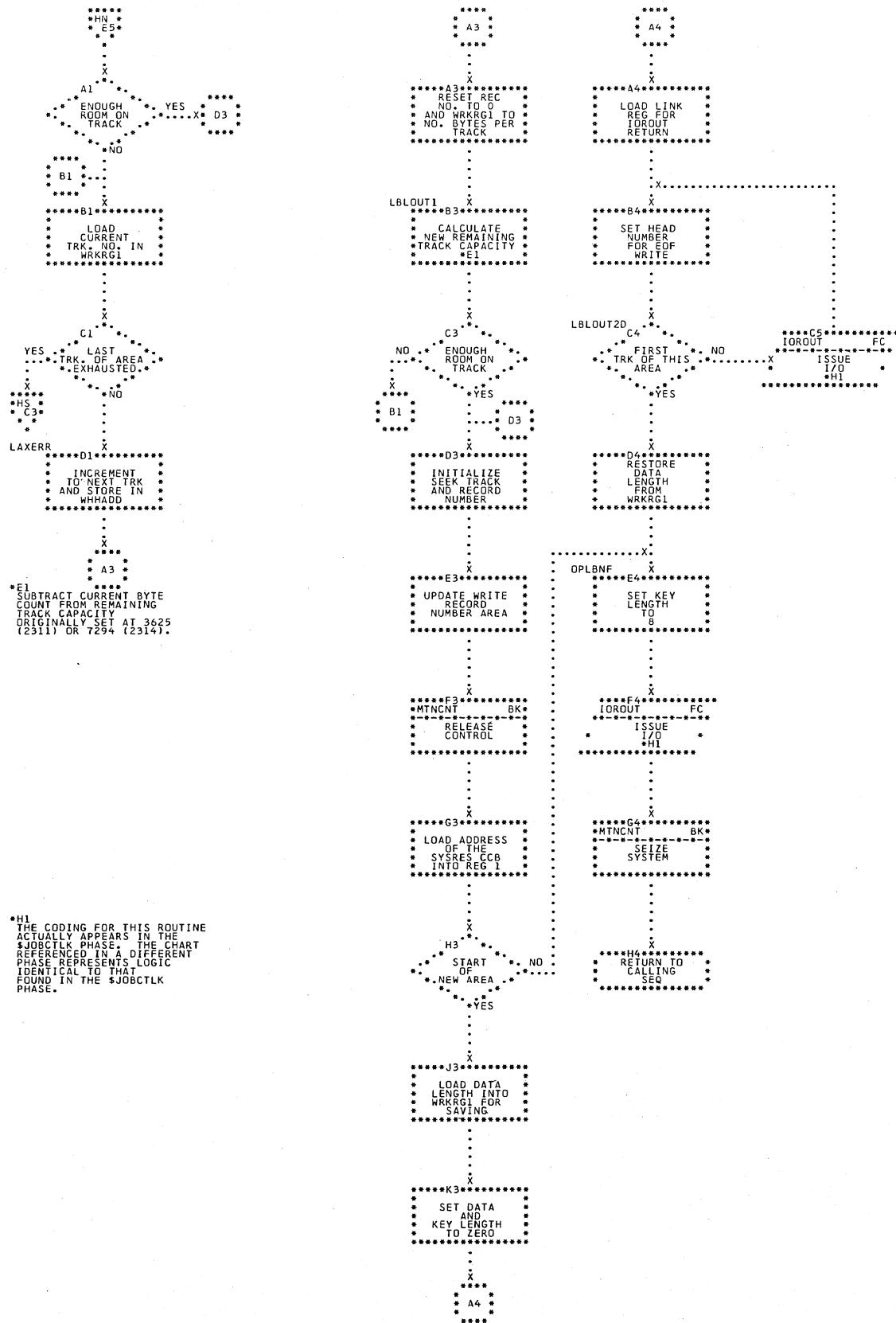
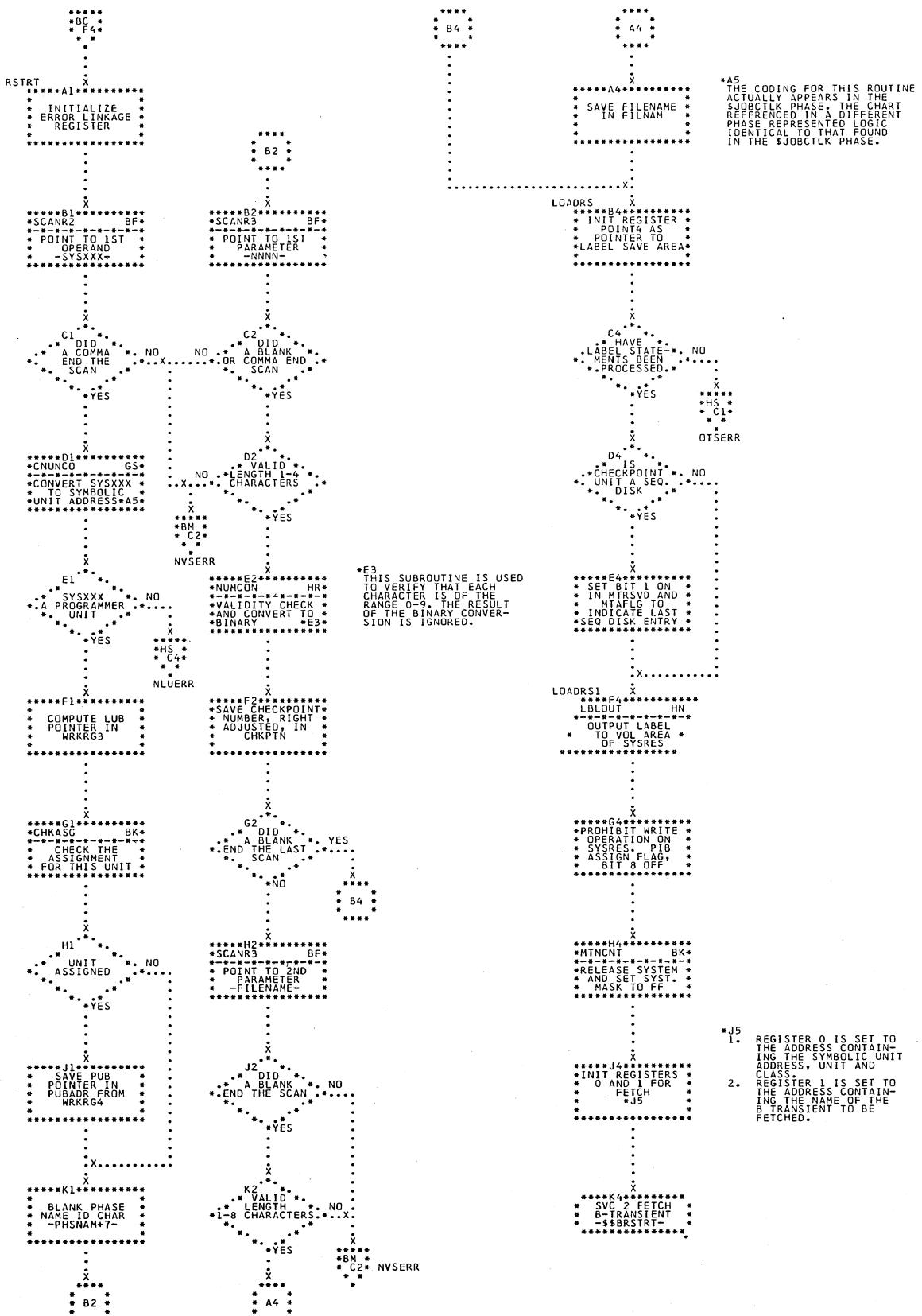


Chart HQ. \$JOBCTLK - RSTRT Statement Processor  
Refer to Chart 14.



**Chart HR. \$JOBCTLK - Miscellaneous Subroutines**  
 Refer to Charts 13 and 14.

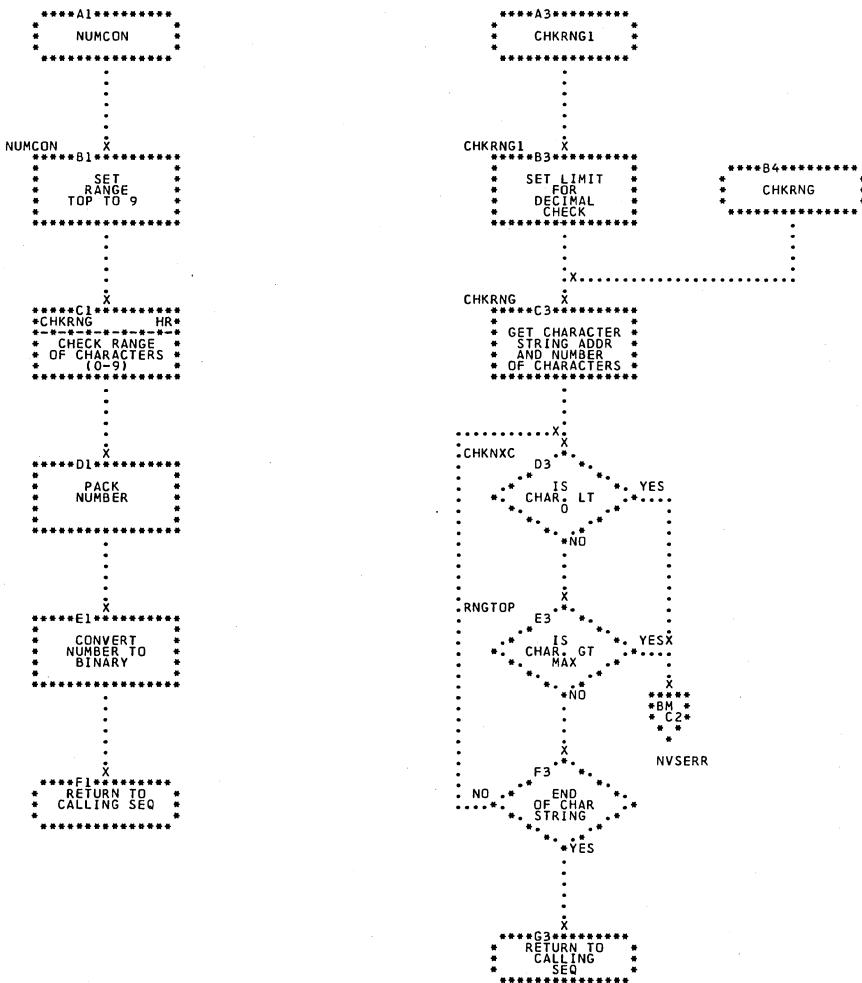
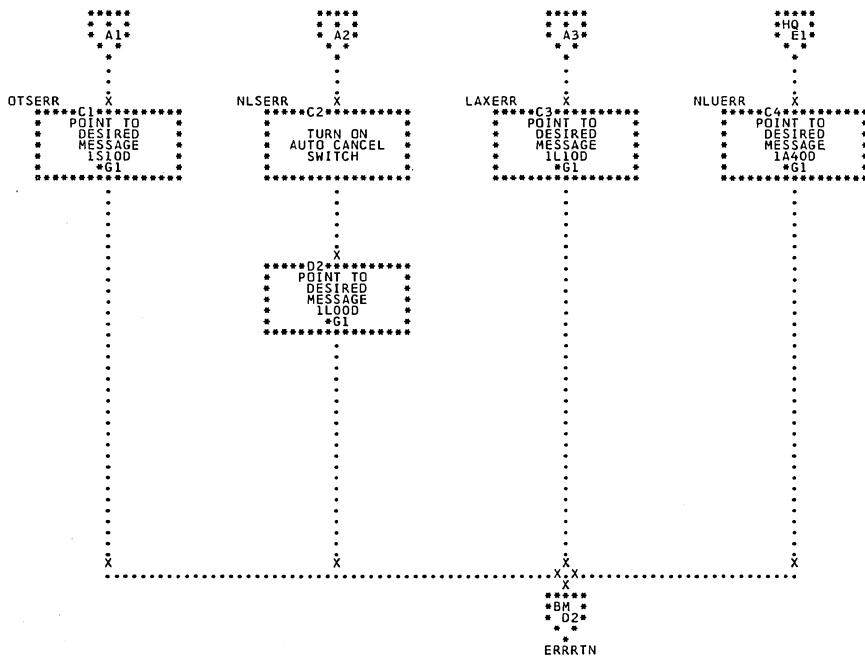


Chart HS. \$JOBCTLK - Error Subroutines  
Refer to Charts 13 and 14.

*A1	*A2
HA-E3	HG-B3
HC-D2	HG-B4,G5,H1,H2,H3,J4,K3
HF-C1	HJ-E4,E5
HG-B4,C4,D4	HK-B1,B2,D4,D5,E1,J1,K3
HH-D4	HL-D4
HK-F1,G1	HM-F2,F3
HP-C4	HP-C1



\*G1

```

: MESSAGE :DESCRIPTION
: 1A40D  :INVALID LOGICAL UNIT SPECIFICATION
: 1L00D  :INVALID LABEL SYNTAX
: 1L10D  :LABEL AREA EXHAUSTED
: 1S10D  :STATEMENT OUT OF SEQUENCE

```

**Chart JA. \$JOBCTL - Recorder File Initialization**  
Refer to Chart 15.

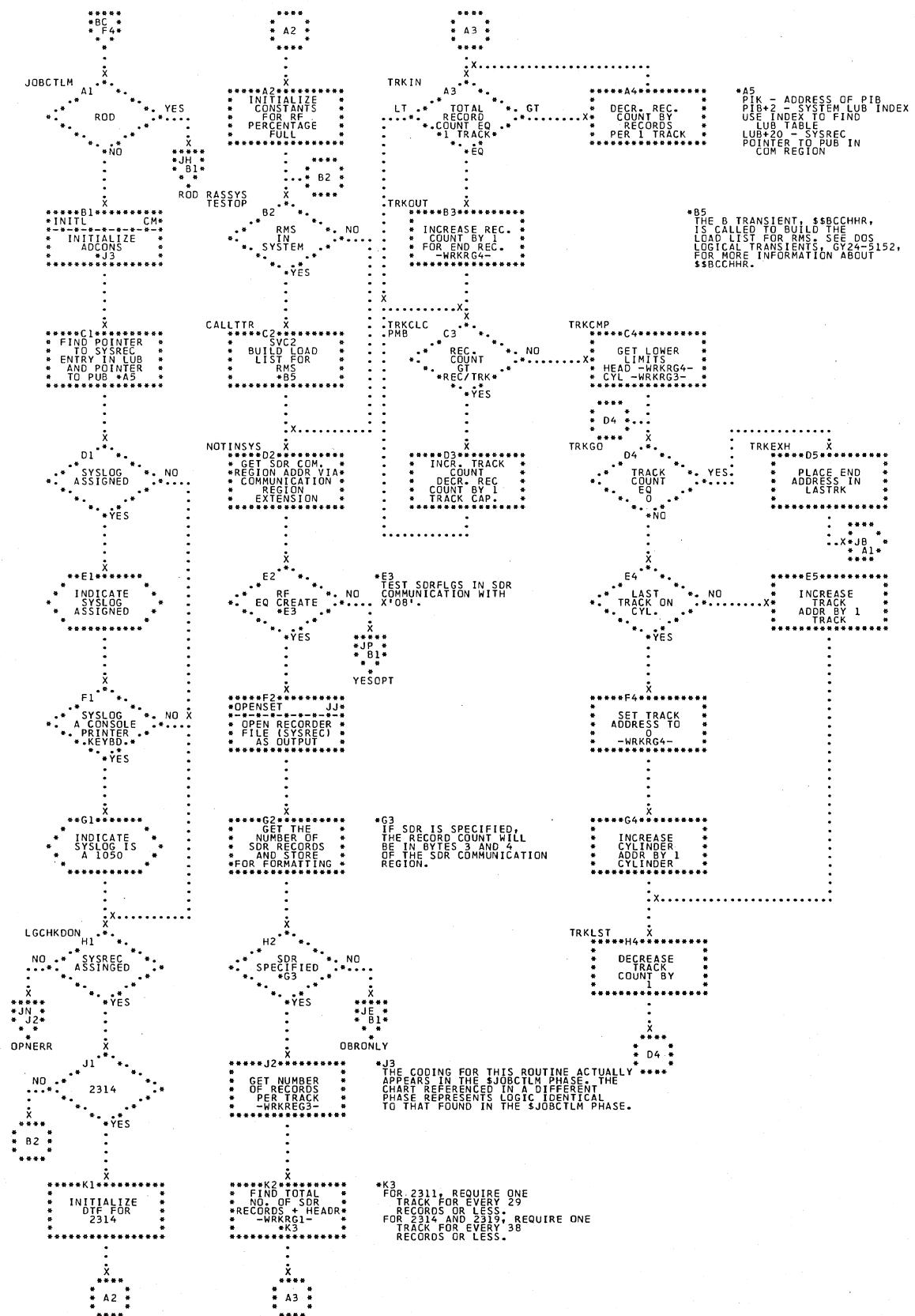


Chart JB. \$JOBCTL - Create Recorder File (Part 1 of 2)  
Refer to Chart 15.

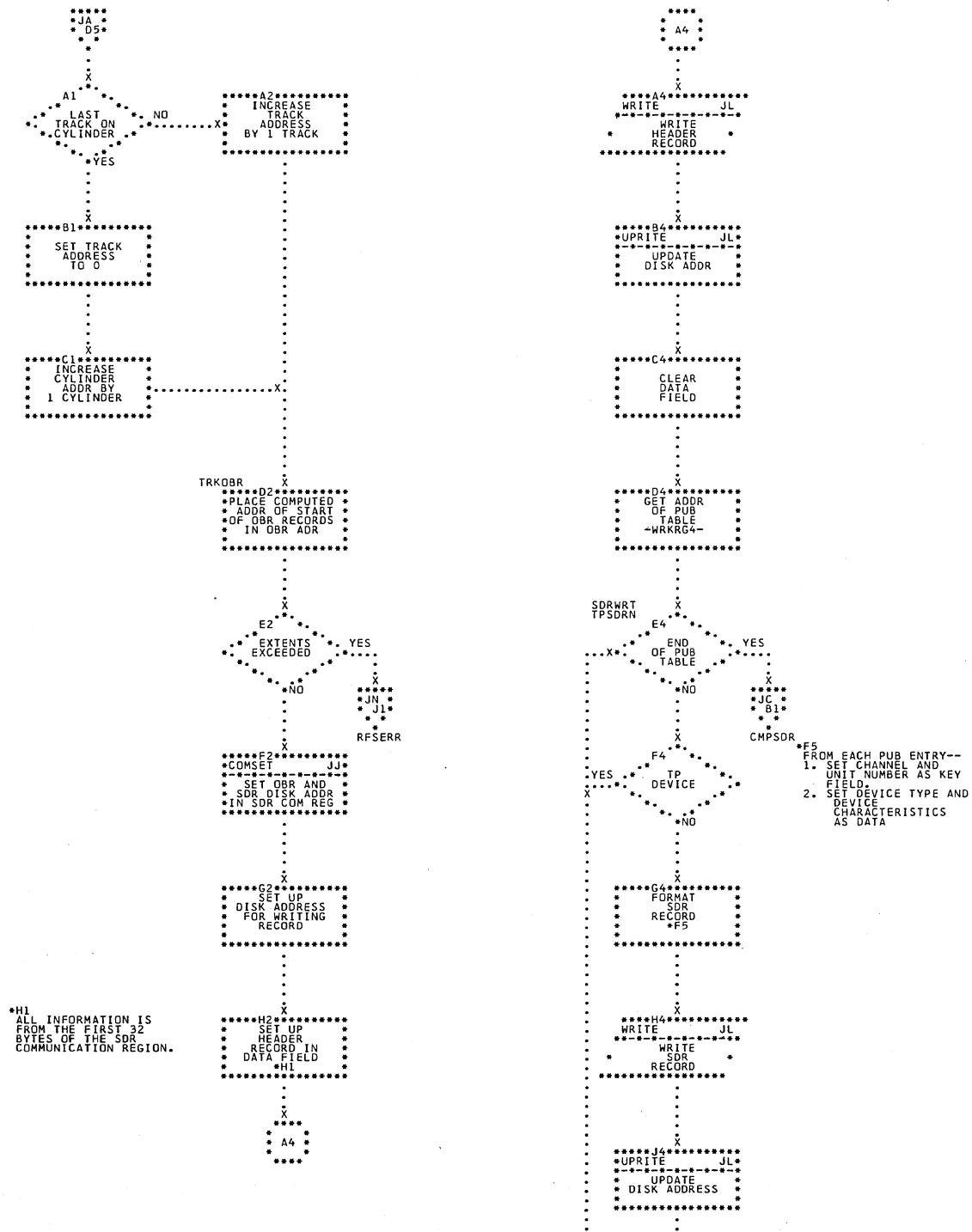


Chart JC. \$JOBCTLM - Create Recorder File (Part 2 of 2)  
 Refer to Chart 15.

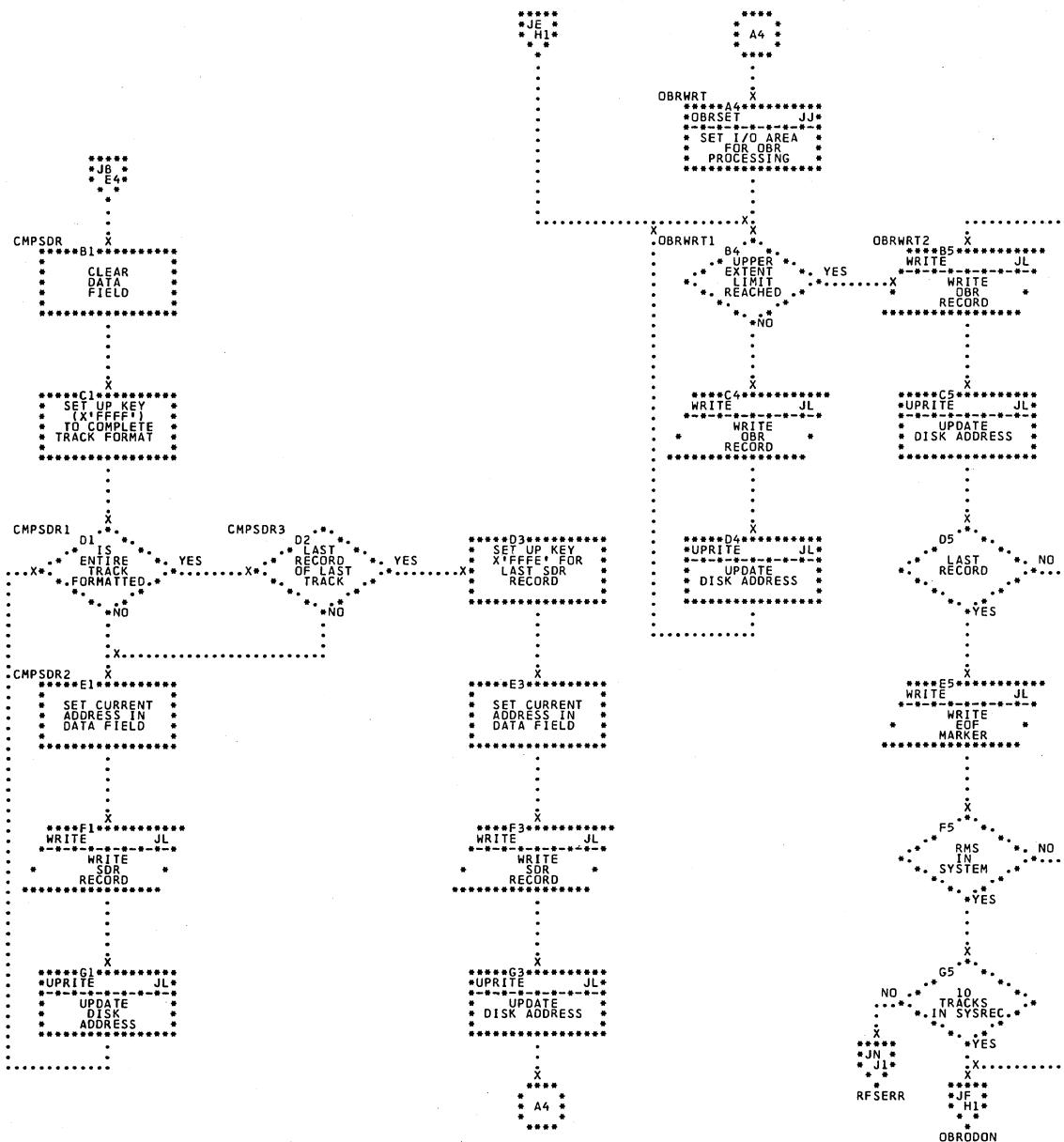


Chart JD. \$JOBCTL - Check Recorder File (Part 1 of 4)  
Refer to Chart 15.

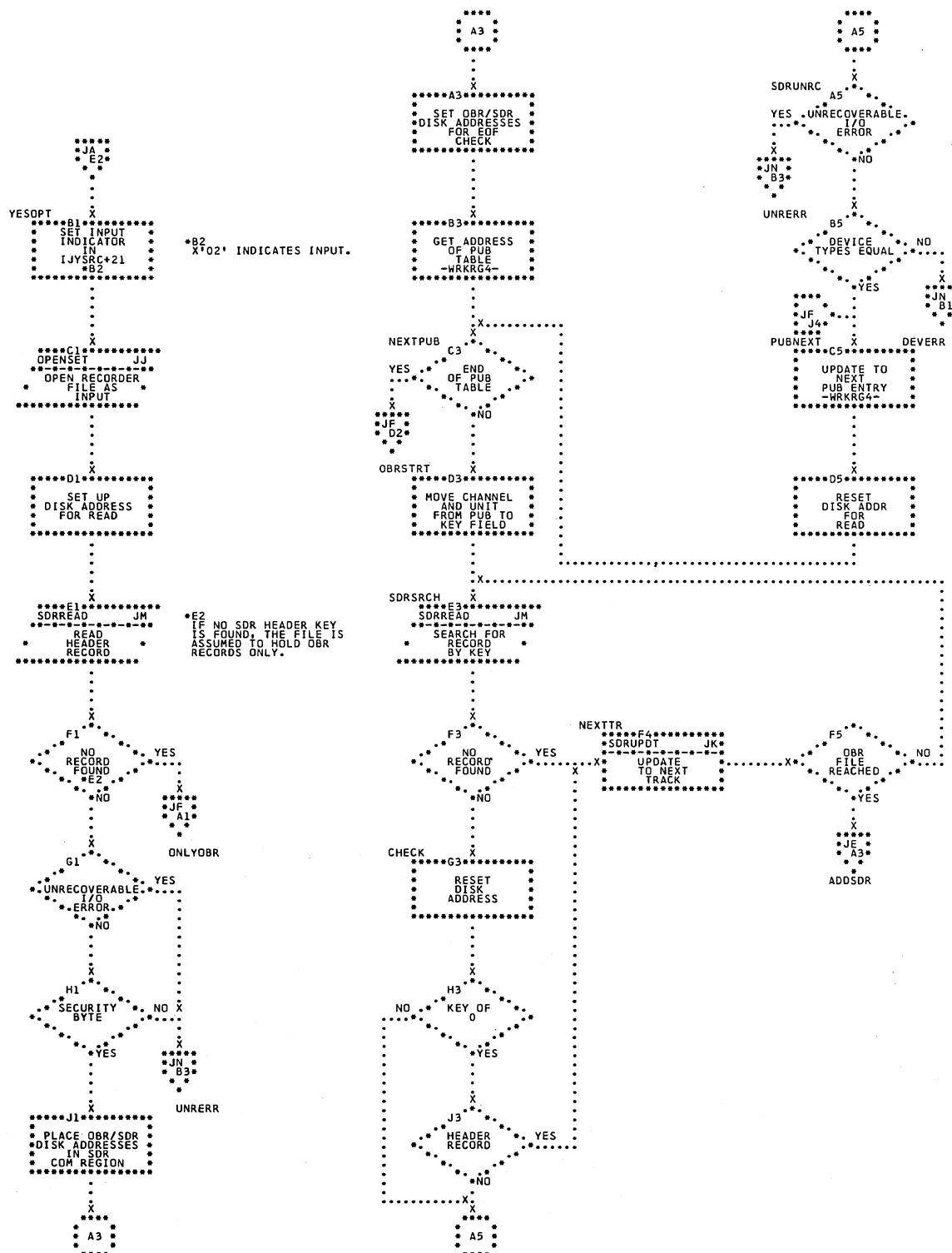
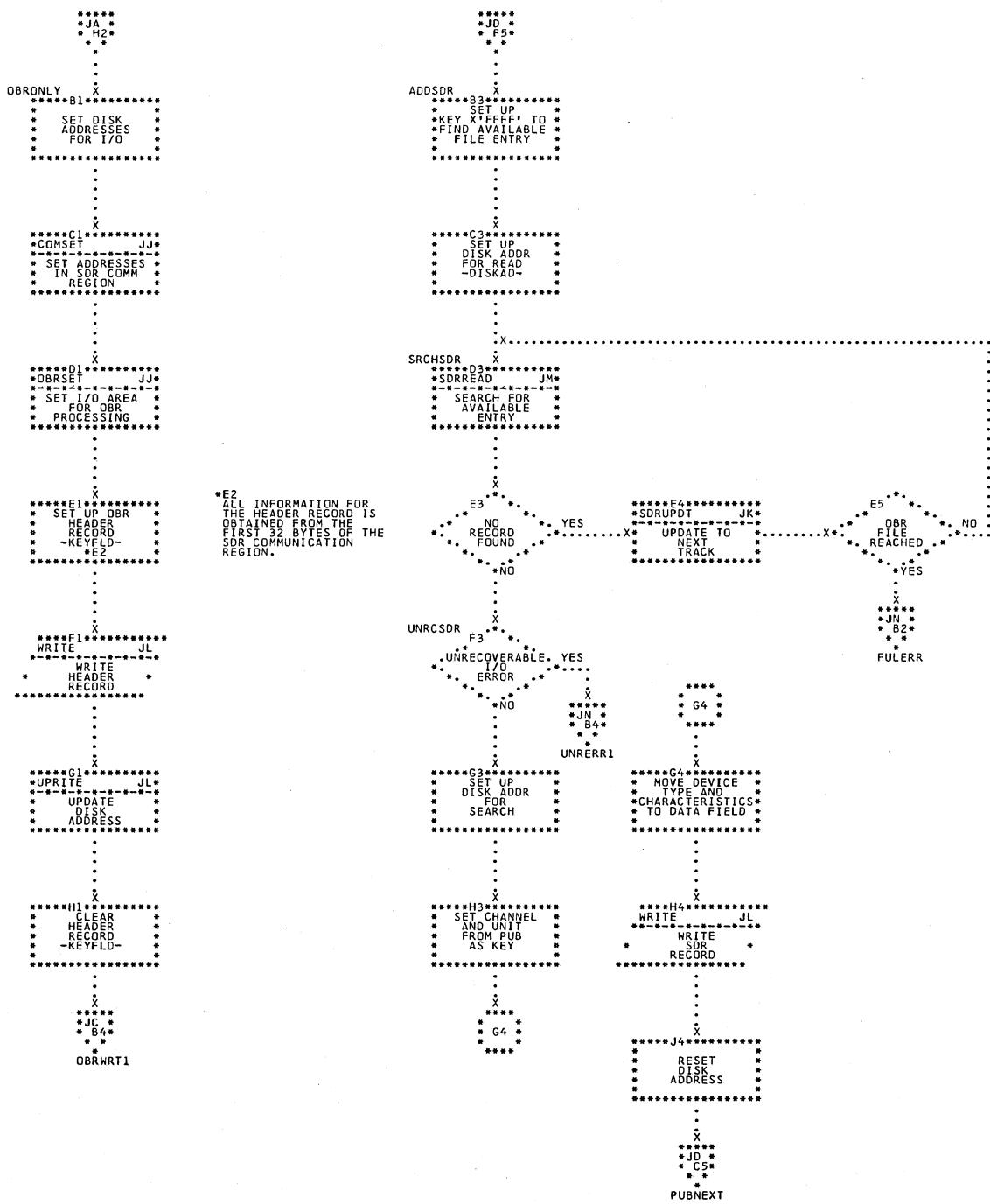


Chart JE. \$JOBCTL - Check Recorder File (Part 2 of 4)  
 Refer to Chart 15.



**Chart JF. \$JOBCTL - Check Recorder File (Part 3 of 4)**  
Refer to Chart 15.

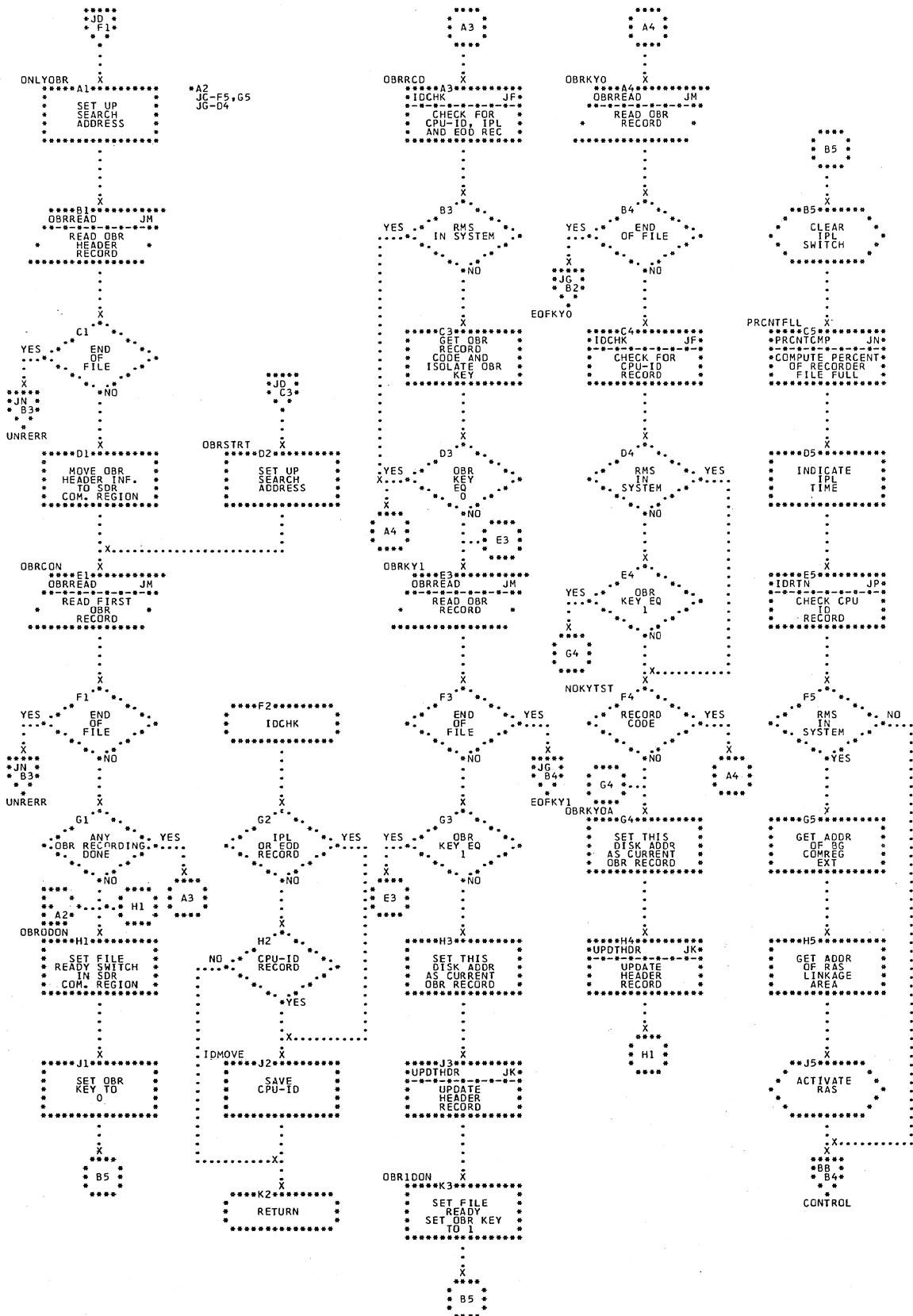


Chart JG. \$JOBCTL - Check Recorder File (Part 4 of 4)  
 Refer to Chart 15.

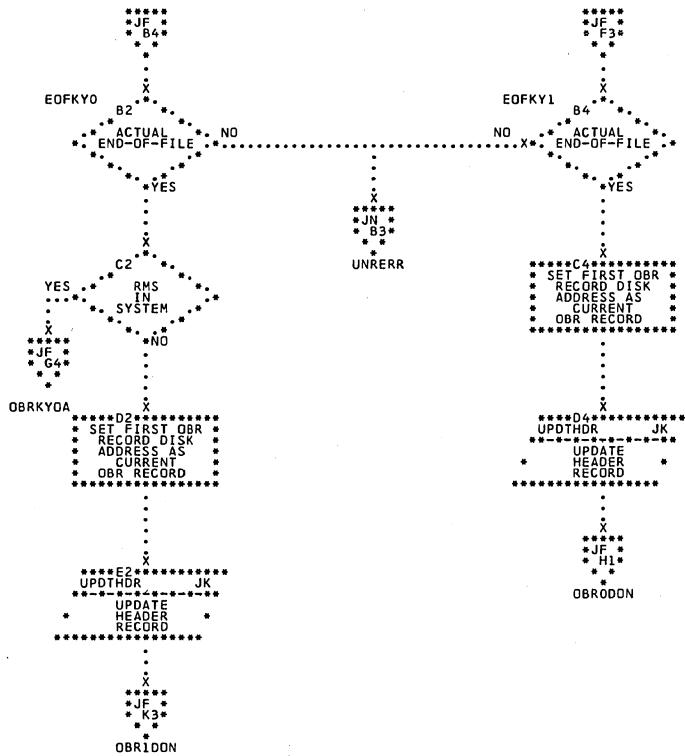


Chart JH. \$JOBCTLM - ROD Statement Processor  
Refer to Chart 15.

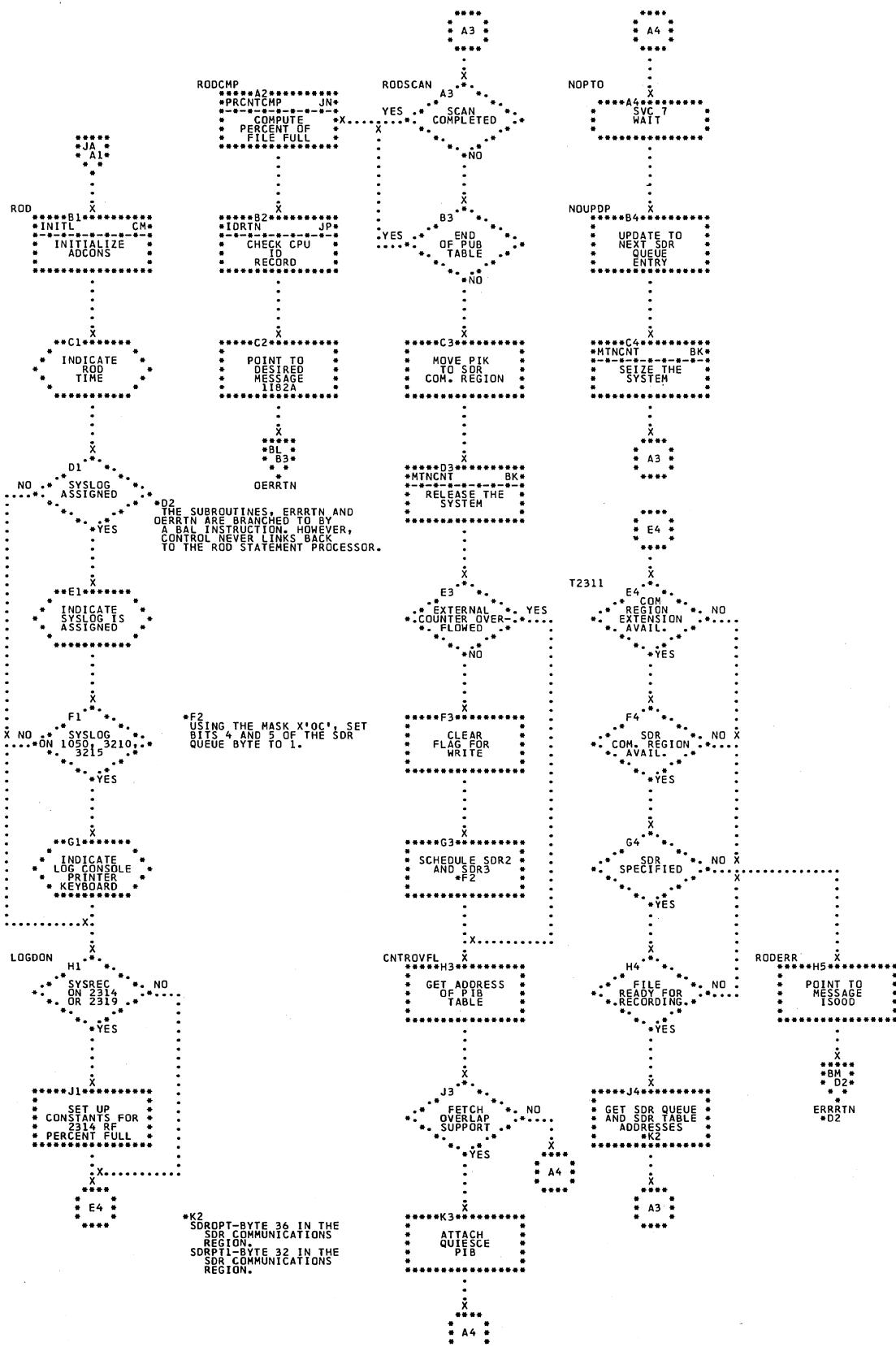
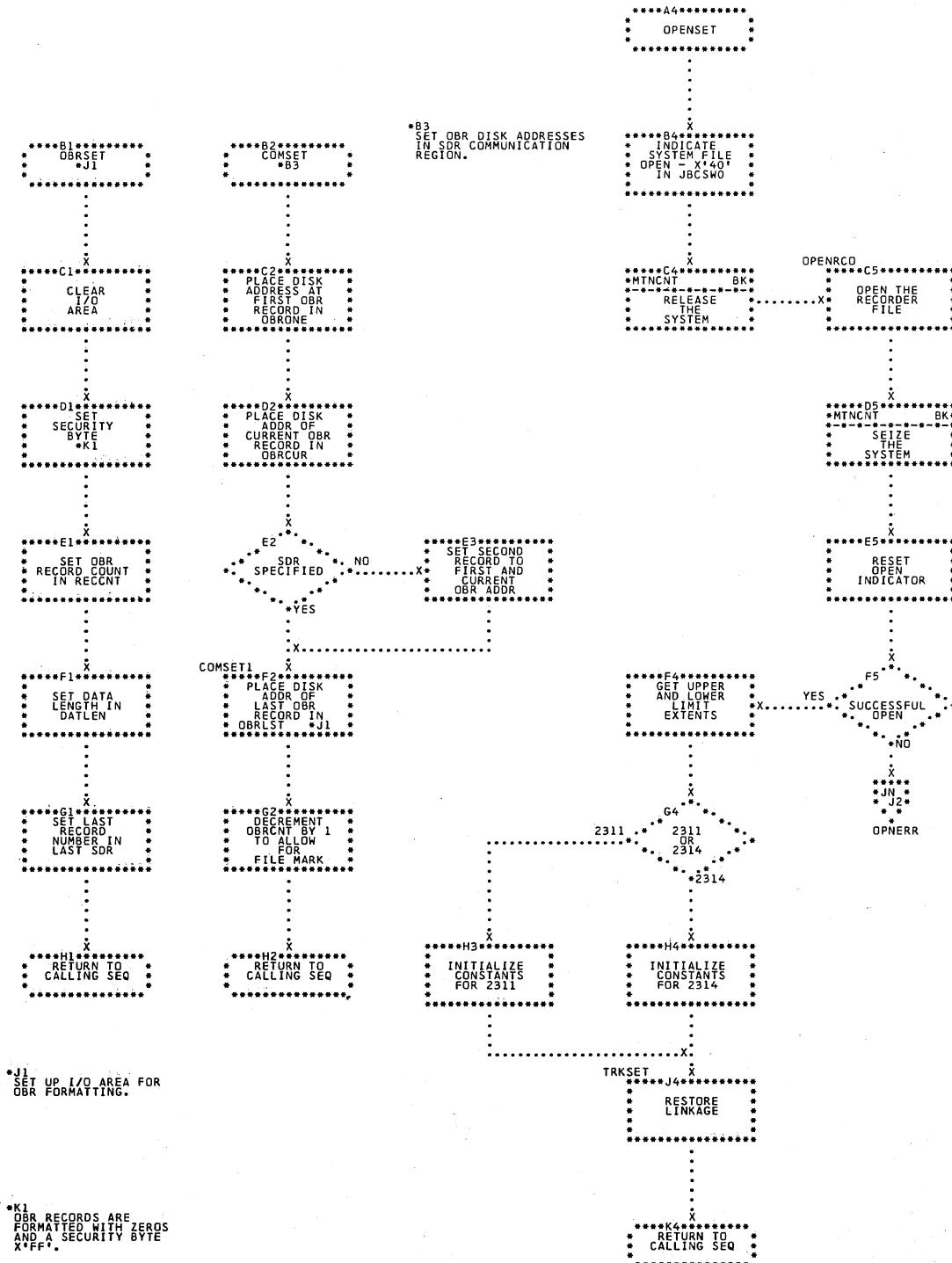


Chart JJ. \$JOBCTL - Miscellaneous Subroutines (Part 1 of 2)  
 Refer to Chart 15.



**Chart JK. \$JOBCTL - Miscellaneous Subroutines (Part 2 of 2)**  
 Refer to Chart 15.

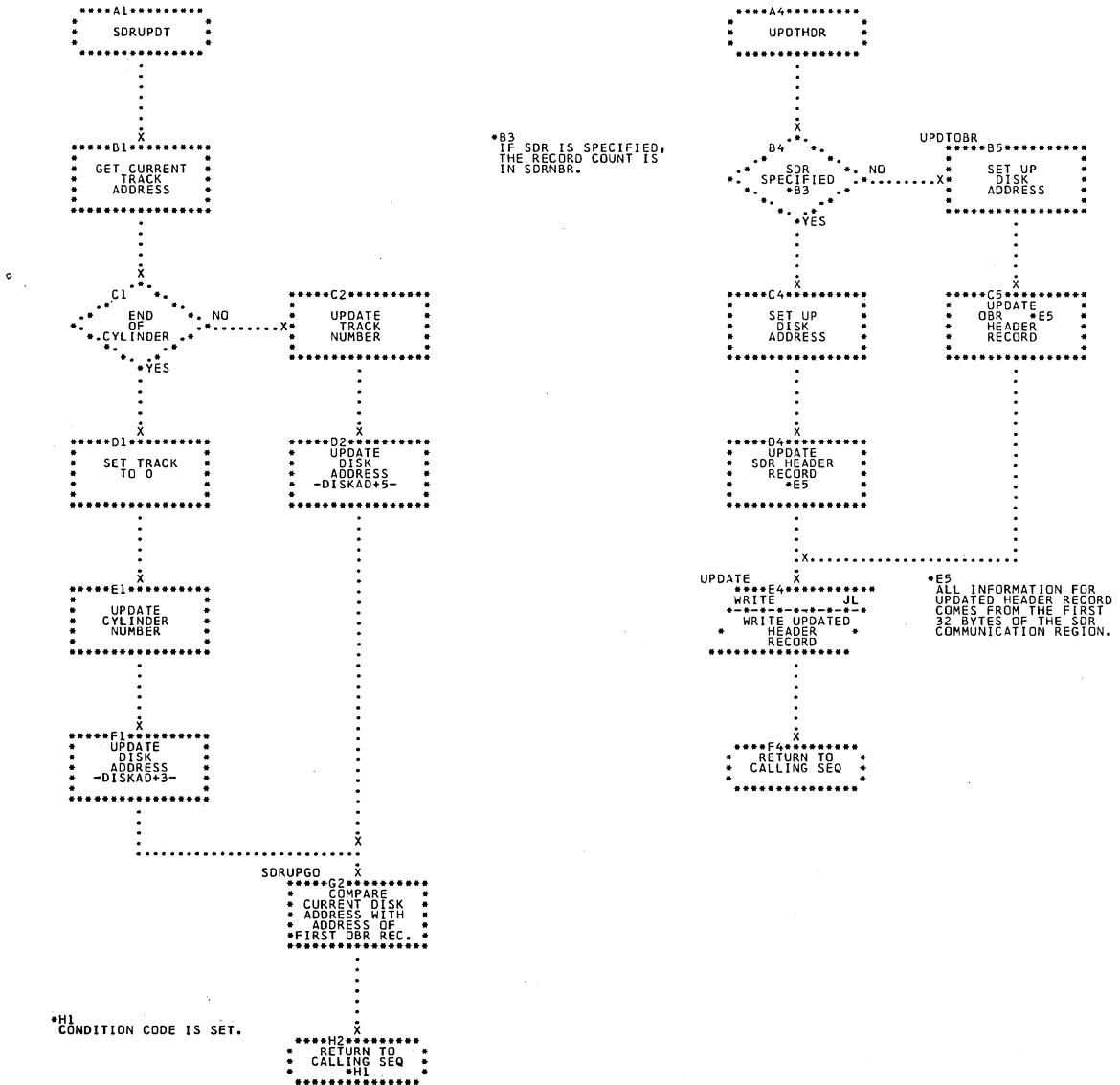


Chart JL. \$JOBCTL - I/O Subroutines (Part 1 of 2)  
 Refer to Chart 15.

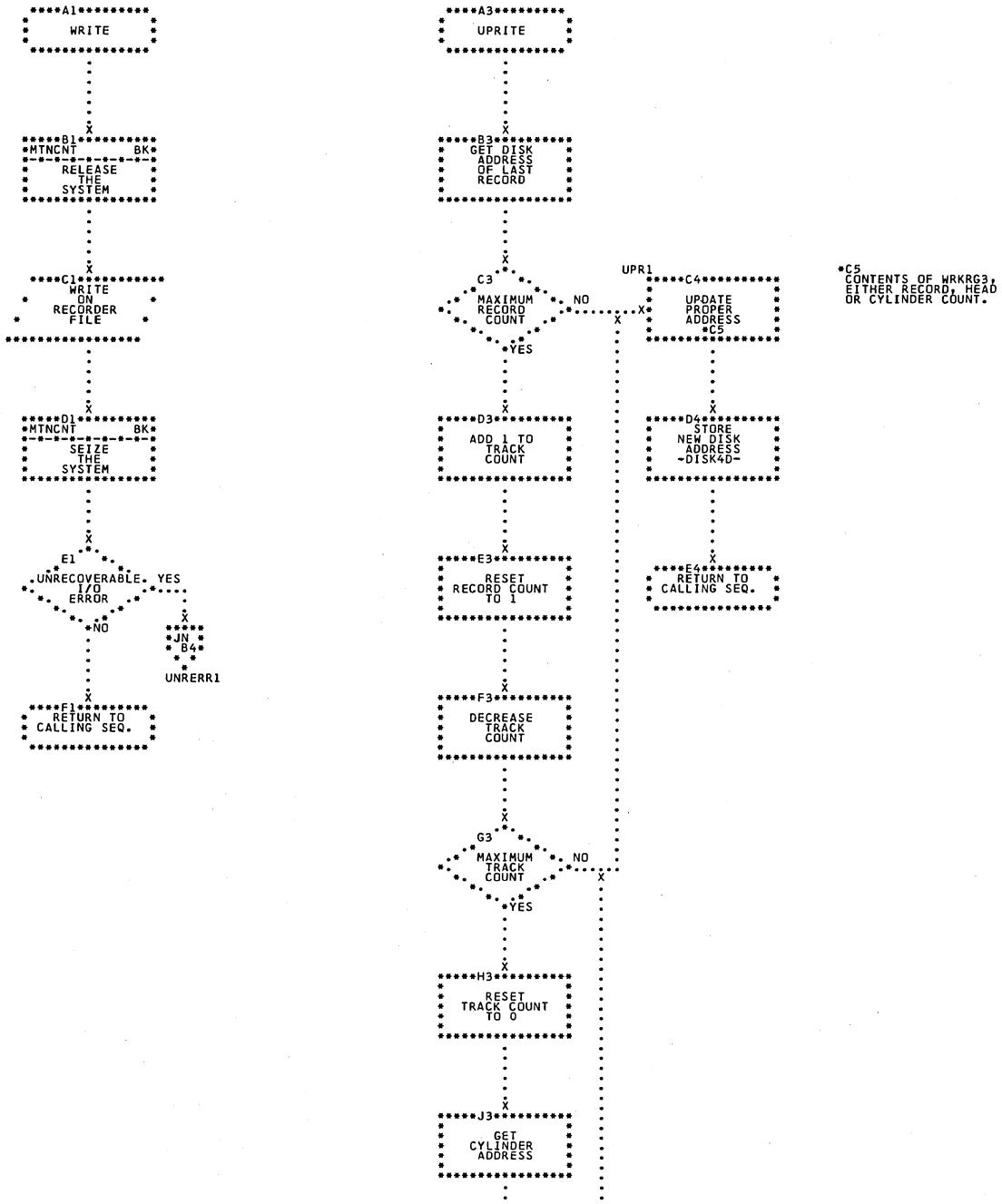


Chart JM. \$JOBCTL - I/O Subroutines (Part 2 of 2)  
 Refer to Chart 15.

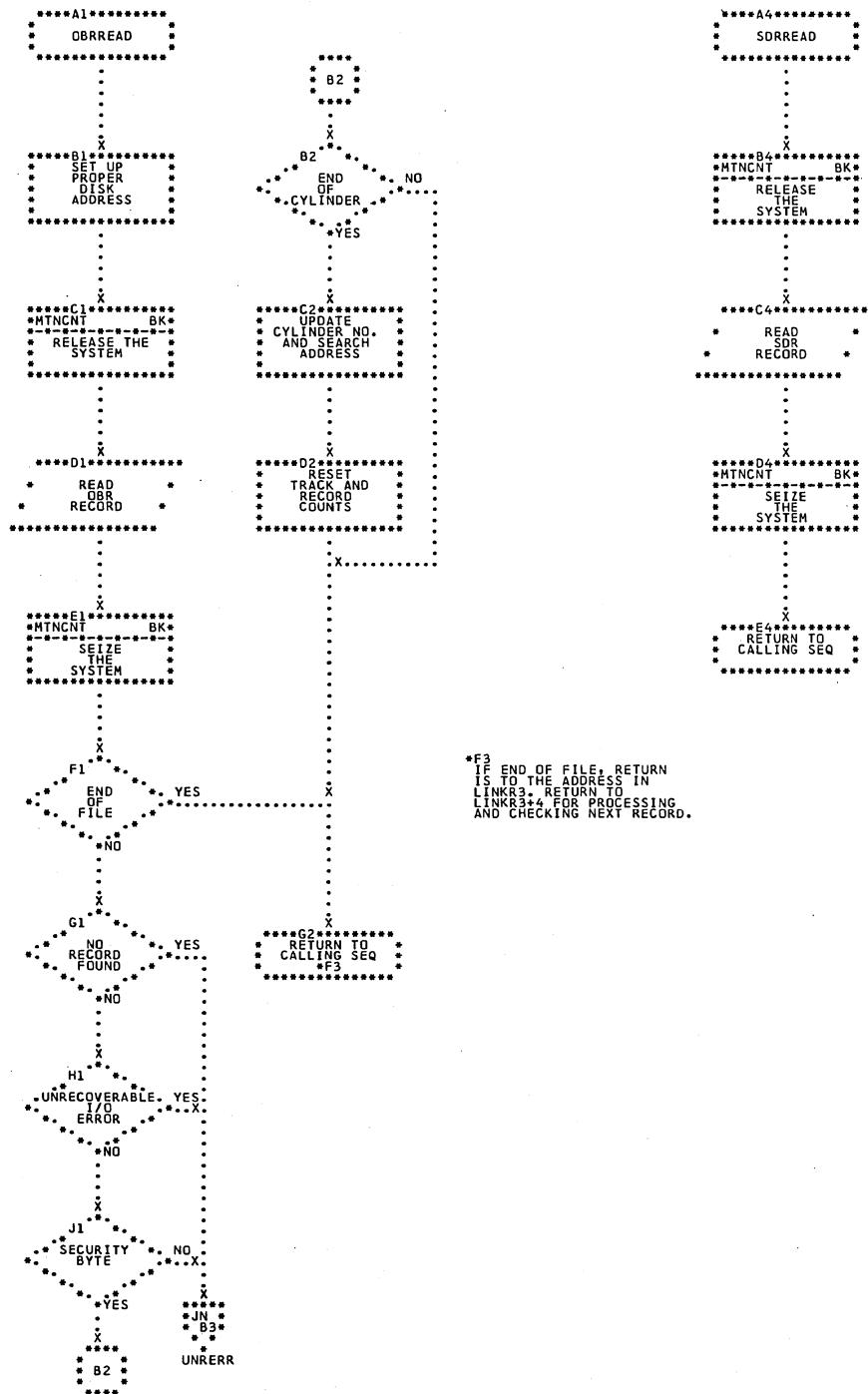


Chart JN. \$JOBCTL - RMS and Error Subroutines  
 Refer to Chart 15.

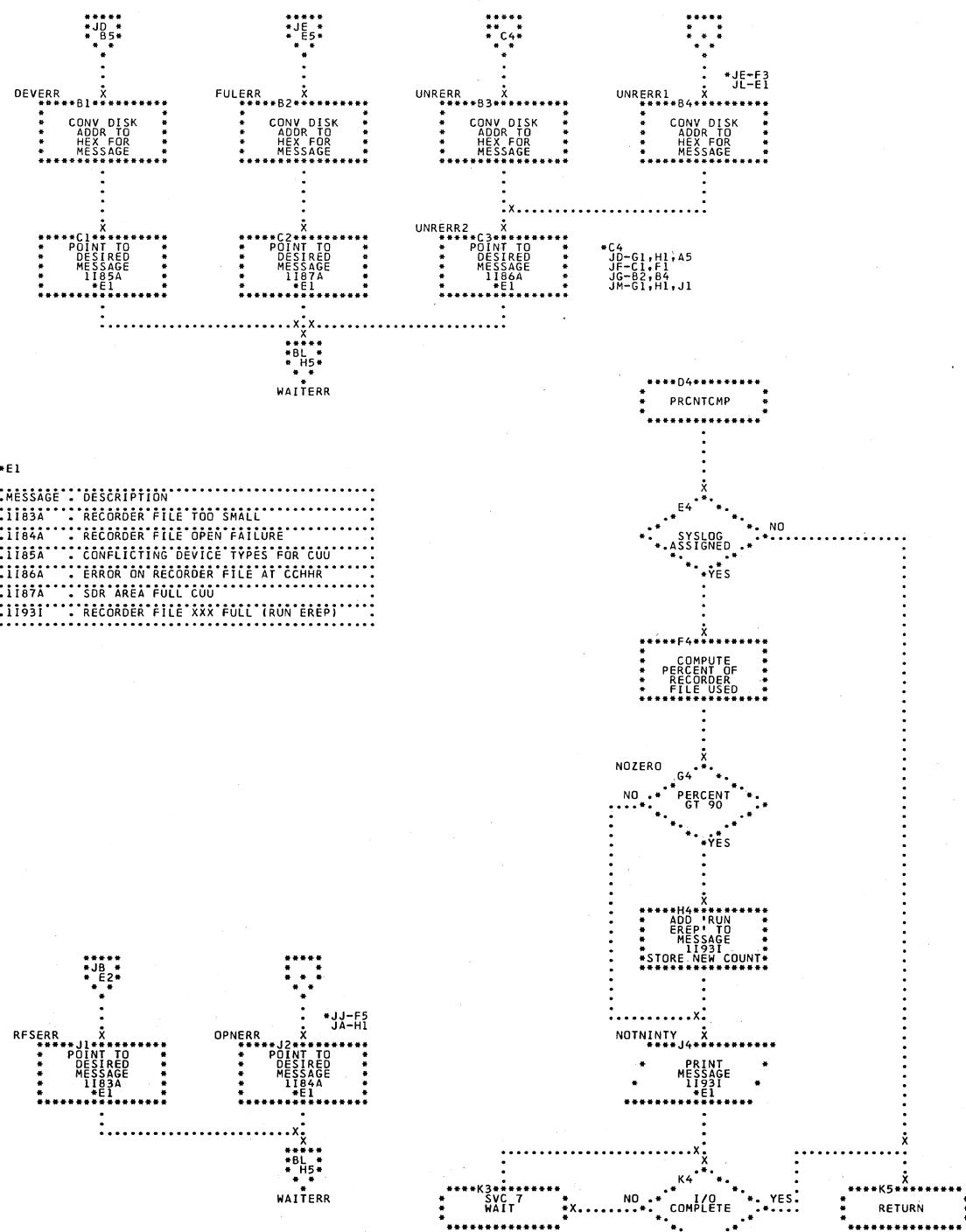
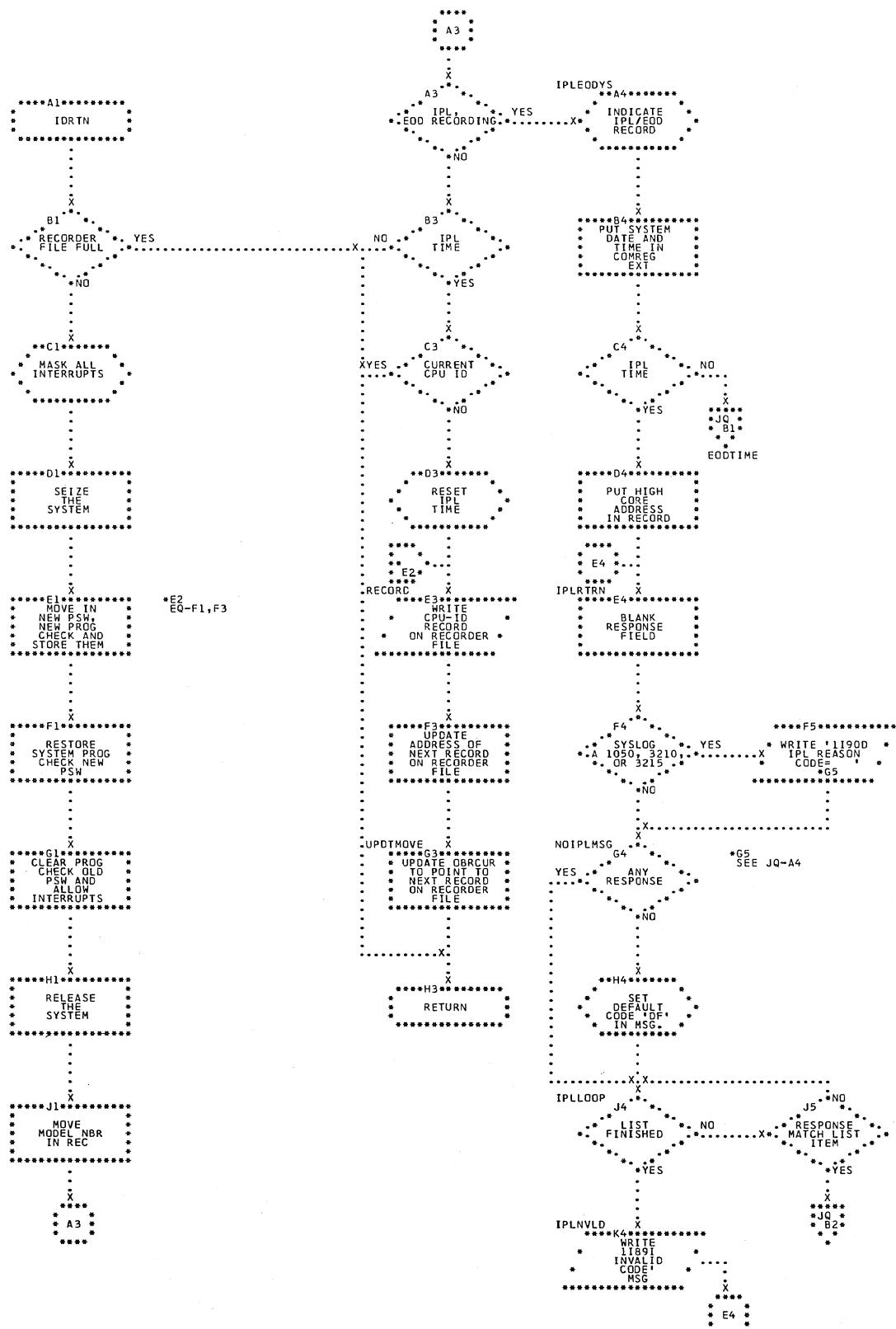


Chart JP. \$JOBCTL - Miscellaneous Subroutines (Part 1 of 2)  
Refer to Chart 15.



**Chart JQ. \$JOBCTL - Miscellaneous Subroutines (Part 2 of 2)**  
 Refer to Chart 15.

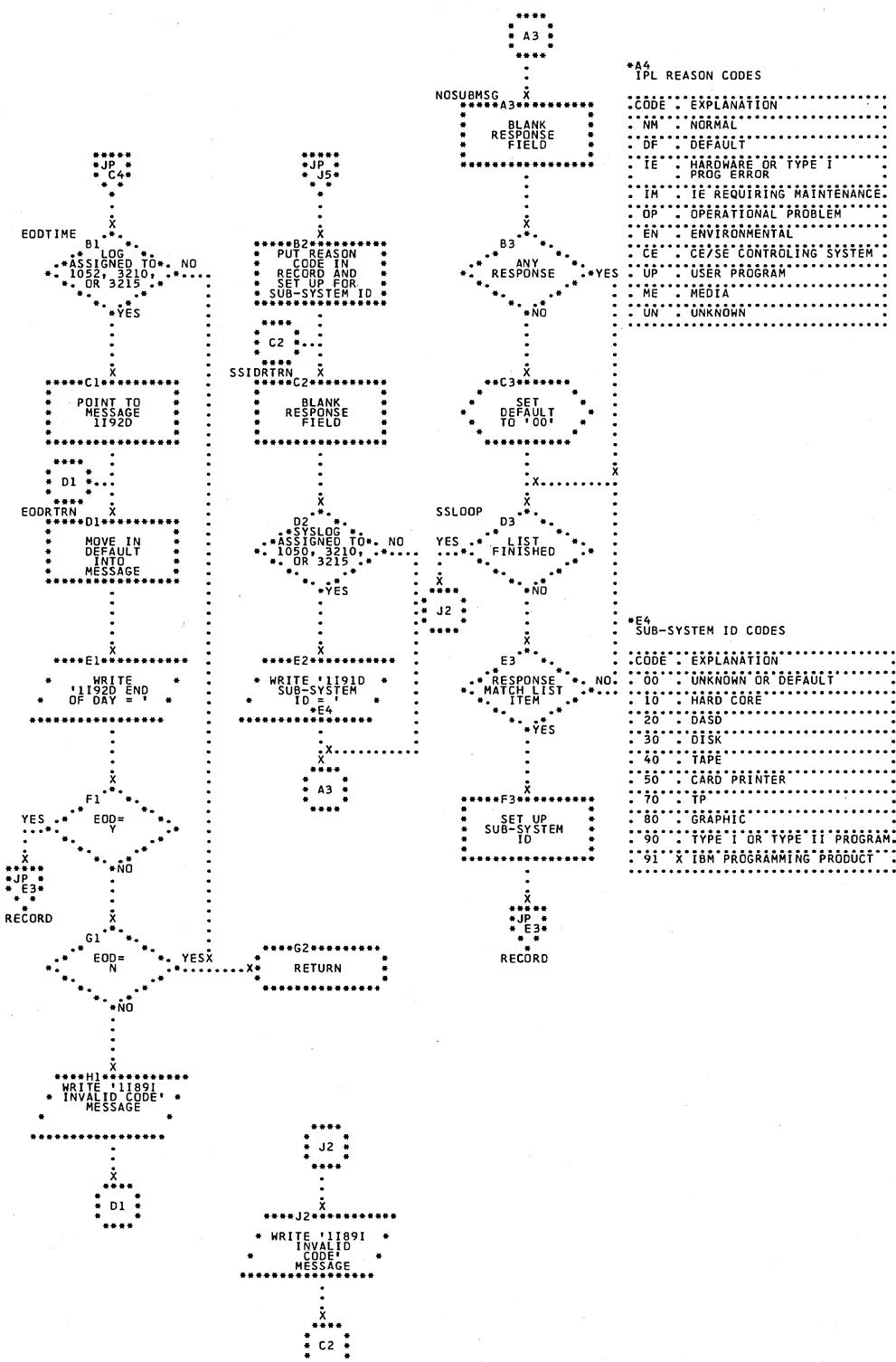


Chart KA. \$JOBCTLN - Job Accounting Interface (Part 1 of 2)  
Refer to Chart 16.

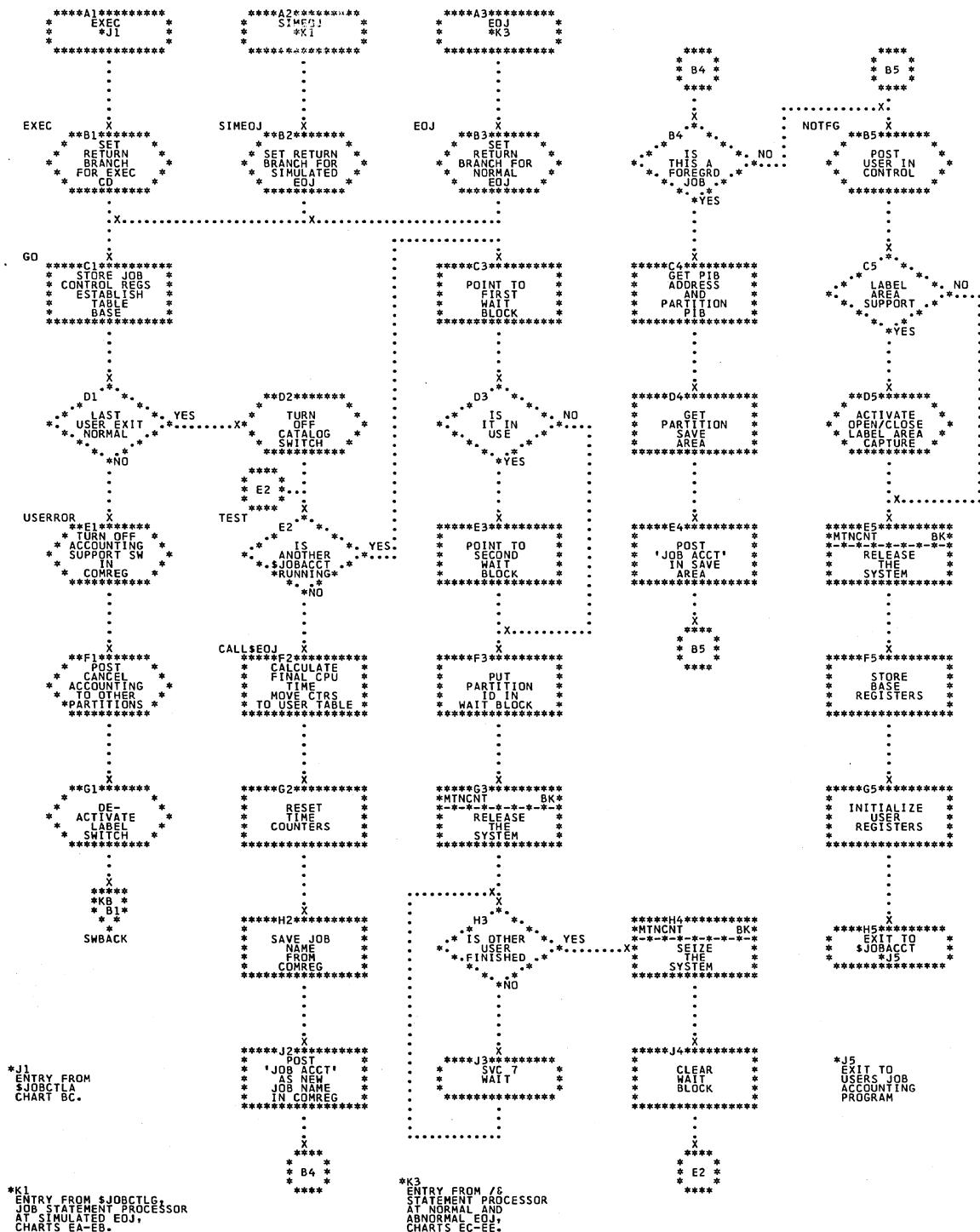


Chart KB. \$JOBCTLN - Job Accounting Interface (Part 2 of 2)  
Refer to Chart 16.

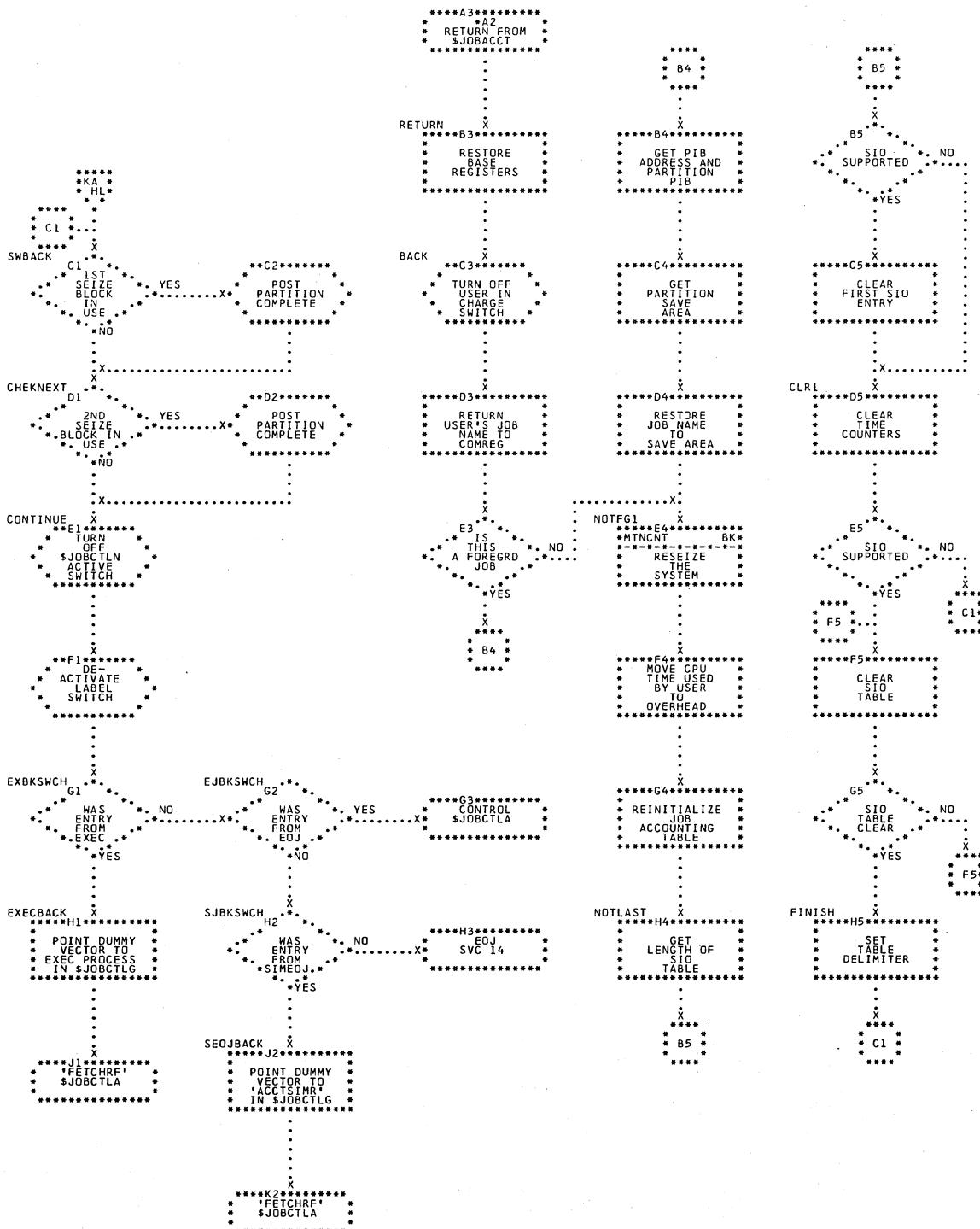
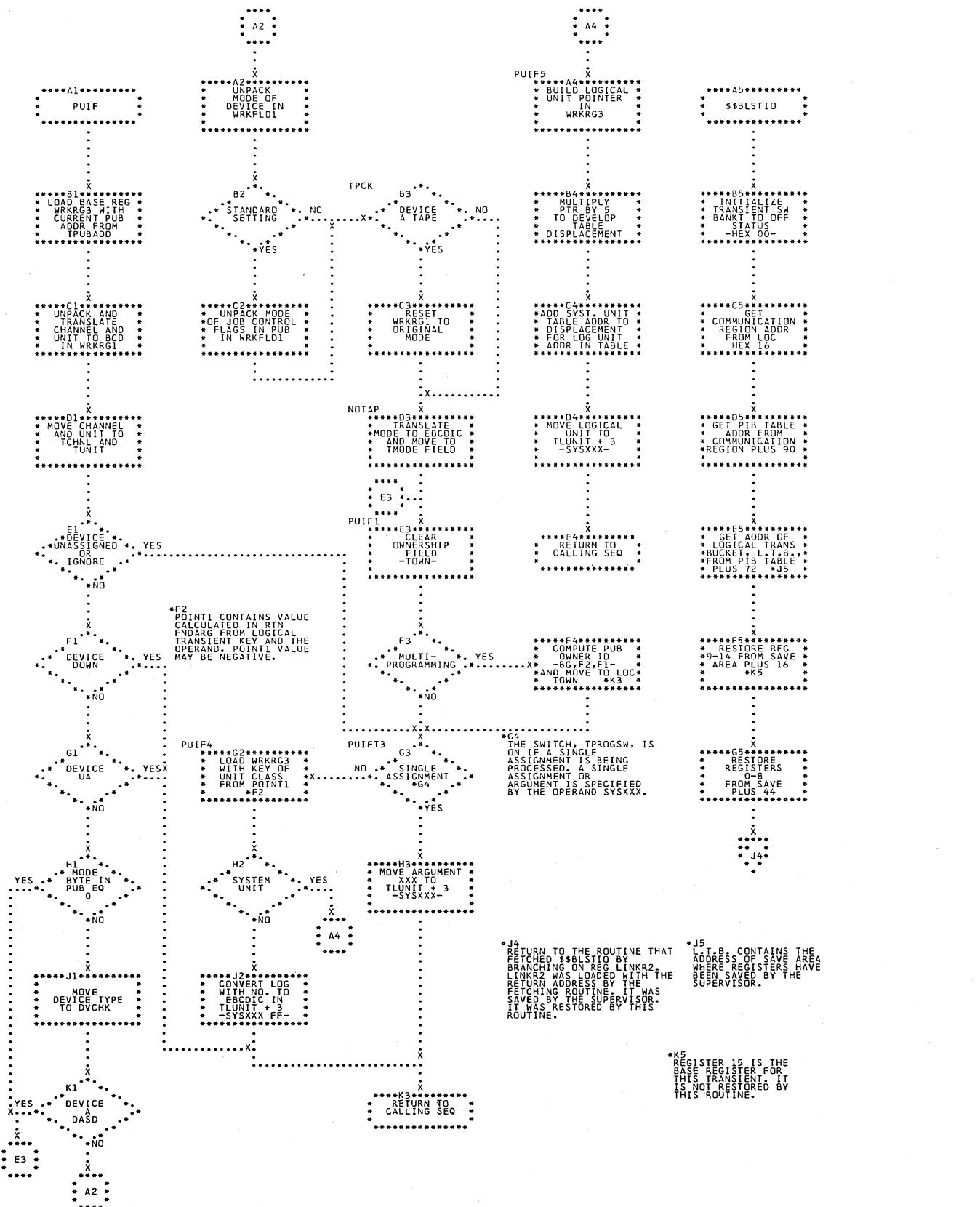


Chart LA. \$\$BLSTIO - Initialization  
Refer to Chart 07.



**Chart LB. \$\$BLSTIO - Operand Identification Subroutine**  
 Refer to Chart 07.

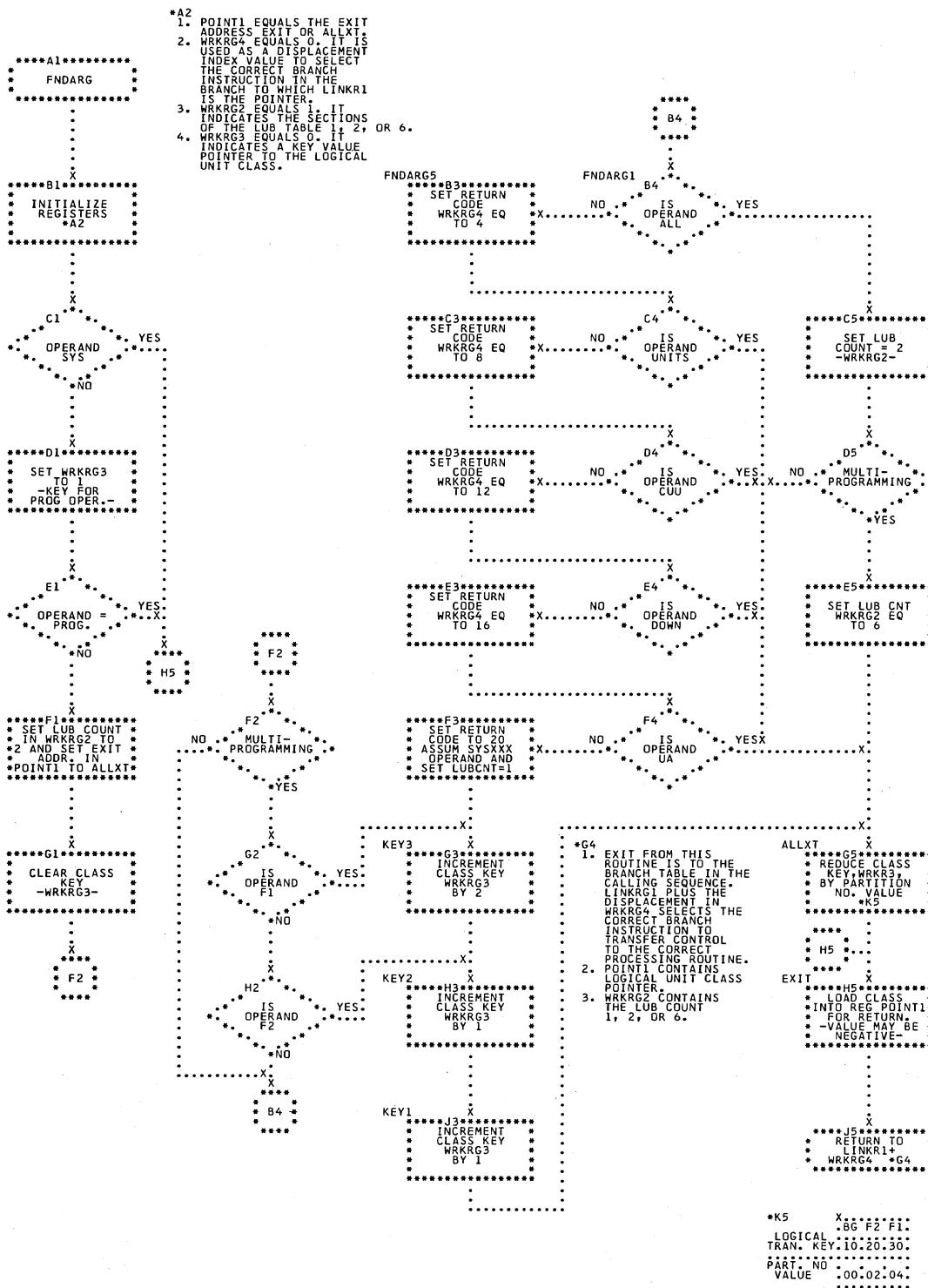
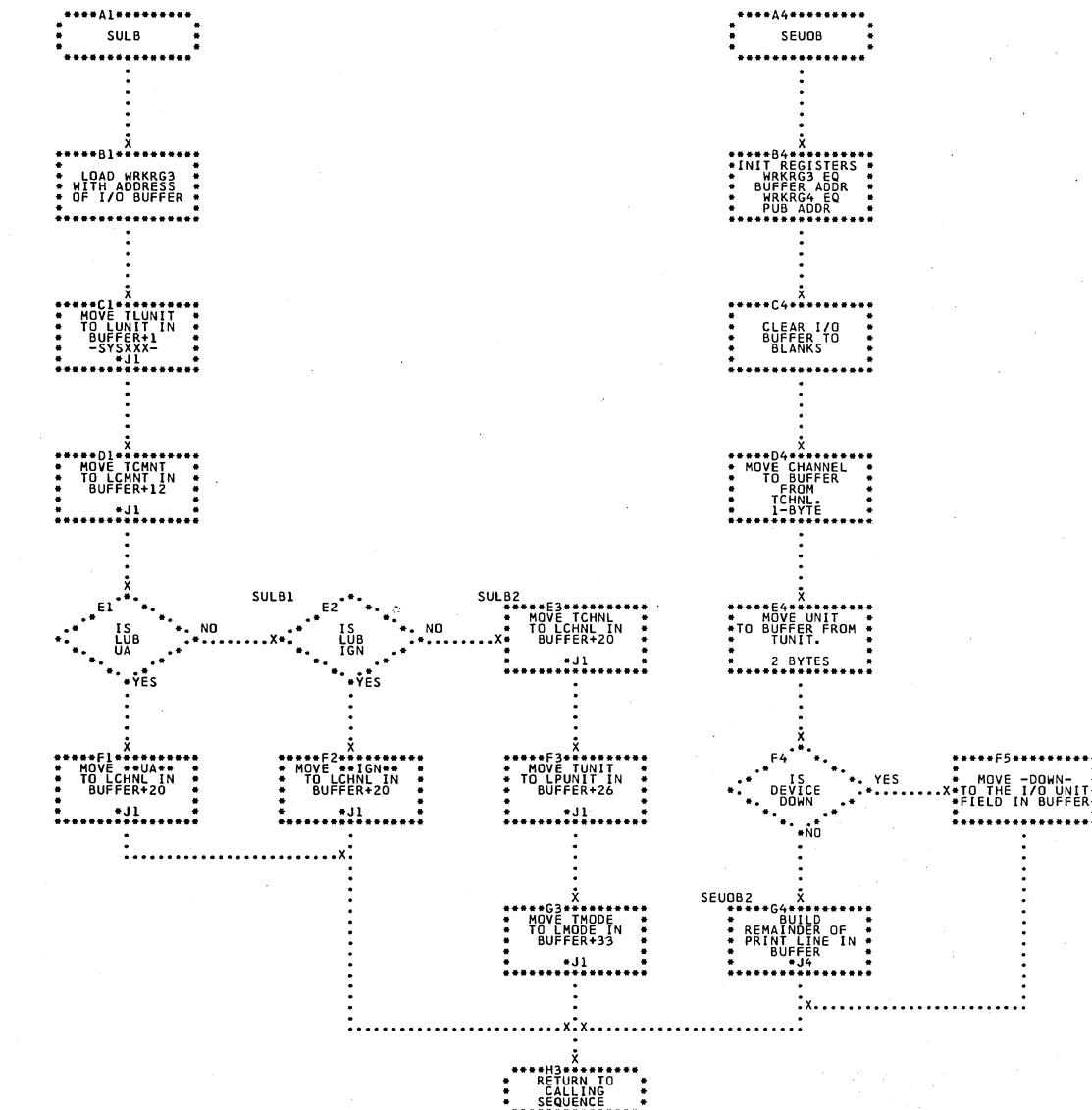


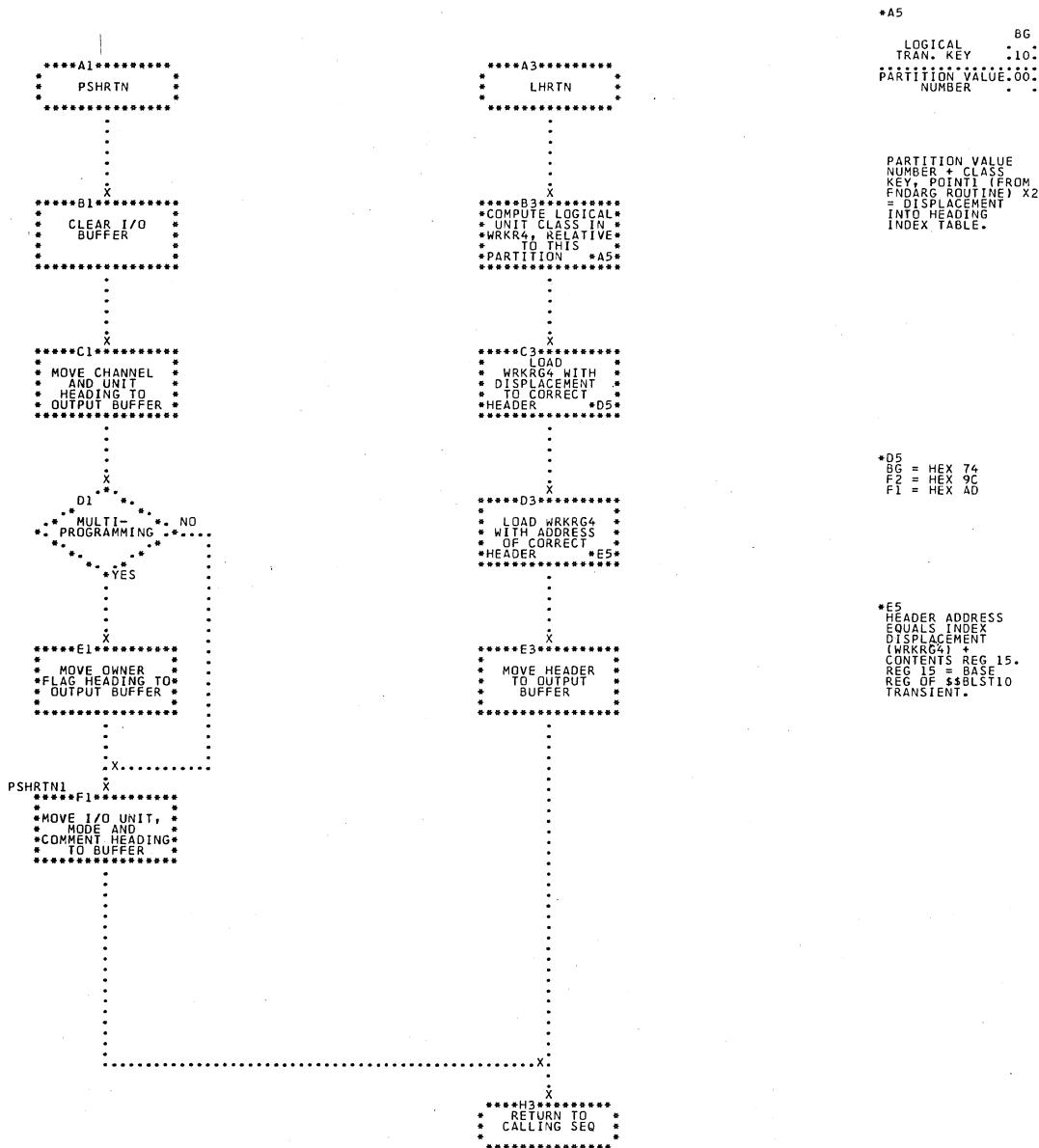
Chart LC. \$\$BLSTIO - Build Print Line Subroutine  
Refer to Chart 07.



J1  
LUNIT --- LCMNT --- LCHNL --- LPUNIT LMODE  
--SYSXXX---XXX---\*\*UA--\*\* XX XX \*\*IGN\*\*

- J4  
1. MOVE OWNER TO OWNER FIELD  
2. MOVE MODE FROM TMODE TO MODE FIELD  
3. MOVE LOGICAL UNIT FROM TLUNIT TO THE  
4. MOVE THE COMMENT FROM TCMNT TO THE  
COMMENT FIELD-MODE LESS 6 BYTES

**Chart LD. \$\$BLSTIO - Build Header Subroutine**  
 Refer to Chart 07.



\*A5  
 BG F2 F1  
 LOGICAL TRAN. KEY 10.20.30  
 PARTITION VALUE 00.02.04  
 NUMBER

PARTITION VALUE  
 NUMBER + CLASS  
 KEY POINTING FROM  
 END OF PARTITION X2  
 DISPLACEMENT  
 INTO HEADING  
 INDEX TABLE.

\*D5  
 BG = HEX 74  
 F2 = HEX 9C  
 F1 = HEX AD

\*E5  
 HEADER ADDRESS  
 EQUALS INDEX  
 DISPLACEMENT  
 (WRKRG4) +  
 CONTENTS REG 15.  
 REG 12 = BASE.  
 REG 0P \$\$BLSTIO  
 TRANSIENT.

APPENDIX A: LABEL LIST

INITIAL PROGRAM LOAD (\$\$A\$IPL1, \$\$A\$IPL2,  
\$IPLRT2, \$IPLRT3, AND \$IPLRT4)  
CHARTS AA-AZE

<u>Label</u>	<u>Phase</u>	<u>Chart</u>	CLEAR	\$\$A\$IPL2	AB
ABNCHK	\$IPLRT2	AQ	CLEARCOR	\$\$A\$IPL2	AB
ABNEND	\$IPLRT2	AQ	COMMA	\$IPLRT2	*
ADCLOP	\$\$A\$IPL2	AC	COMCHK	\$IPLRT4	AZA
ADREST	\$IPLRT4	AZB	COMDOK	\$IPLRT4	AX
ADDRTN	\$IPLRT3	AR	COMLOP	\$IPLRT4	AZA
ALCERR	\$IPLRT2	AM	COMNFD	\$IPLRT4	AX
ALCRTN	\$IPLRT2	AL	COMRGN	\$IPLRT2	*
ALCRT1	\$IPLRT2	AL			Communications region DSECT origin.
ALCRT2	\$IPLRT2	AM	CONTIN	\$\$A\$IPL2	AB
ALCRT3	\$IPLRT2	AM	CPBFEND	\$IPLRT4	AZA
ALTCHANQ	\$\$A\$IPL2	AG	CPYSLB	\$IPLRT4	AZC
	\$IPLRT4	AZA	CPYSRT	\$IPLRT4	AZC
ASNEND	\$IPLRT4	AZD	CTTLUB	\$IPLRT4	AY
ASNLP1	\$IPLRT4	AZD	CYLDP	\$IPLRT4	AY
ASNLP2	\$IPLRT4	AZD	DATERT	\$IPLRT4	AZB
ASNRTN	\$IPLRT4	AZD	DBLADD	\$IPLRT3	AR
ASNSTP	\$IPLRT4	AZD	DBLSCN	\$IPLRT3	AR
BCHEST	\$\$A\$IPL2	AF	DDLUB	\$IPLRT2	AP
	\$IPLRT3	AU	DECLP	\$IPLRT2	AP
BEGIN	\$IPLRT2	AJ	DECRL	\$\$A\$IPL2	AG
BEGIPL	\$\$A\$IPL2	AB	DECRR	\$\$A\$IPL2	AG
BLDPUB	\$\$A\$IPL2	AF	DECRTN	\$IPLRT2	AN
	\$IPLRT3	AU	DELEXT	\$IPLRT3	AT
			DELIM	\$IPLRT2	*
					Translate table for field delimiter.
BSTOFF	\$IPLRT3	AV	DELLOP	\$IPLRT3	AT
BSTOK	\$IPLRT3	AV	DELRTN	\$IPLRT3	AT
CALLBCHR	\$IPLRT2	AY	DSDP1	\$IPLRT4	AZ
CCW1	\$\$A\$IPL1	AA	DSDP2	\$IPLRT4	AZ
CCW2	\$\$A\$IPL1	AA	DSDP3	\$IPLRT4	AZ
CCW3	\$\$A\$IPL1	AA	DSDP4	\$IPLRT4	AZ
CCW4	\$\$A\$IPL1	AA	DSDRET	\$IPLRT4	AZ
CCW5	\$\$A\$IPL1	AA	DSDRTN	\$IPLRT4	AZ
CDSCH	\$IPLRT2	AK	DSKADR	\$\$A\$IPL2	AH
CHCLOP	\$\$A\$IPL2	AF	DVCLST	\$IPLRT2	*
	\$IPLRT3	AU			Device type table used to add PUB for a specified device.
CHCNT	\$\$A\$IPL2	AF	ENADR	\$IPLRT3	AS
	\$IPLRT3	AU	ENDRD	\$\$A\$IPL2	AD
CHEXT	\$IPLRT3	AW	ENFND	\$\$A\$IPL2	AF
CHFIN	\$\$A\$IPL2	AF		\$IPLRT3	AU
	\$IPLRT3	AU	ER1121	\$IPLRT4	AZ
CHGADR	\$\$A\$IPL2	AC	ERR	\$\$A\$IPL2	AB
CHKCOM	\$IPLRT4	AX	ERRHLT	\$\$A\$IPL2	AB
CHKLP	\$IPLRT3	AW	EXIT1	\$\$A\$IPL2	AH
CHSLOP	\$IPLRT3	AW	EXTRTN	\$\$A\$IPL2	AD
CHSRT	\$\$A\$IPL2	AF			Label of the address that the program returns to if SYSRDR is assigned and the operator indicates he has add and/or delete cards in SYSRDR. The return is via an external interrupt.
CHULOP	\$\$A\$IPL2	AH			
	\$IPLRT3	AW			
CHUPD	\$IPLRT3	AW			
CHURTN	\$\$A\$IPL2	AH			
	\$IPLRT3	AW			
CILRNO	\$\$A\$IPL2	AC			
CLDLPI	\$\$A\$IPL2	AG			
	\$IPLRT4	AZA			
CLDLP2	\$\$A\$IPL2	AG			
	\$IPLRT4	AZA			
CLDRTN	\$IPLRT4	AZA			

\*Listing Only.

FDSRTN	\$IPLRT2	AP	MPXMOV	\$IPLRT4	AZE
FNDTYP	\$IPLRT3	AV	MPXRTN	\$IPLRT4	AZE
FOUND	\$\$A\$IPL2	AC	MSGRTN	\$IPLRT2	AQ
FPRTN	\$IPLRT4	AZE	MVCEND	\$\$A\$IPL2	AC
GETADD	\$IPLRT2	AL	NEXTSYS	\$IPLRT4	AZ
GETDEL	\$IPLRT2	AL	NLPYR	\$IPLRT4	AZB
GETDEL1	\$IPLRT2	AL	NODASD	\$IPLRT2	AJ
GETNCL	\$IPLRT4	AZD	NOMTEB	\$IPLRT4	AY
GETSET	\$IPLRT2	AM	NOMVP	\$\$A\$IPL2	AF
GET3	\$IPLRT2	AL	NONBLK	\$IPLRT2	*
GET4	\$IPLRT2	AL			Translate table for nonblank scan.
GO	\$\$A\$IPL2	AD			
HALT	\$\$A\$IPL2	AB	NOSOAD	\$IPLRT3	AU
HARDMC	\$\$A\$IPL2	AB	NOSODL	\$IPLRT3	AT
HEXRTN	\$IPLRT3	AW	NOSOROOM	\$IPLRT3	AU
IJBIPLAD	\$IPLRT3	AR	NOTBG	\$IPLRT4	AZD
IJBIP210	\$\$A\$IPL2	AB	NOTEB	\$IPLRT2	AJ
ILLCD	\$IPLRT2	AQ	NOTFOR4	\$IPLRT2	AJ
IOFLD	\$\$A\$IPL2	*	NOTMPS	\$\$A\$IPL2	AE
	\$IPLRT2	*	NOT4AND3	\$IPLRT2	AM
			NOT4NOW	\$IPLRT2	AL
			NO3211	\$IPLRT4	AZ
IOHALT	\$IPLRT2	AQ	NUMCVT	\$IPLRT3	AS
IOHLD	\$IPLRT2	AQ	OFFINT	\$IPLRT4	AY
IOHL2	\$IPLRT2	AQ	ONINT	\$IPLRT4	AY
IOSTOP	\$IPLRT2	AQ	OPNEND	\$IPLRT4	AZ
IPLEND	\$IPLRT4	AY	OPNLOP	\$IPLRT4	AZ
ITSADWD	\$\$A\$IPL2	AC	OPNRTN	\$IPLRT4	AZ
KEYCHK	\$IPLRT3	AS	OPNUSN	\$IPLRT4	AZ
KEYCHK1	\$IPLRT3	AS	OPRTN	\$IPLRT2	AK
KEYCHK2	\$IPLRT3	AR	ORGEST	\$\$A\$IPL2	AE
LASTRD	\$\$A\$IPL2	AC	PBFEND	\$IPLRT4	AZA
LBLOC	\$\$A\$IPL2	AC	PBFFIN	\$IPLRT4	AX
LBLPED	\$IPLRT2	AP	PBFLOP	\$IPLRT4	AZA
LOAD3NOW	\$IPLRT2	AL	PBFRTN	\$IPLRT4	AZA
LOGRED	\$IPLRT2	AK	PIBTAB	\$IPLRT2	*
LOGSTR	\$IPLRT2	AK			Origin of PIB table DSECT.
LOGWRK	\$\$A\$IPL2	AE			
LOKCLD	\$\$A\$IPL2	AB	POINTLUB	\$IPLRT4	AY
LOOKLUB	\$IPLRT4	AY	PPBEGOK	\$\$A\$IPL2	AD
LOOKUP	\$\$A\$IPL2	AB	PPBEGOR	\$IPLRT2	AJ
LOWCRE	\$IPLRT2	*	PSWGO	\$\$A\$IPL2	AD
			PSWSET	\$\$A\$IPL2	AD
			PUBCLC	\$IPLRT2	AJ
LUBHLP	\$IPLRT2	AE	PUBDEQ	\$IPLRT3	AT
	\$IPLRT4	AY	PUBEXD	\$IPLRT3	AR
LUBLOP	\$\$A\$IPL2	AE	PUBMKE	\$\$A\$IPL2	AE
LUBLPL	\$IPLRT2	AP		\$IPLRT3	AS
LUBMVC	\$\$A\$IPL2	AE	PUBMK1	\$IPLRT3	AS
LUBRTN	\$\$A\$IPL2	AE	RACDVC	\$IPLRT3	AV
LUBSET	\$\$A\$IPL2	AD	RASPCRET	\$IPLRT2	AJ
LUDRTN	\$IPLRT2	AP	RASUPR	\$IPLRT2	AJ
LUUEND	\$\$A\$IPL2	AH	RDBLOK	\$\$A\$IPL2	AC
LUUPLP	\$\$A\$IPL2	AH	RDDIR2	\$\$A\$IPL2	AB
LUURTN	\$\$A\$IPL2	AH	RDRIFT	\$\$A\$IPL2	AD
	\$IPLRT2	AP	RDRST	\$IPLRT4	AX
MAXKEY	\$\$A\$IPL2	*	READER	\$\$A\$IPL2	AB
	\$IPLRT3	*	READGO	\$IPLRT2	AJ
MCR	\$IPLRT3	AV	READRT	\$IPLRT2	AK
MLUUR	\$IPLRT2	AP	RECNO	\$\$A\$IPL2	AH
MODEOK	\$IPLRT2	AJ	REREAD	\$IPLRT2	AK
MONITOR	\$IPLRT2	AJ	RESIDL	\$\$A\$IPL2	AG
MPXCHK	\$IPLRT4	AZE	RESIDR	\$\$A\$IPL2	AN
MPXGO	\$IPLRT4	AZE			
MPXHLT	\$IPLRT4	AZE			
MPXHL1	\$IPLRT4	AZE			
MPXHL2	\$IPLRT4	AZE			
MPXLOP	\$IPLRT4	AZE			

\*Listing Only.

RESNFD	\$IPLRT2	AN	TEBDET	\$IPLRT3	AV
RESWRK	\$IPLRT4	AX	TEBEST	\$IPLRT3	AV
RIGHT	\$\$A\$IPL2	AE	TEBXD	\$IPLRT3	AV
	\$\$A\$IPL2	AG	TEBLOP	\$IPLRT3	AT
RMSMCRET	\$IPLRT2	AN	TEBVCHK1	\$IPLRT3	AT
RMSPCRET	\$\$A\$IPL2	AB	TEBCNT	\$IPLRT2	AJ
RSTCHQ	\$IPLRT4	AZE	TEBVDEQ	\$IPLRT3	AT
RSTLUB	\$IPLRT2	AP	TEBVDET	\$IPLRT3	AV
RTRAK	\$\$A\$IPL2	AC	TEBVDT	\$IPLRT3	AV
SCHCNT	\$\$A\$IPL2	AF	TEBVEST	\$IPLRT3	AV
	\$IPLRT3	AU	TIMERT	\$IPLRT4	AZB
SCHLOP	\$IPLRT3	AV	TMCHK	\$IPLRT4	AX
SCHSCH	\$\$A\$IPL2	AF	TPDVC	\$IPLRT3	AV
	\$IPLRT3	AU	TRKNO	\$\$A\$IPL2	AH
SCHSTA	\$\$A\$IPL2	AF	TRTBRC	\$IPLRT2	AQ
	\$IPLRT3	AU	TRTTAB	\$IPLRT2	*
SCHTST	\$\$A\$IPL2	AF			Control card translate table.
	\$IPLRT3	AU			
SCNEND	\$IPLRT3	AT	TSTSO	\$IPLRT3	AU
SCNLOP	\$IPLRT3	AT	UPDAT	\$\$A\$IPL2	AC
SETBLK	\$\$A\$IPL2	AC	UPLUB	\$IPLRT2	AP
SETFIRST	\$IPLRT2	AM	USEWRK	\$\$A\$IPL2	AE
SETFCL	\$IPLRT3	AW	USNRES	\$IPLRT4	AX
SETKEY	\$\$A\$IPL2	AD	WAIT	\$\$A\$IPL2	AD
SETLOG	\$\$A\$IPL2	AD	YESLOG1	\$IPLRT4	AY
SETPROT	\$\$A\$IPL2	AD	YLPYR	\$IPLRT4	AZB
SETRTN	\$IPLRT4	AX			
SET2	\$IPLRT4	AZD			
SKPINC	\$IPLRT4	AZB			
SKPKEY	\$IPLRT3	AR			
SKPMVT	\$\$A\$IPL2	AE			
SPRSW	\$\$A\$IPL2	AF			
	\$IPLRT3	AU			
SRCH32H	\$IPLRT4	AZ			
SSKLOP	\$IPLRT2	AM			
STORECPU	\$IPLRT2	AJ			
SW	\$\$A\$IPL2	AB			

This switch is initially off (NOP). It is used after the transient directory has been searched and the supervisor nucleus has been found to branch around the instructions used in searching for the supervisor nucleus.

SW1            \$\$A\$IPL2            AC  
NOP/BR switch that is turned on (BR) for reading the last block of the supervisor into main storage.

SW2            \$\$A\$IPL2            AC  
NOP/BR switch that is turned on (BR) when first block of the supervisor is read.

SW3            \$\$A\$IPL2            AB  
This switch is initially off (NOP). It is used after the transient directory address and SYSRES device type has been determined to branch around these instructions.

SW4            \$\$A\$IPL2            AB  
SYSMVC        \$\$A\$IPL2            AG  
                \$IPLRT2            AN  
TAPE            \$IPLRT3            AV  
TEBCLC        \$IPLRT2            AJ  
TEBDEQ        \$IPLRT3            AT

<u>Label</u>	<u>Phase</u>	<u>Chart</u>
ABGF1F2	\$JOBCTL	CG
ACCTBR1	\$JOBCTLA	BB
	\$JOBCTLG	EB
ACCTBR2	\$JOBCTLG	EB
ACCTBR3	\$JOBCTLG	EB
ACCTBR4	\$JOBCTLG	EB
ACCTBR5	\$JOBCTLG	EA
ACCTBR6	\$JOBCTLJ	GM
ACCTCL	\$JOBCTLA	BB
	\$JOBCTLJ	GM
ACCTIGN	\$JOBCTLF	DL
	\$JOBCTLJ	GH
ACCTIGN1	\$JOBCTLA	BC
	\$JOBCTLG	EF
ACCTIGN2	\$JOBCTLA	BC
	\$JOBCTLG	ED
ACCTIGN3	\$JOBCTLA	BA
	\$JOBCTLG	EB
ACCTIGN4	\$JOBCTLA	BB
	\$JOBCTLG	EB
ACCTIGN5	\$JOBCTLG	EB
ACCTIGN6	\$JOBCTLG	EB
ACCTIGN7	\$JOBCTLJ	GM
ACCTIGN8	\$JOBCTLG	EC
ACCTLV1	\$JOBCTLA	BB
ACCTLV1B	\$JOBCTLJ	GM
ACCTNOTP	\$JOBCTLA	BC
ACCTSIMR	\$JOBCTLG	EA

\*Listing Only.

ACCTSwo	\$JOBCTLA	BC	ASSGN23B	\$JOBCTLD	CF
ACCTUPDT	\$JOBCTLF	DL	ASSGN23C	\$JOBCTLD	CF
	\$JOBCTLJ	GM	ASSGN23D	\$JOBCTLD	CF
ACTION	\$JOBCTLJ	GD	ASSGN24	\$JOBCTLD	CF
ACTRSP	\$JOBCTLA	BB	ASSGN25	\$JOBCTLD	CF
ADDLNGTH	\$JOBCTLG	EH	ASSGN26	\$JOBCTLD	CG
ADDSDR	\$JOBCTLM	JE	ASSGN27	\$JOBCTLD	CG
ADR	\$JOBCTLA	BN	ASSGN28	\$JOBCTLD	CG
	\$JOBCTLD	CM	ASSGN29	\$JOBCTLD	CJ
	\$JOBCTLF	DP	ASSGN3	\$JOBCTLD	CA
	\$JOBCTLG	EK	ASSGN30	\$JOBCTLD	CJ
ALLXT	\$\$BLSTIO	KB	ASSGN31	\$JOBCTLD	CJ
ALLOC	\$JOBCTLJ	GP	ASSGN32	\$JOBCTLD	CJ
ALTASW	\$JOBCTLD	*	ASSGN33	\$JOBCTLD	CJ
Bank 1, bit 1: if on, specifies ALT assignment.			ASSGN34	\$JOBCTLD	CH
			ASSGN35	\$JOBCTLD	CH
			ASSGN36	\$JOBCTLD	CH
			ASSGN37	\$JOBCTLD	CH
			ASSGN38	\$JOBCTLD	CJ
			ASSGN4	\$JOBCTLD	CA
			ASSGN40	\$JOBCTLD	CK
			ASSGN402	\$JOBCTLD	CD
			ASSGN403	\$JOBCTLD	CD
			ASSGN404	\$JOBCTLD	CD
			ASSGN41	\$JOBCTLD	CK
ASSGN	\$JOBCTLD	CA	ASSGN42	\$JOBCTLD	CK
ASSGNB3	\$JOBCTLD	CC	ASSGN43	\$JOBCTLD	CK
ASSGNB4	\$JOBCTLD	CD	ASSGN5	\$JOBCTLD	CA
ASSGNB5	\$JOBCTLD	CE	ASSGN5A	\$JOBCTLD	CA
ASSGNB6	\$JOBCTLD	CF	ASSGN6	\$JOBCTLD	CB
ASSGNB6A	\$JOBCTLD	CE	ASSGN7	\$JOBCTLD	CB
ASSGNB6B	\$JOBCTLD	CE	ASSGN8	\$JOBCTLD	CB
ASSGNB6C	\$JOBCTLD	CE	ASSGN9	\$JOBCTLD	CB
ASSGNB6D	\$JOBCTLD	CE	ASSGN901	\$JOBCTLD	CH
ASSGNB6E	\$JOBCTLD	CE	ATNCUU	\$JOBCTLA	BL
ASSGNB7	\$JOBCTLD	CJ	ATNCUU1	\$JOBCTLA	BL
ASSGNB8	\$JOBCTLD	CH	BACK	\$JOBCTLN	KB
ASSGNIT	\$JOBCTLD	CG	BANK1	\$JOBCTLD	*
ASSGNNT	\$JOBCTLD	CA	2-byte switch bank. Reset to zeros in the INITL subroutine.		
ASSGNP	\$JOBCTLD	CP			
Subroutine: Checks assignment of SYSSRLB and SYSSLB.					
ASSGNP1	\$JOBCTLD	CP	Byte 0, bit 0 see CLOSESW		
ASSGNR	\$JOBCTLD	CA	1	see ALTASW	
ASSGNTS	\$JOBCTLD	CA	2	see STDFDSW	
ASSGNV1	\$JOBCTLD	CA	3	see RETSW	
ASSGN0	\$JOBCTLD	CA	4	see EOLSW	
ASSGN1	\$JOBCTLD	CA	5	see PROGSW	
ASSGN1A	\$JOBCTLD	CA	6	see LIOSW	
ASSGN10	\$JOBCTLD	CB	7	see RESETSW	
ASSGN101	\$JOBCTLD	CB	Byte 1, bit 0 see DDSW		
ASSGN11	\$JOBCTLD	CB	1	see TRANSW	
ASSGN12	\$JOBCTLD	CB	2	not used	
ASSGN13	\$JOBCTLD	CC	3	not used	
ASSGN14	\$JOBCTLD	CD	4	not used	
ASSGN15	\$JOBCTLD	CD	5	see UNSW	
ASSGN16	\$JOBCTLD	CD	6	see TMPSW	
ASSGN17	\$JOBCTLD	CD	7	see MODSW	
ASSGN18	\$JOBCTLD	CD	-----		
ASSGN19	\$JOBCTLD	CD	BEGADDR	\$JOBCTLG	EG
ASSGN19A	\$JOBCTLD	CD	BINCON	\$JOBCTLJ	GT
ASSGN2	\$JOBCTLD	CA	Subroutine: Converts DATE and CLOCK parameters of the SET statement to		
ASSGN20	\$JOBCTLD	CD			
ASSGN21	\$JOBCTLD	CE			
ASSGN22	\$JOBCTLD	CG			
ASSGN23	\$JOBCTLD	CF			
ASSGN23A	\$JOBCTLD	CF	*Listing Only		

binary in registers POINT1, POINT2, and POINT3.			CHKLST      \$JOBCTLA      BD CHKNXC      \$JOBCTLD      CM \$JOBCTLK      HR CHKOPN      \$JOBCTLD      CV
BLKCNT      \$JOBCTLJ      GT	DQ		Subroutine: Sets the open indicator off in the DFB if the CUU of the DFB does not equal the CUU of the assignment.
BLNKLD      \$JOBCTLF	GQ		
BRSS      \$JOBCTLJ	BC		
BTLOOP      \$JOBCTLA	EL		
	\$JOBCTLG		
BTOFRD      \$JOBCTLJ	GK		
BTONRT      \$JOBCTLJ	GK		
BUFFER      \$JOBCTLA	*		CHKOPN1      \$JOBCTLD      CV CHKOPN2      \$JOBCTLD      CV CHKOVR      \$JOBCTLA      BJ CHKPGU      \$JOBCTLJ      GS CHKPRN      \$JOBCTLG      GP CHKPUB      \$JOBCTLF      DN CHKPUN      \$JOBCTLK      HH CHKRNG      \$JOBCTLD      CM \$JOBCTLG      HR
120-byte I/O area that allows control statements to be read into the main storage area previously occupied by the job control initialization routine, JOBCTL.			Subroutine: Checks the range of each character in a parameter.
BYPASS      \$JOBCTLJ	GD		
CALL\$EOJ      \$JOBCTLN	KA		
CALLTTR      \$JOBCTLM	JA		
CANCEL      \$JOBCTLG	EE		
CATAL      \$JOBCTLG	EN		
	\$JOBCTLJ		
CHAIN      \$JOBCTLA	GN		
CHECK      \$JOBCTLM	BE		
CHECKNEXT      \$JOBCTLN	JD		
CHECKRF      \$JOBCTLG	KB		
CHGSTT      \$JOBCTLJ	EB		
	GT		
Subroutine: Changes system status from problem program state to supervisor state and from supervisor state to problem program state. Used by the ALLOC statement processor.			
CHKASG      \$JOBCTLA	BK		CHKRNG1      \$JOBCTLJ      GQ \$JOBCTLK      HR CHKTIM      \$JOBCTLJ      GH CHKTIM1      \$JOBCTLJ      GH CIDEND      \$JOBCTLG      EH CKCLAS      \$JOBCTLF      DN CKNDAR      \$JOBCTLJ      GR CKSCST      \$JOBCTLJ      GP CLBLUB      \$JOBCTLA      BK \$JOBCTLF      DN CLOSE      \$JOBCTLD      CL CLOSED      \$JOBCTLD      CL CLOSESW      \$JOBCTLD      *
CHKASG3      \$JOBCTLA	BK		
Equated to CHKASG+4. This is the entry into the CHKASG subroutine that is used when the LUB table address of the unit to be checked has already been loaded in WRKRG3.			
CHKCND      \$JOBCTLG	EG		
CHKCNL      \$JOBCTLA	BM		
CHKCNT      \$JOBCTLA	BE		
Subroutine: Checks the area available for output records in the disk area allocated for SYSPCH or SYSLST. This subroutine is a part of \$JOBCTLA and is overlaid when any other phase of Job Control is loaded.			
CHKDIB      \$JOBCTLA	CE		
CHKJIB      \$JOBCTLA	BD		
CHKLNK      \$JOBCTLG	FB		
Subroutine: Checks SYSLNK assignment and device type.			

\*Listing Only

CLOSE1	\$JOBCTL0	CP
CLOSE10	\$JOBCTL0	CQ
CLOSE11	\$JOBCTL0	CQ
CLOSE12	\$JOBCTL0	CQ
CLOSE2	\$JOBCTL0	CL
CLOSE3	\$JOBCTL0	CL
CLOSE7	\$JOBCTL0	CL
CLOSE8	\$JOBCTL0	CQ
CLOSE9	\$JOBCTL0	CQ
CLRDN	\$JOBCTLF	DN
CLRDOA	\$JOBCTLG	EH

Portion of the EXEC statement processor used as a subroutine to clear the output area to blanks. Register POINT1 is reset to the beginning address of the area.

CLRRTN	\$JOBCTLG	EK
CMNWLM	\$JOBCTLJ	GP
CMPSDR	\$JOBCTLM	JC
CMPSDR1	\$JOBCTLM	JC
CMPSDR2	\$JOBCTLM	JC
CMPSDR3	\$JOBCTLM	JC
CNIOAG	\$JOBCTL0	CY
CNTROVFL	\$JOBCTLM	JH
CNUNCO	\$JOBCTLJ	GS

Subroutine: Converts the operand SYSXXX to system and unit class in the location UNCLOR.

CNUNCO1	\$JOBCTLJ	GS
CNVBCD	\$JOBCTLF	DQ

Subroutine: Converts data to EBCDIC for output.

COMPUBND	\$JOBCTLF	DQ
COMPUTF1	\$JOBCTLG	EC
COMSET	\$JOBCTLM	JJ

Subroutine: Calculates the first, current, and last OBR disk addresses and moves them to the SDR communication region.

COMSET1	\$JOBCTLM	JJ
COMTBL	\$JOBCTLJ	*

Beginning of table of magnetic tape commands.

CONCAT	\$JOBCTLK	HM
--------	-----------	----

Subroutine: Combines the operand fields of two control statements, the first control statement contains a continuation punch.

CONDSCX	\$JOBCTL0	CJ
CONTINUE	\$JOBCTLN	KB
CONT1	\$JOBCTLA	BB
CONTROL	\$JOBCTLA	BB
COPYLP	\$JOBCTLJ	GD
COPYSW	\$JOBCTLF	EG
CRJBSQ	\$JOBCTLG	EA
CRTBLD	\$JOBCTLJ	GP
CTIMR	\$JOBCTLG	*

A 4-byte area in partition register save area used to save job start time.

CTRLSW	\$JOBCTLA	BD
C48	\$JOBCTLG	EM
C60	\$JOBCTLG	EM
DATE	\$JOBCTLJ	GH
DCUXTN	\$JOBCTLA	BD
DDSW	\$JOBCTL0	*

BANK1+1, bit 0: If on specifies DVCDN in progress.

- Set ON: DVCDN statement processor.
- Set OFF: INITL subroutine.
- Controls exit from the RESET statement processor.

DECK	\$JOBCTLG	EM
DETACH	\$JOBCTLF	DL

Entry into the UNBATCH statement processing routine.

DETACH1	\$JOBCTLF	DL
DETACH2	\$JOBCTLF	DL
DETACH2A	\$JOBCTLF	DL
DETACH3	\$JOBCTLF	DL
DETCHR	\$JOBCTLF	DL
DEVERR	\$JOBCTLM	JN
DFB	\$JOBCTLA	*

through \$JOBCTLM

Data file block (Figure 6).

DIBRC	\$JOBCTLJ	GH
DLAB	\$JOBCTLK	HF
DLBL	\$JOBCTLK	HE
DLBL1	\$JOBCTLK	HE
DLBOUT	\$JOBCTLK	HF
DNEERR	\$JOBCTLJ	GU
DOP34	\$JOBCTLK	HM

Subroutine: Converts creation date and expiration date to binary and stores in label output area.

DOWN	\$JOBCTLF	DE
DQXTNT	\$JOBCTLG	FC
DSKIND	\$JOBCTLA	BH
DSKINT	\$JOBCTLA	BD

Subroutine to perform Job Control initialization for DASD. If SYSLST/SYSPCH are assigned to disk, the available record count is checked. All extent JIBs currently attached to programmer units are unassigned and placed on the free list. This subroutine is a part of \$JOBCTLA and is overlaid when any other phase of Job Control is loaded.

DUMP	\$JOBCTLG	EP
DVCDN	\$JOBCTLF	DH
DVCDNC	\$JOBCTLF	DJ
DVCDNL	\$JOBCTLF	DH
DVCDNS	\$JOBCTLF	DJ
DVCDNX	\$JOBCTLF	DJ
DVCDN1	\$JOBCTLF	DH
DVCDN14	\$JOBCTLF	DH
DVCDN14A	\$JOBCTLF	DH

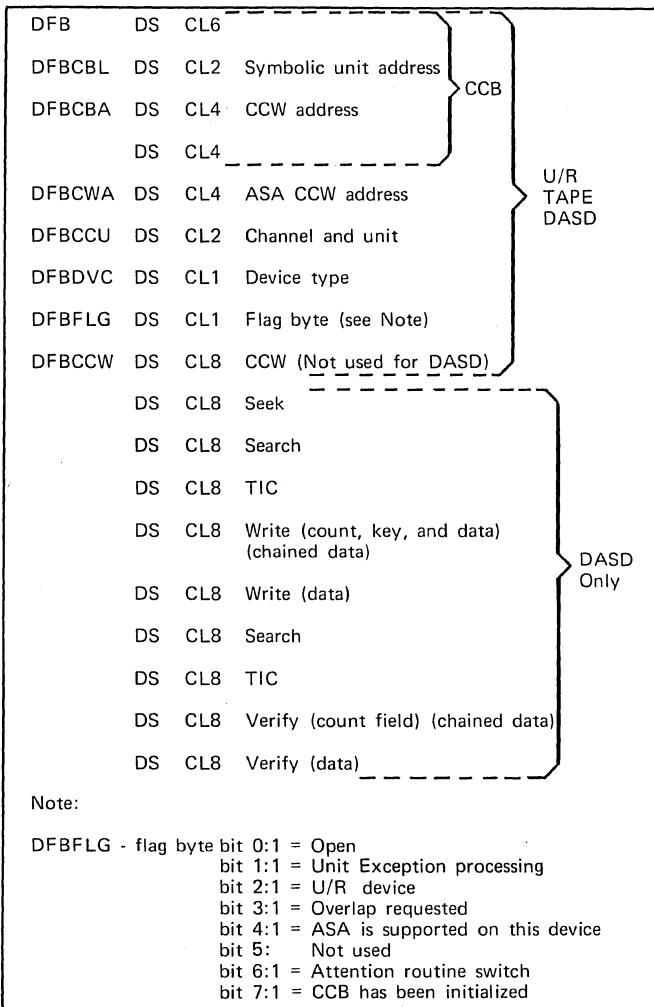


Figure 6. DFB Format

DVCDN14B	\$JOBCTLF	DH
DVCDN2	\$JOBCTLF	DH
DVCDN3	\$JOBCTLF	DH
DVCDN4	\$JOBCTLF	DH
DVCDN5	\$JOBCTLF	DH
DVCDN6	\$JOBCTLF	DJ
DVCDN7	\$JOBCTLF	DJ
DVCDN8	\$JOBCTLF	DJ
DVCUP	\$JOBCTLF	DK
EDTEST	\$JOBCTLG	EK
EJBKSWCH	\$JOBCTLN	KB
ENDINC	\$JOBCTLJ	GD
EODRTRN	\$JOBCTLM	JQ
EODTIME	\$JOBCTLM	JQ
EOFKY0	\$JOBCTLM	JG
EOFKY1	\$JOBCTLM	JG
EOFPRC	\$JOBCTLA	BJ
EOJ	\$JOBCTLG	EC
	\$JOBCTLN	KA
EOJBY	\$JOBCTLG	EB
EOJNRF	\$JOBCTLG	EC
EOJOFF	\$JOBCTLG	EB
EOJ1	\$JOBCTLG	EB

EOLSW            \$JOBCTLTD \*  
BANK1, bit 4: If on, specifies that  
the end of a class in the LUB table  
has been reached.

- Set ON: SCANLUB subroutine.
- Set OFF: INITL subroutine and  
SCANLUB subroutine.
- Controls processing in:
  1. ASSGN statement processor.
  2. DVCDN statement processor.
  3. LISTIO statement processor.

EQTEST	\$JOBCTLG	EL
EREEXIT	\$JOBCTLJ	GF
EREEXIT1	\$JOBCTLJ	GF
ERRAP	\$JOBCTLA	BM
ERRAP1	\$JOBCTLA	BM
ERRCNL	\$JOBCTLA	BM
ERRRTN	\$JOBCTLA	BM
	\$JOBCTLTD	CY
ERRRTN1	\$JOBCTLTD	CY
ERRRTN2	\$JOBCTLA	BM
	\$JOBCTLTD	CY
ERRS	\$JOBCTLG	FQ
EXBKSWCH	\$JOBCTLN	KB
EXCCON	\$JOBCTLA	BJ
EXCECP	\$JOBCTLA	BJ
EXCEDT	\$JOBCTLG	EF
EXCEOFO	\$JOBCTLA	BJ
EXCP	\$JOBCTLTD	CX
EXCPGO	\$JOBCTLA	BJ
EXCPRG	\$JOBCTLA	BH

Root phase subroutine to perform I/O  
on a specified logical unit. If the  
file has not been opened, this routine  
initializes its DFB including the CCB  
before executing the I/O.

EXCPROG            \$JOBCTLTD            CX  
Subroutine: Performs I/O on tape when  
user tape density is to be used.

- The symbolic unit address (class  
and order) is supplied in register  
POINT1.
- The PUB address is supplied in  
register POINT2.

EXCPROG1            \$JOBCTLTD            CX  
Entry point to the EXCPROG subroutine  
when IBM standard tape density is to  
be used.

EXCPROG2	\$JOBCTLTD	CX
EXCPROG3	\$JOBCTLTD	CX
EXCPROG4	\$JOBCTLTD	CX
EXCPSCV	\$JOBCTLA	BJ

\*Listing Only.

EXCUSR	\$JOBCTLG	EF	FETINSRT	\$JOBCTLK	HC	
EXEC	\$JOBCTLG	EF		Subroutine: Inserts numeric operands		
	\$JOBCTLN	KA		in output block for output to label		
EXEC1	\$JOBCTLG	EF		cylinder on SYSRES.		
EXECBACK	\$JOBCTLN	KB				
EXIT	\$\$BLSTIO	LB	FETINSR0	\$JOBCTLK	HC	
EXPEXT	\$JOBCTLA	BJ	FETINSR1	\$JOBCTLK	HC	
EXTNT	\$JOBCTLK	HJ	FETINSR3	\$JOBCTLK	HC	
EXTRLC	\$JOBCTLA	BN	FETINSR4	\$JOBCTLK	HC	
	Root phase subroutine adds relocation			FETINSR5	\$JOBCTLK	HC
	factor to external symbols.			FETK	\$JOBCTLK	HK
EXTRTN	\$JOBCTLA	BN	FETKNO	\$JOBCTLK	HK	
FDDKCODE	\$JOBCTLK	HF	FETKNO1	\$JOBCTLK	HK	
FDKDFAUL	\$JOBCTLJ	*	FETKTK	\$JOBCTLK	HL	
	Default values for disk label vol.					
	sequence through system code.					
FDKIJ	\$JOBCTLK	HE	FETKTK1	\$JOBCTLK	HL	
FDKIJ1	\$JOBCTLK	HE	FETKTK2	\$JOBCTLK	HL	
FDKIJ2	\$JOBCTLK	HE	FETYPE	\$JOBCTLK	HJ	
FDKTDAT	\$JOBCTLK	HD	FETYPED	\$JOBCTLK	HJ	
FDKTDAT1	\$JOBCTLK	HD	FETYPEI	\$JOBCTLK	HK	
FDKTDAT2	\$JOBCTLK	HD	FETYPEI4	\$JOBCTLK	HK	
FDKTID	\$JOBCTLK	HD	FETYPE1	\$JOBCTLK	HJ	
	Subroutine: Processes FILE ID and			FEVSER	\$JOBCTLK	HJ
	DATE operands in DLBL and TLBL			FEVSER1	\$JOBCTLK	HJ
	statements.			FEVSER2	\$JOBCTLK	HJ
FDKTV	\$JOBCTLK	HC	FEVSER3	\$JOBCTLK	HJ	
	Subroutine: Initialization for DLBL,			FEVSER4	\$JOBCTLK	HJ
	TLBL, and VOL statement processing			FICLS	\$JOBCTLA	*
FDKTVNM	\$JOBCTLK	HC				
	Subroutine: Processes FILENAME					
	operand.					
FDKTV1	\$JOBCTLK	HC				
FDKTV2	\$JOBCTLK	HC				
FDK1	\$JOBCTLK	HE				
FDK2	\$JOBCTLK	HE				
FDSYSU	\$JOBCTLJ	GS				
FEBIN	\$JOBCTLK	HB				
FEBIN1	\$JOBCTLK	HB				
	Subroutine: Processes bin numbers					
	operand in EXTENT statement.					
FESEQ	\$JOBCTLK	HL				
	Subroutine: Processes EXTENT SEQUENCE					
	operand.					
FESEQCK	\$JOBCTLK	HK				
FESPLIT	\$JOBCTLK	HK				
FESPLIT1	\$JOBCTLK	HK				
FESPLIT2	\$JOBCTLK	HK				
FESPLIT3	\$JOBCTLK	HK				
FESPLIT4	\$JOBCTLK	HK				
FESYSX3	\$JOBCTLK	HJ				
FESYX2	\$JOBCTLK	HJ				
FETCHN	\$JOBCTLG	EB				
FETCHR	\$JOBCTLG	EK				
FETCHRF	\$JOBCTLA	BC				

---

\*Listing Only.

A 6-byte field that contains first in class lists for background, foreground 2, and foreground 1 system and programmer units, translated from 4-byte FICL in supervisor. (Figure 7)

	BG		F2		F1	
	FICLS	SS	PP	SS	PP	SS
NICLS	SS	PP	SS	PP	SS	PP

Where: SS = System Class  
PP = Programmer Class

Figure 7. Format for NICLS and FICLS

FICNIC	\$JOBCTLA	BA			
FINISH	\$JOBCTLN	KB			
FINIS2	\$JOBCTLG	EK			
FINOPN	\$JOBCTLA	BH			
FLOC	\$JOBCTLID	*			
	Label of a 2-byte location that holds				
	the address of the first LUB of a				
	class. Loaded by the GETLAN				
	subroutine.				
FNDARG	\$\$BLSTIO	LB			
	Subroutine: Used by the LISTIO				
	statement processor to determine the				
	operand of the LISTIO statement.				
FNDARG1	\$\$BLSTIO	LB			
FNDARG5	\$\$BLSTIO	LB			

FNIOAG	\$JOBCTL	CY	IGNORE	\$JOBCTLA	BG							
FSCAN	\$JOBCTLK	HB	ILUS	\$JOBCTL	CY							
Subroutine: Scans operands of label statements and sets condition code for operand omitted, operand length equal to 1, or operand length equal to greater than 1.												
FSCAN1	\$JOBCTLK	HB	INAERR	\$JOBCTLJ	GU							
FTEND	\$JOBCTLK	HB	INCLUDE	\$JOBCTLJ	GD							
FTEND1	\$JOBCTLK	HB	INDSEQ	\$JOBCTLK	HG							
FTKTBL	\$JOBCTLG	*	INDVTP	\$JOBCTL	CY							
Beginning of label cylinder track allocation table.												
FULERR	\$JOBCTLM	JN	INDWR	\$JOBCTLA	BG							
FXDFAUL	\$JOBCTLJ	*	INITL	\$JOBCTL	CM							
Beginning of standard default table for volume serial through logical unit.												
FXDFAUL1	\$JOBCTLJ	*	\$JOBCTLF	DP								
Beginning of current default table built by DLBL statement processing routine to be used when processing EXTENT statement.			\$JOBCTLG	EK								
GETDUR	\$JOBCTLG	FA	INNEN	\$JOBCTLJ	GP							
Subroutines: Computes job duration and converts to EBCDIC.			INSW	\$JOBCTL	CK							
GETIME	\$JOBCTLG	FA	INVASGN	\$JOBCTL	CJ							
Subroutine: Computes time of day and converts it to EBCDIC.			IROUT	\$JOBCTLG	FC							
GETJIB	\$JOBCTL	CR	Subroutine: Performs I/O and waits for traffic bit to be posted.									
GETJIBSW	\$JOBCTL	CR	IROUTW	\$JOBCTLG	FC							
GETJIB1	\$JOBCTL	CR	Subroutine: Waits for previous I/O operation to complete.									
GETJIB2	\$JOBCTL	CR	IPLEODYS	\$JOBCTLM	JP							
GETLAN	\$JOBCTL	CM	IPLLOOP	\$JOBCTLM	JP							
Subroutine: Determines number of logical units in a class and address of first LUB of the class in any one partition.			IPLNVLD	\$JOBCTLM	JP							
GETPUB	\$JOBCTLG	FD	IPLRTRN	\$JOBCTLM	JP							
Subroutine: Computes the address of a given PUB entry and stores it in register POINT3.			ISCKSQ	\$JOBCTLK	HG							
GO	\$JOBCTLN	KA	ISSUIO	\$JOBCTLA	BJ							
GOCAT	\$JOBCTLG	EN	ISTYP4	\$JOBCTLK	HG							
GTNAME	\$JOBCTLG	EG	IVDS	\$JOBCTL	CY							
GTNXNT	\$JOBCTLG	EH	IVSERR	\$JOBCTLJ	GU							
GTNXOP	\$JOBCTLG	GP	JBCSW0	\$JOBCTLA	*							
HEADNO	\$JOBCTLA	BJ	Displacement 56 in the communication region (COMREG + 56). See Figure 10.									
HEXCON	\$JOBCTL	CP	JBCSW1	\$JOBCTLA	*							
Subroutine: Converts the operand X'CUU' to binary and saves it in the location IORGSA.			Displacement 57 in the communication region (COMREG + 57). See Figure 10.									
HOLD	\$JOBCTLJ	GA	JBCSW2	\$JOBCTLA	*							
HOLD1	\$JOBCTLJ	GA	Displacement 58 in the communication region (COMREG + 58). See Figure 10.									
HOLD2	\$JOBCTLJ	GA	JBCSW3	\$JOBCTLA	*							
IDCHK	\$JOBCTLM	JF	Displacement 59 in the communication region (COMREG + 59). See Figure 10.									
IDMOVE	\$JOBCTLM	JF	JBCSW4	\$JOBCTL	*							
IDRTN	\$JOBCTLM	JP	Bit 0: 0 = Job control statement started with a //.									
			1 = Job control statement started without a //.									
			Bit 1: 0 = Statement is to be logged on SYSLST in case of error.									
			1 = Statement is not to be logged on SYSLST in case of error.									
			Bit 2: 0 = Statement is to be logged on SYSLOG in case of error									
			-----									
			*Listing Only.									

1 = Statement is not to be logged on SYSLOG in case of error.	LBLOUT	\$JOBCTLG	FE
		\$JOBCTLK	HN
Subroutine: Writes label information in the VOL area of SYSRES from the job control label area.			
Bit 3: 0 = No time stamping on SYSLOG or SYSLST.	LBLOUTF	\$JOBCTLK	HN
	LBLOUTOA	\$JOBCTLG	FE
1 = Time stamping will occur. Turned on by TIMOUT routine \$JOBCTLG if timer is running.	LBLOUTOB	\$JOBCTLG	FE
	LBLOUT1	\$JOBCTLK	HN
Bit 4: 0 = Reserved.	LBLOUT2D	\$JOBCTLG	FE
	LBLTYP	\$JOBCTLK	HP
Bit 5: 0 = No auto cancel.	LBTOUT	\$JOBCTLK	HA
	LDBASE	\$JOBCTLG	HA
1 = Automatic cancel on error.	LGCHKDON	\$JOBCTLM	EJ
Bit 6-7: Reserved.	LHRTN	\$SBLSTIO	JA
	LINCNT	\$JOBCTLA	LD
JBCSW5 \$JOBCTLA *			*
Bits 0-2: Always 0.			Maximum line count for SYSLST.
			Maintained in COMREG + 78.
Bit 3: 0 = A label block is in output area ready to be written on the SYSRES label cylinder.	LINK	\$JOBCTLG	EN
	LIOCUU	\$JOBCTLF	DD
Bit 4: 1 = Required label statement to follow.	LIOEOJ	\$JOBCTLF	DA
	LIOEOJ1	\$JOBCTLF	DA
Bit 5: 1 = Only an XTENT statement may follow.	LIOL	\$JOBCTLF	DB
	LIOLL201	\$JOBCTLF	DB
Bit 6: 1 = Only an EXTENT statement may follow.	LIOLL3	\$JOBCTLF	DB
	LIOL1	\$JOBCTLF	DB
Bit 7: 1 = Only a DLAB or TPLAB statement may follow.	LIOL2	\$JOBCTLF	DB
	LIOL202	\$JOBCTLF	DB
	LIOL4	\$JOBCTLF	DB
	LIOSW	\$JOBCTLD	*
JBINPR \$JOBCTLG EC			BANK1, bit 6: If ON specifies LISTIO in progress.
JIBCHN \$JOBCTLD *			
Label of a 2-byte location used as a 2-byte work area for LUBs and JIBs.			
JIBPTR \$JOBCTLA BD			• Set ON: LISTIO statement processor.
	\$JOBCTLG FC		
JIBSCN \$JOBCTLG FD			• Set OFF: UNPA subroutines SFPPE subroutine INITL subroutine.
Subroutine: Scans the JIB table and makes the next JIB in a chain available for processing.			• Controls processing in the SFPPE subroutine.
JOB \$JOBCTLG EA			
JOBCtl \$JOBCTLA BA			
JOBCtlM \$JOBCTLM JA			
JOBCtl4 \$JOBCTLA BB			
JOBCtl5 \$JOBCTLA BB			
KEY1 \$SBLSTIO LB			
KEY2 \$SBLSTIO LB			
KEY3 \$SBLSTIO LB			
LAXERR \$JOBCTLG FG			
	\$JOBCTLK HS		
LBLDST \$JOBCTLJ *			
Beginning of DSECT used in building label information for output to label cylinder on SYSRES.			
	LIOSYX	\$JOBCTLF	DD
	LIST	\$JOBCTLG	EM
	LISTIO	\$JOBCTLF	DA
	LISTX	\$JOBCTLG	EM
	LNAERR	\$JOBCTLG	FG
	LNKINT	\$JOBCTLA	BH
	LNKNOP	\$JOBCTLG	EG
	LNKOUT	\$JOBCTLJ	GT
			Subroutine: Controls block count and byte count for writing on SYSLNK.
	LOADRS	\$JOBCTLK	HQ
	LOADRS1	\$JOBCTLK	HQ
	LOG	\$JOBCTLJ	GL
			-----
			*Listing Only

LOGCHK	\$JOBCTLA	BG	MTNCNT	\$JOBCTLA	BK
	Root phase subroutine sets switches in JBCSW0 (COMREG + 56) bits 6 and 7 to indicate SYSLOG device type and assignment.		MVKIND	\$JOBCTLF	DQ
LOGDON	\$JOBCTLG	JH	MVMVRT	\$JOBCTLG	EJ
LOGIN	\$JOBCTLA	BG	MVQUO	\$JOBCTLG	EP
LOGOUT	\$JOBCTLA	BE	NDSCAN	\$JOBCTLA	BF
	Root phase subroutine outputs a control statement or a message on SYSLOG.			\$JOBCTLG	EP
LOGPRT	\$JOBCTLA	BG		\$JOBCTLK	HD
LOOP	\$JOBCTLG	EP	NDSCAN1	\$JOBCTLK	HD
LSTOUT	\$JOBCTLA	BE	NDSERR	\$JOBCTLJ	GU
	Root phase subroutine outputs a control statement or a message on SYSLST.		NDSERR1	\$JOBCTLJ	GF
LUBADD	\$JOBCTLG	*	NDTERR	\$JOBCTLG	FG
	Label of a 2-byte location that holds the address of the LUB being assigned. Loaded by the subroutine, SYSXXX.		NEWLUB	\$JOBCTLJ	GU
LUBCOM	\$JOBCTLG	*		\$JOBCTLG	*
	Label of a 2-byte location used for temporary storage of a LUB for comparison. Used by the SCNLUB subroutine.		NEWXTN	\$JOBCTLK	HH
LUBCOUNT	\$JOBCTLG	FB	NEXT	\$JOBCTLJ	GN
LUBSCN	\$JOBCTLG	FD	NEXTPUB	\$JOBCTLM	JD
	Subroutine: Makes the address of the next LUB entry of a class available in register POINT1.		NEXTTR	\$JOBCTLM	JD
MAP	\$JOBCTLF	DK	NICLS	\$JOBCTLA	*
MMDDYY	\$JOBCTLJ	GG		through \$JOBCTLK	
MMDD1	\$JOBCTLJ	GG			
MODSW	\$JOBCTLG	*			
	BANK1+1, bit 7: If on, specifies that the mode must be set for this assignment.		NLISTX	\$JOBCTLG	EM
	• Set ON: ASSGN statement processor.		NLSERR	\$JOBCTLK	HS
	• Set OFF: INITL subroutine.		NLST	\$JOBCTLG	EG
	• Controls processing in the ASSGN statement processor.		NLUERR	\$JOBCTLJ	GU
MOVLOP	\$JOBCTLG	EK	NOC	\$JOBCTLK	HS
MSGOUT	\$JOBCTLA	BE		\$JOBCTLG	*
	Root phase entry into the LOGOUT subroutine. Sets switches to allow output on both SYSLST and SYSLOG.				
MTC	\$JOBCTLJ	GE	NOCATL	\$JOBCTLG	ED
MTC1	\$JOBCTLJ	GE	NOCLEAR	\$JOBCTLG	EC
MTC2	\$JOBCTLJ	GE	NODCUX	\$JOBCTLK	HH
MTC3	\$JOBCTLJ	GE	NODECK	\$JOBCTLG	EM
MTC4	\$JOBCTLJ	GE	NODSYS	\$JOBCTLA	BD
MTC5	\$JOBCTLJ	GF	NODUMP	\$JOBCTLG	EP
MTFILL	\$JOBCTLJ	*	NOEERR	\$JOBCTLA	BL
	Default value for system code in tape label '0000000DOS/TOS/360'.		NOEERT	\$JOBCTLA	BL
			NOEOJ	\$JOBCTLA	BG
			NOERRS	\$JOBCTLG	EM
			NOEXC	\$JOBCTLG	CQ
			NOFP	\$JOBCTLG	EG
			NOIPLMSG	\$JOBCTLG	JP
			NOIPT	\$JOBCTLG	ED
			NOIPTA	\$JOBCTLG	ED
			NOIPT1	\$JOBCTLG	ED

\*Listing Only.

NOIPT2	\$JOBCTLG	ED	OBRSTRT	\$JOBCTL	JF
NOIPT3	\$JOBCTLG	ED	OBRWRT	\$JOBCTL	JC
NOIPT7	\$JOBCTLG	ED	OBRWRT1	\$JOBCTL	JC
NOKYTST	\$JOBCTL	JF	OBRWRT2	\$JOBCTL	JC
NOLEAP	\$JOBCTLJ	GG	OBR0DON	\$JOBCTL	JF
NOLINK	\$JOBCTLG	EP	OBR1DON	\$JOBCTL	JF
NOLIST	\$JOBCTLG	EM	OERRTN	\$JOBCTLA	BL
NOLOG	\$JOBCTLJ	GL	OLDPUB	\$JOBCTL	*
NOMPSS	\$JOBCTLG	EG			
NOMRJB	\$JOBCTL	CY			
NOPHDB	\$JOBCTLG	EH			
NOPSE	\$JOBCTLG	ED			
NOPTO	\$JOBCTL	JH			
NORAS	\$JOBCTLJ	GJ			
NOREL	\$JOBCTL	CM	ONLYOBR	\$JOBCTL	JF
	\$JOBCTLF	DP	ONOLOG	\$JOBCTLJ	GL
NORELSTW	\$JOBCTL	CM	OPAUSE	\$JOBCTLJ	GL
	\$JOBCTLF	DP	OPENRCO	\$JOBCTL	JJ
NORESET	\$JOBCTLJ	GF	OPENSET	\$JOBCTL	JJ
NORESTOR	\$JOBCTLG	EC			
NOSUBMSG	\$JOBCTL	JQ			
NOSYM	\$JOBCTLG	EM			
NOTAP	\$\$BLSTIO	LA			
NOTDSK	\$JOBCTLA	BJ			
NOTFG	\$JOBCTLN	KA			
NOTFG1	\$JOBCTLN	KB			
NOTINSYS	\$JOBCTL	JA	OPLBNF	\$JOBCTLG	FF
NOTLAST	\$JOBCTLN	KB		\$JOBCTLK	HP
NOTLIST	\$JOBCTLG	EE	OPLOG	\$JOBCTLJ	GL
NOTNINTY	\$JOBCTL	JN	OPNERR	\$JOBCTL	JN
NOTNOS	\$JOBCTLG	EL	OPNLNK	\$JOBCTLG	FA
NOTSEQ	\$JOBCTLK	HG			
NOUPDP	\$JOBCTL	JH			
NOXREF	\$JOBCTLG	EM			
NOZERO	\$JOBCTL	JN	OPNUMH	\$JOBCTLA	*
NO2314	\$JOBCTLA	BA			
NTINJB	\$JOBCTLA	BL		A 1-byte field which is the data byte	
NULCHK	\$JOBCTLG	GB		in a LOAD instruction. This byte	
NUMCON	\$JOBCTL	CM		maintains a parameter count by the	
	\$JOBCTLJ	GT		control statement processing routines.	
	\$JOBCTLK	HR		It is reset to zero following each	
				successful control statement read	
				operation.	
Subroutine: Converts EBCDIC character 0-9 to binary in WRKRG1.			OPTION	\$JOBCTLG	EL
NVAERR	\$JOBCTLJ	GU	OPTLOG	\$JOBCTLG	EP
NVOERR	\$JOBCTLG	FG	OPTNLG	\$JOBCTLG	EM
NVOPTN	\$JOBCTLG	EL	OTSERR	\$JOBCTLG	FG
NVSERR	\$JOBCTLA	BM		\$JOBCTLJ	GU
NXPBNT	\$JOBCTLJ	GR		\$JOBCTLK	HS
NXTBIT	\$JOBCTLJ	GK	OTSERR1	\$JOBCTLG	FG
NXTJIB	\$JOBCTLA	BD		\$JOBCTLJ	GU
NXTLUB	\$JOBCTLA	BD	OUTLBL	\$JOBCTLK	HH
NXTOPRD	\$JOBCTLJ	GN	OUTPUT	\$JOBCTL	CN
OBRCON	\$JOBCTL	JF	OUTPUTS	\$JOBCTL	CN
OBRKY0	\$JOBCTL	JF		\$JOBCTLF	DP
OBRKY0A	\$JOBCTL	JF	OUTPUT1	\$JOBCTL	CN
OBRKY1	\$JOBCTL	JF		\$JOBCTLF	DP
OBRONLY	\$JOBCTL	JE	OUTPUT2	\$JOBCTL	CN
OBRRCD	\$JOBCTL	JF		\$JOBCTLF	DP
OBREAD	\$JOBCTL	JM	OVRLP1	\$JOBCTLA	BJ
			OVRVCT	\$JOBCTLA	*
Subroutine: Issues read commands for the OBR portion of the recorder file.					

OBRSET        \$JOBCTL        JM  
 Subroutine: Sets up the I/O area for  
 OBR record formatting.

-----  
 \*Listing Only.

root phase. \$JOBCTLA initialized the base register, BASRG2, with this address each time it is loaded.

PACKAG	\$JOBCTLK	HH
PARSTD	\$JOBCTLG	EN
PARSTD1	\$JOBCTLG	EN
PASSRF	\$JOBCTLG	EB
PAUSE	\$JOBCTLJ	GL
PCHOUT	\$JOBCTLJ	GN
PCILSUP	\$JOBCTLG	EN
PCILSUPR	\$JOBCTLG	EN
PIKCMA	\$JOBCTLD	CU
PIKCPL	\$JOBCTLD	CU
PIKFLG	\$JOBCTLD	CU
PIKFLG7X	\$JOBCTLD	CU
PMB	\$JOBCTLM	JA
PNPERR	\$JOBCTLG	FG
	\$JOBCTLJ	GU
PRCNTCMP	\$JOBCTLM	JN
PRCNTFLL	\$JOBCTLM	JF
PREOPR	\$JOBCTLJ	GP
PRGUNT	\$JOBCTLJ	GS
PROGSW	\$JOBCTLD	*

BANK1, bit 5: If ON, specifies that the programmer LUBs are to be scanned.

- Set ON: LISTIO statement processor, ASSGN statement processor, RESET statement processor.
- Set OFF: INITL subroutine ASSGN statement processor.
- Controls processing in:
  1. LISTIO statement processor.
  2. ASSGN statement processor.

PSHRTN	\$\$BLSTIO	LD
Subroutine: Used by the LISTIO statement processor to build a header print line.		

PSHRTN1	\$\$BLSTIO	LD
PTR	\$JOBCTLA	BN
	\$JOBCTLG	CM
	\$JOBCTLF	DP
	\$JOBCTLG	EK
PUBMSK	\$JOBCTLG	CS
	\$JOBCTLF	DR
PUBNEXT	\$JOBCTLM	JD
PUIF	\$\$BLSTIO	LA
Subroutine: Used by the LISTIO and DVCDN statement processors. Extracts information from the PUB preparatory to building a print line.		

PUIFT3	\$\$BLSTIO	LA
PUIF1	\$\$BLSTIO	LA
PUIF4	\$\$BLSTIO	LA
PUIF5	\$\$BLSTIO	LA
QEND	\$JOBCTLG	EP
RANXJB	\$JOBCTLG	FD

RASCAN      \$JOBCTLG      FD  
 Subroutine: Scans program LUBs and sets assignment flags in assigned PUBs.

RASSGN	\$JOBCTLG	FD
RASSYS	\$JOBCTLM	JA
RDCID	\$JOBCTLG	EG
RDRIN	\$JOBCTLA	BG
RDSTM	\$JOBCTLA	BG
Root phase subroutine reads a statement from SYSRDR or SYSLOG. The job control switch, JBCSW0 (COMREG + 56) bit 0, is tested to determine if SYSRDR or SYSLOG is to be used. If SYSLOG is specified but it is not a 1052, the switch is changed to indicate SYSRDR and the subroutine is reentered via the control routine.		

RECORD      \$JOBCTLM      JP  
 RELEASE      \$JOBCTLJ      GA  
 RELJBC      \$JOBCTLA      BN  
 Root phase subroutine modifies address constants making Job Control relocatable.

RELLOP	\$JOBCTLG	CM
	\$JOBCTLF	DP
	\$JOBCTLG	EK
RELRG	\$JOBCTLG	EJ
RELTST	\$JOBCTLG	EJ
RESDN	\$JOBCTLA	BA
RESET	\$JOBCTLF	DF
	\$JOBCTLJ	GF
RESETCL	\$JOBCTLF	DF
RESETSW	\$JOBCTLG	*

BANK1, bit 7: If ON, specifies RESET in progress.

- Set ON: RESET statement processor
- Set OFF: INITL subroutine.
- Controls exit from the RSTSTD subroutine.

RESET01	\$JOBCTLF	DF
RESET1	\$JOBCTLF	DF
RESET101	\$JOBCTLF	DG
RESET11	\$JOBCTLF	DG
RESET2	\$JOBCTLF	DG
RESET3	\$JOBCTLF	DG
RESET4	\$JOBCTLF	DG
RESET5	\$JOBCTLF	DG
RESET8	\$JOBCTLF	DG
RESFCH	\$JOBCTLG	EF
RESTORE	\$JOBCTLG	EC
RETADD	\$JOBCTLF	DA
RETSW	\$JOBCTLG	*

BANK1, bit 3:

- Set ON: SFPPE subroutine. ASSGN statement processor.

-----  
 \*Listing Only.

- Set OFF: ASSGN statement processor.
- DVCDN statement processor INITL subroutine.
- Controls processing in the SFPPE subroutine.
  - Controls processing in the GETJIB subroutine.

RETURN	\$JOBCTLN	KB
RFEND	\$JOBCTLJ	GJ
RFNO	\$JOBCTLJ	GJ
RFSEERR	\$JOBCTLM	JN
RFSET	\$JOBCTLJ	GJ
RFYES	\$JOBCTLJ	GJ
RLINDT	\$JOBCTLA	BG
RLNCNT	\$JOBCTLA	*

Save area for SYSLST current line count.

RLSENT	\$JOBCTLJ	GA
RMR	\$JOBCTLD	CK
RNAERR	\$JOBCTLA	BL
RNAME	\$JOBCTLG	*

An 8-byte area in partition register save area used to save job name.

RNGTOP	\$JOBCTLD	CM
	\$JOBCTLK	HR
ROD	\$JOBCTLM	JH
RODCMP	\$JOBCTLM	JH
RODERR	\$JOBCTLM	JH
RODSCAN	\$JOBCTLM	JH
RSPASG	\$JOBCTLG	FB
RSPPAE1	\$JOBCTLJ	GR
RSPPEA	\$JOBCTLJ	GR
RSSASG	\$JOBCTLG	EA
RSSASG1	\$JOBCTLG	EA
RSSASG2	\$JOBCTLG	EB
RSTASG	\$JOBCTLG	FB

Subroutine: Restores system and programmer assignments to standard.

RSTCOM	\$JOBCTLG	EB
RSTRTR	\$JOBCTLK	HQ
RSTSTD	\$JOBCTLD	CR

Subroutine: Restores a LUB to its standard I/O assignment or unassigns a nonstandard assignment.

RSTSTD1	\$JOBCTLD	CR
RSTSTD3	\$JOBCTLD	CR
RSTSTD4	\$JOBCTLD	CR
RSTSTD5	\$JOBCTLD	CR
RSTSWS4	\$JOBCTLG	EA
SAMEDIB	\$JOBCTLD	CJ
SAMELUB	\$JOBCTLD	CJ
SCANR1	\$JOBCTLA	BF

Root phase subroutine scans a control statement and makes a parameter available for processing.

- A comma, blank, or equal sign ends the scan.

- Register POINT1 contains the address of the 1st character of the parameter.
- Register POINT2 contains the number of characters remaining to be scanned.
- Register POINT3 contains a count of the number of characters in the parameter. This character count is 1 less than the actual character count and is used to control the character count in move, and compare instructions.
- Between entries into the subroutine, registers POINT1 and POINT2 are saved in an area labeled TMPAR1.
- The entry SCANR1, when it is desired, scans for the operation field.
- The entry SCANR2 scans for prime operands.
- The entry SCANR3 scans for the parameter of a prime operand.

#### Example:

SET RCLST = 2000, LINCNT = 99,...

SCANR2	\$JOBCTLA	BF
SCANR3	\$JOBCTLA	BF
SCNINT	\$JOBCTLG	FB

Subroutine: Initializes for LUB and PUB table scan.

SCNJIB	\$JOBCTLD	CV
--------	-----------	----

Subroutine: Computes address of a JIB in register POINT3. The stored LUB of the JIB is moved to the location JIBCHN.

SCNLUB	\$JOBCTLD	CV
--------	-----------	----

Subroutine: Makes all LUBs of a class available one at a time. The current LUB is made available in the location JIBCHN. The address of the next LUB is saved in the location SLADD. The residual number of LUBs (in a class) is saved in the location SNICL.

SCNRLL1	\$JOBCTLA	BF
SCNRLL2	\$JOBCTLA	BF
	\$JOBCTLK	HD
SDRREAD	\$JOBCTLM	JM

Subroutine: Issues read commands for the SDR portion of the recorder file.

SDRSRCH	\$JOBCTLM	JD
SDRUNRC	\$JOBCTLM	JD
SDRUPDT	\$JOBCTLM	JK

Subroutine: Updates the seek address when searching for an available entry

in the SDR portion of the recorder file.			SIMEND	\$JOBCTLG	EA
SDRUPGO	\$JOBCTLM	JK	SIMEOJ	\$JOBCTLN	KA
SEOJBACK	\$JOBCTLN	KB	SIMRET	\$JOBCTLG	EB
SDRWRT	\$JOBCTLM	JB	SJBKSWCH	\$JOBCTLN	KB
SELRLD	\$JOBCTLA	BN	SKIPLN	\$JOBCTLD	CQ
SET	\$JOBCTLJ	GG	SLADD	\$JOBCTLD	*
SETEXT	\$JOBCTLJ	GG			
SETFLAGS	\$JOBCTLK	HF			
SETLOD	\$JOBCTLG	EF			
SETOUTPT	\$JOBCTLJ	GD			
SETWRT	\$JOBCTLA	BJ	SLINCT	\$JOBCTLJ	GG
SETYES	\$JOBCTLJ	GJ	SPCEXC	\$JOBCTLA	BJ
SEUOB	\$\$BLSTIO	LC	SRCHSDR	\$JOBCTLM	JE
	Subroutine: Used by the LISTIO statement processor to build a print line in the output area labeled BUFFER.		SSIDRTRN	\$JOBCTLM	JQ
SEUOB2	\$\$BLSTIO	LC	SSLLOOP	\$JOBCTLM	JQ
SFNC	\$JOBCTLD	CY	STDASGN	\$JOBCTLG	FC
SFPPE	\$JOBCTLD	CS	STDFDSW	\$JOBCTLD	*
	Subroutine: Used by DVCDN Statement processor to unassign LUBs and JIBs attached to DOWNed device.				
SFPPE01	\$JOBCTLD	CS			
	\$JOBCTLF	DR			
SFPPE02	\$JOBCTLD	CS	STDLBL	\$JOBCTLG	EN
	\$JOBCTLF	DR	STILOK	\$JOBCTLG	EP
SFPPE03	\$JOBCTLD	CU	STLLMT	\$JOBCTLJ	GQ
	\$JOBCTLF	DR	STLNKA	\$JOBCTLG	EG
SFPPE1	\$JOBCTLD	CS	STOP	\$JOBCTLJ	GM
	\$JOBCTLF	DR	STORBACK	\$JOBCTLG	EC
SFPPE10	\$JOBCTLF	DS	STSCLB	\$JOBCTLG	FD
SFPPE11	\$JOBCTLF	DS	STSRWR	\$JOBCTLG	EH
SFPPE12	\$JOBCTLF	DS	STUCRL	\$JOBCTLF	DQ
SFPPE13	\$JOBCTLF	DS			
SFPPE14	\$JOBCTLF	DS			
SFPPE15	\$JOBCTLF	DS			
SFPPE16	\$JOBCTLF	DS			
SFPPE17	\$JOBCTLF	DS			
SFPPE18	\$JOBCTLF	DS			
SFPPE19	\$JOBCTLF	DS			
SFPPE2	\$JOBCTLD	CS			
	\$JOBCTLF	DR			
SFPPE201	\$JOBCTLF	DS			
SFPPE3	\$JOBCTLD	CU			
	\$JOBCTLF	DR			
SFPPE4	\$JOBCTLD	CS			
	\$JOBCTLF	DR			
SFPPE5	\$JOBCTLD	CS			
	\$JOBCTLF	DR			
SFPPE5A	\$JOBCTLD	CS			
	\$JOBCTLF	DR			
SFPPE6	\$JOBCTLD	CS			
	\$JOBCTLF	DR			
SFPPE7	\$JOBCTLD	CS			
	\$JOBCTLF	DR			
SFPPE8	\$JOBCTLD	CT			
	\$JOBCTLF	DS			
SFPPE8A	\$JOBCTLD	CT			
	\$JOBCTLF	DS			
SFPPE8B	\$JOBCTLF	DS			
SFPPE9	\$JOBCTLD	CT			
	\$JOBCTLF	DS			

\*Listing Only

SVCBTRNS      \$JOBCTLF      DA  
 Subroutine: Fetches the B-transient  
 \$\$BLSTIO. The register LINKR2 returns  
 control to the calling sequence from  
 the B-transient.

SWBACK	\$JOBCTLN	KB
SXTPOK	\$JOBCTLK	HG
SYM	\$JOBCTLG	EM
SYSDATE	\$JOBCTLJ	GG
SYSPRM	\$JOBCTLG	EP
SYSRF	\$JOBCTLJ	GJ
SYSTABLE	\$JOBCTLF	*

Beginning of logical unit system table used to determine logical unit type and LUB table displacement.

SYSUPI	\$JOBCTLJ	GG
SYSUPI1	\$JOBCTLJ	GK
SYSUTB	\$JOBCTLJ	*

Beginning of table of LIOCS system units.

SYSXXX	\$JOBCTLD	CW
--------	-----------	----

Subroutine: Converts a logical unit designated as SYSXXX, into:

1. Symbolic unit address (class and order) in the location CLOARD.
2. LUB address in the location LUBAD.
3. PUB pointer of the PUB currently assigned to this logical unit in the location OLDPUB.
4. The internal representation of device type in location NEWTYP.

SYSXXX01	\$JOBCTLD	CW
SYSXXX1	\$JOBCTLD	CW
SYSXXX2	\$JOBCTLD	CW
SYSXXX3	\$JOBCTLD	CW
SYSXXX4	\$JOBCTLD	CW
SYSXXX5	\$JOBCTLD	CW
SYSXXX6	\$JOBCTLD	CW
TAPINT	\$JOBCTLA	BH
TBLADR	\$JOBCTLA	*

Label of the Phase-Vector Table contained in the root phase (\$JOBCTLA). This table is used to determine the correct phase and processing routine required to process a given control statement.

The operation field of the control statement is compared to each entry in the table until an equal is found. The equal entry identifies the correct phase and the displacement within the phase of the branch instruction that directs the program to the correct processing routine. The entry also contains a 1-byte condition switch

-----  
 \*Listing Only.

bank used to control processing for format verification, logging conventions, and cancel procedures for the statement. Figure 8 shows the format of an entry in the Phase-Vector Table.

Byte	0	6 7	8	9
	Operation Field	Condition Switches	Branch Vector Displacement	Phase Identification Letter

Figure 8. Phase-Vector Table Entry Format

Operation Field: EBCDIC representation of the operation field.

Condition Switches:

Bit 0 - reserved.

1 - statement is to be processed even though a cancel condition exists.

2,3 - Both on; suppress logging.  
 2 off, 3 on; unconditional  
 SYSLOG logging and  
 conditional SYSLST logging.  
 Both off; conditional  
 logging on SYSLOG and  
 SYSLST.

4 - statement may start with //.

5 - statement may start without //.

6 - statement may start in column 1.

7 - statement may start in other than column 1.

Branch Vector Displacement:

Displacement within the phase that is added to the phase origin address to develop the address of a branch instruction which transfers control to the correct processing routine.

Phase Identification Letter: Contains the EBCDIC character A, D, F, G, J, K, M or N and identifies the job control phase containing the processing routine.

Example of the JOB control statement entry:

DC CL7'JOB'

DC X'7A'

DC AL1(12)

DC C'G'

1. The JOB statement is to be processed even if a cancel is being executed.
2. Logging on both SYSLOG and SYSLST is suppressed.
3. The statement may not start without // and may not start in other than column one.
4. The branch-vector table entry is located at a displacement of 12 bytes from the beginning of the phase with suffix 'G' (\$JOBCTLG).

TBNHDR	\$JOBCTLG	EE
TBNXPB	\$JOBCTLG	EE
TBPBLP	\$JOBCTLG	EE
TEBHDR	\$JOBCTLG	EE
TEBLOP	\$JOBCTLG	EE
TEBS	\$JOBCTLG	EE
TEST	\$JOBCTLN	KA
TESTOP	\$JOBCTLM	JA
TEST1	\$JOBCTLF	DE
TEST2	\$JOBCTLF	DE
TFILL	\$JOBCTLK	HA
TIAERR	\$JOBCTLD	CY
TIME	\$JOBCTLA	*

An 8-byte area used to contain system timer and system time of day, loaded by \$JOBCTLA initialization from Hex locations 50-57.

TIMOUT	\$JOBCTLG	FA
Subroutine: Used by the JOB and EOJ statement processors to log the time of day.		

TIMOUT1	\$JOBCTLG	FA
TIMOUT2	\$JOBCTLG	FA
TLBL	\$JOBCTLK	HB
TMPSW	\$JOBCTLD	*

BANK1+1, bit 6: If ON, specifies that the assignment is temporary.

- Set ON: ASSGN statement processor.
- Set OFF: INITL subroutine.
- Controls processing in the ASSGN statement processor.

TNVSERR	\$JOBCTLF	DA
TOOMANY	\$JOBCTLJ	GN
TPCK	\$\$BLISTIO	LA
TPLAB	\$JOBCTLK	HA
TPLEND	\$JOBCTLK	HA
TRANSW	\$JOBCTLD	*

BANK1+1, bit 1: If ON, specifies that the Job Control transient, \$\$BLISTIO,

has been loaded in the B-transient area.

- Set ON: SVCBTRNS subroutine.
- Set OFF: LISTIO statement processor, INITL subroutine.
- Controls processing in:
  1. SFPPE subroutine
  2. ERRRTN common error routine.

TRKCLC	\$JOBCTLM	JA
TRKCMP	\$JOBCTLM	JA
TRKEXH	\$JOBCTLM	JA
TRKG0	\$JOBCTLM	JA
TRKIN	\$JOBCTLM	JA
TRKLST	\$JOBCTLM	JA
TRKOBR	\$JOBCTLM	JB
TRKOUT	\$JOBCTLM	JA
TRKSET	\$JOBCTLM	JJ
TSLNKSW	\$JOBCTLJ	GD
TSTYPE	\$JOBCTLA	BC
TXCUU	\$JOBCTLD	CP
	\$JOBCTLF	DP
	\$JOBCTLJ	GS

Subroutine: Converts the operand X'CUU' from hex to binary.

- PUB address is saved in register POINT4.
- PUB pointer is computed in register WRKRG3.
- Device type (from the PUB) is saved in the location DVCTYP.

TXCUU1	\$JOBCTLD	CP
	\$JOBCTLF	DP
	\$JOBCTLJ	CY
TXCUU1+8	\$JOBCTLD	CP
TXCUU2	\$JOBCTLD	CP
	\$JOBCTLF	DP
	\$JOBCTLJ	GS
TXCUU3	\$JOBCTLD	CP
	\$JOBCTLF	DP
TXCUU4	\$JOBCTLD	CP
	\$JOBCTLF	DP
T2311	\$JOBCTLM	JH
UA	\$JOBCTLF	DE
UADN1	\$JOBCTLF	DE
UADN2	\$JOBCTLF	DE
UADN3	\$JOBCTLF	DE
UADN4	\$JOBCTLF	DE
UADN5	\$JOBCTLF	DE
UADN6	\$JOBCTLF	DE
UALUB	\$JOBCTLF	DJ
UALUB1	\$JOBCTLF	DJ
UANXJB	\$JOBCTLG	FC
UASCAN	\$JOBCTLG	FC

Subroutine: Restores program LUBs to standard assignments. Detaches program JIBs and places them in free list. Resets program ownership flags in PUBs.

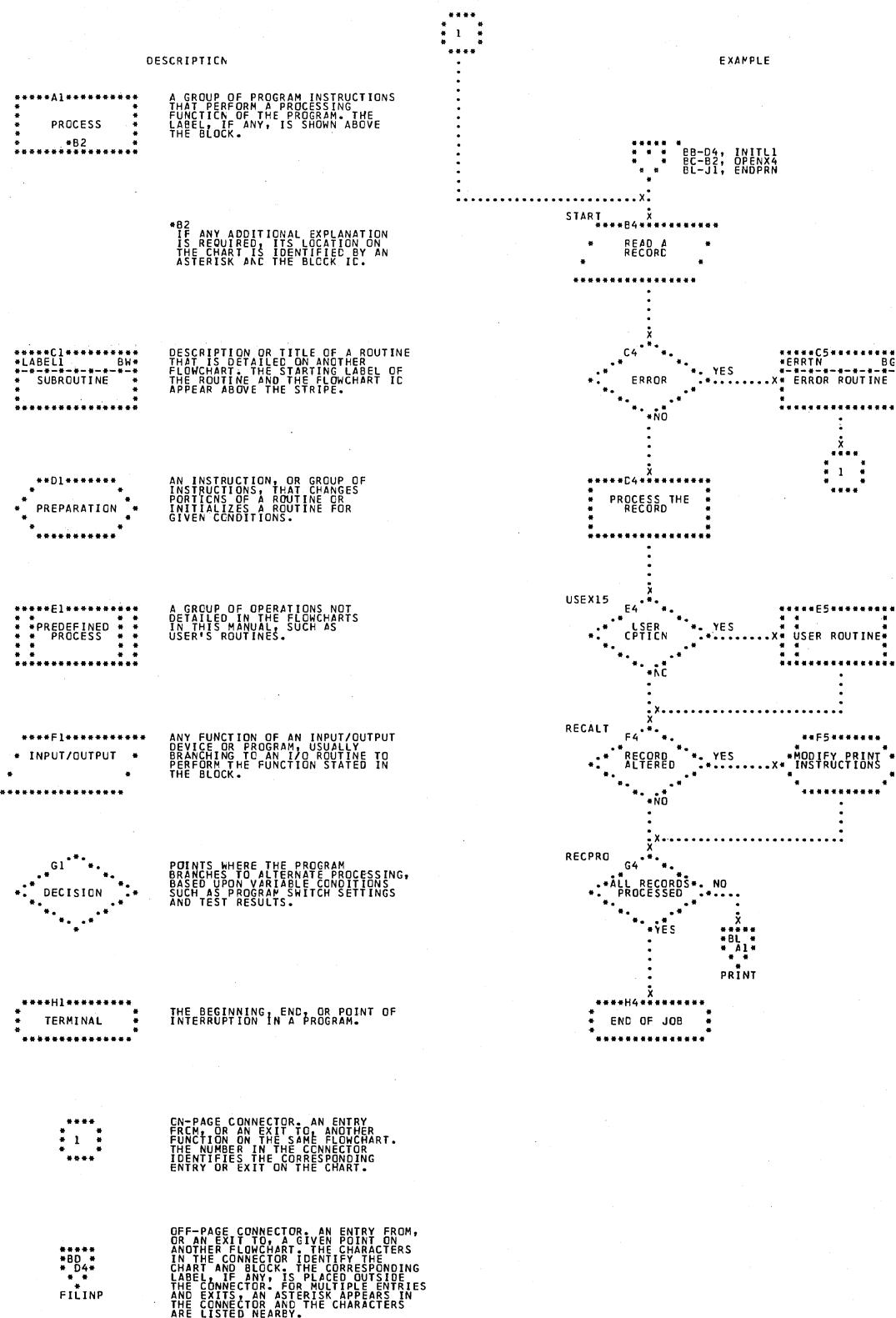
\*Listing Only.

UASTD	\$JOBCTLF	DJ	UPDHDR	\$JOBCTLA	BJ
UCS	\$JOBCTLJ	GB	UPDOPT	\$JOBCTLJ	GD
UCSDN	\$JOBCTLJ	GC	UPDSAV	\$JOBCTLJ	GT
UCSSCN	\$JOBCTLJ	GC		Subroutine: Moves data from input buffer to SYSLNK output area.	
UCS1	\$JOBCTLJ	GB		Maintains a block count that is used for blocking records.	
UCS2	\$JOBCTLJ	GB	UPDTHDR	\$JOBCTLM	JK
UCS3	\$JOBCTLJ	GC		Subroutine: At completion of SDR and/or OBR processing, outputs an updated header record onto the recorder file.	
UCS4	\$JOBCTLJ	GC	UPDTMOVE	\$JOBCTLM	JP
UNA	\$JOBCTLF	DM	UPDTOBR	\$JOBCTLM	JK
UNAB	\$JOBCTLF	DM	UPRITE	\$JOBCTLM	JL
UNAE	\$JOBCTLF	DM		Subroutine: After each write, updates the disk address for the next write.	
UNANENT	\$JOBCTLD	CU	UPR1	\$JOBCTLM	JL
UNASGN	\$JOBCTLG	FC	UPSI	\$JOBCTLJ	GK
UNASSGN	\$JOBCTLD	CK	UPSICH	\$JOBCTLJ	GK
UNA1	\$JOBCTLF	DM	USEROR	\$JOBCTLN	KA
UNA1A	\$JOBCTLF	DM	USRLBL	\$JOBCTLG	EN
UNA2	\$JOBCTLF	DN	USRLBL1	\$JOBCTLG	EN
UNA2A	\$JOBCTLF	DN	VMBUCK	\$JOBCTLJ	GN
UNA2AA	\$JOBCTLF	DN	VMEND	\$JOBCTLJ	GN
UNA2B	\$JOBCTLF	DN	VMRTN	\$JOBCTLJ	GN
UNBLKD	\$JOBCTLJ	GD	VOL	\$JOBCTLK	HA
UNCLOG	\$JOBCTLA	BC	WAITERR	\$JOBCTLA	BL
UNCU	\$JOBCTLF	DC	WAITR	\$JOBCTLJ	GM
UNITS	\$JOBCTLF	DC	WRITE	\$JOBCTLM	JL
UNITS1	\$JOBCTLF	DC		Subroutine: Issues all write commands to the recorder file.	
UNITS2	\$JOBCTLF	DC	WRTPHD	\$JOBCTLG	EH
UNITS3	\$JOBCTLF	DC		Subroutine: Used by the EXEC statement processor to write the phase directory.	
UNITS401	\$JOBCTLF	DC	WRTPHDR	\$JOBCTLG	EH
UNITS402	\$JOBCTLF	DC	XREF	\$JOBCTLG	EM
UNITS5	\$JOBCTLF	DC	XTENT	\$JOBCTLK	HG
UNITS501	\$JOBCTLF	DC	XTOP12	\$JOBCTLK	HM
UNITS6	\$JOBCTLF	DC		Subroutine: Checks, converts, and stores extent type and sequence number in label output area.	
UNITS7	\$JOBCTLF	DC	XTOP12A	\$JOBCTLK	HM
UNITS8	\$JOBCTLF	DC	XTOP12B	\$JOBCTLK	HM
UNITS9	\$JOBCTLF	DC	XTOP3	\$JOBCTLK	HG
UNPA	\$JOBCTLD	CU	XTOP34	\$JOBCTLK	HM
Entry into the SFPPE subroutine to unassign a standard assignment. This entry is used by the ASSGN routine and the RSTSTD subroutine.				Subroutine: Checks, converts, and stores lower and upper extent limits in the label output area.	
UNPA1	\$JOBCTLD	CU	XTOP5	\$JOBCTLK	HH
UNPA3	\$JOBCTLD	CU	XTOUT	\$JOBCTLK	HH
UNRCSDR	\$JOBCTLM	JE	XTUNIT	\$JOBCTLK	HH
UNRERR	\$JOBCTLM	JN	YDISK	\$JOBCTLD	CE
UNRERR1	\$JOBCTLM	JN	YESOPT	\$JOBCTLM	JD
UNRERR2	\$JOBCTLM	JN	ZRMVDN	\$JOBCTLA	BE
UNSW	\$JOBCTL	*	ZRMVLP	\$JOBCTLA	BE
BANK1+1, bit 5: If ON, specifies a single unit is to be listed.					
<ul style="list-style-type: none"> <li>• Set ON: LISTIO statement processor when performing a LIOCUU function.</li> <li>• Set OFF: INITL subroutine.</li> <li>• Controls processing in the LISTIO statement processor.</li> </ul>					
UNTFND	\$JOBCTLJ	GS			
UPDATE	\$JOBCTLM	JK			
<hr/> *Listing Only.					

APPENDIX B: ERROR MESSAGE CROSS REFERENCE

<u>Message</u>	<u>Phase</u>	<u>Chart ID</u>			
0I00A	\$\$A\$IPL2	AB	1A9ND	\$JOBCTL	CE
0I01A	\$\$A\$IPL2	AD	1C00A	\$JOBCTLA	BL
0I10A	\$IPLRT2	AK	1C10A	\$JOBCTLA	BL
0I11I	\$IPLRT2	AQ	1C30A	\$JOBCTLJ	GD
0I12I	\$IPLRT3	AT	1C3NI	\$JOBCTLG	FG
0I13I	\$IPLRT3	AR	1C70D	\$JOBCTLA	BE
0I14I	\$IPLRT3	AV	1C80D	\$JOBCTLA	BL
0I15I	\$IPLRT3	AR	1I00A	\$JOBCTLA	BB
0I16A	\$IPLRT4	AX	1I50I	\$JOBCTLA	BL
0I17A	\$IPLRT4	AX	1I70I	\$JOBCTLA	BM
0I18A	\$IPLRT2	AK	1I83A	\$JOBCTL	JB
0I20I	\$IPLRT4	AY	1I84A	\$JOBCTL	JA, JJ
0I22I	\$IPLRT4	AZC	1I85A	\$JOBCTL	JD
0I23I	\$IPLRT4	AZ	1I86A	\$JOBCTL	JD, JF, JG, JM
0I24A	\$IPLRT3	AU	1I87A	\$JOBCTL	JE
0I25I	\$IPLRT4	AY	1I88I	\$JOBCTLJ	GJ
1A0ND	\$JOBCTL	CC, CD, CE, CG, CH, CJ	1I89I	\$JOBCTL	JP, JQ
1A1ND	\$JOBCTL	CA, CB	1I90D	\$JOBCTL	JP
1A2ND	\$JOBCTL	CA, CB, CH, CL, CP	1I91D	\$JOBCTL	JQ
1A2ND	\$JOBCTL	CL, CP	1I92D	\$JOBCTL	JQ
1A20D	\$JOBCTLG	FB	1I93I	\$JOBCTL	JN
	\$JOBCTLJ	GB, GE	1L00D	\$JOBCTLK	HF, HG, HJ, HK, HL, HM
1A3ND	\$JOBCTL	CG, CR	1L10D	\$JOBCTLG	FE
1A4ND	\$JOBCTL	CA, CL, CH, CW		\$JOBCTLK	HG, HJ, HP
	\$JOBCTLF	DD, DF	1P00D	\$JOBCTLG	GQ
1A40D	\$JOBCTLJ	GS	1S00A	\$JOBCTLA	See BM-B1
	\$JOBCTLK	HQ		through	
1A5ND	\$JOBCTL	CP		\$JOBCTL	
	\$JOBCTLF	DP	1S00D	\$JOBCTLG	FG
1A50D	\$JOBCTLJ	GS		\$JOBCTL	
1A6ND	\$JOBCTL	CA	1IS03I	\$JOBCTLJ	JH
	\$JOBCTLF	DM	1S10D	\$JOBCTLG	GN
1A7ND	\$JOBCTL	CA		\$JOBCTLJ	EF, EN, FE
	\$JOBCTLF	DK		\$JOBCTL	GD, GN
1A70D	\$JOBCTLJ	GE		\$JOBCTLK	HA, HC, HF, HG, HJ, HK, HP
1A90D	\$JOBCTLF	DL			

## APPENDIX C: EXPLANATION OF FLOWCHART SYMBOLS



**APPENDIX D: SAMPLE LISTIO PRINTOUTS**

<b>1</b> <pre>// LISTIO SYS *** BACKGROUND *** I/O UNIT CMNT CHNL UNIT MODE SYSRDR 0 OC SYSIPT 0 OC SYSPCH 0 OD SYSLST 1 OA SYSLOG 0 1F SYSLNK 1 91 SYSRES 1 92 SYSSLB ** UA ** SYSRLB ** UA ** SYSREC 1 91</pre>	<b>4</b> <pre>// LISTIO ALL *** BACKGROUND *** I/O UNIT CMNT CHNL UNIT MODE SYSRDR 0 OC SYSIPT 0 OC SYSPCH 0 OD SYSLST 1 OA SYSLOG 0 1F SYSLNK 1 91 SYSRES 1 92 SYSSLB ** UA ** SYSRLB ** UA ** SYSREC 1 91</pre>	<b>*** FOREGROUND 1 ***</b> <table border="1"> <thead> <tr> <th>I/O UNIT</th> <th>CMNT</th> <th>CHNL</th> <th>UNIT</th> <th>MODE</th> </tr> </thead> <tbody> <tr><td>SYS000</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS001</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS002</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS003</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS004</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS005</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS006</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS007</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS008</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS009</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS010</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS011</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS012</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS013</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS014</td><td></td><td></td><td>** UA **</td><td></td></tr> <tr><td>SYS015</td><td></td><td></td><td>** UA **</td><td></td></tr> </tbody> </table>	I/O UNIT	CMNT	CHNL	UNIT	MODE	SYS000			** UA **		SYS001			** UA **		SYS002			** UA **		SYS003			** UA **		SYS004			** UA **		SYS005			** UA **		SYS006			** UA **		SYS007			** UA **		SYS008			** UA **		SYS009			** UA **		SYS010			** UA **		SYS011			** UA **		SYS012			** UA **		SYS013			** UA **		SYS014			** UA **		SYS015			** UA **	
I/O UNIT	CMNT	CHNL	UNIT	MODE																																																																																			
SYS000			** UA **																																																																																				
SYS001			** UA **																																																																																				
SYS002			** UA **																																																																																				
SYS003			** UA **																																																																																				
SYS004			** UA **																																																																																				
SYS005			** UA **																																																																																				
SYS006			** UA **																																																																																				
SYS007			** UA **																																																																																				
SYS008			** UA **																																																																																				
SYS009			** UA **																																																																																				
SYS010			** UA **																																																																																				
SYS011			** UA **																																																																																				
SYS012			** UA **																																																																																				
SYS013			** UA **																																																																																				
SYS014			** UA **																																																																																				
SYS015			** UA **																																																																																				
<b>2</b> <pre>// LISTIO PROG *** BACKGROUND *** I/O UNIT CMNT CHNL UNIT MODE SYS000 0 91 SYS001 0 91 SYS002 0 91 SYS003 0 91 SYS004 ** UA ** SYS005 ** UA **</pre>	<b>5</b> <pre>// LISTIO SYSRDR *** BACKGROUND *** I/O UNIT CMNT CHNL UNIT MODE SYSRDR 0 OC</pre>	<b>6</b> <pre>// LISTIO UNITS CHNL UNIT OWNER I/O UNIT CMNT MOD 0 OC BG SYSRDR 0 OC BG SYSIPT 0 OD BG SYSPCH 0 OE * UA * 0 1F BG SYSLOG 0 1F BG SYSIN 1 OA BG SYSLST 1 90 * UA * SYSRDR ** UA ** SYSIPT ** UA ** SYSPCH ** UA ** SYSLST ** UA ** SYSLOG ** UA ** SYSLNK ** UA ** SYSRES 1 92 SYSSLB ** UA ** SYSRLB ** UA ** SYSREC 1 91</pre>																																																																																					
<b>3</b> <pre>// LISTIO F2 *** FOREGROUND 2 *** I/O UNIT CMNT CHNL UNIT MODE SYSRDR ** UA ** SYSIPT ** UA ** SYSPCH ** UA ** SYSLST ** UA ** SYSLOG ** UA ** SYSLNK ** UA ** SYSRES 1 92 SYSSLB ** UA ** SYSRLB ** UA ** SYSREC 1 91</pre>	<b>7</b> <pre>// LISTIO UA *** UNASSIGNED *** CHNL UNIT 1 80 1 81 1 82 1 83 1 84</pre>	<b>8</b> <pre>// LISTIO DOWN *** DOWN *** CHNL UNIT ** NONE **</pre>																																																																																					
<b>1</b> <p>Note: The 1st line of each sample shows the control statement as it was logged by job control.</p> <ol style="list-style-type: none"> <li>1. List all system units.</li> <li>2. List all background programmer units.</li> <li>3. List all foreground 2 units.</li> <li>4. List all units.</li> <li>5. List a specific unit (SYSXX).</li> <li>6. List the logical units assigned to all physical devices.</li> <li>7. List all unassigned units.</li> <li>8. List all down units.</li> <li>9. List all logical units assigned to a specified physical unit.</li> </ol>	<b>9</b> <pre>// LISTIO X@01F@</pre>	<table border="1"> <thead> <tr> <th>CHNL</th> <th>UNIT</th> <th>OWNER</th> <th>I/O UNIT</th> <th>CMNT</th> <th>MODE</th> </tr> </thead> <tbody> <tr><td>0</td><td>1F</td><td>BG</td><td>SYSLOG</td><td></td><td></td></tr> </tbody> </table>	CHNL	UNIT	OWNER	I/O UNIT	CMNT	MODE	0	1F	BG	SYSLOG																																																																											
CHNL	UNIT	OWNER	I/O UNIT	CMNT	MODE																																																																																		
0	1F	BG	SYSLOG																																																																																				

Figure 9. Sample LISTIO Printouts

APPENDIX E: COMREG AND CCB

COMREG*											
Displacement hexadecimal	0	8	0A	0C	17	18	20	24	28	2C	
Displacement decimal	0	8	10	12	23	24	32	36	40	44	
	Date	Address of PPBEG	Address of EOSSP	Problem Program Use	UPSI Byte	Job Name	Highest Storage Address of the Partition	End Address of Last Phase Fetched or Loaded	Address of Uppermost Byte of Phase with Highest Ending Address	Label Area Length	
	XXXXXXXXXX	XX	XX	XXXXXXXXXXXXXX	X	XXXXXXXXXX	XXXX	XXXX	XXXX	XX	
Displacement hexadecimal	2E	30	34	35	36	37	38	39	3A	3B	3C
Displacement decimal	46	48	52	53	54	55	56	57	58	59	60
	PIK (PID)	End of Storage Address	Machine Config. Byte	System Config. Byte	Standard Language Translator I/O Options	Dump, Log and ASCII Options	Job Control Byte	Linkage Control Byte	Language Translator Control Byte	Job Duration Indicator Byte	Disk Address of Label Cylinder
	XX	XXXX	X	X	X	X	X	X	X	XX	XX
Job Control Switches											
Displacement hexadecimal	40	42	44	46	48	4A	4C	4E	4F	58	5A
Displacement decimal	64	66	68	70	72	74	76	78	79	88	90
	Address of PUB	Address of FAVP	Address of JIB	Address of TEB	Address of FICL	Address of NICL	Address of LUB	Line Count for SYSLST	System Date	LIOCS Comm. Bytes	Address of 1st Part of PIB Table
	XX	XX	XX	XX	XX	XX	XX	X	XXXXXXXXXX	XX	XX
Displacement hexadecimal	5E	60	62	64	66	68	6A	6C	6E		
Displacement decimal	94	96	98	100	102	104	106	108	110		
	Length of LUB ID Queue = No. of Channel Queue Entries	Address of Disk Information Block (DIB)	Address of Error Recovery Block	Address of PC Option Table less 8 bytes	Address of IT Option Table less 8 bytes	Address of OC Option Table less 8 bytes	Key of Program with Timer Support	Address of the LUBID Queue	Logical Transient Key		
	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	
Displacement hexadecimal	70	7C	7E	80	84	86	87	88			
Displacement decimal	112	124	126	128	132	134	135	136			
	Supervisor Constants	Address of 2nd Part of PIB Table	Address of MICR DTF Table (PDTABB)	Address of QTAM Vector Table	Address of BG Comm. Region	Op- tion Indi- cator	System Con- figura- tion Byte 2	Pointer to Comm. Region Extension			
	XXXXXXXXXXXXXX	XX	XX	XXXX	XX	X	X	XXXX			

\* The address of the communications region is in fixed location X'14' - X'17'.

Displacement values illustrated can be used to access the listing and/or the key that follows the figure.  
The key offers more detailed information about each area when necessary.

Figure 10. Supervisor Communications Region (Part 1 of 5)

Key to Communications Region Displacements:

- [0] MM/DD/YY or DD/MM/YY obtained from the job control date statement. Format controlled by COMREG + 53 (System Configuration Byte, date convention bit 0).
- [8] Address of the problem program area.
- [10] Address of the beginning of the problem program area. Y (EOSSP)=Y (PPBEG) if the storage protection option has not been selected. Y (EOSSP) equals the first main storage location with a storage protection key of 1, if storage protection is supported.
- [12] User area. If seek separation option is specified, bytes 12 and 13 are used at IPL time for the address of the seek address block.
- [23] User program switch indicator.
- [24] Job name set by the job control program from information found in the job statement.
- [32] Address of the uppermost byte of the problem program area as determined by the IPL program (Clear storage routine determines the address, ENDRD routine of \$\$A\$IPL2 stores it.), or the address of the uppermost byte of the partition as determined during processing of the ALLOC statement.
- [36] Address of the uppermost byte of the last phase of the problem program fetched or loaded. The initial value (as shown) is overlaid by the first fetch or load to the problem program area.
- [40] Highest ending main-storage address of the phase among all the phases having the same first four characters as the operand on the EXEC statement. For the background partition only, job control builds a phase directory of these phases. The address value may be incorrect if the program loads any of these phases above its link-edited origin address. If the EXEC statement has no operand, job control places in this location the ending address of the program just link-edited.
- [44] Length of the problem program label area.
- [46] Program Interrupt Key - PIK (if asynchronous processing is not supported): Value is equal to the displacement from the start of the PIB table to the PIB for the task.  
OR  
 Partition Identifier - PID (if asynchronous processing is supported): Value is hex 10, 20, or 30 to identify the partition in which a maintask or a subtask is running. (See the communications region extension, displacement 18, for the PIK in an asynchronous processing supervisor.)
  - First byte - always zero.
  - Second byte - contains the key of the program that was last enabled for interrupts, or the partition identifier in an AP supervisor.

Task	PIK (PID) Value
*All Bound	X'00'
BG	X'10'
*F2	X'20'
*F1	X'30'
Attn Rtn	X'40'
Quiesce I/O	X'50'
Supervisor	X'60'

\*These tasks do not exist in a non-MPS supervisor.
- [48] Logical end of main storage address.

Figure 10. Supervisor Communications Region (Part 2 of 5)

Key to Communications Region Displacements:

52

Machine Configuration Byte (Values set at supervisor generation time.)

- Bit 0: 1 = Storage protect feature  
0 = No storage protect feature
- 1: 1 = Decimal feature  
0 = No decimal feature
- 2: 1 = Floating-point feature  
0 = No floating-point feature
- 3: 1 = Physical transient overlap option  
0 = No physical transient overlap option
- 4: 1 = Timer feature  
0 = No timer feature
- 5: 1 = Channel switching device  
0 = No channel switching device
- 6: 1 = Burst mode on multiplex channel support  
0 = No burst mode on multiplex channel support
- 7: Reserved

53

System Configuration Byte

- Bit 0: 1 = DDMMYYJJ      (Date convention bit set at generation time by STDJC)  
0 = MMDDYYJJ
- 1: 1 = Multiprogramming environment  
0 = Batch job environment
- 2: 1 = DASD file-protect supported  
0 = No file-protect support for DASD
- 3: 1 = DASD SYSIN - SYSOUT  
0 = No DASD SYSIN - SYSOUT
- 4: 1 = Teleprocessing  
0 = No teleprocessing
- 5: 1 = Batch job in foreground  
0 = No BJF
- 6: 1 = Asynchronous processing  
0 = No AP
- 7: 1 = Track Hold  
0 = No Track Hold

54

This byte contains the standard language translator I/O options (set by the STDJC macro).

- Bit 0: DECK option      1 = yes, output object modules on SYSPCH
- 1: LIST option      1 = yes, output source module listings and diagnostics on SYSLST
- 2: LISTX option      1 = yes, output hexadecimal object module listings on SYSLST (compilers only)
- 3: SYM option      1 = yes, output symbol tables on SYSLST/SYSPCH
- 4: XREF option      1 = yes, output symbolic cross reference list on SYSLST
- 5: ERRS option      1 = yes, output diagnostics on SYSLST (compilers only)
- 6: CHARSET option      1 = 48, input on SYSIPT is 48 or 60 character set
- 7: Reserved

55

This byte contains the standard supervisor options for abnormal EOJ and control statement display, and the indicator for the presence of the ASCII-EBCDIC and EBCDIC-ASCII translation tables.

- Bit 0: Always on
- 1: DUMP option      1 = yes, dump registers and storage on SYSLST
- 2: Reserved
- 3: LOG option      1 = yes, list all control statements on SYSLST
- 4-6: Reserved
- 7: ASCII option      1 = yes, ASCII supported

Figure 10. Supervisor Communications Region (Part 3 of 5)

Key to Communications Region Displacement:

56 Job control byte

- Bit 0: 1 = Job Accounting  
Interface (JA) not supported  
0 = Job Accounting  
Interface (JA) is supported
- 1: 1 = Return to caller on LIOCS disk open failure  
0 = Do not return to caller on LIOCS disk open failure
- 2: 1 = Job control input from SYSRDR  
0 = Job control input from SYSLOG
- 3: 1 = Job control output on SYSLOG  
0 = Job control output not on SYSLOG
- 4: 1 = Cancel job  
0 = Do not cancel job
- 5: 1 = Pause at end-of-job step  
0 = No pause at end-of-job step
- 6: 1 = SYSLOG is not a 1052  
0 = SYSLOG is a 1052
- 7: 1 = SYSLOG is assigned to the same device as SYSLST  
0 = SYSLOG is not assigned to the same device as SYSLST

57 Linkage control byte

- Bit 0: 1 = SYSLNK open for output  
0 = SYSLNK not open for output
- 1: 1 = \$ or FG program phase deleted, renamed, or cataloged (flag bit for \$MAINEOJ)
- 2: 1 = Allow EXEC  
0 = Suppress EXEC
- 3: 1 = Catalog linkage editor output  
0 = Do not catalog linkage editor output
- 4: 1 = Supervisor has been updated  
0 = Supervisor has not been updated
- 5: 1 = Executing in AUTOTEST mode  
0 = Not executing in AUTOTEST mode
- 6: 1 = Relocate or condense in progress
- 7: 1 = Fetch \$MAINEOJ at end of job to update system directory  
0 = Do not fetch \$MAINEOJ at end of job for update

58

Language processor control byte. This is a set of switches used to specify nonstandard language translator options. The switches within the byte are controlled by job control OPTION statements and when set to 1, override standard options. The format of this byte is identical to the standard option byte (displacement 54) with one exception: Bit 7 in this byte is used to indicate to LIOCS that the rewind and unload option has been specified.

59

Job duration indicator byte

- Bit 0: 1 = Within a job condition  
0 = Outside a job condition
- 1: 1 = Dump on an abnormal end-of-job condition  
0 = No dump on abnormal EOJ
- 2: 1 = Pause at EOJ step } Set by Attention Routine for Job Control  
0 = No pause at EOJ }
- 3: 1 = Job control output on SYSLST  
0 = Output not on SYSLST
- 4: 1 = Job is being run out of sequence with a temporary assignment for SYSRDR  
0 = Conditions for 1 setting not met
- 5: 1 = PCIL is being condensed  
0 = PCIL is not being condensed
- 6: Reserved
- 7: 1 = Batch command just issued  
0 = Condition for 1 setting did not occur

Figure 10. Supervisor Communications Region (Part 4 of 5)

Key to Communications Region Displacements:

- 60 Binary disk address of the volume label area (label cylinder).
- 62 → 76 As illustrated (Figures for information blocks, I/O tables, and pointers begin at Figure 21 which refers to more detailed Figures).
- 78 Set to the value nn specified in the LINES = nn parameter of the STDJC macro.
- 79 The format of the system date contained within this field is determined by the IPL program from information supplied in the date convention byte (displacement 53). Bytes 85-87 contain the day count.
- 88 Bytes reserved for use by LIOCS. Transient dump programs insert a key to indicate to the LIOCS end-of-volume routine, SSBCMT07, that it was called by a B-transient.
- 90 Address of the first part of the program information block (PIB) table. (See Figures 18 and 19).
- 92 ID number of the last checkpoint. Temporary indicator of file protected DASD. Used at IPL time, when DASDFF is specified.
- 94 Length of the LUBID queue (in bytes). This equals the number of channel queue entries. It can also be used to access the REQID, LUBDSP, and TKREQID queues: (See Figure 17 - GY24-5151).
- 96 Address of disk I/O position data. This is the starting address of the disk information block (DIB) table (See Figure 17).
- 98 Address of the beginning of the error recovery block. The error recovery block contains addresses of error recovery exits, error recovery queue information that can be used by physical transients routines, and defines storage for the error queue entries (See Figure 43 - GY24-5151).
- 100 → 104 Option Tables. (See Figure 13 - GY24-5151).
- 106 Key of the program (BG, F2, or F1) that has timer support.
- 108 Address of LUBID queue. (See Figure 17 - GY24-5151).
- 110 Logical Transient Key (LTK) contains the same value as the PIK (PID) (Displacement 46) when the logical transient is requested. When the transient area is not in use, LTK is equal to zero. The SVC 2 routine sets the LTK. The SVC 11 routine resets the LTK.
- 112 Supervisor constants:
  - DOLLARBO (4 bytes) = C'SSBO'
  - SSKADR (5 bytes) = XL5'0'
  - LTAREA (3 bytes) = Adcon of LTSVPT, logical transient save pointer
- 124 Address of second part of program information block (PIB) table (See Figure 20).
- 126 Address of PDTABB, table of DTF addresses for MICR support.
- 128 Address of QTAM vector table (IJLQTTAD).
- 132 Address of background communications region.
- 134 Option Indicator Byte
  - Bit 0: 1 = MCRR indicated for OBR writer  
0 = No MCRR indicated for OBR writer
  - 1: 1 = EU interface active  
0 = EU interface not active
  - 2: 1 = TP request  
0 = No TP request
  - 3: 1 = Supervisor support for only 9-track tape  
0 = Supervisor does not support 9-track tape exclusively
  - 4: Reserved
  - 5: 1 = RETAIN/370 support generated  
0 = RETAIN/370 support not generated
  - 6-7: Reserved
- 135 System Configuration Byte 2
  - Bit 0: 1 = PCIL supported  
0 = PCIL not supported
  - 1-7: Reserved
- 136 Pointer to communications region extension (See Figure 11).

Figure 10. Supervisor Communications Region (Part 5 of 5)

BGXTNSN (See Note)

0 (Hexadecimal Displacement)	4	8	0C	10	12	14	18	1C	20
0 (Decimal Displacement)	4	8	12	16	18	20	24	28	32
CE Table Address	Track Hold Table Address (THTABAD)	Difference Between 1st and 2nd Part of PIB Table (PIBDIFF)	AB Termination Table Address -8 (ABPTR)	ID of Task Owning LTA (LID)	ID of Task Running (PIK)	Task Requester ID Table Address (TKIDPTR)	Address Used by QTAM (MVCFLD)	SDR Table Address (SDRTABLE)	TEBV Table Address (TEBVTAB)
XXXX	XXXX	XXXX	XXXX	XX	XX	XXXX	XXXX	XXXX	XXXX

24 (Hexadecimal Displacement)	28	2C	30	34	38	3C
36 (Decimal Displacement)	40	44	48	52	56	60
OLTEP Linkage Address	RMS Linkage Address (RASLINK)	ASCII-EBCDIC Translation Table Address	(Reserved)	JAI Common Table Address (ACCTCOMN)	JAI Partition Table Address (ACCTxx)	&SYSPARM Field Address
XXXX	XXXX	XXXX	XXXX	XXXX	XXXX	XXXX

Key to displacements:

- 0 CE Table Address.
- 4 Track Hold Table Address (THTABAD).
- 8 Difference between addresses of first part of PIB table and second part of PIB table (PIBDIFF).
- 12 Abnormal Termination Table Address (minus 8) (ABPTR).
- 16 Identification (LID) of the task owning the Logical Transient Area. Contains same value as PIK (displacement 18) when LTA is in use. Contains zero when LTA is not in use.
- 18 Program Interrupt Key (PIK) if asynchronous processing is supported. Value is equal to the displacement of the start of the PIB table to the PIB of the main task or subtask being selected (running).
  - First byte - zero
  - Second byte - contains the displacement into the PIB table for a maintask or a subtask.
  - Maintask - PIK value is hex 10, 20, or 30.
  - Subtask - PIK value is hex 70, 80, 90, . . . F0.
- 20 Task Requester ID Table Address (TKIDPTR).
- 24 MVCFLD address used by QTAM.
- 28 Statistical Data Recorder Table Address (SDRTABLE).
- 32 Tape Error Blocks by Volume Table Address (TEBVTAB).
- 36 Pointer to OLTEP Linkage Addresses
- 40 RMS Linkage Area Address (RASLINK)
- 44 ASCII-EBCDIC Translation Table Address.
- 48 (Reserved)
- 52 JAI Common Table Address (ACCTCOMN)
- 56 JAI Partition Table Address (ACCTxx; where xx = BG, F2, or F1).
- 60 Address of &SYSPARM Field.

Note: If communications regions are generated for the foreground partitions, the labels in those extensions will be F2XTNSN and F1XTNSN. The extensions, wherever used, are generated by the COMMNE macro. Following the background extension (and immediately preceding the MCRR Linkage Table) is a six-byte area. The first four bytes are the address of the background save area (BGSAV), and the last two bytes are the value 4,096, used to restore base registers.

Figure 11. Background Communications Region Extension

0 (SDR TABLE) SDR Flags	1 Partition ID	2 Number of SDR Records	3 4 First SDR ID BBCCHHR	10 11 First OBR ID BBCCHHR	17 18 Current OBR ID BBCCHHR	24 25 Last OBR ID BBCCHHR	31
32	35	36	39 40	43 44			71
Address of SDR Accumulator		Address of SDR Unit Switches	Reserved			List Save Area	
72	75	76 SDR1 Work Area	95	96 Test Under Mask Table	103 Temporary Work Area	104 Test Under Mask Instruction	107 111 112 115 116 117 SDR Queue Save Area
118		135 SDR2 Work Area	136 Area Modified by A - Transients		155 156 SDR Error Message Save Area	159 160 Branch Instruction	163 164 167 OBR/SDR Flag Byte Address
168							250 Data Area for OBR/SDR Records

Key to SDR Communications Region Displacements:

<input type="checkbox"/> 0	SDR Flags:
	Bit 0: Key of OBR 1: RDE option 2: Initial IPL time 3: RF option = NO, recording is suppressed
	Bit 4: RF option = CREATE 5: RF option = YES 6: Error while recording 7: Recorder file ready
	Set and tested by Job Control.
<input type="checkbox"/> 1	Set by EREP transient \$\$BSDRUP to identify the partition making the call for EREP recording.  Settings: X'10' if EREP is running in BG. X'20' if EREP is running in F2. X'30' if EREP is running in F1. X'01' with one of the above if recorder file is ready. X'00' with one of the above if recorder file is not ready.
<input type="checkbox"/> 2	Initial number of SDR records specified. If SDR record count is not specified, the file is formatted for OBR records only (\$JOBCTL, see Chart 14)
<input type="checkbox"/> 4	Disk address of first SDR record.
<input type="checkbox"/> 11	Disk address of first OBR record.
<input type="checkbox"/> 18	Disk address of current OBR record.
<input type="checkbox"/> 25	Disk address of last OBR record.

Figure 12. SDR Communications Region - SDRTABLE (Part 1 of 2)

Key to SDR Communications Region Displacements:

32 Address of SDR accumulator area which contains half-byte counters and accumulated error conditions.

36 Address of SDR unit switches.

SDR switch byte (1 for each PUB):

X'80' - Update operations complete

X'40' - Counters on external file overflowed

X'20' - I/O error during write

X'08' - SDR update half-byte counters routine required

X'04' - Update SDR record routine required

Other - Reserved

When entry contains X'01000000', indicates MCRR, no SDR supported.

40 Reserved.

44 SDR1 register save area.

72 Mask formats for interpretive error accumulator, SDR1:

X'FF' - End of update

X'FE' - Bypass counter

X'FD' - Set up 'OR' condition to previous counter

X'FC' - Ignore list item

Other - Test bit in error queue

76 Used by the interpretive error accumulator routine to process list passed by OBR/SDR A - transient.

96 Used by the interpretive error accumulator routine.

104 Used by the interpretive error accumulator routine for address alignment.

108 Executed by the interpretive error accumulator routine.

112 Loop counter for the SDR counter update.

116 Save area for pointers to entries in the SDR error queue.

118 Work area where half byte error counters are unpacked and updated.

136 List of devices passed to the SDR processor and \$\$ANERAD.

156 Used by SDR/OBR recorder phases to pass error message displacements and disk error addresses in event of an error.

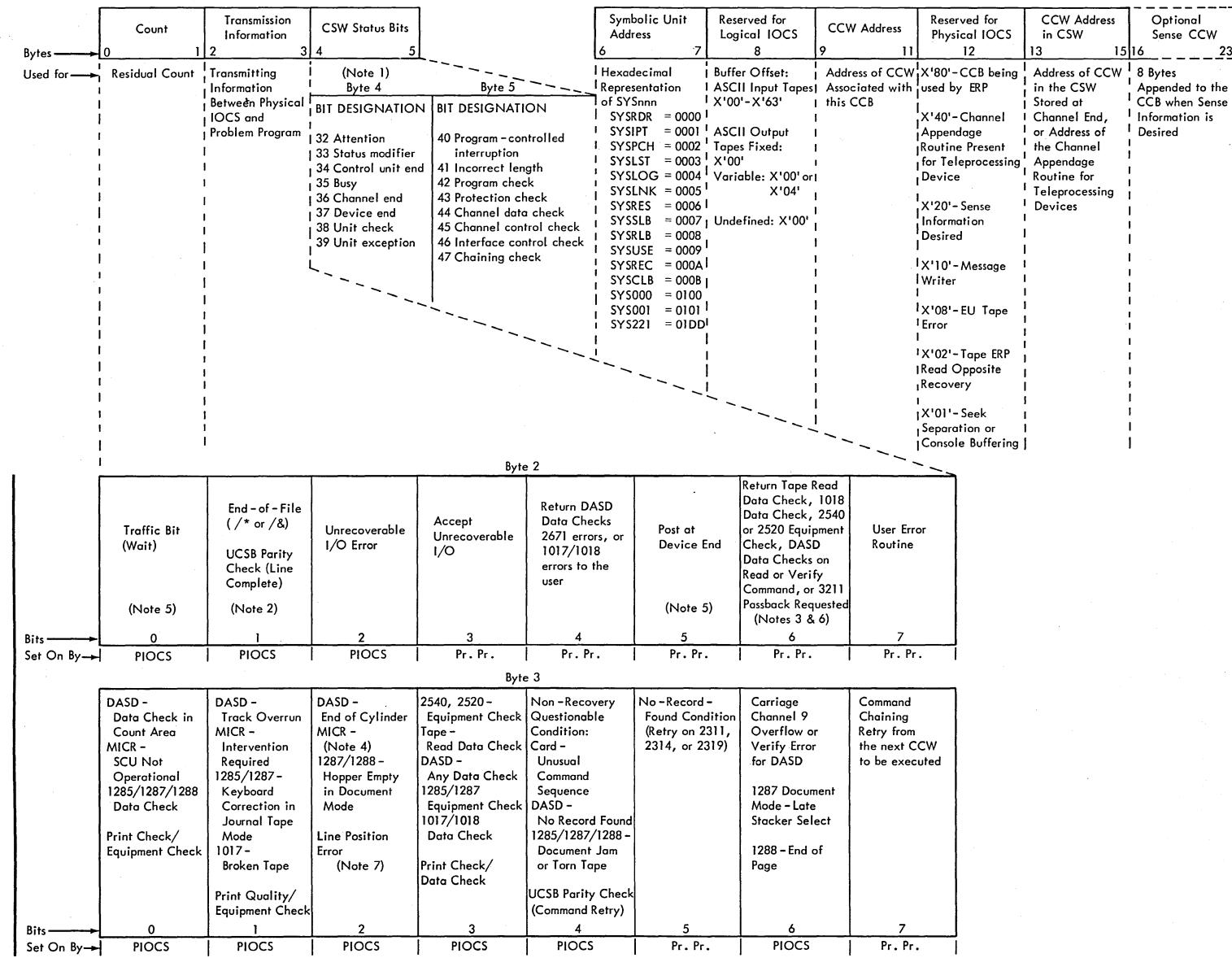
160 Entry point from OBR/SDR A - transients. Branches to label SDRMM.

164 Pointer into the OBR/SDR unit switches. Status posted by recorder phases. (See byte 36).

168 OBR and SDR records formatted by the recorder phases.

Figure 12. SDR Communications Region - SDRTABLE (Part 2 of 2)

Figure 13. Command Control Block (CCB)



PIOCS = Physical IOCS  
Pr. Pr. = Problem Program

Note 1. Bytes 4 and 5 contain the status bytes of the Channel Status Word (Bits 32-47). If byte 2, bit 5 is on and device end results as a separate interrupt, device end will be ORed into CCB byte 4.

Note 2. Indicates /\* or /& statement encountered on SYSRDR or SYSIPT. Byte 4, bit 7 (unit exception) is also on.

Note 3. DASD data checks on count not returned.

Note 4. For 1255/1270/1275/1412/1419, disengage. For 1275/1419D, I/O Error in external interrupt routine (channel data check or busout check).

Note 5. The traffic bit (Byte 2, bit 0) is normally set on at channel end to signify that the I/O was completed. If byte 2, bit 5 has been set on, the traffic bit and bits 2 and 6 in byte 3 will be set on at device end. Also see Note 1.

Note 6. 1018 ERP does not support the Error Correction Function.

Note 7. This error occurs as an equipment check, data check, or FCB parity check.

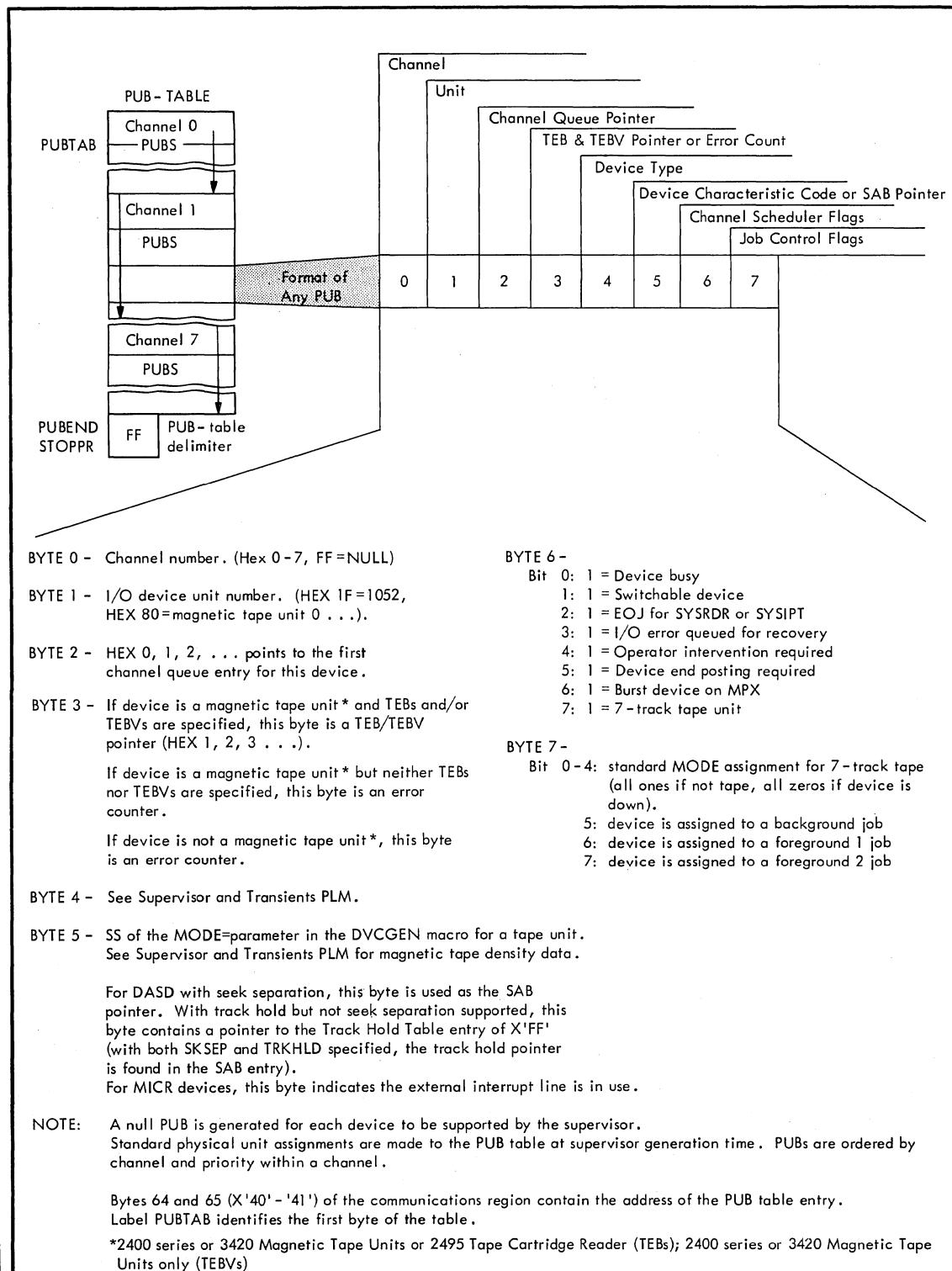
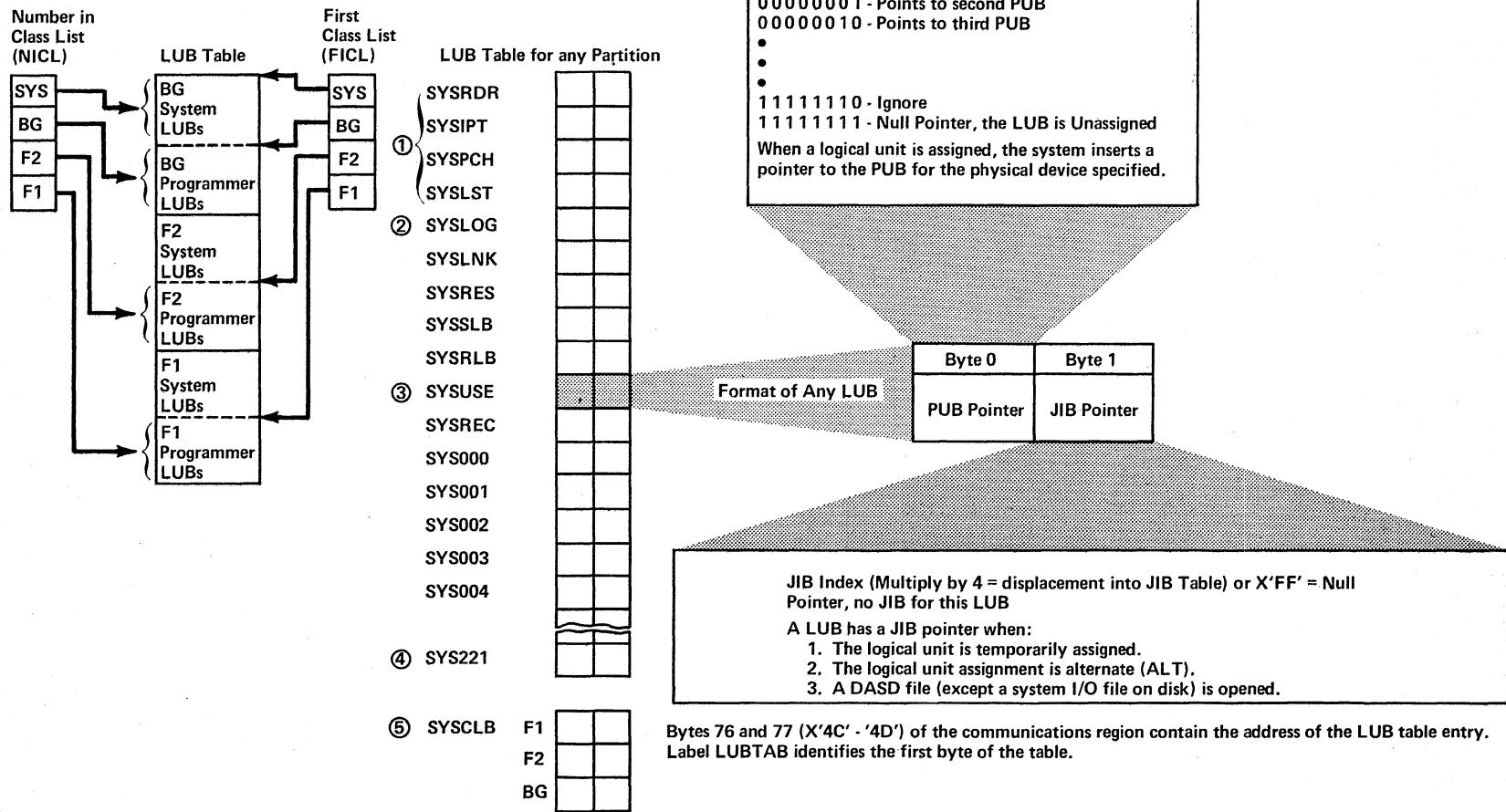


Figure 14. PUB Table

Figure 15. NICL, FICL, and LUB Tables



- ① When in Single Program Initiation mode (Foreground 1 or 2): Must be unit record device and can be referenced by the program.
- ② When in Single Program Initiation mode (Foreground 1 or 2): Can be referenced by the program.
- ③ SYSUSE may be called SYSCTL in error recovery messages.
- ④ The maximum number of programmer logical units in the system is 222 if MPS = BJF, or 244 if MPS = YES or NO.
- ⑤ The SYSCLB (Private Core Image Library) LUB entry functions the same as other LUB entries, but is not part of the LUB Table. To locate the SYSCLB LUB in supervisor, perform the following steps:
  1. Divide the PIK by 8.
  2. Subtract the result in step 1 from the address of the PIB extension block.
  3. If option AP = YES, the result of step 2 is the location of SYSCLB LUB. If option AP = NO, add 16 (for the all-bound PIBX) to the result of step 2.

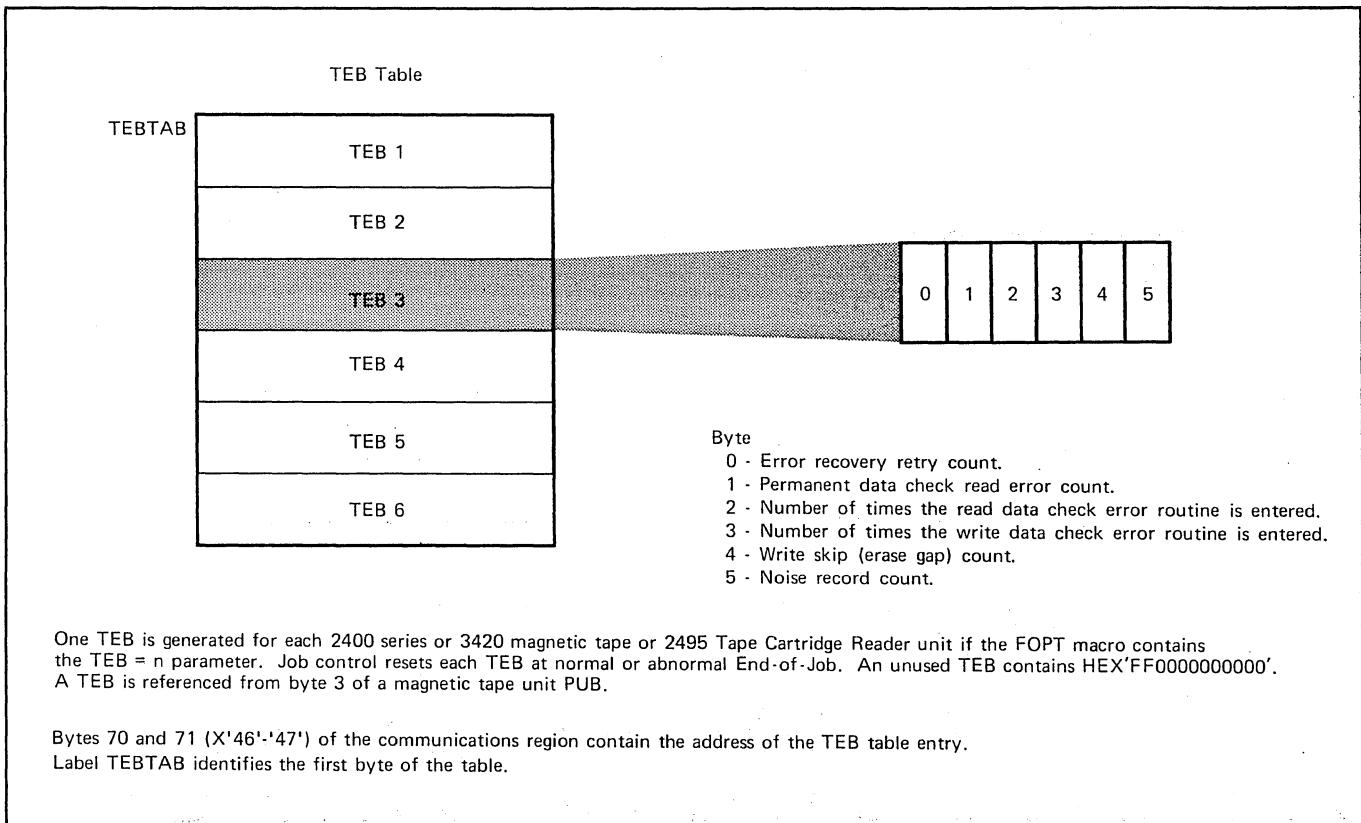


Figure 16. Tape Error Block (TEB)

	Current Address										End Address				R	U.L.	L.L.	R.C.	Reserved
SYSLNK	B	B	C	C	H	H	R	P	This area not used for SYSLNK DIB										
SYSIN	B	B	C	C	H	H	R	K	D	D	B	B	C	C	H	H	X	H	XX
SYSPCH																			
SYSLST																			
Number of Bytes								7				3							
																1	1	1	
																1	2	2	
																1	1	1	

KEY: Current Address: The next address to be used (for both input and output).

End Address : The last address within the limits of the extent.

R : Maximum number of records per track.

U.L. : Upper head limit.

L.L. : Lower head limit.

R.C. : Record Count - residual capacity for beginning of operator notification. This is set at system generation time with the SYSFIL parameter, or after IPL with the SET statement (RCLST and/or RCPCH operands). A warning message is issued by job control after end-of-job step when the minimum number of remaining records has been reached or exceeded during the previous job.

P : Starting cylinder of Private Core Image Library, if PCIL is assigned.

KDD : Key and data length for the symbolic device.

KDD for SYSIN = X'000050'

KDD for SYSPCH = X'000051'

KDD for SYSLST = X'000079'

Bytes 96 and 97 (X'60' - '61') of the communications region contain the address of the SYSLNK entry.

Label DSKPOS identifies the first byte of the table.

Figure 17. Disk Information Block (DIB) Table

PIB TABLE

Byte Number	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	= 16 Byte Length
All Bound PIB	Flag Byte See A *	Reserved	SP Prefix		Branch Instruction to the All Bound Routine							Reserved					
Problem Program PIB (Note 1)	Flag Byte See B *	Cancel Code (Fig. 31)	SYSLOG ID (BG, F2, or F1)	NOP Instruction (CR)	Address of the Partition Save Area		Number of Core Blocks (Note 2)	Address of the Origin of the Partition		PIB Assign Flag See D *	User LUB Index	Number of Program LUBs	Flag Byte See C *				
Attention PIB	Flag Byte See E *	Cancel Code (Fig. 31)	SYSLOG ID (AR)	Branch Code (BC)	Active = Address of Save Area Inactive = Remainder of BC Instruction		Switch Byte See F *	Logical Transient Bucket (contains save area address)	X'07' See D *	Reserved		Address of the Logical Transient					
Quiesce PIB	Flag Byte See A *	Cancel Code (Fig. 31)	C' / & '		Branch Instruction to Quiesce I/O Routine		Scratch Byte X'00'	X'00'	X'04'	X'08'	X'OC'	X'10'	X'14'	X'18'			
Supervisor PIB	Flag Byte See A *	Cancel Code (Fig. 31)	SP Prefix		Branch Instruction to General Exit Routine		Address of SYSRES PUB	Length of Error Queue Entry			Constants to Clear Bytes 2 - 5 of CCB						
Subtask PIB for AP (Note 3)	Flag Byte See B *	Cancel Code (Fig. 31)	SYSLOG ID (BG, F2, or F1)	NOP Instruction	Address of the Save Area		Number of Core Blocks (Note 2)	Address of the Origin of the Main Task		PIB Assign Flag See D *	User LUB Index	Number of LUBs	Flag Byte See C *				

Note 1: Three problem program PIBs are built in this sequence when the MPS or BJF feature is selected as a generation option:

{ Background PIB  
Foreground 2 PIB  
Foreground 1 PIB

When a batch-only environment is established at generation time, the All Bound and Foreground PIBs are excluded from the table, and only one (BG) problem program PIB is built. However, the X'20' bytes that F2 and F1 PIBs normally occupy (between PIBBG and PIBAR) are filled with 32 bytes of DIBs data.

Note 2: Number is in multiples of 2K for F2 and F1. BG is always 10K (X'0A').

Note 3: Total of nine subtask PIBs are generated, and only when AP is specified at generation time.

\* See Figure 14 for flag byte expansions A, B, C, D, E and F.

Bytes 90 and 91 (X'5A' - '5B') of the communications region contain the address of the first part of the PIB Table. Label PIBTAB identifies the first byte of the table.

Figure 18. First Part of PIB Table

**A**

## Supervisor, Quiesce, and ALL Bound PIB Flags:

Bit 0: 1 = Always one  
 1-4: 0 = Always zero  
 5: 1 = Always one  
 6: 1 = Active  
 0 = Inactive  
 7: 1 = Active  
 0 = Inactive

Note: If PTO = YES is specified, Bit 6 is a one in the Quiesce I/O PIB when attached by the supervisor. Otherwise it is always zero.

**B**

## Problem Program PIB Flag (First Byte in PIB):

Bit 0: 1 = Registers stored  
 0 = Registers not stored  
 1-3: 0 = Always zero  
 4: 1 = QTAM Wait active  
 0 = QTAM Wait inactive  
 5: 0 = Normal execution  
 1 = Program has seized the system  
 6: 1 = Unbound  
 0 = SVC 2-bound (B-transient in progress)  
 7: 1 = Unbound  
 0 = SVC 7-bound (waiting for an I/O interrupt)

X'80' indicates the program is not present in the system  
 X'87' indicates the program is PTO bound  
 X'89' indicates the program is IDRA bound

**C**

## Problem Program PIB Flag (Last Byte in PIB):

Bit 0: 1 = Batched Job in Foreground  
 0 = No BJF  
 1: Cancel in LTA and Device not Assigned  
 2: 1 = /& on SYSIN if DASD  
 0 = No /& on SYSIN  
 3-4: Reserved  
 5: 1 = Task is canceled  
 0 = Task not canceling  
 6: 1 = Subtask (s) attached  
 0 = No subtasks attached  
 7: 1 = In AB Routine  
 2 = Not in AB Routine

**D**

## PIB Assign Flag

X'80' = SYSRES DASD file protect inhibited (allow write operation on SYSRES)  
 X'40' = Channel appendage exit allowed (BTAM)  
 X'20' = Cancel in progress (used in terminator function)  
 X'10' = Cancel control (set on a foreground cancel)  
 X'08' = Hold-Release flag for foreground assignments  
 X'07' = Supervisor or Attention routine PIB assign flag setting  
 X'04' = Background program PIB assign flag setting  
 X'02' = Foreground 1 program PIB assign flag setting  
 X'01' = Foreground 2 program PIB assign flag setting

**E**

## Attention PIB Flag

Bit 0: 1 = Registers stored  
 0 = Registers not stored  
 1-5: 0 = Always zero  
 6: 1 = Attention routine active  
 0 = Attention routine SVC 2-bound  
 7: 1 = Active  
 0 = SVC 7-bound

X'80' indicates the attention routine is not present in the system.  
 X'89' indicates the program is IDRA bound

**F**

## Attention PIB Switch Byte

Bit 0-2: Reserved  
 3: 1 = PTAFTCH (Fetch \$\$ANERRY, Z, or 0) Switch ON  
 0 = PTAFTCH (Fetch \$\$ANERRY, Z, or 0) Switch OFF  
 4: 1 = Detach Logical Attention Routine (\$\$BATTNA) Switch ON  
 0 = Detach Logical Attention Routine (\$\$BATTNA) Switch OFF  
 1 = Physical Attention Recall Switch ON  
 0 = Physical Attention Recall Switch OFF  
 1 = Attention Request Switch ON  
 0 = Attention Request Switch OFF  
 1 = External Interrupt Request Switch ON  
 0 = External Interrupt Request Switch OFF

**Figure 19. PIB Flag Expansions**

Byte Number	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	= 16 Byte Length
All Bound PIB			Reserved				H'16' Priority of All Bound PIB (Lowest)		Reserved			H'0' All Bound PIB Displacement		Reserved			
Background PIB	Address of BG Comm. Region	System LUB Index	Reserved		Priority of BG PIB ④		Address of Termination ECB, if any, or F'0'		X'0010' BG PIB Displacement		Reserved						
FG2 PIB ①	Address of Area Comm. Region ②	System LUB Index	Reserved		Priority of F2 PIB ④		Address of Termination ECB, if any, or F'0'		X'0020' F2 PIB Displacement		Reserved						
FG1 PIB ①	Address of Area Comm. Region ②	System LUB Index	Reserved		Priority of F1 PIB ④		Address of Termination ECB, if any, or F'0'		X'0030' F1 PIB Displacement		Reserved						
Attention PIB	Address of BG Comm. Region	0	0	Reserved	H'3' Priority of Attention PIB		F'0'		X'0040' Attention PIB Displacement		Reserved						
Quiesce I/O PIB		Reserved			H'2' Priority of Quiesce I/O PIB		F'0'		X'0050' Quiesce PIB Displacement		Reserved						
Supervisor PIB		Reserved			H'1' Priority of Supervisor PIB (Highest)		F'0'		X'0060' Supervisor PIB Displacement		Reserved						
Subtask PIB ③	Address of Area Comm. Region	System LUB Index	Reserved		Priority of Subtask ④		ECB Address for Subtask, or F'0'		PIB Displacement of Maintask		Reserved						

① Generated only if MPS is specified.

② Always background communications region except when MPS=BJF.

③ Total of nine subtasks generated, and only when AP is specified.

④ Is filled in with halfword indicating the relative priority of task in the system (range H'4' to H'15', the lower the number the higher the priority).

Bytes 124 and 125 (X'7C' - '7D') of the communications region contain the address of the second part of the PIB table. Label PIB2AD identifies the first byte of the table.

Figure 20. Second Part of PIB Table

Decimal Displace- ment	Label	Byte Length	Description
<b>(TEBV Status Block portion of TEBV Table, see <u>Note 1</u>)</b>			
0	TEBLEN	1	Length of TEBV Error Block (for each Error Block generated)
1	TSBLEN	1	Length of TEBV Status Block (4, 6, or 22 bytes, see <u>Note 1</u> )
2	EVARTH	1	EVA Read Error Threshold
3	EVAWTH	1	EVA Write Error Threshold
...	.....	...	.....
4	TEBSTAT	1	DASD ESTV File Status
5	TEBUDC	1	ESTVFLE Label Update Counter
...	.....	...	.....
6	TEBDEV	1	Data Set Device Code
7	UPXTNT	4	Disk Address of Upper Extent of Data Set (cchh)
11	TEBRPT	1	Number of Records per Track
12	NXTESR	5	Disk Address of Next Available Space for Data Record (cchhr)
17	ESTVLABL	5	Pointer to ESTVFLE Label in VTOC (cchhr)
...	.....	...	.....
<b>(TEBV Error Block portion of TEBV Table, see <u>Note 2</u>)</b>			
22	TEBV	1	Status Indicator (giving status of posting and writing error conditions)
23		1	Usage Indicator (X'00'=TEBV Error Block in use, X'FF'=Error Block generated but not serving any tape unit)
24		1	Retry Counter
25		1	Permanent Read Errors
26		1	Temporary Read Errors
27		1	Temporary Write Errors
28		1	Erase Gaps
29		1	Noise Blocks
30		1	Permanent Write Errors
31		1	Cleaner Actions
32		2	Number of Start I/Os
34		6	Volume Serial Number (volume ID)
...	.....	...	.....
40	(repeat bytes 22-39 for each TEBV Error Block)		

Figure 21. TEBV Table Showing Status Block and Error Blocks (Part 1 of 2)

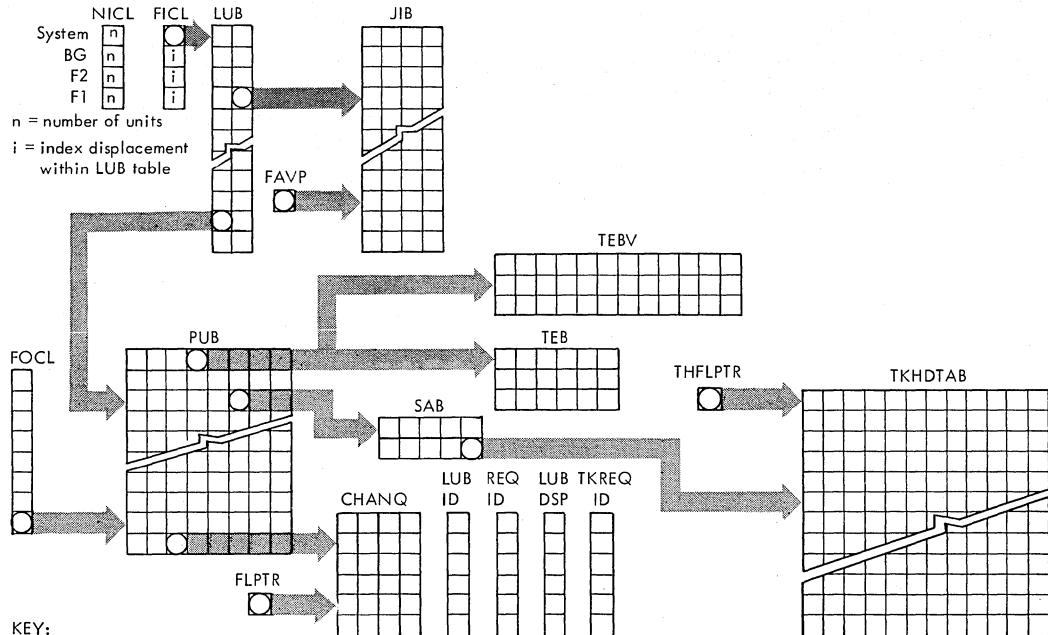
Note 1: The TEBV (Tape Error Block by Volume) Table is composed of one Status Block and (n) Error Blocks, and is addressed symbolically by label TEBVTAB.

Supervisor generation options in the FOPT macro determine the size of the TEBV Status Block at generation time:

- When EVA is chosen without ESTV, the TEBV Status Block is four bytes long (bytes 0-3), followed by TEBV Error Blocks, so that bytes 4-21 are omitted.
- When ESTV output is to SYSLOG, the TEBV Status Block is six bytes long (bytes 0-5), followed by TEBV Error Blocks, so that bytes 6-21 are omitted.
- When ESTV output is to DASD, the TEBV Status Block is 22 bytes long (bytes 0-21, such as shown in this Figure), followed by TEBV Error Blocks.

Note 2: The number of TEBV Error Blocks generated corresponds to the (n) parameter of the FOPT macro for TEB, TEBV, or EVA options. A TEBV Error Block always contains 18 bytes, as shown in bytes 22-39 of this Figure. Therefore, the TEBV Table is composed of one TEBV Status Block (with its byte length dependent on supervisor generation options, as described in Note 1), followed by (n) number of 18-byte TEBV Error Blocks.

Figure 21. TEBV Table Showing Status Block and Error Blocks  
(Part 2 of 2)



- KEY:**
- NICL (Number in Class)
    - : The first byte contains the number of system class units. The second, third, and fourth bytes contain the number of programmer class units (BG, F2, F1) (Figure 21).
  - FICL (First in Class)
    - : The first byte points to the first system class unit in the LUB table. (Always the first LUB table entry.) The second byte points to the first programmer class unit in the LUB table BG area. The third points to the first programmer class unit in the LUB table F2 area. The fourth points to the first programmer class unit in the LUB table F1 area (Figure 21).
  - LUB (Logical Unit Block) Table
    - : The first byte points to a PUB table entry (if the logical unit is assigned) or contains X'FF'. The second byte points to a JIB table entry or contains X'FF' (Figure 21).
  - PUB (Physical Unit Block) Table
    - : The first two bytes contain the channel and unit address of the physical device; the third a CHANQ pointer; the fourth a TEB pointer; the fifth device type codes; the sixth a device characteristic code or a SAB pointer; the seventh the channel scheduler flag; and the eighth has the job control flag (See Figure 19).
  - FOCL (First on Channel List)
    - : The first byte points to the first PUB (highest priority) on channel zero. The next byte points to the first PUB (highest priority) on channel one, etc. A hexadecimal FF indicates the associated channel is not supported.
  - TEB (Tape Error Block by Unit)
    - : One TEB is built for each tape unit at supervisor generation time if tape error statistics by unit are required (Figure 16).
  - TEBV (Tape Error Block by Volume)
    - : One TEBV is built for each tape unit at supervisor generation time if tape error statistics by volume are required (Figure 21).
  - FAVP (First Available Pointer)
    - : A one-byte pointer to the next available JIB entry.
  - JIB (Job Information Block)
    - : The first two bytes contain extent or LUB information. The third contains ownership and JIB flags. The fourth contains JIB chaining information (See Supervisor PLM -GY24-5151).
  - CHANQ (Channel Queue) Table
    - : The first byte contains the chain field (a pointer to the next in queue). The last three bytes contain the CCB address (Figure 18).
  - LUBID (LUB Identification)
    - : A one-byte pointer to the LUB making the I/O request.
  - REQID (Requestor Identification)
    - : A one-byte pointer to the program containing the CCB (Figure 18).
  - LUBDSP (LUB Displacement)
    - : A one-byte value equal to the absolute LUB number (CCB byte 7).
  - FLPTR (Free List Pointer)
    - : A one-byte pointer to the next free entry in the channel queue (Figure 18).
  - SAB (Seek Address Block)
    - : A four-byte (BCCH) address that is the current disk address of the device plus a fifth byte that contains a Track Hold Table pointer of X'FF'. If the Track Hold function is not supported, the fifth byte contains X'00'.
  - TKHDTAB (Track Hold Table)
    - : The first byte contains a pointer to the next available entry (or X'FF'); bytes 2 - 4 have CCB address of the requesting task; bytes 5 - 10 have a disk address (BBCCHH) of track being held; byte 11 has key of owning track; and byte 12 has two uses: bit 0=1 means a task is waiting for the track, and bits 4 - 7 count the number of holds on the track. (See Supervisor PLM -GY24-5151). Note: The number of holds is one more than the value of bits 4 - 7 of the last byte.
  - THFLPTR (Track Hold Free List Pointer)
    - : A one-byte pointer to the next free entry in the Track Hold Table.
  - TKREQID (Track Requestor Identification)
    - : A one-byte pointer to the PIB of the task requesting I/O.

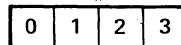
**Figure 22. I/O Table Interrelationship**

JIB Table

JIB 1
JIB 2
JIB 3
JIB 4
JIB 5
JIB 6

Number (length of JIB table)  
determined at supervisor generation

Note: Two JIBs are required for a 2321 extent; one for lower limit and one for upper limit. The lower limit defining JIB must be chained to the upper limit defining JIB. Byte 1 of this type JIB contains the sub-cell number times 10 plus the strip number in binary.



Type of Entry

Stored standard assignment	LUB entry of stored standard assignment (PUB and JIB pointers)
Alternate assignment	PUB pointer of alternate X'00' assignment
① 2311 Extent	C <sub>L</sub> C <sub>L</sub> C <sub>H</sub> C <sub>H</sub> ②
① 2321 Extent	B <sub>L</sub> B <sub>L</sub> C <sub>L</sub> C <sub>L</sub> or B <sub>H</sub> B <sub>H</sub> C <sub>H</sub> C <sub>H</sub> ③

- ① Only when file-protect on DASD
- ② Lower Cylinder  
Upper Cylinder
- ③ Cell or combined sub-cell and strip

Flag Type	Bit	Meaning if Bit = 1
Contents	0	Stored standard assignment
	1	Alternate assignment
	2	2311 Extent
	3	2321 Extent
Ownership	4	Standard assignment for DASD extent
	5	Background
	6	Foreground 1
	7	Foreground 2

Chain Byte.  
Contains the displacement index of the next JIB.  
A hexadecimal 'FF' defines the end of the chain.

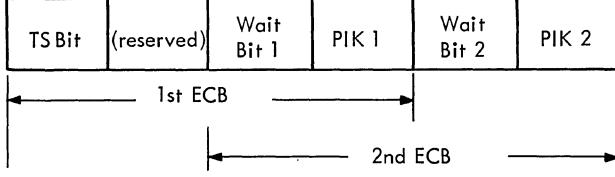
Bytes 68 - 69 (X'44' - '45') of the communications region contain the address of the JIB table entry. Label JIBTAB identifies the first byte of the table.

Figure 23. Job Information Block (JIB) Table

Displacement	Label	Description
0-3	ACCTWK1* (ACCTABLE)*	Work area used in SIO update.
4-7	ACCTWK2	Work area used with ACCTWK1 in start/stop time routine.
8-11	ACCTSVPT	Job card pointer; address of job card field following jobname.
12	ACCTPART	ID of partition in charge (partition switch name).
13	ACCTRES2	Reserved.
14-15	ACCTLEN	Length of SIO area = $6n+1$ , where n=number of devices for this partition in SYSGEN option JA=(n1, n2, n3).
16-21	ACCTLOAD	Label area instruction; moves JAI label area address to OPEN/CLOSE transients.
22-23	ACCTRES3	Reserved.
24-27	ACCTLADD	Address of alternate label area.
28-31	ACCTCPU	Counter for CPU time elapsed in a jobstep, counted in 300ths of a second.
32-35	ACCTOVHT	Counter for overhead time; time not charged to any partition.
36-39	ACCTBNDT	Counter for all-bound time; system wait state time divided between running partitions.
40-47	ACCTSVJN	Save area for job name during simulated EOJ.
----- JOB ACCOUNTING TABLE (user's portion of Partition Table) -----		
48-55	ACCTJBNM	Job name; taken from job card.
56-71	ACCTUSRS	User information; 16 bytes from Job card.
72-73	ACCTPTID	Partition ID; 'BG', 'F2', or 'F1' in EBCDIC format.
74	ACCTCNCL	Cancel code; see Cancel Codes and Messages (Figure 32).
75	ACCTYPER	Type of record: 'S'=job step, 'L'=last step of job.
76-83	ACCTDATE	Date in format specified at SYSGEN (MM/DD/YY or DD/MM/YY).
84-87	ACCTSTRT	Start time of job, in packed decimal (OHHMMSSF; F=sign).
88-91	ACCTSTOP	Stop time of job, in same format as ACCTSTRT.
92-95	ACCTRES	Reserved.
96-103	ACCTEXEC	Phase name; taken from execute card.
104-107	ACCTHICR	High core address of active program phase, from COMREG.
108-111	ACCTIMES	CPU time elapsed in a job step; counted in 300ths of a second.
112-115	-----	Overhead time; elapsed time not charged to any partition, in 300ths of a second.
116-119	-----	All-bound time; system wait state time divided between running partitions, in 300ths of a second.
120	ACCTSIO	SIO tables: 6 bytes for each device specified by SYSGEN options, as follows: 2 bytes for device address (Ocuu), 4 bytes for count of SIOs in current jobstep.
-----	-----	Overflow byte: normally X'20', but is X'30' if more devices are used within a partition than specified by SYSGEN options.

\*Note: DSECT ACCTABLE symbolically addresses the JAI Partition Tables with labels as shown. Each partition in which JAI is supported has its own JAI Partition Table, labeled ACCTBG, ACCTF2, ACCTF1, for active partitions BG, F2, and F1 respectively.

Figure 24. Job Accounting Interface Partition Table

Displacement	Label	Description	
0-15	(ACCTCOMM) ACCTSVRG	Temporary register save area.	
16-17	ACCTSRX	Save area for remainder of overhead counter times distributed by partition on exit.	
18-19	ACCTSVRE	Save area for remainder of all-bound counter times distributed by partition on entry.	
20-23	ACCTPCNT	Count of partitions using JAI.	
24	ACCTSAID	Owner of physical transient area*.	
25	ACCTFAID	Interrupted program*.	
26	ACCTRAID	Active program*.	
27	ACCTSWCH	Accounting switches: if bit = 1, true; if bit = 0, not true.  bit 0 - cancel accounting bit 1 - no active partitions bit 2 - catalog in process bit 3 - alternate label area	bit 4 - IPL indicator bit 5 - \$JOBACCT in F1 bit 6 - \$JOBACCT in F2 bit 7 - \$JOBACCT in BG
28-31	ACCTIME	Start time of current accounting interval, in complement format.	
32-33	ACCTRESC	Reserved.	
34-35	ACCTUSEP	Address of user save area (ACCTUSER).	
36-39	ACCTBLES	Address of BG Job Accounting Table.	
40-43	- - - -	Address of F2 Job Accounting Table if BJF; otherwise zero.	
44-47	- - - -	Address of F1 Job Accounting Table if BJF; otherwise zero.	
48-53	ACCTSEAS	Seize blocks; serve as overlapped Event Control Blocks.   TS Bit: X'00' = no \$JOBACCT running X'FF' = \$JOBACCT active	
54-55	ACCTUSEL	Length of user save area, set with 4th operand of global AG39.	

\*Note: X'00' = all bound, X'10' = BG, X'20' = F2, X'30' = F1, X'40' = overhead and FG if SPI.

Figure 25. Job Accounting Interface Common Table (ACCTCOMM)

Figure 26. RMS Machine Check Record on SYSREC

Record 1	0 Machine Check ID (X'10')	1-2 Record Sequence Counter	3 CPU Model Number	4 Reserved	5-7 CPU Serial	8-9 CPU ID	10-11 MCEL Length	12-15 Date	16-19 Time of day in 300ths of a second Timer units	20-27 Reserved	28-35 Job ID	36-43 Machine Check Old PSW	44-77 Reserved	78 Damage Assessment	79 End of Record Indicator (X'FF')
Record 2	0-3 (Same as Record 1)	4 Sub Class Codes	5 Tense Codes	6-7 Error and Validity Codes	8-11 Extended Logout Length	12-19 Reserved	20-23 Failing Storage Address	24-25 Region Code ECC Information	26-27 Region Code Control Word Address	28-78 Reserved			79 (Same as Record 1)		
Record 3	0-3 (Same as Record 1)	4-48 Reserved								49-78 Floating Point Register Area			79 (Same as Record 1)		
Record 4	0-3 (Same as Record 1)	4-5 Floating Point Regs Save Area (Continued)	6-69 General Registers Save Area							70-78 Control Registers Save Area			79 (Same as Record 1)		
Record 5	0-3 (Same as Record 1)	4-58 Control Registers Save Area							59-78 MCEL (Machine Check Extended Logout)			79 (Same as Record 1)			
Record 6-17	0-3 (Same as Record 1)	4-78 MCEL (Machine Check Extended Logout)										79 (Same as Record 1)			
Record 18	0-3 (Same as Record 1)	4-75 MCEL (Machine Check Extended Logout)								76-78 Reserved (X'000000')		79 (Same as Record 1)			

\* Note: The Model 155 uses Records 1-18, as shown. The Model 145 uses only Records 1-8. In Record 8 for the Model 145, Bytes 0-3 are the same as Record 1, Bytes 4-25 contain the remainder of the MCEL (Machine Check Extended Logout) area, and the remainder of the record is not used.

0	4	8	12	16	20	24	28	32
LD00SLOT (\$\$RAST00) ① ②	LD01SLOT (\$\$RAST01) ③	LD02SLOT (\$\$RAST02) ⑤	LD03SLOT (\$\$RAST03) ⑤	LD04SLOT (\$\$RAST04) ⑤	LD05SLOT (\$\$RAST05) ⑤	LD06SLOT (\$\$RAST06) ⑤	LD07SLOT (\$\$RAST07) ⑤	LD08SLOT (\$\$RAST08) ⑤
36	40	44	48	50	52	54	56	57
LD09SLOT (\$\$RAST09) ⑤	LD10SLOT (\$\$RAST10) ④	LD11SLOT (\$\$RAST11) ⑤	RASCCB Residual Count	RASTIB Transmission information	----- CCW Status bytes	----- SYSRES LUB	RASCBF RAS CCB indicator	----- RAS Fetch CCWs address
60	64	72	80	88	96	103	104	106
---- CCW stored address	RASCCWS RAS seek CCW	RASRCG RAS search CCW	RASTIC TIC CCW	RASREAD CCW to read module into RTA	RASEEK Seek Address	RTAOWN Index into load list for RTA owner	MCPIK PIK of task interrupted by machine check	RTAID RTA I/O requestor ID
107	108	112	116	120	124	128	132	136
ERPID WTOR request return load index	RASRETR RTA return address after I/O operation	RASIOA RAS I/O request address	RASFCHA RAS FETCH request address	ERPIBA ERPIB queue address	RASDEQA CCB DEQ routine address	XCANRASA RAS cancel routine address	CCENTADR Channel Check entry address	RASRES SYSRES I/O address
138	140	142	144	148	156	160	164	
RASREC SYSREC I/O address	RASLOG SYSLOG device address	RASEMIOA Emergency SIO address	RASCQDSP CCB look-up routine address	SUPRETR Save area for registers 9 and 10	SUPBB Base address (X'1000') for supervisor	SUPBC Base address (X'2000') for supervisor	SUPBD Base address (X'3000') for supervisor	
168 (HIR - Hardware Instruction Retry accumulators)				180 (ECCMAIN - Main storage error accumulators)				
168	170	172	176	180	182	184	188	
HIRACNT HIR accumulated count	HIR1CNT Count threshold value	HIR1TIME Time of day for first error of group	HIRLTIME Time threshold in clock units	ECMACNT Accumulated ECC count for main storage	ECMLCNT Count threshold value	ECM1TME Time of day for first error of count	ECMLTME Time threshold in clock units	
192	194	196	197	198	199	200	201	
RESTARTA Disk restart address	RESTARTP PUB address of unit to be restarted	MCMODE Mode status for machine checks	BUFDEL Count of buffers deleted	RASMSG1 Message byte 1 ⑥	RASMSG2 Message byte 2 ⑦	EOR Records/track for SYSREC	EOT Tracks/cylinder for SYSREC	

Figure 27. RMS Monitor Table - RASTAB (Part 1 of 2)

Notes:

- 1 Areas labeled LDxxSLOT (bytes 0 - 47) are called the Load List and each of the 12 entries are formatted as follows:

BYTE	0	1	2	3
Flag Byte	Cylinder - Head - Record (disk address of R-transient in the core image directory)			

- 2 LD00SLOT flag byte:

Bit	Flag	Description
0	X'80'	\$\$RAST00 module activated.
1	X'40'	Machine check analysis to be performed.
2	X'20'	Channel check analysis to be performed.
3	X'10'	Active I/O units are valid.
4	X'08'	System termination situation.
5	X'04'	Reserved.
6	X'02'	Reserved.
7	X'01'	Attempt made to record in system termination situation.

- 3 LD01SLOT flag byte:

Bit	Flag	Description
0	X'80'	\$\$RAST01 module activated.
1	X'40'	Build and record channel check records.
2-7	--	Reserved.

- 4 LD10SLOT flag byte:

Bit	Flag	Description
0	X'80'	\$\$RAST10 module activated.
1	X'40'	Refetch calling module after issuing message.
2-7	--	Reserved.

- 5 LDxxSLOT flag byte:

Bit	Flag	Description
0	X'80'	\$\$RASTxx module activated; that is, should be fetched.
1-7	--	Reserved.

- 6 RASMSG1:

Bit	Flag	Description
0-3	--	Reserved.
4	X'08'	Timer damage.
5	X'04'	ECC in Quiet mode.
6	X'02'	Reserved.
7	X'01'	MCAR repair failed.

- 7 RASMSG2:

Bit	Flag	Description
0	X'80'	Check damage.
1	X'40'	Last track on SYSREC.
2	X'20'	C40 buffer pages deleted.
3	X'10'	Soft machine checks disabled.
4	X'08'	ECC MCI disabled.
5	X'04'	SYSREC full-run EREP.
6	X'02'	Error on SYSREC at BBCCHHR.
7	X'01'	Soft machine check.

Figure 27. RMS Monitor Table - RASTAB (Part 2 of 2)

RASLINK

0 (Decimal Displacement) CPUID	8 RASDMC	9 RASFLAGS	10 MCFLAGS	11 RASMODEL	12 RASTABA	16 RASBASE
CPU ID field	Damaged Channel byte	RAS flag byte	Machine Check flags	CPU Model	RAS Table (RASTAB) address	Base address for RAS Monitor
XXXXXXXX	X	X	X	X	XXXX	XXXX

Key to RAS Linkage Area displacements:

0	CPU ID field.
8	Address of damaged channel, or X'FF' if no channel damaged.
9	RAS Flag byte: <u>bit</u> <u>flag</u> <u>description</u>
	0      X'80'      RAS active
	1      X'40'      RAS SIO flag
	2      X'20'      RTA in control
	3      X'10'      RAS I/O delayed
	4      X'08'      Channel check on error SIO
	5      X'04'      Reserved
	6      X'02'      Channel check on SIO
	7      X'01'      I/O active for SIO
10	Machine Check Flags: <u>bit</u> <u>flag</u> <u>description</u>
	0 - 4      —      Reserved
	5      X'04'      Hard machine check
	6      X'02'      All machine records built
	7      X'01'      All channel check records built
11	Largest CPU Model.
12	Address of RAS Table (RASTAB).
16	Address used for base register in RAS Monitor Program.

Figure 28. RMS Linkage Area (RASLINK)

Displacement	0	1	2	3	4- 11	12- 15	16- 19	20- 27	28- 43	44- 51
Label	CCKEY	CCN1	CCN2	CCMOD	CCCPUID	CCDATE	CCTIME	CCNAME	CCAIOU	CCFCCW
Record 1 *	Record ID	Record Number	Total Records	CPU Model Code	CPU ID Information	Date	Time of Day	Job Name	Active I/O Units	Failing CCW

Displacement	52- 59	60- 63	64- 65	66	67- 69	70- 73	74- 77	78	79
Label	CCCSW	CCECSW	CCDEVTYPE	CCCHID	CCCUA	CCMPI		CCSYSCON	CCGUARD
Record 1 (continued)	CSW	Extended CCW	Device Type	Channel ID	Control Unit	Multi-processing Information	Reserved	System Condition Byte	Guard Byte X'FF'

Displacement	0- 3	4- 78	79
Record 2	Same as Record 1	Channel Logout Area	Same as Record 1

Displacement	0- 3	4- 24	25- 78	79
Record 3	Same as Record 1	Channel Logout Area	Unused	Same as Record 1

\* Note: Only Record 1 is written for the Model 155.

Record 1 is also addressed symbolically as CCREC with the logout data area, Bytes 4-78, addressed as CCLOGD.

Figure 29. RMS Channel Check Record on SYSREC

APPENDIX G: LABEL INFORMATION CYLINDER RECORD FORMAT

Job Control Statements or Operator Initiation Commands

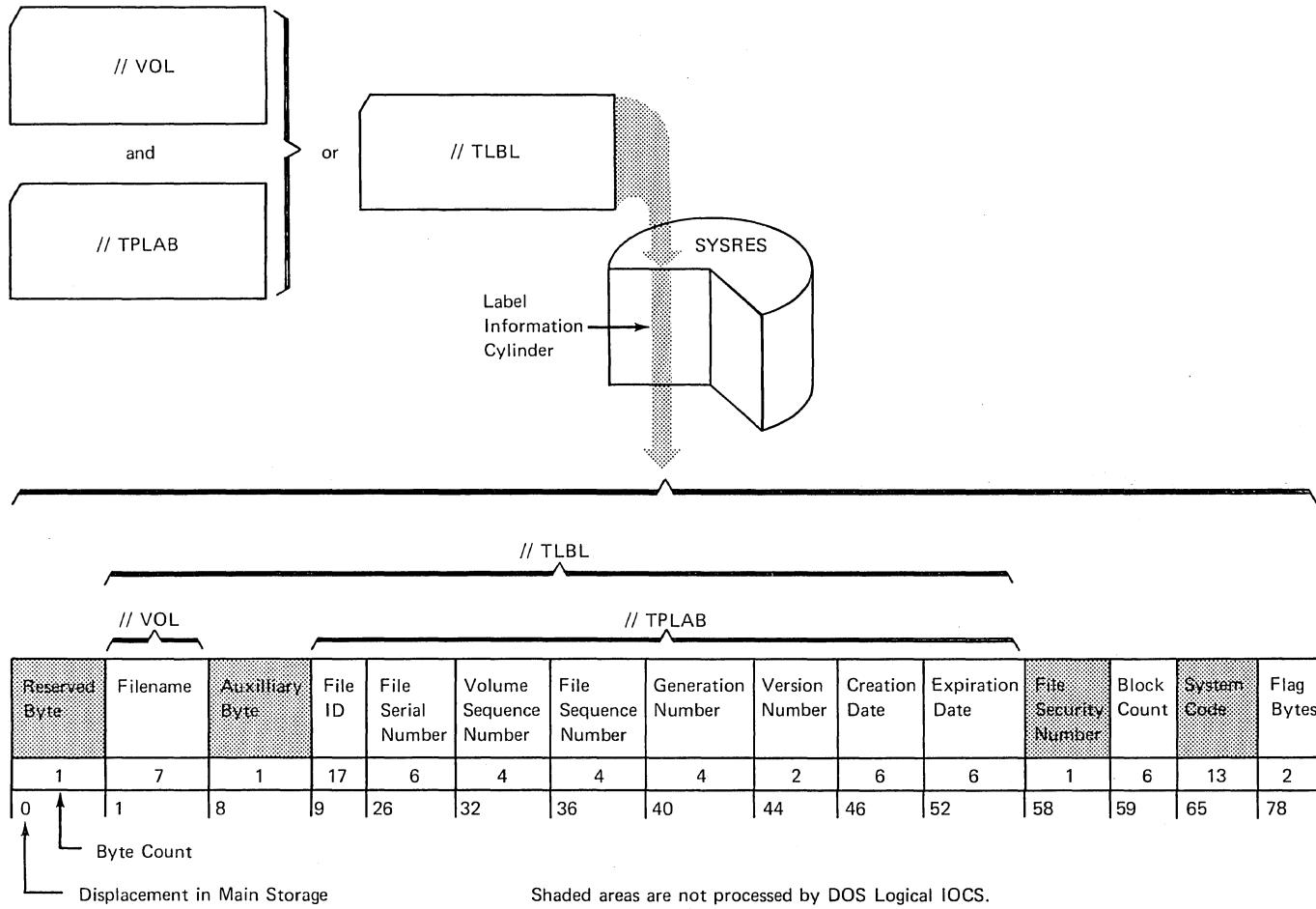


Figure 30. Format of SYSRES Tape Label Information

1	2	3		4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	Field
1	DLBL-EXTENT Indicator	Filename	DA/IS Switch	File ID	Format ID	File Serial Number	Volume Seq. No.	Creation Date	Expiration Date	Reserved	Open Code	System Code	Volume Serial Number	EXTENT Type	EXTENT Seq. No.	Extent Lower Limit	Extent Upper Limit	Logical (Symbolic) Unit Address	Another Extent if DA or ISFMS	
1	7	1	44	1	6	2	3	3	2	1	13	6	90	1	1	4	4	2	1	Bytes
0	1	8	9	53	54	60	62	65	68	70	71	84	91	92	96	100	102	103	103	Displacement

Field	Name	Description	Field	Name	Description
1.	DLBL-EXTENT	SD Bit 0: 1 = Next extent on a new pack. Bit 1: 1 = Last extent. Bit 2: 1 = Bypass extent. Bit 3: 1 = New volume on same unit. Bit 4: 1 = Extent limits omitted. Bit 5: 1 = Extent converted to DASD address. Bit 6: 1 = No EXTENT/XTENT card. Bit 7: 1 = Unused. DA or ISFMS Number of extents.	12.	System Code	Initialized to contain DOS/360 VER 3. This field is not processed by DOS.
2.	Filename		13.	Volume Serial No.	Volume serial number for extent.
3.	DA/IS Switch	Bits 0-3: Unused. Bit 4: 1 = Extent limits omitted. Bit 5: 1 = Extent converted to DASD address. Bit 6 & 7: Unused.	14.	Extent Type	Same codes as in Format - 1 label: X'00' = Next three fields do not indicate any extent. X'01' = Prime data area (ISFMS) or consecutive area, etc., (that is the extent containing the user's data records). X'02' = Overflow area of an ISFMS file. X'04' = Cylinder index or master index of an ISFMS file. X'40' = User label track area. X'8n' = Shared cylinder indicator, where n = 1, 2, or 4.
4.	File ID	File identifier including generation and version numbers. If field is missing on DLBL card, Filename padded with blanks is inserted.	15.	Extent Seq. No.	Number of extent as determined by the extent card sequence.
5.	Format ID	Numeric 1 is inserted.	16.	Extent Lower & Upper Limits	Before the OPEN, DLBL/EXTENT information is in the relative track form of HHNNT followed by three bytes of binary zeros. HH = Relative (to 0) start address in tracks. NN = Number of tracks. T = 0 or upper track number for split cylinder in SD files.
6.	File Serial No.	Volume serial number from first extent.	17.		Following an OPEN on DLBL/EXTENT cards, or whenever DLAB/XTENT cards are used, the extent lower and upper limits are each in the CCHH format.
7.	Volume Seq. No.	Always initialized to X'0001'.	18.	Logical (Symbolic) Unit Address	This 2-byte field identifies the logical unit with the same code as that used in a CCB. The first byte identifies the unit class: X'00' = System Logical Unit X'01' = Programmer Logical Unit The second byte identifies the logical unit within its class.
8.	Creation Date	Initialized with 3 bytes of X'00'.	19.	2321 Lower Cell	Thus X'0003' denotes SYSLST and X'0103' denotes SYS003'.
9.	Expiration Date	If date is in the form YYDDD, it is converted to YDD. If date is in retention period form, 1 to 4 characters, the field is padded with binary zeros.		2321 Upper Cell	2321 extent lower and upper cell limit. This 2-byte field contains zeros for 2311/2314/2319 disk.
10.	Reserved	The retention period, if specified is converted to a 2-byte number and inserted in this field.			
11.	Open Code	DLBL type: S = Sequential D = Direct Access C or E = Indexed sequential File Management System where: C = Load create function E = Load extend function			

Note: For Sequential Disk files, a complete 104-byte block is repeated for each new EXTENT.  
For Direct Access and ISFMS files, only fields 13 through 18 are repeated for each EXTENT.

Figure 31. SYSRES DASD Label Information

APPENDIX H: MICROFICHE CROSS-REFERENCE INDEX

The index gives the relationship of core-image phase names, relocatable module names, microfiche labels, and microfiche identification numbers to each other.

An asterisk indicates the microfiche label. If the microfiche label differs from both the phase and the module name, it is so indicated in parentheses.

When a phase or module takes up more than one microfiche card, the identification number of only the first card is shown.

For the complete microfiche cross-reference index, see Introduction to DOS Logic listed in the Preface.

<u>Core Image Phase Name</u>	<u>Relocatable Module Name</u>	<u>Card ID</u>
\$A\$IPL1*	None	CTL.001.00
\$A\$IPL2 (\$A\$IPL1)	None	CTL.001.00
\$IPLRT2	IJB IPL*	CTL.201.00
\$IPLRT3	IJB IPL3 (IJB IPL)	CTL.201.00
\$IPLRT4	IJB IPL4 (IJB IPL)	CTL.201.00
\$JOBACCT* <sup>1</sup>	\$JOBACCT	CTL.187.25
\$JOBCTLA	IJB JC1*	CTL.202.00
\$JOBCTLB	IJB JC2*	CTL.203.00
\$JOBCTLF	IJB JC5*	CTL.206.00
\$JOBCTLG	IJB JC3*	CTL.204.00
\$JOBCTLJ	IJB JC4*	CTL.205.00
\$JOBCTLK	IJB JC6*	CTL.207.00
\$JOBCTLM	IJB JC7*	CTL.208.00
\$JOBCTLN	IJB JC8*	CTL.208.50

<sup>1</sup>\$JOBACCT is a dummy entry to be replaced by a user-written routine.

## GLOSSARY

For a more complete list of data processing terms, refer to IBM Data Processing Techniques, A Data Processing Glossary, GC20-1699.

American National Standard Label Format: The tape file format used when the label is written in the ASCII mode.

ASCII (American National Standard Code for Information Interchange): A 128-character, 7-bit code. The high-order bit in the System/360 8-bit environment is zero.

block prefix: An optional, 0-99 byte field preceding an ASCII record. It contains user specified data or, for variable-length (format D) records, the physical record length.

CCH (Channel Check Handler): A feature that assesses System/370 channel errors to determine if the system can continue operations.

channel inboard error: An error that occurs between one I/O device and the central processing unit.

data conversion: The process of changing data from one form of representation to another.

DOS Volume Statistics: A facility that monitors and records the number of temporary read and write errors on currently accessed tape volumes. This facility has two options, Error Statistics by Tape Volume (ESTV) and Error Volume Analysis (EVA).

EREP (Environmental Recording, Editing, and Printing): A program that processes the data contained on the system recorder file.

ESTV (Error Statistics by Tape Volume): One of the two options of the DOS Volume Statistics. With ESTV support, the system collects data on tape errors by volume for any tape volumes used by the system.

EVA (Error Volume Analysis): One of the two options of the DOS Volume Statistics. With this option, the system issues a message to the operator when a number of temporary read or write errors (specified by the user at system generation time) has been exceeded on a currently accessed tape volume.

file section number: For ASCII files, the number indicates the order of a volume in a given file or multifile set. The first file must be numbered 0001. The ASCII file section number is equivalent to the EBCDIC volume sequence number.

I/O (Input/Output) error logging: The process of recording OBR and SDR records on the system recorder file.

job accounting interface: A function that accumulates accounting information for each job step to: charge usage of the system, help plan new applications, and help supervise system operation more efficiently.

load point: The beginning of the recording area on a reel of magnetic tape.

MCAR (Machine Check Analysis and Recording): A feature that records System/370 machine check interrupt error information on the system recorder file and then attempts to recover from the interrupt.

MCI (Machine Check Interrupt): The interrupt that occurs if the central processing unit fails to operate.

MCRR (Machine Check Recording and Recovery): The recording of pertinent data on the system recorder file after either a machine check interrupt or a channel inboard error occurred on System/360 Model 30, Model 40, or Model 50.

nonstandard labels: Labels that do not conform to the System/360 standard label specifications. They can be any length, need not have a specified identification, and do not have a fixed format.

OBR (Outboard Recorder): A feature that records pertinent data on the system recorder file when an unrecoverable I/O error occurs.

PCIL (Private Core Image Library): A file referenced in the same manner and for the same purposes as the system core image library, but distinct from the system core image library. PCIL increases available core image library space to enable compiling, linkage editing, and executing in the foreground partition, when a private core image library is assigned to that foreground partition.

RDE: See Reliability Data Extractor.

Reliability Data Extractor (RDE): A function that provides hardware reliability data that is analyzed by IBM.

RMS (Recovery Management Support): A feature for System/370 that consists of the MCAR (Machine Check Analysis and Recording) and CCH (Channel Check Handler) functions. RMS gathers information about System/370

hardware reliability and attempts certain error recovery operations. RMS is a part of the entire reliability, availability, and serviceability support for System/370.

SDR (Statistical Data Recorder): A feature that records the cumulative error status of an I/O device on the system recorder file.

system recorder file: The file that is used to record hardware reliability data.



Indexes to systems reference library manuals are consolidated in the publication DOS Master Index, GC24-5063. For additional information about any subject listed below, refer to other publications listed for the same subject in the Master Index.

ACCTCOMM (job accounting interface common)  
table 239  
ACTION statement processor  
    detail chart 143  
    general chart 31  
add routine 15  
ADD statement 16  
ALLOC statement processor  
    detail chart 153  
    general chart 30  
American National Standard Label Format,  
    definition 248  
ASCII, definition 248  
ASSGN statement processor  
    detail chart 79  
    general chart 25

background communications region extension  
    223  
block prefix 248

CANCEL statement processor  
    detail chart 123  
    general chart 29  
CATALR card processor  
    detail chart 152  
    general chart 32  
CCB (command control block) 226  
CCH (channel check handler), definition  
    248  
channel inboard error, definition 248  
CLOSE statement processor  
    detail chart 89  
    general chart 26  
command control block (CCB) 226  
communications region extension, background  
    223  
communications region, SDR 224  
communications region, supervisor 218  
COMREG and CCB 218  
cylinder record format label information  
    245

DASD label information SYSRES 246  
data conversion, definition 248  
data file block format (DFB) 203  
DATE statement processor  
    detail chart 147  
    general chart 32  
DEL statement 16

delete routine 15  
DFB (data file block) format 203  
DIB (disk information block) table 230  
disk information block table (DIB) 230  
Disk Operating System program flow 10  
DLAB statement processor  
    detail chart 164  
    general chart 33  
DLBL statement processor  
    detail chart 163  
    general chart 34  
DOS volume statistics, definition 248  
DVCDN statement processor  
    detail chart 109  
    general chart 27  
DVCUP statement processor  
    detail chart 111  
    general chart 27

ENTRY statement processor  
    detail chart 143  
    general chart 31  
EOJ (/&) statement processor  
    detail chart 121  
    general chart 29  
EREP, definition 248  
error messages 215  
ESTV (error statistics by tape volume),  
    definition 248  
EVA (error volume analysis), definition  
    248  
EXEC statement processor  
    detail chart 124  
    general chart 29  
explanation of flowchart symbols 216  
EXTENT statement processor  
    detail chart 167  
    general chart 34

FICL (first-in-class-list) table 228  
file section number, definition 248  
first-in-class-list table (FICL) 228  
flowchart symbols, explanation of 216  
flowcharts  
    detail 37  
    general 21  
formats  
    DFB 203  
    FICL 204  
    label information cylinder record  
        245  
    NICL 204  
phase-vector table entry 212

glossary 248

HOLD statement processor  
    detail chart 140

HOLD statement processor (CONT.)  
     general chart 31  
  
 I/O error logging, definition 248  
 I/O flow 17  
 I/O table interrelationship 236  
 I/O tables (also see tables) 227  
     one-device system 12  
     two-device system 12  
 INCLUDE statement processor  
     detail chart 143  
     general chart 32  
 initial program load  
     (see IPL)  
 introduction 11  
 IPL (initial program load) 12  
 IPL phases  
     (see phases, IPL)  
 IPL  
     ADD statement 16  
     DEL statement 16  
     label list 197  
     main storage map 13  
     SET statement 16  
  
 JIB (job information block) table 237  
 job accounting interface common table 239  
 job accounting interface partition table  
     238  
 job accounting interface  
     definition 248  
     detail chart 191  
     general chart 36  
 job control label list 199  
 job control phases  
     (see phases, job control)  
 job control program 17  
     I/O flow 17  
     program flow 17  
     storage allocation 19  
 job control statement processor  
     see specific processor  
 job information block table (JIB) 237  
 JOB statement processor  
     detail chart 119  
     general chart 29  
  
 label information cylinder record format  
     245  
 label list  
     IPL 197  
     job control 199  
 LBLTYP statement processor  
     detail chart 159  
     general chart 33  
 LISTIO printout 217  
 LISTIO statement processor  
     detail chart 102  
     general chart 27  
 load point, definition 248  
 LOG statement processor  
     detail chart 150  
  
 LOG statement processor (CONT.)  
     general chart 30  
 logical unit block table (LUB) 228  
 LUB (logical unit block) table 228  
  
 main storage map, IPL 13  
 MAP statement processor  
     detail chart 111  
     general chart 27  
 MCAR, definition 248  
 MCI (machine check interrupt), definition  
     248  
 MCRR, definition 248  
 microfiche cross-reference index 247  
 MTC statement processor  
     detail chart 144  
     general chart 32  
  
 next-in-class-list (NICL) 228  
 NICL (next-in-class-list) 228  
 NOLOG statement processor  
     detail chart 150  
     general chart 30  
 nonstandard labels, definition 248  
  
 OBR (outboard recorder), definition 248  
 OPTION statement processor  
     detail chart 129  
     general chart 28  
  
 PAUSE statement processor  
     detail chart 150  
     general chart 30  
 PCIL, definition 248  
 PHASE statement processor  
     detail chart 143  
     general chart 31  
 phase-vector table entry format 212  
 phases, IPL  
     \$\$A\$IP1 detail chart 37  
     \$\$A\$IP1 general chart 21  
     \$\$A\$IP1 text 12  
     \$\$A\$IP2 detail chart 38  
     \$\$A\$IP2 general chart 21  
     \$\$A\$IP2 text 12  
     \$IPRT2 detail chart 45  
     \$IPRT2 general chart 22  
     \$IPRT2 text 14  
     \$IPRT3 detail chart 52  
     \$IPRT3 general chart 23  
     \$IPRT3 text 14,15  
     \$IPRT4 detail chart 58  
     \$IPRT4 general chart 23  
     \$IPRT4 text 15  
 phases, job control  
     \$\$BLSTIO detail chart 193  
     \$\$BLSTIO general chart 27  
     \$\$BLSTIO text 18  
     \$JOBCTLA detail chart 66  
     \$JOBCTLA general chart 24

phases, job control (CONT.)  
 \$JOBCTLA text 17  
 \$JOBCTLB detail chart 79  
 \$JOBCTLB general chart 25  
 \$JOBCTLB text 18  
 \$JOBCTLF detail chart 102  
 \$JOBCTLF general chart 27  
 \$JOBCTLF text 18  
 \$JOBCTLG detail chart 119  
 \$JOBCTLG general chart 28  
 \$JOBCTLG text 18  
 \$JOBCTLJ detail chart 140  
 \$JOBCTLJ general chart 30  
 \$JOBCTLJ text 18  
 \$JOBCTLK detail chart 159  
 \$JOBCTLK general chart 33  
 \$JOBCTLK text 18  
 \$JOBCTLM detail chart 176  
 \$JOBCTLM general chart 35  
 \$JOBCTLM text 18  
 \$JOBCTLN detail chart 191  
 \$JOBCTLN general chart 36  
 \$JOBCTLN text 18  
 physical unit block (PUB) 227  
 PIB (program information block)  
     flag expansions 232  
     table 231  
 PUB (physical unit block) table 227  
  
 RASLINK (RMS linkage area) 243  
 RASTAB (RMS monitor) table 241  
 RDE (reliability data extractor),  
     definition 249  
 record format, label information cylinder  
     245  
 LSE statement processor  
     detail chart 140  
     general chart 31  
 RESET statement processor  
     detail chart 107  
     general chart 27  
 RF statement processor  
     detail chart 176  
     general chart 35  
 RMS (recovery management support),  
     definition 249  
 RMS channel check record on SYSREC 244  
 RMS linkage area 243  
 RMS machine check record on SYSREC 240  
 RMS monitor table 241  
 ROD statement processor  
     detail chart 183  
     general chart 35  
 RSTRRT statement processor  
     detail chart 173  
     general chart 34  
  
 SDR (statistical data recorder), definition  
     249  
 SDR communications region 224  
 set routine 15  
 SET statement 16  
 SET statement processor  
     detail chart 146  
     general chart 32  
  
 statement processors  
     (see specific processor)  
 STOP statement processor  
     detail chart 151  
     general chart 30  
 storage protection 14  
 supervisor communications region 218  
 SYSRES DASD label information 246  
 SYSRES tape label information format 245  
 system recorder file, definition 249  
 SYSUSE 14  
  
 tables  
     DIB 230  
     FICL 228  
     I/O 12,227  
     I/O interrelationship 236  
     JIB 237  
     job accounting interface 238,239  
     LUB 228  
     NICL 228  
     PIB (first part of table) 231  
     PIB (second part of table) 233  
     PUB 227  
     RMS monitor 241  
     TEBV 234  
     tape error block (TEB) 229  
     TEB (tape error block) 229  
     TEBV error blocks 234  
     TEBV status blocks 234  
     TEBV table 234  
     TLBL statement processor  
         detail chart 160  
         general chart 34  
     TPLAB statement processor  
         detail chart 159  
         general chart 33  
  
 UCS statement processor  
     detail chart 141  
     general chart 31  
 UNA statement processor  
     detail chart 114  
     general chart 27  
 UNBATCH statement processor  
     detail chart 112  
     general chart 27  
 UPSI statement processor  
     detail chart 149  
     general chart 32  
  
 version and modification level (see intro.  
     PLM index)  
 VOL statement processor  
     detail chart 159  
     general chart 33  
  
 XTENT statement processor  
     detail chart 165  
     general chart 33



# Technical Newsletter

File No. S360-36 (DOS Release 26)

Base Publ. No. GY24-5086-4

This Newsletter No. GN33-8686

Date: October 28, 1971

Previous Newsletter Nos. None

## DOS IPL AND JOB CONTROL PROGRAM LOGIC MANUAL

©IBM Corp. 1968, 1969, 1970, 1971

This Technical Newsletter, a part of Release 26 of the IBM Disk Operating System, provides replacement pages for your publication. These replacement pages remain in effect for subsequent DOS releases unless specifically altered. Pages to be inserted and/or removed are:

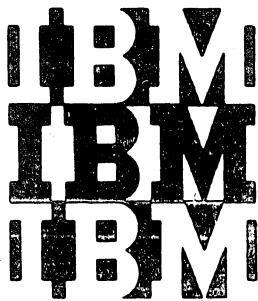
Cover- 10	137-140
41, 42	167,168
61, 62	215,216
67, 68	221,222
77, 78	225,226
121,122	231,232
125,126	235-238

A change to an illustration is indicated by a vertical line to the left of the change.

### Summary of Amendments

This TNL contains documentation changes for enhancements to OLTEP. It also contains maintenance changes and technical corrections.

Note: Please insert this page in your publication to provide a record of changes.



## Program Logic

### DOS IPL and Job Control

#### Program Number 360N-CL-453

This reference publication describes the internal logic of the IBM Operating System, Initial Program Load (IPL) and Job Control Programs. It is intended for use by persons involved in program maintenance and by system programmers who are altering the program design.

Program logic information is not needed for normal operation of the IPL and Job Control Programs. It is designed to be used as a supplement to the program listing.

Effective use of this manual requires an understanding of IBM System/360 or System/370 operation and of the IBM Disk Operating System control and service programs, macro instructions, and operating procedures. Reference publications for this information are listed in the Preface of this manual.

For titles and abstracts of other associated publications, see the IBM System/360 and System/370 Bibliography, GA22-6822.

Fifth Edition (June, 1971)

This publication was formerly titled IBM System/360 Disk Operating System IPL and Job Control Programs. Although titles of some DOS publications (including this one) have been simplified, the change does not affect the contents of the publications.

This edition, with Technical Newsletter GN33-8686, applies to Release 26 of the IBM Disk Operating System and to all subsequent releases until otherwise indicated in new editions or Technical Newsletters. Changes are continually made to the specifications herein; before using this publication in connection with the operation of IBM systems, consult the latest System/360 and System/370 SRL Newsletter, GN20-0360, for the editions that are applicable and current.

This edition is a major revision of, and obsoletes, GY24-5086-3.

Summary of Amendments

This edition contains maintenance changes, and it reflects Job Accounting Interface, OLTEP, Data Set Security, PCIL (Private Core Image Library), MCAR/CCH support, and RDE (Reliability Data Extractor). The IBM 3211 Printer and the IBM 1255/1259 Magnetic Character Readers are also supported in this edition.

Technical Newsletter GN33-8686 contains maintenance changes and technical corrections.

The flowchart symbols used in this manual conform with American National Standards Institute, Inc., flowcharting standards. See Appendix C for an explanation of the new symbols.

Changes or additions to the text and illustrations are indicated by a vertical line to the left of the change.

Request for copies of IBM publications should be made to your IBM representative or to the IBM branch office serving your locality.

A form for readers' comments is provided at the back of this publication. If the form has been removed, comments may be addressed to IBM Laboratory, Publications Dept., P.O. Box 24, Uithoorn, Netherlands. Comments become the property of IBM.

This Program Logic Manual (PLM) is a detailed guide to the IBM Disk Operating System IPL and Job Control programs. It supplements the program listings by providing descriptive text and flowcharts.

Note: Although titles of some DOS publications have been simplified, the change does not affect the contents of the publications.

For overall system control logic description, this PLM is used with six other PLMs:

- Introduction to DOS Logic, GY24-5017.
- DOS Linkage Editor, GY24-5080.
- DOS Supervisor and Related Transients, GY24-5151.
- DOS Logical Transients, GY24-5152.
- DOS System Service Programs, GY24-5153.
- DOS Librarian, GY24-5079.

Prerequisite publications that will aid in the use of this manual are:

- IBM System/360 Principles of Operation, GA22-6821.
- DOS System Control and Service, GC24-5036.
- IBM System/360 Disk and Tape Operating Systems, Assembler Specifications, GC24-3414.

Publications related in subject matter to the seven system control PLMs are:

- DOS Supervisor and I/O Macros, GC24-5037.
- DOS System Generation, GC24-5033.
- DOS Operating Guide, GC24-5022.
- DOS Messages, GC24-5074.
- DOS Data Management Concepts, GC24-3427.

Titles and abstracts of other related publications are listed in the IBM System/360 and System/370 Bibliography, GA22-6822.

This manual consists of four major sections. The first section is an introduction to the IPL and Job Control programs. The next two sections are a discussion of contents of the IPL and Job Control phases. The last section of the manual, the appendixes, contains label lists, error messages, charts, and tables for use as references in analyzing program details.

The detailed flowcharts are identified by letters AA through ZZ. Numerals such as 00 for the program level flowcharts identify the more general flowcharts.



INTRODUCTION . . . . .	11	Initial Program Load (\$\$A\$IPL1, \$\$A\$IPL2, \$IPLRT2, \$IPLRT3, and \$IPLRT4) Charts AA-AZE . . . . .	197
Initial Program Load (IPL) . . . . .	11	Job Control (\$JOBCTLA, \$JOBCTL <sub>D</sub> , \$JOBCTL <sub>F</sub> , \$JOBCTL <sub>G</sub> , \$JOBCTL <sub>J</sub> , \$JOBCTL <sub>K</sub> , \$JOBCTL <sub>M</sub> , \$JOBCTL <sub>N</sub> , and \$\$BLSTIO) Charts BA-LD . . . . .	199
Job Control Program (\$JOBCTLA-\$JOBCTLN) . . . . .	11		
IPL PROGRAM . . . . .	12		
Initial Program Load Program (IPL),			
Chart 01 . . . . .	12		
\$IPLRT2, Chart 02 . . . . .	14		
\$IPLRT3, Chart 03 . . . . .	14		
\$IPLRT4, Chart 03 . . . . .	15		
JOB CONTROL PROGRAM . . . . .	17	APPENDIX C: EXPLANATION OF FLOWCHART SYMBOLS . . . . .	216
I/O Flow . . . . .	17	APPENDIX D: SAMPLE LISTIO PRINTOUTS . .	217
Program Flow . . . . .	17	APPENDIX E: COMREG AND CCB . . . . .	218
\$JOBCTLA (Chart 04) . . . . .	17	APPENDIX F: I/O TABLES . . . . .	227
\$JOBCTL <sub>D</sub> (Charts 05 and 06) . . . . .	18	APPENDIX G: LABEL INFORMATION CYLINDER RECORD FORMAT . . . . .	245
\$JOBCTL <sub>F</sub> (Chart 07) . . . . .	18	APPENDIX H: MICROFICHE CROSS-REFERENCE INDEX . . . . .	247
\$JOBCTL <sub>G</sub> (Charts 08 and 09) . . . . .	18	GLOSSARY . . . . .	248
\$JOBCTL <sub>J</sub> (Charts 10, 11, and 12) . . . . .	18		
\$JOBCTL <sub>K</sub> (Charts 13 and 14) . . . . .	18		
\$JOBCTL <sub>M</sub> (Chart 15) . . . . .	18		
\$JOBCTL <sub>N</sub> (Chart 16) . . . . .	18		
\$\$BLSTIO . . . . .	18		
CHARTS . . . . .	21	INDEX . . . . .	251
APPENDIX A: LABEL LIST . . . . .	197		

CHARTS

Chart 00. Disk Operating System	
Program Flow . . . . .	10
Chart 01. Initial Program Load (\$\$A\$IPL1 and \$\$A\$IPL2) . . . . .	21
Chart 02. Initial Program Load (\$IPLRT2) . . . . .	22
Chart 03. Initial Program Load (\$IPLRT3 and \$IPLRT4) . . . . .	23
Chart 04. Job Control (\$JOBCTLA) Root Phase . . . . .	24
Chart 05. Job Control (\$JOBCTL0) Statement Processor (Part 1 of 2) . . . . .	25
Chart 06. Job Control (\$JOBCTL0) Statement Processor (Part 2 of 2) . . . . .	26
Chart 07. Job Control (\$JOBCTLF) Statement Processor . . . . .	27
Chart 08. Job Control (\$JOBCTLG) Statement Processor (Part 1 of 2) . . . . .	28
Chart 09. Job Control (\$JOBCTLG) Statement Processor (Part 2 of 2) . . . . .	29
Chart 10. Job Control (\$JOBCTLJ) Statement Processor (Part 1 of 3) . . . . .	30
Chart 11. Job Control (\$JOBCTLJ) Statement Processor (Part 2 of 3) . . . . .	31
Chart 12. Job Control (\$JOBCTLJ) Statement Processor (Part 3 of 3) . . . . .	32
Chart 13. Job Control (\$JOBCTLK) Statement Processor (Part 1 of 2) . . . . .	33
Chart 14. Job Control (\$JOBCTLK) Statement Processor (Part 2 of 2) . . . . .	34
Chart 15. Job Control (\$JOBCTLM) Statement Processor . . . . .	35
Chart 16. Job Control (\$JOBCTLN) Statement Processor . . . . .	36
Chart AA. \$\$A\$IPL1 - IPL Bootstrap . . . . .	37
Chart AB. \$\$A\$IPL2 - Clear Storage and Load Supervisor (Part 1 of 2) . . . . .	38
Chart AC. \$\$A\$IPL2 - Load Supervisor (Part 2 of 2) . . . . .	39
Chart AD. \$\$A\$IPL2 - Build Two-Device System . . . . .	40
Chart AE. \$\$A\$IPL2 - Move I/O Tables . . . . .	41
Chart AF. \$\$A\$IPL2 - Build PUB Table . . . . .	42
Chart AG. \$\$A\$IPL2 - Common Move Subroutine . . . . .	43
Chart AH. \$\$A\$IPL2 - Update Subroutines . . . . .	44
Chart AJ. \$IPLRT2 - Initialization Routine . . . . .	45
Chart AK. \$IPLRT2 - Monitor, Read Control Card, and Operation Scan Routines . . . . .	46
Chart AL. \$IPLRT2 - Monitor Core Usage for ADD and DEL Cards and Allocation Subroutine . . . . .	47
Chart AM. \$IPLRT2 - Monitor Core Usage for SET Card and Allocation Subroutine . . . . .	48
Chart AN. \$IPLRT2 - Move Routine . . . . .	49
Chart AP. \$IPLRT2 - Update LUB, Get Operand, and Conversion Subroutines . . . . .	50
Chart AQ. \$IPLRT2 - I/O Subroutines . . . . .	51
Chart AR. \$IPLRT3 - ADD a Device (Part 1 of 2) . . . . .	52
Chart AS. \$IPLRT3 - ADD a Device (Part 2 of 2) . . . . .	53
Chart AT. \$IPLRT3 - Delete a PUB . . . . .	54
Chart AU. \$IPLRT3 - Build PUB Table Subroutine . . . . .	55
Chart AV. \$IPLRT3 - Device Type Conversion Subroutine . . . . .	56
Chart AW. \$IPLRT3 - Conversion and Update FOCL Subroutines . . . . .	57
Chart AX. \$IPLRT4 - SET Statement Processor and Assign SYSLOG . . . . .	58
Chart AY. \$IPLRT4 - Assign SYSRES and Move I/O Tables . . . . .	59
Chart AZ. \$IPLRT4 - I/O and Check Device Type Subroutines . . . . .	60
Chart AZA. \$IPLRT4 - Find PUB and I/O Subroutines . . . . .	61
Chart AZB. \$IPLRT4 - Date and Time Subroutines . . . . .	62
Chart AZC. \$IPLRT4 - Copy Subroutine . .	63
Chart AZD. \$IPLRT4 - Set Job Control Flags Subroutine . . . . .	64
Chart AZE. \$IPLRT4 - Reorder MPX Channel LUBs and PUBS . . . . .	65
Chart BA. \$JOBCTLA - Initialization . .	66
Chart BB. \$JOBCTLA - Initialization and Control Statement Read . . . . .	67
Chart BC. \$JOBCTLA - Phase Vector Table Lookup . . . . .	68
Chart BD. \$JOBCTLA - DSKINT Subroutine	69
Chart BE. \$JOBCTLA - Message Subroutines . . . . .	70
Chart BF. \$JOBCTLA - Operand Scan Subroutines . . . . .	71
Chart BG. \$JOBCTLA - Miscellaneous Subroutines . . . . .	72
Chart BH. \$JOBCTLA - EXCP Subroutines (Part 1 of 2) . . . . .	73
Chart BJ. \$JOBCTLA - EXCP Subroutines (Part 2 of 2) . . . . .	74
Chart BK. \$JOBCTLA - Miscellaneous Subroutines . . . . .	75
Chart BL. \$JOBCTLA - Error Subroutines (Part 1 of 2) . . . . .	76
Chart BM. \$JOBCTLA - Error Subroutines (Part 2 of 2) . . . . .	77
Chart BN. \$JOBCTLA - Relocation Subroutines . . . . .	78
Chart CA. \$JOBCTL0 - ASSGN Statement Processor (Part 1 of 10) . . . . .	79
Chart CB. \$JOBCTL0 - ASSGN Statement Processor (Part 2 of 10) . . . . .	80
Chart CC. \$JOBCTL0 - ASSGN Statement Processor (Part 3 of 10) . . . . .	81
Chart CD. \$JOBCTL0 - ASSGN Statement Processor (Part 4 of 10) . . . . .	82
Chart CE. \$JOBCTL0 - ASSGN Statement Processor (Part 5 of 10) . . . . .	83
Chart CF. \$JOBCTL0 - ASSGN Statement Processor (Part 6 of 10) . . . . .	84

Chart CG. \$JOBCTLD - ASSGN Statement		Chart EB. \$JOBCTLG - JOB Statement	
Processor (Part 7 of 10) . . . . .	85	Processor (Part 2 of 2) . . . . .	120
Chart CH. \$JOBCTLD - ASSGN Statement		Chart EC. \$JOBCTLG - /& Statement	
Processor (Part 8 of 10) . . . . .	86	Processor (Part 1 of 3) . . . . .	121
Chart CJ. \$JOBCTLD - ASSGN Statement		Chart ED. \$JOBCTLG - /& Statement	
Processor (Part 9 of 10) . . . . .	87	Processor (Part 2 of 3) . . . . .	122
Chart CK. \$JOBCTLD - ASSGN Statement		Chart EE. \$JOBCTLG - /& Statement	
Processor (Part 10 of 10) . . . . .	88	Processor (Part 3 of 3) and CANCEL	
Chart CL. \$JOBCTLD - CLOSE Statement		Statement Processor . . . . .	123
Processor . . . . .	89	Chart EF. \$JOBCTLG - EXEC Statement	
Chart CM. \$JOBCTLD - Miscellaneous		Processor (Part 1 of 5) . . . . .	124
Subroutines (Part 1 of 3) . . . . .	90	Chart EG. \$JOBCTLG - EXEC Statement	
Chart CN. \$JOBCTLD - Miscellaneous		Processor (Part 2 of 5) . . . . .	125
Subroutines (Part 2 of 3) . . . . .	91	Chart EH. \$JOBCTLG - EXEC Statement	
Chart CP. \$JOBCTLD - Miscellaneous		Processor (Part 3 of 5) . . . . .	126
Subroutines (Part 3 of 3) . . . . .	92	Chart EJ. \$JOBCTLG - EXEC Statement	
Chart CQ. \$JOBCTLD - Close Subroutine	. 93	Processor (Part 4 of 5) . . . . .	127
Chart CR. \$JOBCTLD - Miscellaneous		Chart EK. \$JOBCTLG - EXEC Statement	
Subroutines (Part 1 of 5) . . . . .	94	Processor (Part 5 of 5) . . . . .	128
Chart CS. \$JOBCTLD - Miscellaneous		Chart EL. \$JOBCTLG - OPTION Statement	
Subroutines (Part 2 of 5) . . . . .	95	Processor (Part 1 of 4) . . . . .	129
Chart CT. \$JOBCTLD - Miscellaneous		Chart EM. \$JOBCTLG - OPTION Statement	
Subroutines (Part 3 of 5) . . . . .	96	Processor (Part 2 of 4) . . . . .	130
Chart CU. \$JOBCTLD - Miscellaneous		Chart EN. \$JOBCTLG - OPTION Statement	
Subroutines (Part 4 of 5) . . . . .	97	Processor (Part 3 of 4) . . . . .	131
Chart CV. \$JOBCTLD - Miscellaneous		Chart EP. \$JOBCTLG - OPTION Statement	
Subroutines (Part 5 of 5) . . . . .	98	Processor (Part 4 of 4) . . . . .	132
Chart CW. \$JOBCTLD - SYSXXX Operand		Chart FA. \$JOBCTLG - Time Stamping	
Processor . . . . .	99	Subroutines . . . . .	133
Chart CX. \$JOBCTLD - EXCP Subroutines	. 100	Chart FB. \$JOBCTLG - Miscellaneous	
Chart CY. \$JOBCTLD - Error Subroutines	. 101	Subroutines (Part 1 of 3) . . . . .	134
Chart DA. \$JOBCTLF - LISTIO Statement		Chart FC. \$JOBCTLG - Miscellaneous	
Processor (Part 1 of 5) . . . . .	102	Subroutines (Part 2 of 3) . . . . .	135
Chart DB. \$JOBCTLF - LISTIO Statement		Chart FD. \$JOBCTLG - Miscellaneous	
Processor (Part 2 of 5) . . . . .	103	Subroutines (Part 3 of 3) . . . . .	136
Chart DC. \$JOBCTLF - LISTIO Statement		Chart FE. \$JOBCTLG - Label Processing	
Processor (Part 3 of 5) . . . . .	104	Subroutines (Part 1 of 2) . . . . .	137
Chart DD. \$JOBCTLF - LISTIO Statement		Chart FF. \$JOBCTLG - Label Processing	
Processor (Part 4 of 5) . . . . .	105	Subroutines (Part 2 of 2) . . . . .	138
Chart DE. \$JOBCTLF - LISTIO Statement		Chart FG. \$JOBCTLG - Error Subroutines	139
Processor (Part 5 of 5) . . . . .	106	Chart GA. \$JOBCTLJ - RELSE and HOLD	
Chart DF. \$JOBCTLF - RESET Statement		Statement Processors . . . . .	140
Processor (Part 1 of 2) . . . . .	107	Chart GB. \$JOBCTLJ - UCS Statement	
Chart DG. \$JOBCTLF - RESET Statement		Processor (Part 1 of 2) . . . . .	141
Processor (Part 2 of 2) . . . . .	108	Chart GC. \$JOBCTLJ - UCS Statement	
Chart DH. \$JOBCTLF - DVCDN Statement		Processor (Part 2 of 2) . . . . .	142
Processor (Part 1 of 2) . . . . .	109	Chart GD. \$JOBCTLJ - ACTION and	
Chart DJ. \$JOBCTLF - DVCDN Statement		INCLUDE Statement Processors . . . . .	143
Processor (Part 2 of 2) . . . . .	110	Chart GE. \$JOBCTLJ - MTC Statement	
Chart DK. \$JOBCTLF - DVCUP and MAP		Processor (Part 1 of 2) . . . . .	144
Statement Processors . . . . .	111	Chart GF. \$JOBCTLJ - MTC Statement	
Chart DL. \$JOBCTLF - UNBATCH Statement		Processor (Part 2 of 2) . . . . .	145
Processor . . . . .	112	Chart GG. \$JOBCTLJ - SET Statement	
Chart DM. \$JOBCTLF - Error Subroutines		Processor (Part 1 of 3) . . . . .	146
and UNA Statement Processor		Chart GH. \$JOBCTLJ - SET Statement	
(Part 1 of 2) . . . . .	113	Processor (Part 2 of 3) . . . . .	147
Chart DN. \$JOBCTLF - UNA Statement		Chart GJ. \$JOBCTLJ - SET Statement	
Processor (Part 2 of 2) . . . . .	114	Processor (Part 3 of 3) . . . . .	148
Chart DP. \$JOBCTLF - Miscellaneous		Chart GK. \$JOBCTLJ - UPSI Statement	
Subroutines (Part 1 of 4) . . . . .	115	Processor . . . . .	149
Chart DQ. \$JOBCTLF - Miscellaneous		Chart GL. \$JOBCTLJ - PAUSE, LOG, and	
Subroutines (Part 2 of 4) . . . . .	116	NOLOG Statement Processors . . . . .	150
Chart DR. \$JOBCTLF - Miscellaneous		Chart GM. \$JOBCTLJ - STOP Statement	
Subroutines (Part 3 of 4) . . . . .	117	Processor . . . . .	151
Chart DS. \$JOBCTLF - Miscellaneous		Chart GN. \$JOBCTLJ - CATALR Card	
Subroutines (Part 4 of 4) . . . . .	118	Processor . . . . .	152
Chart EA. \$JOBCTLG - JOB Statement		Chart GP. \$JOBCTLJ - ALLOC Statement	
Processor (Part 1 of 2) . . . . .	119	Processor (Part 1 of 3) . . . . .	153

Chart GQ. \$JOBCTLJ - ALLOC Statement Processor (Part 2 of 3) . . . . .	154
Chart GR. \$JOBCTLJ - ALLOC Statement Processor (Part 3 of 3) . . . . .	155
Chart GS. \$JOBCTLJ - Miscellaneous Subroutines (Part 1 of 2) . . . . .	156
Chart GT. \$JOBCTLJ - Miscellaneous Subroutines (Part 2 of 2) . . . . .	157
Chart GU. \$JOBCTLJ - Error Subroutines	158
Chart HA. \$JOBCTLK - LBLTYP, VOL, & TPLAB Statement Processors . . . . .	159
Chart HB. \$JOBCTLK - TLBL Statement Processor . . . . .	160
Chart HC. \$JOBCTLK - Label Processing Subroutines (Part 1 of 2) . . . . .	161
Chart HD. \$JOBCTLK - Label Processing Subroutines (Part 2 of 2) . . . . .	162
Chart HE. \$JOBCTLK - DLBL Statement Processor . . . . .	163
Chart HF. \$JOBCTLK - DLAB Statement Processor . . . . .	164
Chart HG. \$JOBCTLK - XTENT Statement Processor (Part 1 of 2) . . . . .	165
Chart HH. \$JOBCTLK - XTENT Statement Processor (Part 2 of 2) . . . . .	166
Chart HJ. \$JOBCTLK - EXTENT Statement Processor (Part 1 of 3) . . . . .	167
Chart HK. \$JOBCTLK - EXTENT Statement Processor (Part 2 of 3) . . . . .	168
Chart HL. \$JOBCTLK - EXTENT Statement Processor (Part 3 of 3) . . . . .	169
Chart HM. \$JOBCTLK - Label Processing Subroutines (Part 1 of 3) . . . . .	170
Chart HN. \$JOBCTLK - Label Processing Subroutines (Part 2 of 3) . . . . .	171
Chart HP. \$JOBCTLK - Label Processing Subroutines (Part 3 of 3) . . . . .	172
Chart HQ. \$JOBCTLK - RSTRT Statement Processor . . . . .	173
Chart HR. \$JOBCTLK - Miscellaneous Subroutines . . . . .	174
Chart HS. \$JOBCTLK - Error Subroutines	175
Chart JA. \$JOBCTLK - Recorder File Initialization . . . . .	176
Chart JB. \$JOBCTLK - Create Recorder File (Part 1 of 2) . . . . .	177
Chart JC. \$JOBCTLK - Create Recorder File (Part 2 of 2) . . . . .	178
Chart JD. \$JOBCTLK - Check Recorder File (Part 1 of 4) . . . . .	179
Chart JE. \$JOBCTLK - Check Recorder File (Part 2 of 4) . . . . .	180
Chart JF. \$JOBCTLK - Check Recorder File (Part 3 of 4) . . . . .	181
Chart JG. \$JOBCTLK - Check Recorder File (Part 4 of 4) . . . . .	182
Chart JH. \$JOBCTLK - ROD Statement Processor . . . . .	183
Chart JJ. \$JOBCTLK - Miscellaneous Subroutines (Part 1 of 2) . . . . .	184
Chart JK. \$JOBCTLK - Miscellaneous Subroutines (Part 2 of 2) . . . . .	185
Chart JL. \$JOBCTLK - I/O Subroutines (Part 1 of 2) . . . . .	186
Chart JM. \$JOBCTLK - I/O Subroutines (Part 2 of 2) . . . . .	187
Chart JN. \$JOBCTLK - RMS and Error Subroutines . . . . .	188
Chart JP. \$JOBCTLK - Miscellaneous Subroutines (Part 1 of 2) . . . . .	189
Chart JQ. \$JOBCTLK - Miscellaneous Subroutines (Part 2 of 2) . . . . .	190
Chart KA. \$JOBCTLN - Job Accounting Interface (Part 1 of 2) . . . . .	191
Chart KB. \$JOBCTLN - Job Accounting Interface (Part 2 of 2) . . . . .	192
Chart LA. \$\$BLSTIO - Initialization . . . . .	193
Chart LB. \$\$BLSTIO - Operand Identification Subroutine . . . . .	194
Chart LC. \$\$BLSTIO - Build Print Line Subroutine . . . . .	195
Chart LD. \$\$BLSTIO - Build Header Subroutine . . . . .	196

FIGURES

Figure 1. I/O Table for One-Device System . . . . .	12	Figure 17. Disk Information Block (DIB) Table . . . . .	230
Figure 2. I/O Table for Two-Device System . . . . .	12	Figure 18. First Part of PIB Table . . . . .	231
Figure 3. IPL Main Storage Map . . . . .	13	Figure 19. PIB Flag Expansions . . . . .	232
Figure 4. ADD, DEL, and SET Statements . . . . .	16	Figure 20. Second Part of PIB Table . . . . .	233
Figure 5. Job Control Storage Allocation . . . . .	19	Figure 21. TEBV Table Showing Status Block and Error Blocks (Part 1 of 2) . . . . .	234
Figure 6. DFB Format . . . . .	203	Figure 22. I/O Table Interrelationship . . . . .	236
Figure 7. Format for NICLS and FICLS . . . . .	204	Figure 23. Job Information Block (JIB) Table . . . . .	237
Figure 8. Phase-Vector Table Entry Format . . . . .	212	Figure 24. Job Accounting Interface Partition Table . . . . .	238
Figure 9. Sample LISTIO Printouts . . . . .	217	Figure 25. Job Accounting Interface Common Table (ACCTCOMN) . . . . .	239
Figure 10. Supervisor Communications Region (Part 1 of 5) . . . . .	218	Figure 26. RMS Machine Check Record on SYSREC . . . . .	240
Figure 11. Background Communications Region Extension . . . . .	223	Figure 27. RMS Monitor Table - RASTAB (Part 1 of 2) . . . . .	241
Figure 12. SDR Communications Region - SDRTABLE (Part 1 of 2) . . . . .	224	Figure 28. RMS Linkage Area (RASLINK) . . . . .	243
Figure 13. Command Control Block (CCB) . . . . .	226	Figure 29. RMS Channel Check Record on SYSREC . . . . .	244
Figure 14. PUB Table . . . . .	227	Figure 30. Format of SYSRES Tape Label Information . . . . .	245
Figure 15. NICL, FICL, and LUB Tables . . . . .	228	Figure 31. SYSRES DASD Label Information . . . . .	246
Figure 16. Tape Error Block (TEB) . . . . .	229		

Chart 60. Disk Operating System Program Flow

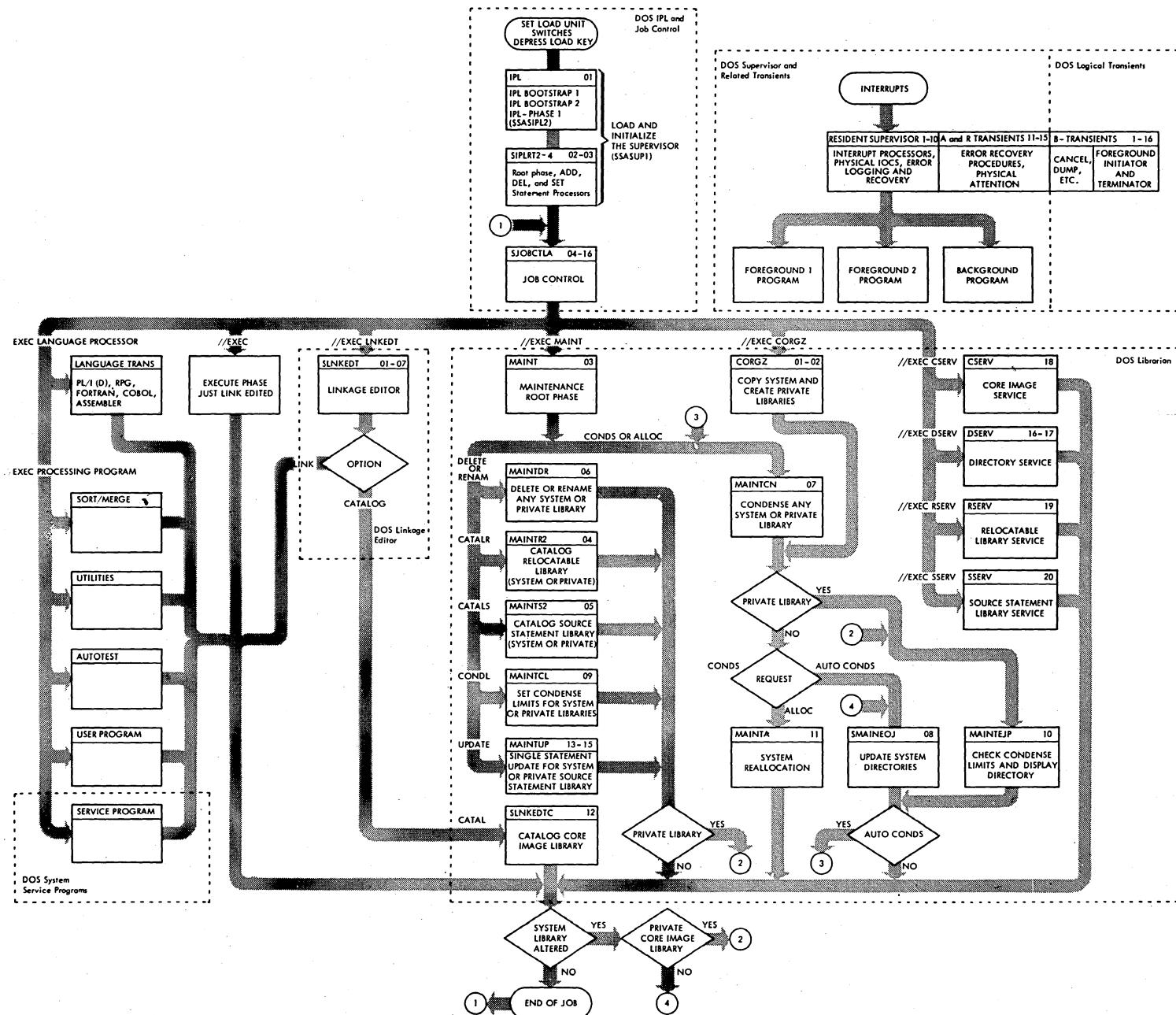


Chart AE. \$\$A\$IPL2 - Move I/O Tables  
Refer to Chart 01.

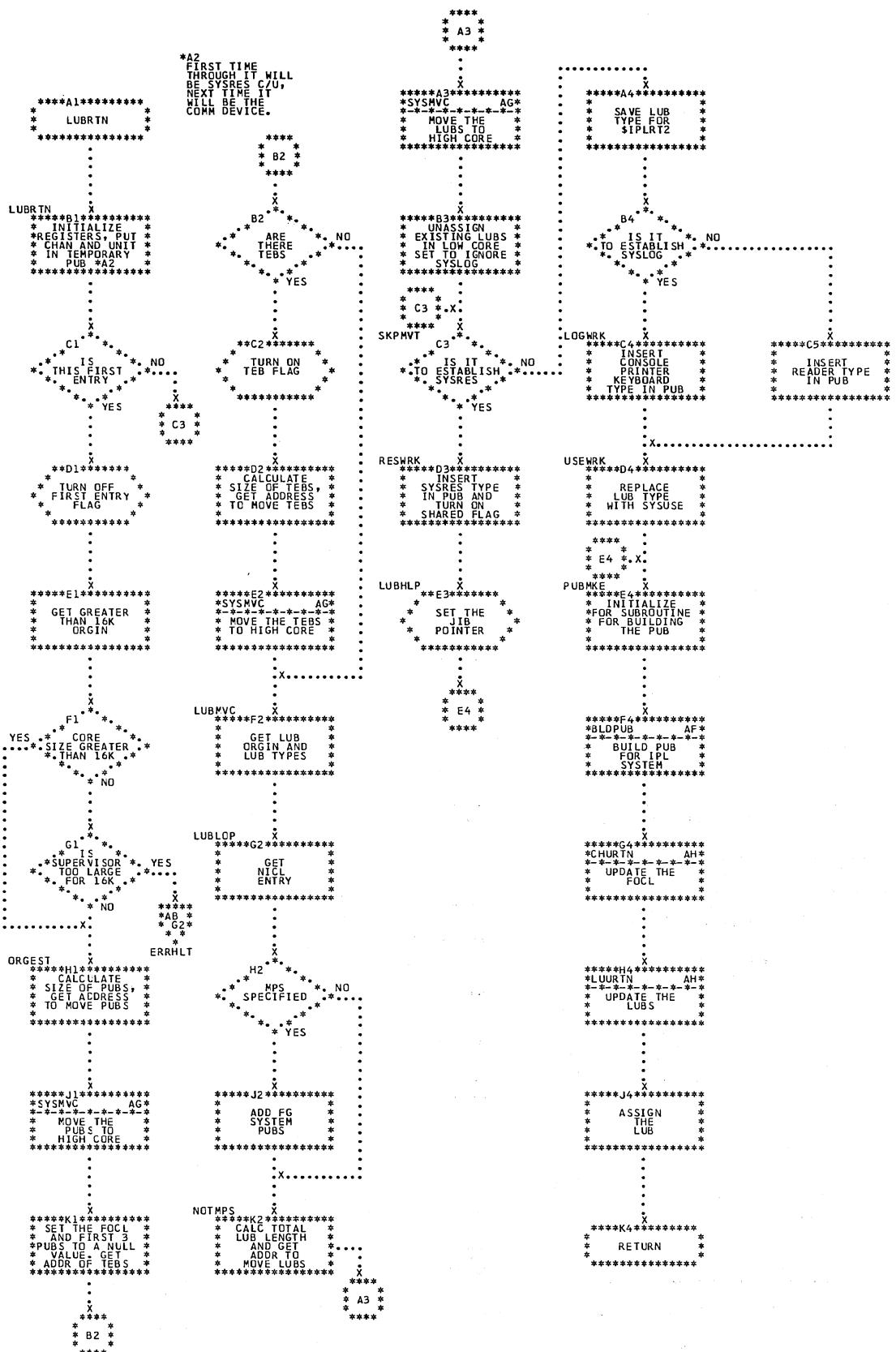


Chart AF. \$\$A\$IP12 - Build PUB Table  
Refer to Chart 01.

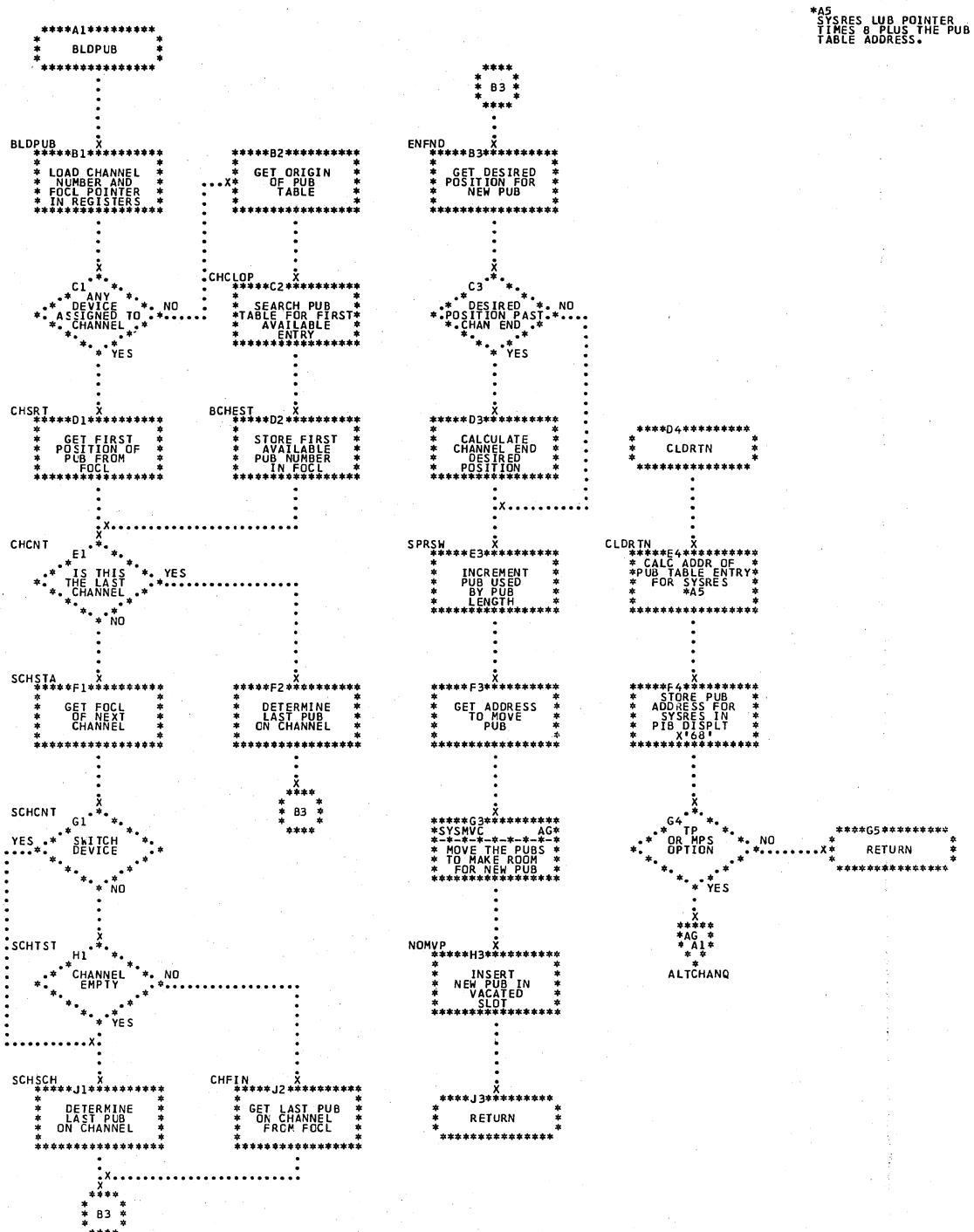
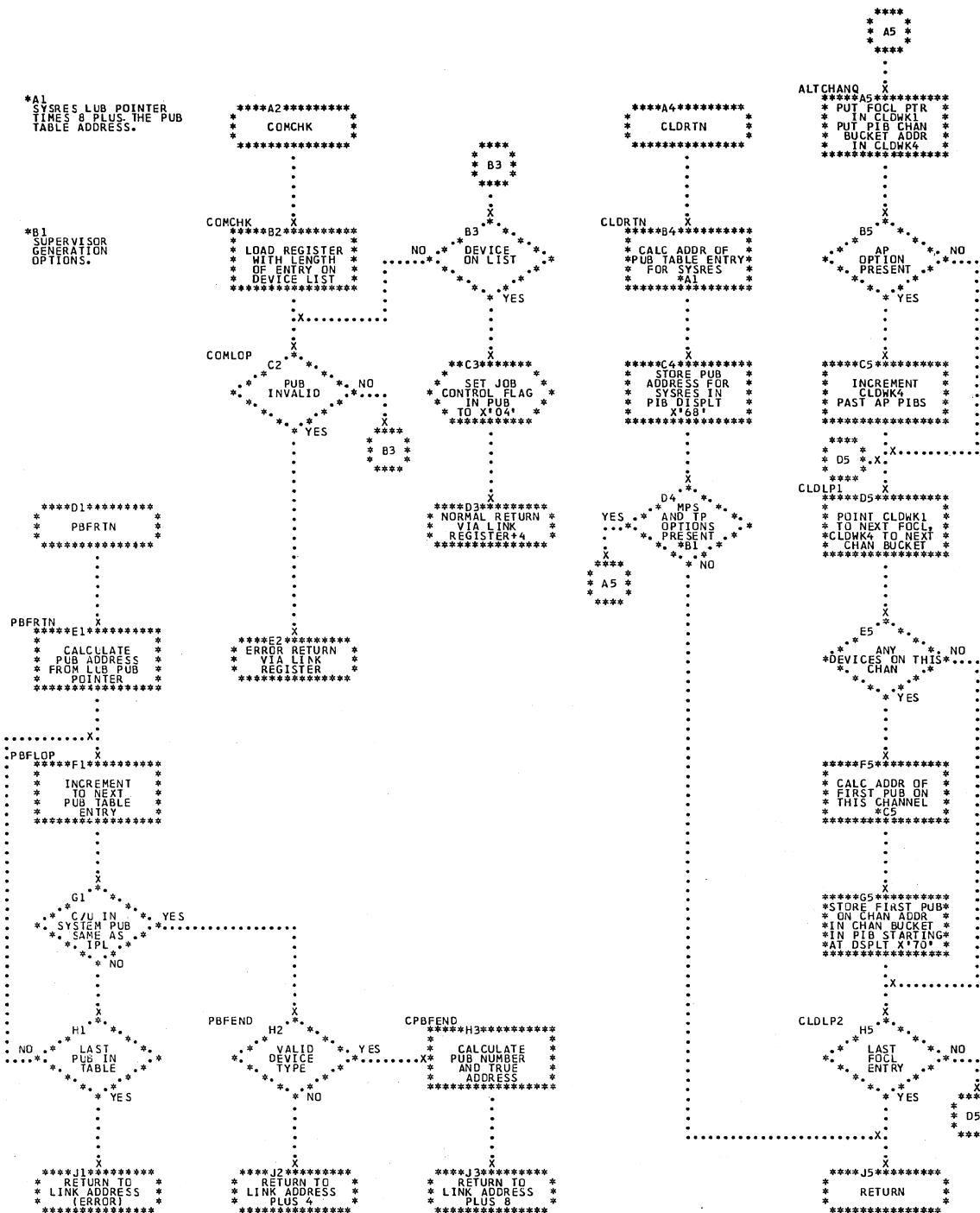


Chart AZA. \$IPLRT4 - Find PUB and I/O Subroutines  
Refer to Chart 03.



**Chart AZB. \$IPLRT4 - Date and Time Subroutines**  
Refer to Chart 03.

```

*****A1***** * * * * *
* DATERT * * * * *
*****A2***** * * * * *
* DATE FIELD * * * * *
* 13 BYTES * * * * *
* LONG * * * * *
* YES * * * * *
* C1 * * * * *
* IS YEAR * * * * YES
* GREATER THAN * * * * X
* 99 * * * * *
* NO * * * * *
* AQ * * * * *
* C1 * * * * *
* ILLCD * * * * X
*****D1***** * * * * *
* SET UP TO * * * * *
* CHECK MONTH * * * * *
* FIRST * * * * *
* NO * * * * *
*****E1***** * * * * AP*
* ADDRESS * * * * AP*
* GET MONTH * * * * *
* FIELD ADDRESS * * *
* AND LENGTH * * * *
*****E2***** * * * * *
* DECREASE * * * * *
* DAY COUNT * * * X
* BY ONE * * * *
*****F1***** * * * * AV*
* ADDRESS * * * * AP*
* FOR DAY * * * *
* FIRST, RESET * * *
* ADDRESS OF * * *
* TO DECIMAL * * *
*****F2***** * * * * *
* G1 * * * * *
* IS GT 0 OR * * * * YES
* GT 12 * * * * *
* NO * * * * *
* AQ * * * * *
* C1 * * * * *
* ILLCD * * * * X
*****H1***** * * * * *
* CALC JULIAN * * * *
* ENDING DATE * * * *
* OF THE PREVIOUS * * *
* MONTH * * * * *
*****H2***** * * * * AV*
* DECRN * * * * AV*
* THE DATE * * * *
* THE YEAR AND * * *
* DAY TO * * * *
* DECIMAL * * * *
*****H3***** * * * * AV*
* DECRN * * * * AV*
* THE DATE * * * *
* THE YEAR AND * * *
* CONVERT * * * *
* TO DECIMAL * * *
*****J3***** * * * * *
* MOON * * * * *
* COMMUNICATION * * *
* REGION, DISPL * * *
* HEX 4F OR * * *
* DECIMAL 79 * * *
*****K1***** * * * * *
* IF THE PREVIOUS * * *
* MONTH ENDING DATE * * *
* IS 31, THE CURRENT * * *
* DATE IS IN FEBRUARY. * * *
* THE ROUTINE MUST * * *
* MAKE A LEAP YEAR * * *
* CHECK. * * *
*****A3***** * * * * *
* ADREST * * * * X
* DAY FIRST * * * * NO
* DATE FIELD * * * *
* YES * * * * *
*****C3***** * * * * *
* CALCULATE * * * *
* ADDRESS OF * * *
* SECOND FIELD * * *
*****C4***** * * * * *
* GET HOUR * * * *
* FIELD ADDRESS * * *
* AND LENGTH * * *
*****D3***** * * * * *
* CALCULATE FIRST * * *
* FIELD ADDRESS * * *
* AND GET * * *
* SUBFIELD * * *
* LENGTH * * *
*****D4***** * * * * AV*
* DECRN * * * * AV*
* CONV * * * * *
* HOUR FIELD * * *
* TO DECIMAL * * *
*****E3***** * * * * *
* RETURN * * * *
*****E4***** * * * * X
* RESULT * * * * YES
* IS GT 24 * * * * X
* NO * * * * *
* AQ * * * * *
* C1 * * * * *
* ILLCD * * * * X
*****F4***** * * * * *
* CONV * * * * *
* HOURS * * * *
* TO SECONDS * * *
* AND STORE * * *
* IN TIMREG * * *
*****F5***** * * * * *
* ADD SECONDS * * *
* TO TOTAL * * *
* IN TIMREG * * *
*****G2***** * * * * AP*
* ADDRESS * * * * AP*
* GET DAY * * * *
* FIELD ADDRESS * * *
* AND LENGTH * * *
*****G3***** * * * * X
* IS GT 0 OR * * * * YES
* GT 31 * * * * *
* NO * * * * *
* AQ * * * * *
* C1 * * * * *
* ILLCD * * * * X
*****G4***** * * * * X
* GET MINUTE * * * *
* FIELD ADDRESS * * *
* AND LENGTH * * *
*****G5***** * * * * X
* MULTIPLY TOTAL * *
* IN TIMREG * * *
* BY 300 AND * * *
* CALCULATE * * *
* ACTUAL TIME * * *
*****H4***** * * * * X
* DECRN * * * * AV*
* THE DATE * * * *
* THE YEAR AND * * *
* CONVERT * * * *
* MINUTE FIELD * * *
* TO DECIMAL * * *
*****H5***** * * * * X
* MOVE TOTAL TO * *
* CORE STORAGE * *
* LOCATION * * *
* HEX 154 OR * *
* DECIMAL 84 * * *
*****J4***** * * * * X
* RESULT * * * * NO
* IS GT 60 * * * *
* YES * * * * *
* B5 * * * * *
*****K2***** * * * * K5
* TIME FIELD AT X*54 * *
* SYSTEM SECONDS * *
* IN THE FORM * *
* WHEN 300 THE USER * *
* IS EQUAL TO * *
* SPECIFIED TIME OF DAY PLUS * *
* MAXIMUM TIMER INTERVAL * *
* (IS 1/2 HRS) MINUS ELAPSED * *
* TIME. * *
*****K3***** * * * * *
* RETURN * * * *
*****ILLCD***** * * * * X
* AQ * * * * *
* C1 * * * * *

```

\*K1  
IF THE PREVIOUS  
MONTH ENDING DATE  
IS 31, THE CURRENT  
DATE IS IN FEBRUARY.  
THE ROUTINE MUST  
MAKE A LEAP YEAR  
CHECK.

Chart BB. \$JOBCTLA - Initialization and Control Statement Read  
Refer to Chart 04.

\*K1  
 POINT4 IS ULTIMATELY INITIALIZED TO A  
 VALUE OF 5 OR 6. IT IS USED AS A MASK  
 TO CHECK THE CONDITION SWITCHES IN THE  
 FUNCTION OF A FOUND ARGUMENT IN THE  
 BRANCH TABLE LOOKUP ROUTINE, BTLOOP.  
  
 \*A2 BD-J4  
 \*A3 BC-D1  
 \*A3 BC-B3  
 \*A3 BM-B2  
 \*A3 CL-F5, H5  
 \*A3 CL-B2, C4, E3, D2, K3  
 \*A3 CL-K4  
 \*A3 CP-H5  
 \*A3 DU-J5  
 \*A3 EB-J4, H4, K1  
 \*A3 EE-B4  
 \*A3 EL-F3  
 \*A3 GA-L4  
 \*A3 GC-B1  
 \*A3 GD-F2, J5  
 \*A3 GE-K2  
 \*A3 GG-F3  
 \*A3 GH-F5  
 \*A3 GL-C2, C4, C5, D3, E1  
 \*A3 GM-H3  
 \*A3 GK-J3  
 \*A3 HA-J3, G5, H4  
 \*A3 HF-F5  
 \*A3 HH-E3, H5  
 \*A3 JF-J5  
  
 \*A3 CONTROL B4  
 \*A3 UNIT B4  
 \*A3 EXCEPTION B4  
 \*A3 ON SYSLST B4  
 \*A3 YES B4  
 \*A3 NO B4  
 \*A3 B5  
 \*A3 C5  
 \*A3 D5  
 \*A3 E5  
 \*A3 F5  
 \*A3 G5  
 \*A3 H5  
  
 \*A3 ATNCUU C5  
 \*A3 DID-A5  
 \*A3 ALAN-A5  
 \*A3 SDP-B5  
 \*A3 THE-C5  
 \*A3 SCAN-D5  
 \*A3 IN-NO  
 \*A3 READ-B5  
 \*A3 STATEMENT-C5  
  
 \*D1 JOBCTL5 C2\*\*\*\*\*  
 \*D1 ACCTBRI D2\*\*\*\*\*  
 \*D3 OPNUMH IS THE 1 BYTE  
 DATA INFORMATION  
 FIELD IN THE  
 INSTRUCTION. IT IS  
 USED TO MAINTAIN A  
 COUNT OF THE PARAMETERS  
 PROCESSED FOR THE CURRENT  
 CONTROL STATEMENT.  
  
 \*E1 ACCTLV1 E2\*\*\*\*\*  
 \*E1 PARTITIONS E2\*\*\*\*\*  
 \*E1 OTHER E2\*\*\*\*\*  
 \*E1 NO E2\*\*\*\*\*  
 \*E1 NO ACTIVE E2\*\*\*\*\*  
 \*E1 SWITCH E2\*\*\*\*\*  
 \*E1 YES E2\*\*\*\*\*  
  
 \*E2 ACCTLV1 E3\*\*\*\*\*  
 \*E2 PARTITIONS E3\*\*\*\*\*  
 \*E2 OTHER E3\*\*\*\*\*  
 \*E2 NO E3\*\*\*\*\*  
 \*E2 TURN OFF E3\*\*\*\*\*  
 \*E2 NO ACTVE E3\*\*\*\*\*  
 \*E2 SWITCH E3\*\*\*\*\*  
  
 \*E3 ACCTLV1 E4\*\*\*\*\*  
 \*E3 THIS STMT E4\*\*\*\*\*  
 \*E3 BLANK E4\*\*\*\*\*  
 \*E3 NO E4\*\*\*\*\*  
 \*E3 YES E4\*\*\*\*\*  
 \*E3 LT B5 CHAR E4\*\*\*\*\*  
 \*E3 US-B5 E4\*\*\*\*\*  
 \*E3 B6-B5 E4\*\*\*\*\*  
 \*E3 B7-B5 E4\*\*\*\*\*  
 \*E3 B8-B5 E4\*\*\*\*\*  
 \*E3 B9-B5 E4\*\*\*\*\*  
 \*E3 B10-B5 E4\*\*\*\*\*  
 \*E3 IGNORE E4\*\*\*\*\*  
 \*E3 CHCKN E4\*\*\*\*\*  
  
 \*E4 ACCTLV1 E5\*\*\*\*\*  
 \*E4 INIT REG E5\*\*\*\*\*  
 \*E4 POINT4 E5\*\*\*\*\*  
 \*E4 WITH 1 TO E5\*\*\*\*\*  
 \*E4 PART 1 E5\*\*\*\*\*  
 \*E4 COL 1 SKL E5\*\*\*\*\*  
  
 \*E5 ACCTLV1 E6\*\*\*\*\*  
 \*E5 INIT FOR E6\*\*\*\*\*  
 \*E5 BRANCH TABLE E6\*\*\*\*\*  
 \*E5 LOOK UP E6\*\*\*\*\*  
 \*E5 K5 E6\*\*\*\*\*  
  
 \*E6 ACCTLV1 E7\*\*\*\*\*  
 \*E6 BC-A5 E7\*\*\*\*\*  
 \*E6 AL-B5 E7\*\*\*\*\*  
 \*E6 K6 E7\*\*\*\*\*  
  
 \*E7 ACCTLV1 E8\*\*\*\*\*  
 \*E7 CLEAR TIME E8\*\*\*\*\*  
 \*E7 COUNTERS IN E8\*\*\*\*\*  
 \*E7 PARTITION E8\*\*\*\*\*  
 \*E7 TABLE E8\*\*\*\*\*  
  
 \*E8 ACCTLV1 E9\*\*\*\*\*  
 \*E8 B4\*\*\*\*\*  
 \*E8 B5\*\*\*\*\*  
  
 \*E9 ACCTLV1 G4\*\*\*\*\*  
 \*E9 CLEAR TIME G4\*\*\*\*\*  
 \*E9 COUNTERS IN G4\*\*\*\*\*  
 \*E9 PARTITION G4\*\*\*\*\*  
 \*E9 TABLE G4\*\*\*\*\*  
  
 \*G4 ACCTLV1 G5\*\*\*\*\*  
 \*G4 COL 1 BLANK G5\*\*\*\*\*  
 \*G4 NO G5\*\*\*\*\*  
  
 \*H5 ACCTLV1 H6\*\*\*\*\*  
 \*H5 INCR POINT4 BY 4 TO H6\*\*\*\*\*  
 \*H5 REG POINT4 BY \* H6\*\*\*\*\*  
 \*H5 1 TO INDICATE \* H6\*\*\*\*\*  
 \*H5 NDBLANK IN \* H6\*\*\*\*\*  
 \*H5 COL 1 SKL \* H6\*\*\*\*\*  
  
 \*H6 ACCTLV1 H7\*\*\*\*\*  
 \*H6 INCR POINT4 BY 4 TO H7\*\*\*\*\*  
 \*H6 SET VALUE TO 5 OR 6. SEE NOTE K1. H7\*\*\*\*\*  
 \*H6 MODIFY INST TTYPE, H7\*\*\*\*\*  
 \*H6 TTYPE, TTYPE MASK HEX H7\*\*\*\*\*  
 \*H6 C5 OR HEX 06. H7\*\*\*\*\*  
 \*H6 SET BIT 0 OF JBCSW4, H7\*\*\*\*\*  
 \*H6 IF ON SET OFF, IF OFF SET ON. H7\*\*\*\*\*  
  
 \*J5 ACCTLV1 J6\*\*\*\*\*  
 \*J5 THE PARAM LENGTH IS J6\*\*\*\*\*  
 \*J5 CHECKED BY COMPARING J6\*\*\*\*\*  
 \*J5 THE VALUE IN REG J6\*\*\*\*\*  
 \*J5 POINT4 THE C5 J6\*\*\*\*\*  
 \*J5 ROUTINE HAS SET THIS J6\*\*\*\*\*  
 \*J5 VALUE TO PARAM LENGTH J6\*\*\*\*\*  
 \*J5 MINUS 1 BYTE. J6\*\*\*\*\*  
  
 \*K5 ACCTLV1 K6\*\*\*\*\*  
 \*K5 THE BRANCH TABLE LOOKUP K6\*\*\*\*\*  
 \*K5 ROUTINE IS USED TO K6\*\*\*\*\*  
 \*K5 DETERMINE THE ADDRESS OF K6\*\*\*\*\*  
 \*K5 THE CONTROL STATEMENT BEING K6\*\*\*\*\*  
 \*K5 PROCESSED. WHEN A FIND K6\*\*\*\*\*  
 \*K5 IS MADE, THE FUNCTIONS K6\*\*\*\*\*  
 \*K5 OF THE ARGUMENT ARE UTILIZED. K6\*\*\*\*\*  
 \*K5 1. CLEAR OLD ARGUMENT K6\*\*\*\*\*  
 \*K5 AND MOVE NEW ARGUMENT K6\*\*\*\*\*  
 \*K5 TO TABLE -ARGUMT-. K6\*\*\*\*\*  
 \*K5 2. SET THE REGISTER K6\*\*\*\*\*  
 \*K5 POINT4 EQUAL K6\*\*\*\*\*  
 \*K5 BRANCH TABLE ADDRESS K6\*\*\*\*\*  
 \*K5 MINUS 10 BYTES. K6\*\*\*\*\*  
  
 \*K1  
 POINT4 IS ULTIMATELY INITIALIZED TO A  
 VALUE OF 5 OR 6. IT IS USED AS A MASK  
 TO CHECK THE CONDITION SWITCHES IN THE  
 FUNCTION OF A FOUND ARGUMENT IN THE  
 BRANCH TABLE LOOKUP ROUTINE, BTLOOP.

\*K1  
POINT4 IS ULTIMATELY INITIALIZED TO A  
VALUE OF 5 OR 6. IT IS USED AS A MASK  
TO CHECK THE CONDITION SWITCHES IN THE  
FUNCTION OF A FOUND ARGUMENT IN THE  
BRANCH TABLE LOOKUP ROUTINE, BTLCOP.

\*\*\*\*\* K4 \*\*\*\*\*  
 \*SCANR1 BF\*  
 \*-\*- -\*- -\*- -\*- -\*- -\*- -\*- -\*- -\*-  
 \* GET ST OPND -\*  
 \* SET COND CODE -\*  
 \* FOR STOP CHAR -\*  
 \*\*\*\*

X IS MADE, THE FUNCTIONS  
 OF THE ARGUMENT ARE UTIL-  
 IZED AS IN FIGURE 8.  
 1. CLEAR OLD ARGUMENT  
 AND MOVE NEW ARGUMENT  
 TO TABLE -ARGUMT-.  
 2. SET THE REGISTER  
 POINTI EQUAL TO

Chart BC. \$JOBCTLA - Phase Vector Table Lookup  
Refer to Chart 04.

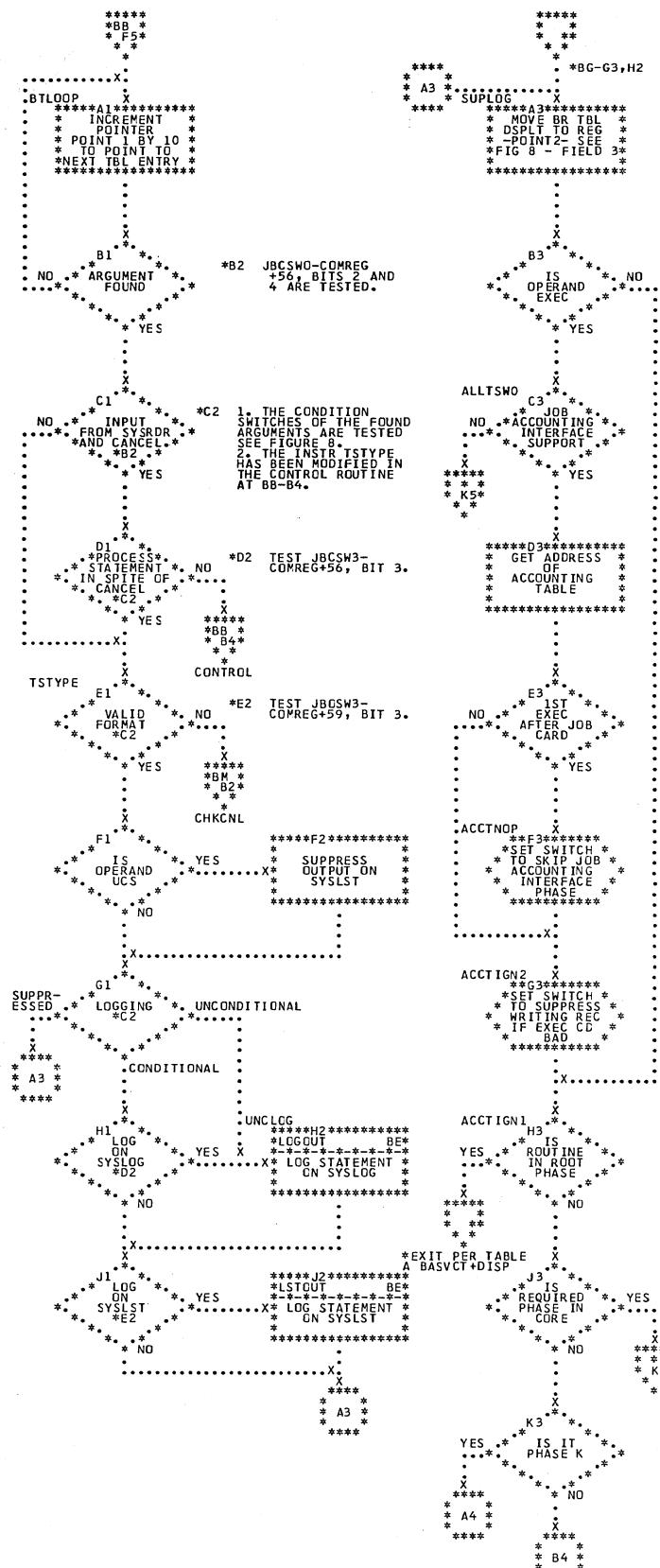


TABLE A ROOT PHASE (A)

```

STMNT  ROUTINE  CHART
//  SCANR2  BF
/*  CONTROL  BB
*   CONTROL  BB
IGNORE  IGNORE  BG

```

FETCHR X

```

****A3***** *BG-G3,H2
****A3***** *MOVE BR TBL *
****A3***** *DSPLTE TO REG *
****A3***** *POINT12 TO 3*
****A3***** *FIG 8 - FIELD 3*
****A3***** *PUT OUT *
****A3***** *VARIABLE LENGTH *
****A3***** *LABEL INFORM *
****A3***** *BB-KL AND CONTAINS THE
****A3***** *ADDRESS OF BB-A4.

```

JOB CONTROL STATEMENT TABLE

```

STMNT  PHASE  ROUTINE  CHART
ACTION  J  ACTION  GD
SAVE BEGIN  *  *  *
ALLOC  J  ALLOC  GP
RELEASE ADDRESS  *  *  *
OF LOC CONTROL  *  *  *
ASSGN  D  ASSGN  CA
CANCEL  G  CANCEL  EE
CATAL  J  CATAL  GA
CLOSE  D  CLOSE  CL
DATE  J  DATE  GH
DLAB  K  DLAB  HF
MTNCNT  BK  MTNCNT  HA
DLBL  K  DLBL  HE
DVCDN  F  DVCDN  DH
DVCUP  F  DVCUP  DK
ENTRY  J  ENTRY  GD
EOJ  G  EOJ  EC
EXEC  G  EXEC  EF
EXTENT  K  EXTENT  HJ
HOLD  J  HOLD  GA
LOAD REQUIRED  *  *  *
INCLUDE  J  INCLUDE  GD
JOB  G  JOB  EA
LBLTYP  K  LBLTYP  HA
LISTIO  F  LISTIO  DA
LOG  J  LOG  GL
MAP  F  MAP  DK
MCT  J  MCT  GE
NOLOG  J  NOLOG  GL
OPTION  G  OPTION  EL
PAUSE  J  PAUSE  GL
PHASE  J  ACTION  GP
RELEASE  J  RELEASE  GA
RESET  F  RESET  DF
ROD  M  ROD  JH
RSTRTR  K  RSTRTR  HQ
SET  J  SET  GG
STOP  J  STOP  GM
TLBL  K  TLBL  HB
TFLAB  K  TFLAB  HA
UCS  J  UCS  GB
UNA  F  UNA  DM
UNBATCH  F  DETACH  DL
UPS1  J  UPS1  GR
VOL  K  VOL  HA
XTENT  K  XTENT  HG

```

\*J5 PHASE N=THE JOB ACCOUNTING INTERFACE PHASE IS CALLED BY THE JOB, EOJ, AND EXEC STATEMENT PROCESSORS.

\*K5 EXIT PER TABLE

Chart BM. \$JOBCTLA - Error Subroutines (Part 2 of 2)  
Refer to Chart 04.

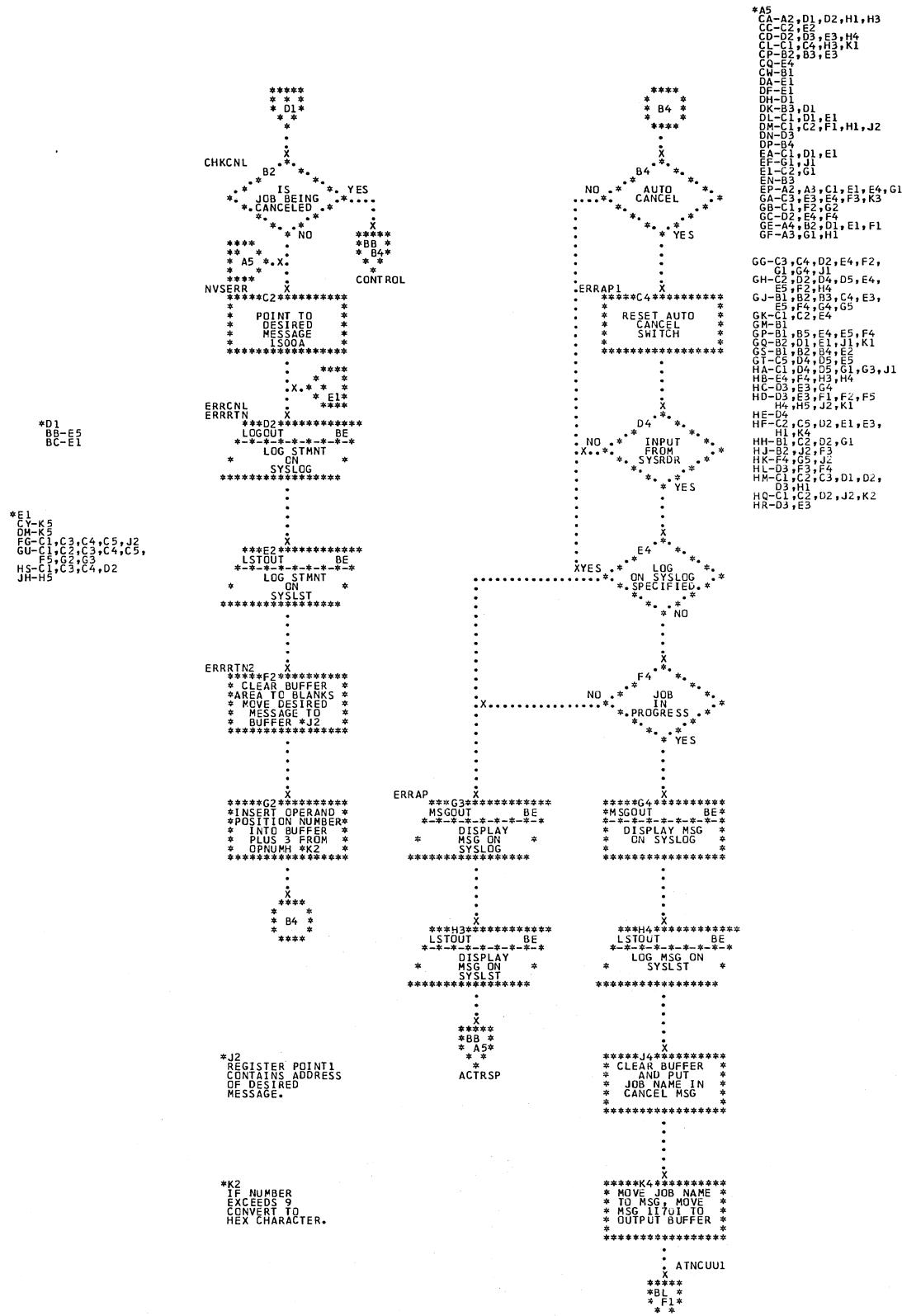


Chart BN. \$JOBCTLA - Relocation Subroutines  
Refer to Chart 04.

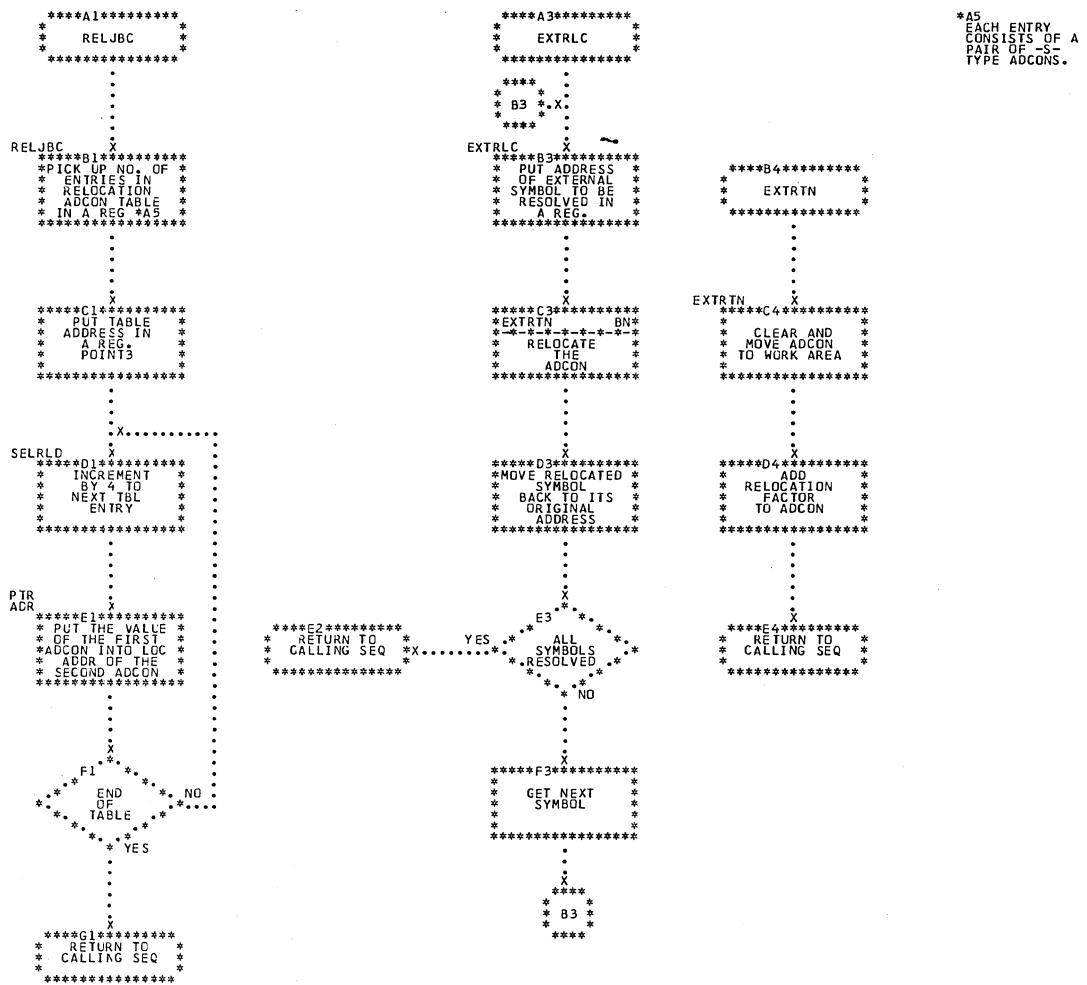


Chart EC. \$JOBCTLG - /& Statement Processor (Part 1 of 3)  
Refer to Chart 09.

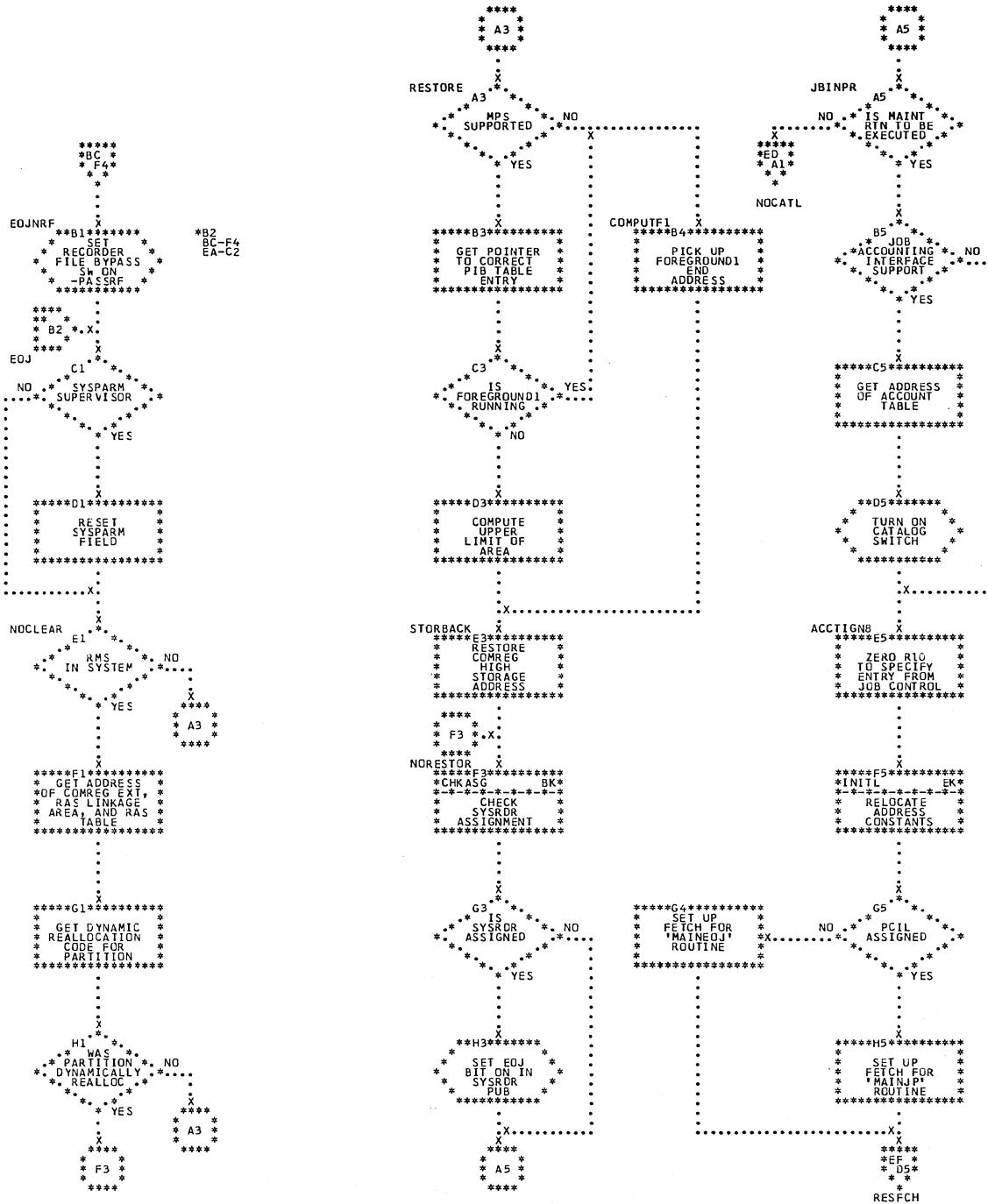


Chart ED. \$JOBCTLG - /& Statement Processor (Part 2 of 3)  
Refer to Chart 09.

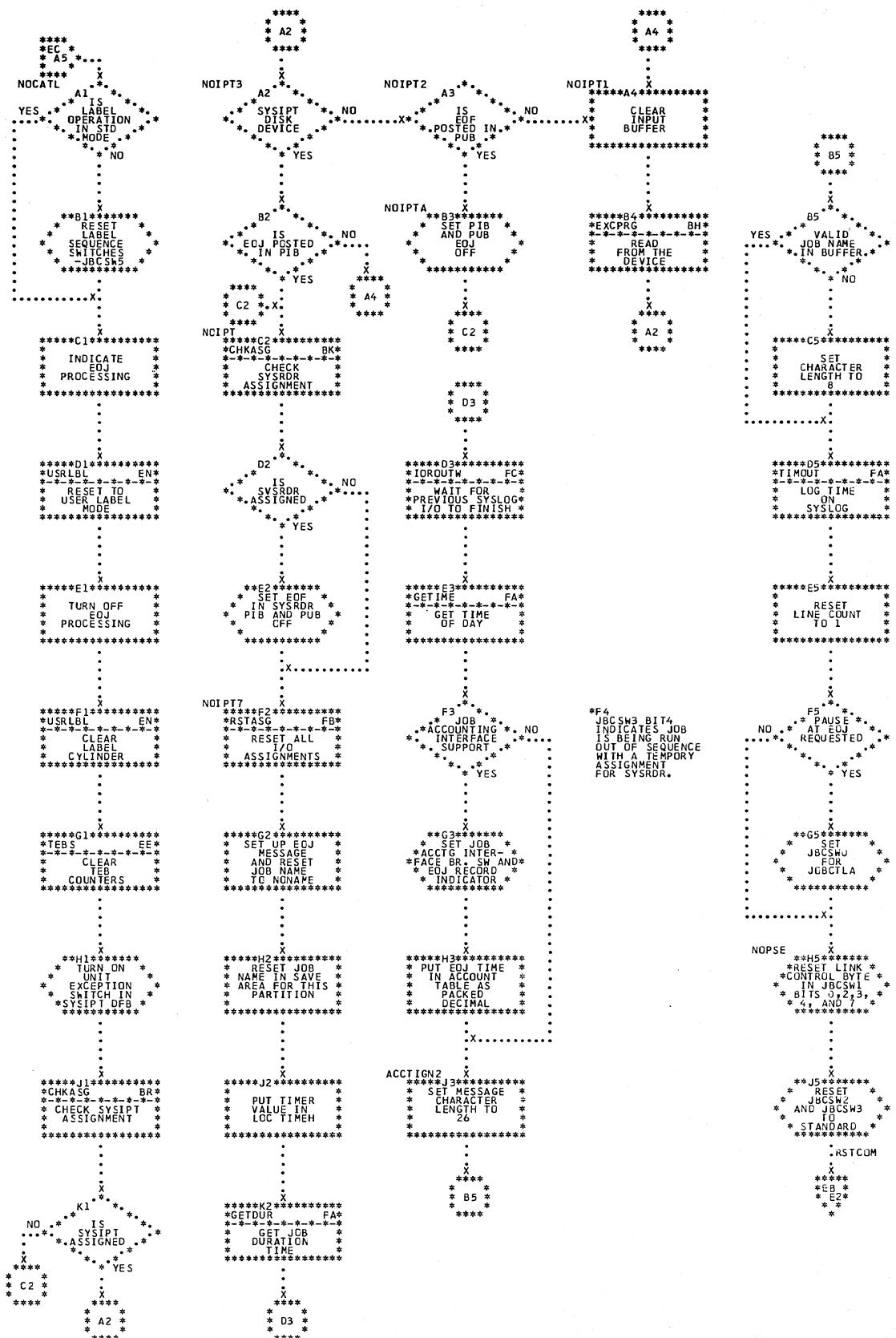


Chart EG. \$JOBCTLG - EXEC Statement Processor (Part 2 of 5)  
Refer to Chart 09.

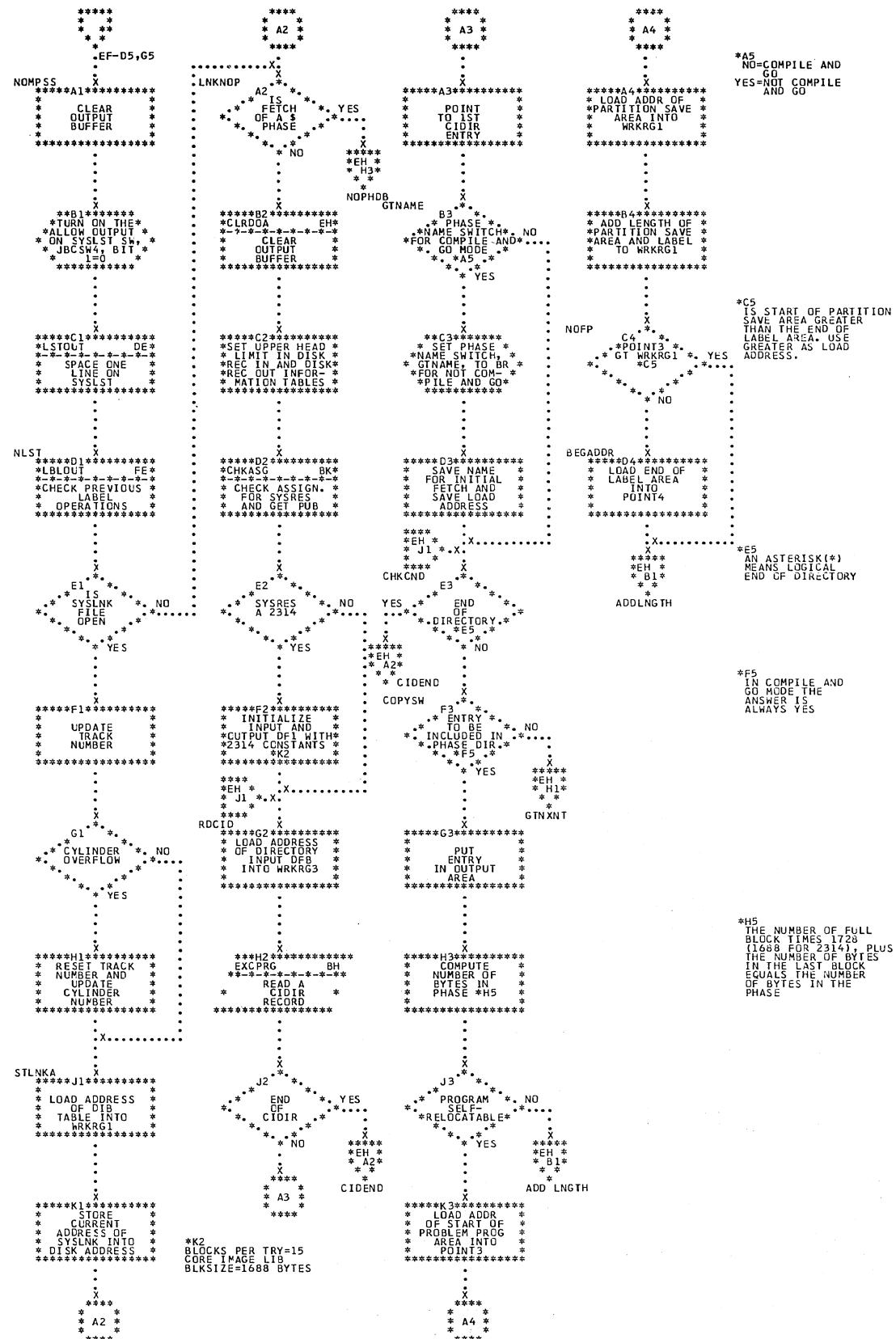


Chart EH. \$JOBCTLG - EXEC Statement Processor (Part 3 of 5)  
Refer to Chart 09.

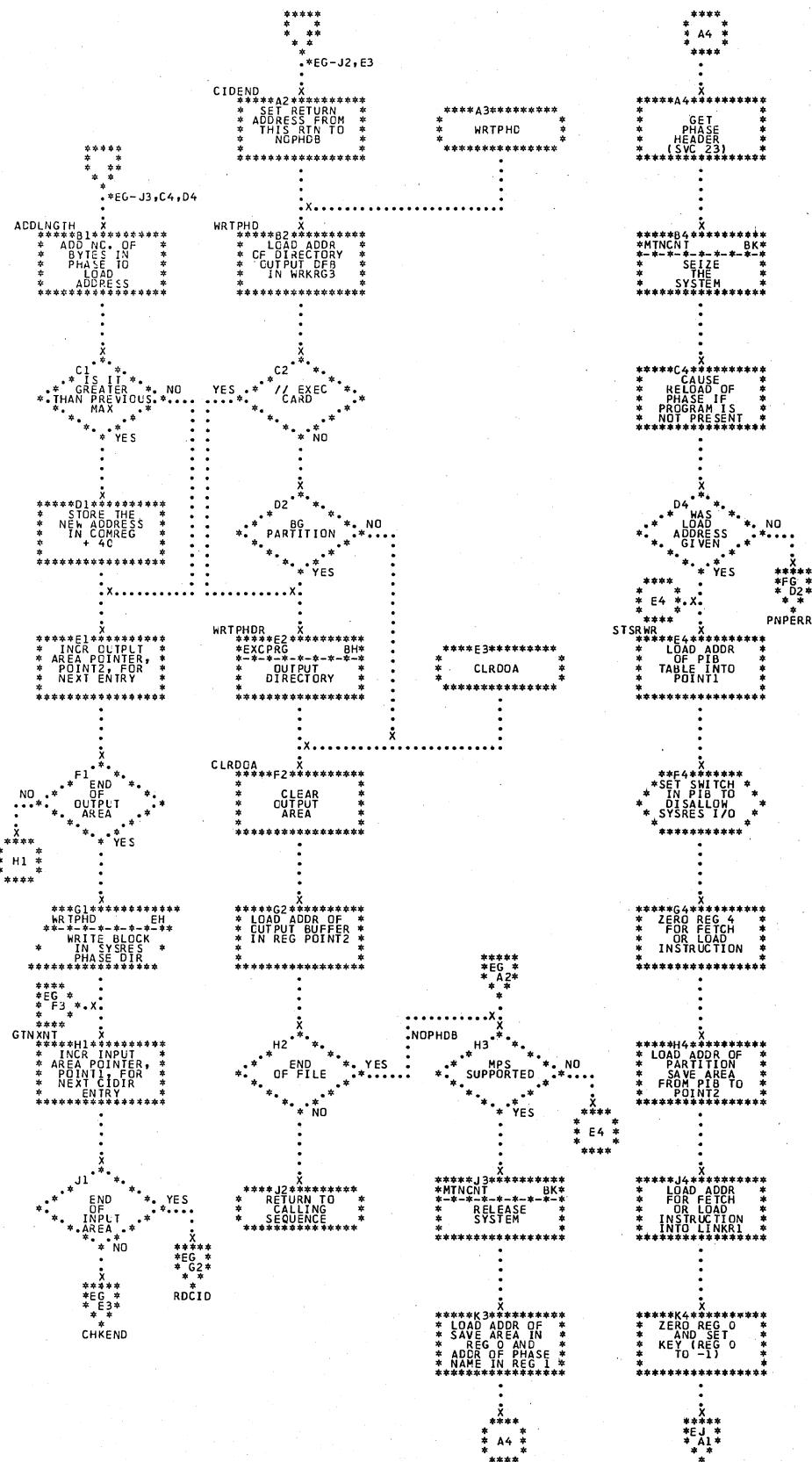


Chart FE. \$JOBCTLG - Label Processing Subroutines (Part 1 of 2)  
Refer to Charts 08 and 09.

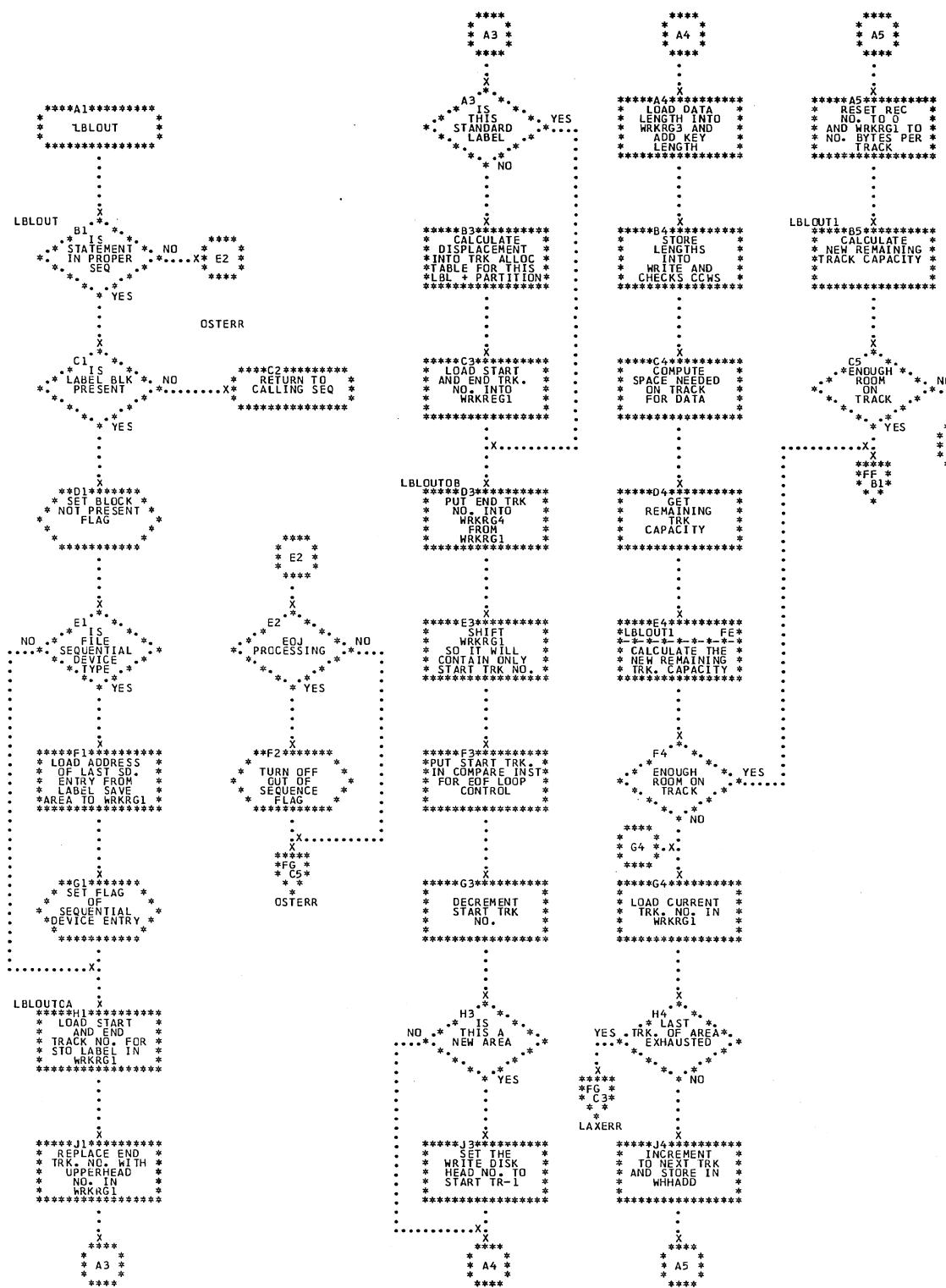


Chart FF. \$JOBCTLG - Label Processing Subroutines (Part 2 of 2)  
Refer to Charts 08 and 09.

```

***** F5 ****
***** B1 ****
* INITIALIZE *
* SEEK TRACK *
* AND RECORD *
* NUMBER *
***** C1 ****
* UPDATE WRITE *
* RECORD *
* NUMBER *
* AREA *
***** D1 ****
* MINTCNTR BK*
* RELEASE CONTROL *
***** E1 ****
* LOAD ADDRESS *
* OF THE *
* SYRES CCB *
* INT0 REG1 *
***** F1 ****
* START OF NEW AREA *
* NO YES *
***** G1 ****
* LOAD DATA *
* LENGTH INTO *
* WRKRG1 FOR *
* SAVING *
***** H1 ****
* SET DATA *
* AND *
* KEY LENGTH *
* TO ZERO *
***** B2 ****
* LOAD LINK *
* REC FOR *
* IOROUT *
* RETURN *
***** C2 ****
* SET HEAD *
* NUMBER *
* FOR EOF *
* WRITE *
***** D2 ****
LBLOUT2D D2
* FIRST AREA *
* NO ISSUE I/O *
* YES *
***** E2 ****
* SET KEY *
* LENGTH *
* TO B *
***** F2 ****
* RESTORE *
* DATA LENGTH *
* FROM WRKRG1 *
***** G2 ****
* IOROUT FC *
* ISSUE I/O *
***** H2 ****
* MINTCNTR BK*
* SEIZE SYSTEM *
***** J2 ****
* RETURN TO *
* CALLING SEQ *
***** I2 ****

```

**Chart FG. \$JOBCTLG - Error Subroutines**  
Refer to Charts 08 and 09.

**Chart GA. \$JOBCTLJ - RELSE and HOLD Statement Processors**  
Refer to Chart 11.

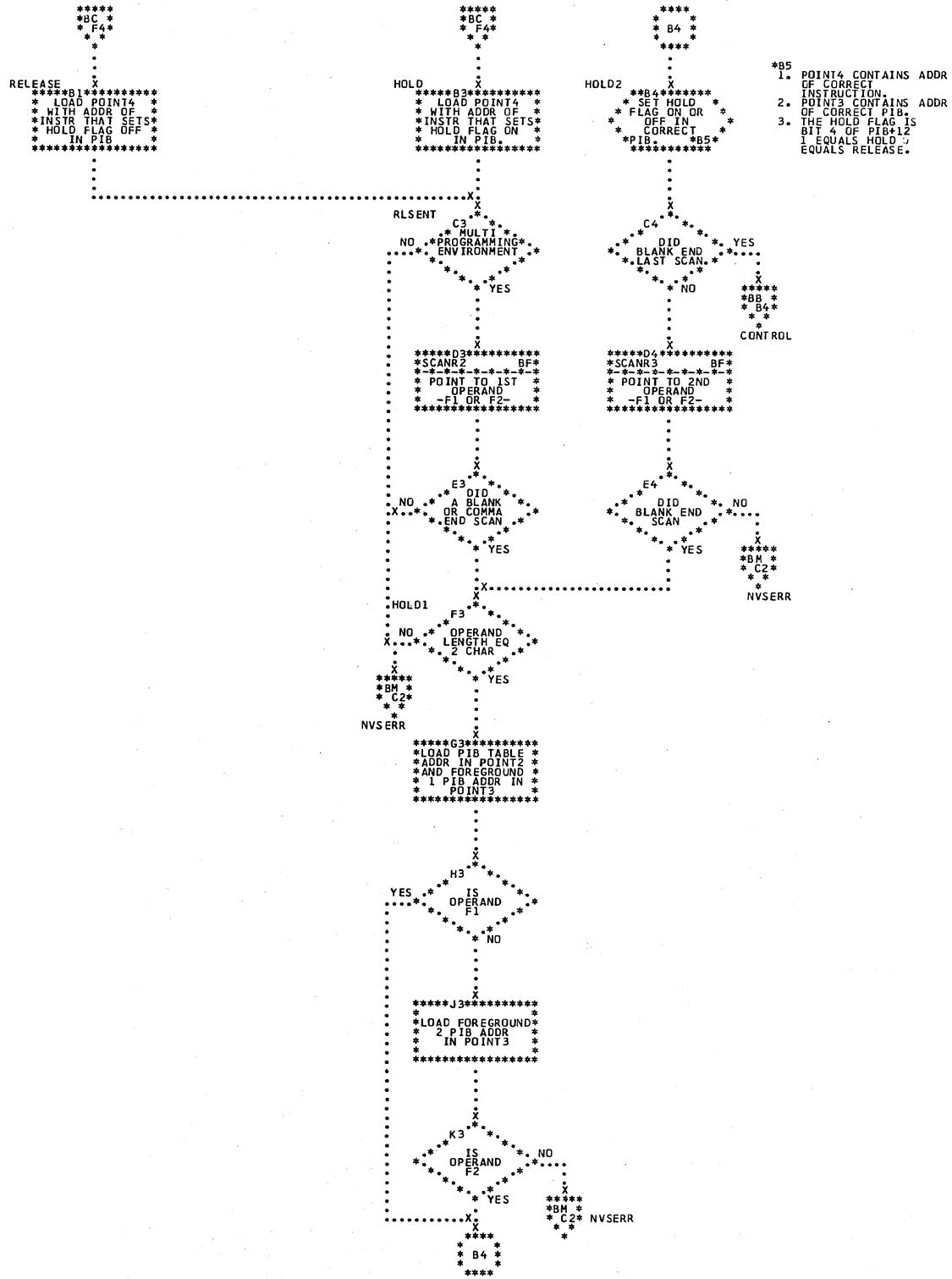


Chart HJ. \$JOBCTLK - EXTENT Statement Processor (Part 1 of 3)  
Refer to Chart 14.

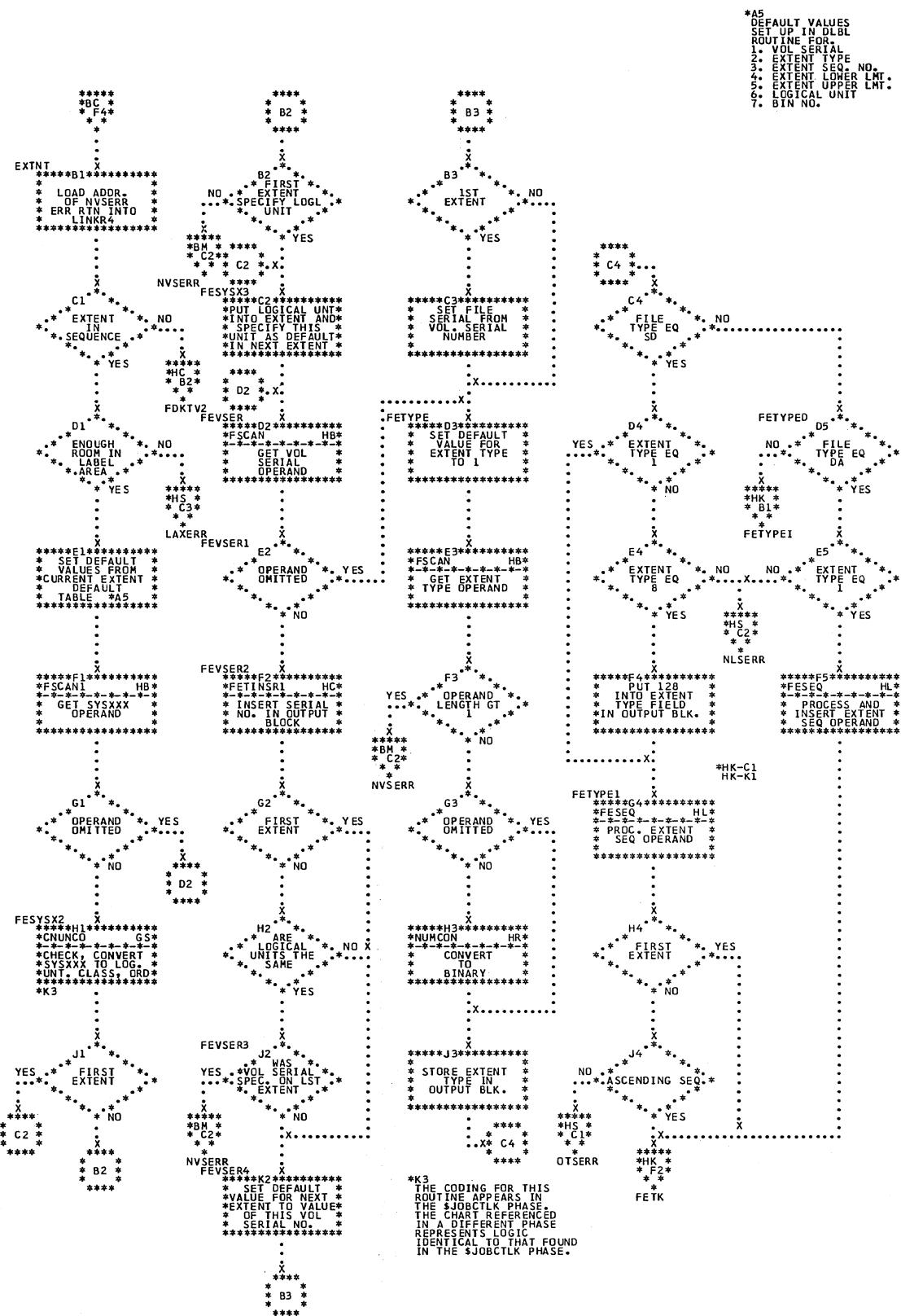
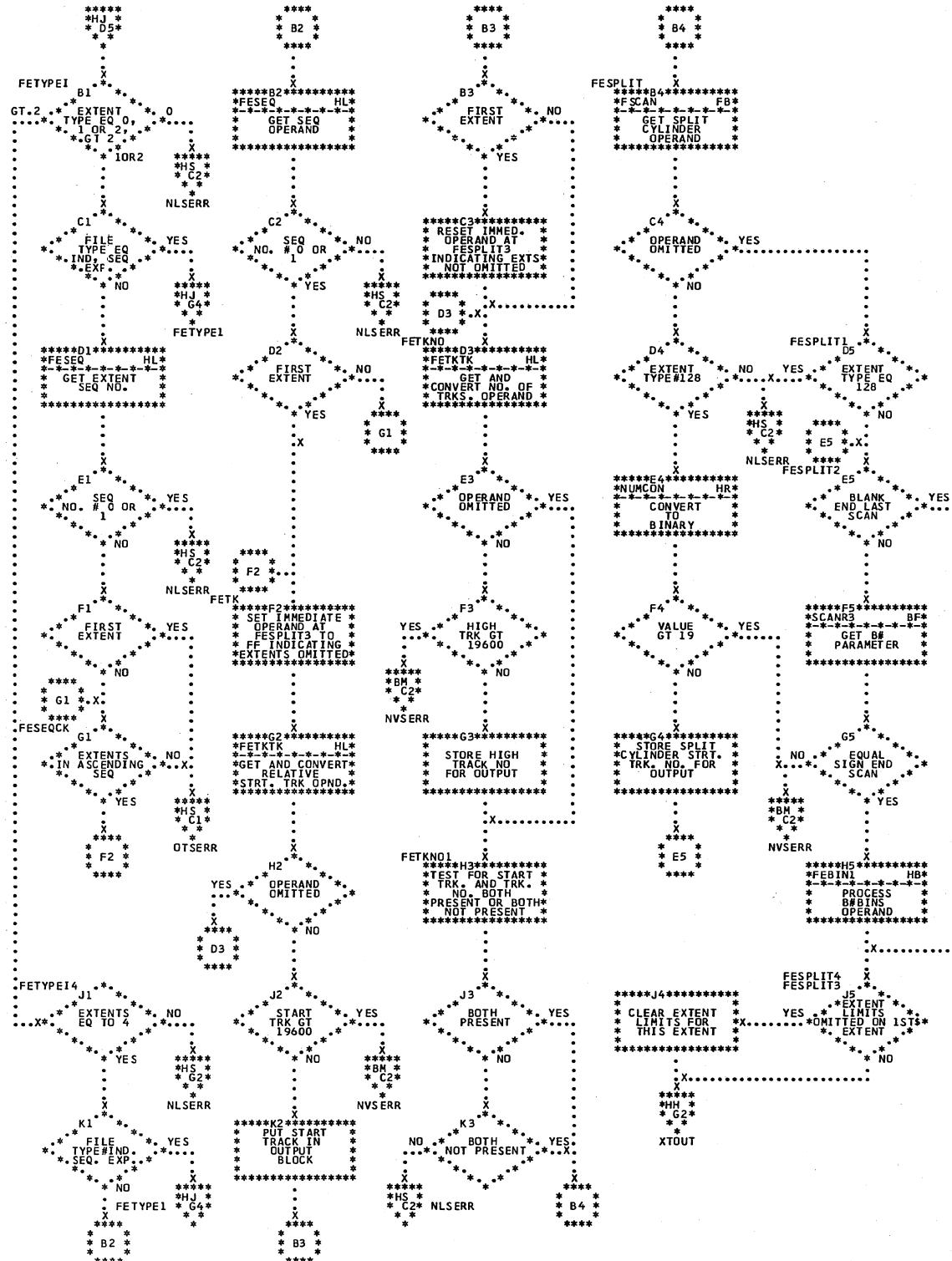


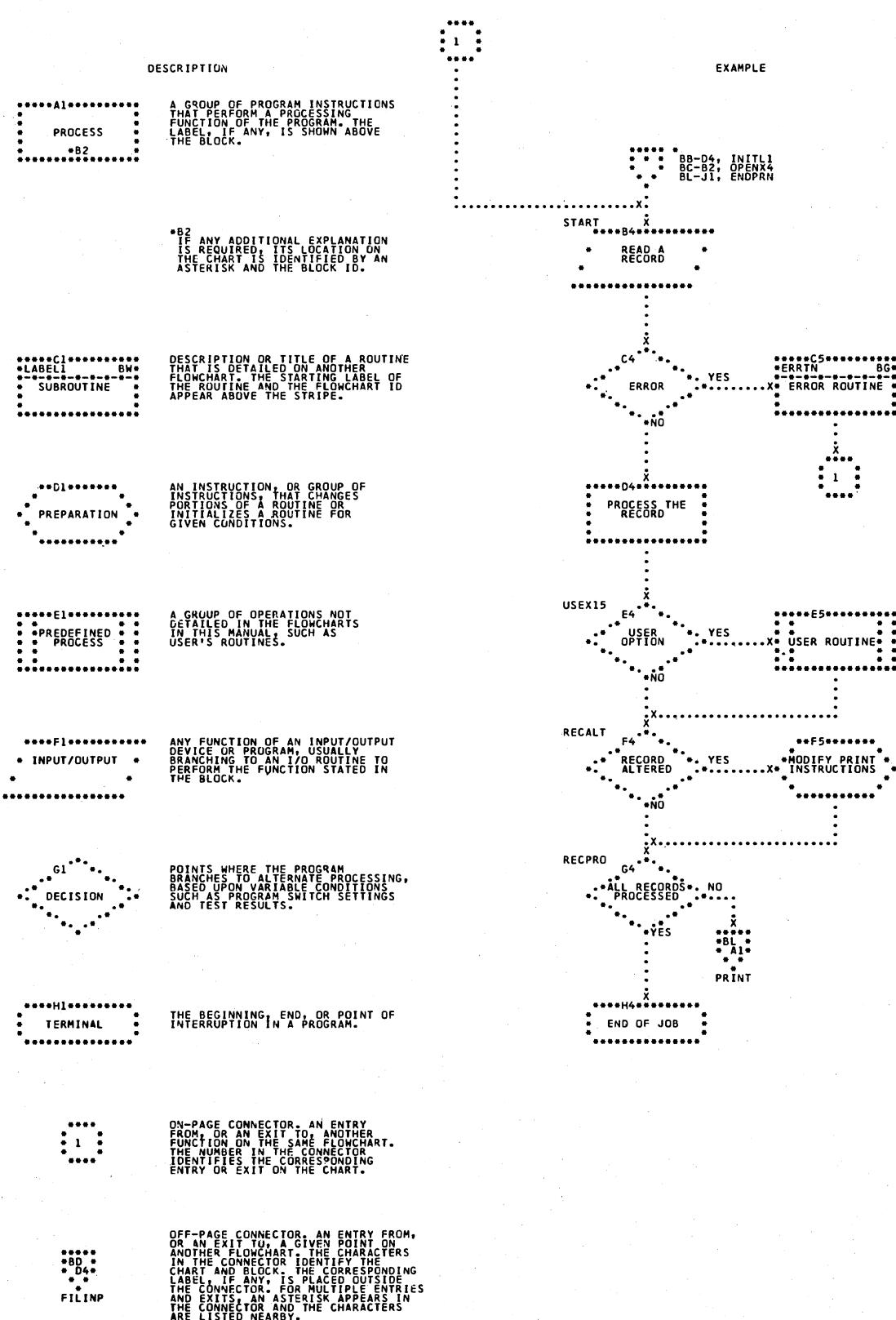
Chart HK. \$JOBCTLK - EXTENT Statement Processor (Part 2 of 3)  
Refer to Chart 14.



APPENDIX B: ERROR MESSAGE CROSS REFERENCE

<u>Message</u>	<u>Phase</u>	<u>Chart ID</u>	<u>1A9nD</u>	<u>\$JOBCTL</u>	<u>CE</u>
0I00A	\$\$A\$IPL2	AB	1C00A	\$JOBCTLA	BL
0I01A	\$\$A\$IPL2	AD		\$JOBCTLG	FG
0I10A	\$IPLRT2	AK		\$JOBCTLJ	GD
0I11I	\$IPLRT2	AQ	1C30A	\$JOBCTLJ	GB,GM
0I12I	\$IPLRT3	AT	1C3nI	\$JOBCTLG	FG
0I13I	\$IPLRT3	AR	1C70D	\$JOBCTLA	BE
0I14I	\$IPLRT3	AV	1C80D	\$JOBCTLA	BL
0I15I	\$IPLRT3	AR	1I00A	\$JOBCTLA	BB
0I16A	\$IPLRT4	AX	1I50I	\$JOBCTLA	BL
0I17A	\$IPLRT4	AX	1I70I	\$JOBCTLA	BM
0I18A	\$IPLRT2	AK	1I83A	\$JOBCTLM	JB
0I20I	\$IPLRT4	AY	1I84A	\$JOBCTLM	JA,JJ
0I22I	\$IPLRT4	AZC	1I85A	\$JOBCTLM	JD
0I23I	\$IPLRT4	AZ	1I86A	\$JOBCTLM	JD, JF, JG, JM
0I24A	\$IPLRT3	AU	1I87A	\$JOBCTLM	JE
0I25I	\$IPLRT4	AY	1I88I	\$JOBCTLJ	GJ
1A0nD	\$JOBCTL	CC,CD,CE, CG,CH,CJ	1I89I	\$JOBCTLM	JP, JQ
1A1nD	\$JOBCTL	CA,CB	1I90D	\$JOBCTLM	JP
1A2nD	\$JOBCTL	CA,CB,CH, CL,CP	1I91D	\$JOBCTLM	JQ
1A2nD	\$JOBCTL	FB	1I92D	\$JOBCTLM	JQ
1A20D	\$JOBCTLG \$JOBCTLJ	GB,GE	1I93I	\$JOBCTLM	JN
1A3nD	\$JOBCTL	CG,CR	1L10D	\$JOBCTLG	HF,HG,HJ, HK,HL,HM
1A4nD	\$JOBCTL	CA,CL,CH,CW		\$JOBCTLK	FE
	\$JOBCTLF	DD,DF	1P00D	\$JOBCTLG	HG,HJ,HP
1A40D	\$JOBCTLJ	GS	1S00A	\$JOBCTLA	GQ
	\$JOBCTLK	HQ		through	See BM-B1
1A5nD	\$JOBCTL	CP		\$JOBCTLM	
	\$JOBCTLF	DP	1S00D	\$JOBCTLG	FG
1A50D	\$JOBCTLJ	GS		\$JOBCTLM	JH
1A6nD	\$JOBCTL	CA	1IS03I	\$JOBCTLJ	GN
	\$JOBCTLF	DM	1S10D	\$JOBCTLG	EF,EN,FE
1A7nD	\$JOBCTL	CA		\$JOBCTLJ	GD,GN
	\$JOBCTLF	DK		\$JOBCTLK	HA,HC,HF,HG,
1A70D	\$JOBCTLJ	GE			HJ, HK, HP
1A90D	\$JOBCTLF	DL			

## APPENDIX C: EXPLANATION OF FLOWCHART SYMBOLS



Key to Communications Region Displacement:

56

Job control byte

- Bit 0: 1 = Job Accounting  
Interface (JA) not supported  
0 = Job Accounting  
Interface (JA) is supported
- 1: 1 = Return to caller on LIOCS disk open failure  
0 = Do not return to caller on LIOCS disk open failure
- 2: 1 = Job control input from SYSRDR  
0 = Job control input from SYSLOG
- 3: 1 = Job control output on SYSLOG  
0 = Job control output not on SYSLOG
- 4: 1 = Cancel job  
0 = Do not cancel job
- 5: 1 = Pause at end-of-job step  
0 = No pause at end-of-job step
- 6: 1 = SYSLOG is not a 1052  
0 = SYSLOG is a 1052
- 7: 1 = SYSLOG is assigned to the same device as SYSLST  
0 = SYSLOG is not assigned to the same device as SYSLST

57

Linkage control byte

- Bit 0: 1 = SYSLNK open for output  
0 = SYSLNK not open for output
- 1: 1 = \$ or FG program phase deleted, renamed, or cataloged (flag bit for SMAINEOJ)
- 2: 1 = Allow EXEC  
0 = Suppress EXEC
- 3: 1 = Catalog linkage editor output  
0 = Do not catalog linkage editor output
- 4: 1 = Supervisor has been updated  
0 = Supervisor has not been updated
- 5: 1 = Executing in AUTOTEST mode  
0 = Not executing in AUTOTEST mode
- 6: 1 = Relocate or condense in progress  
0 = Do not fetch SMAINEOJ at end of job to update system directory
- 7: 1 = Fetch SMAINEOJ at end of job to update system directory  
0 = Do not fetch SMAINEOJ at end of job for update

58

Language processor control byte. This is a set of switches used to specify nonstandard language translator options. The switches within the byte are controlled by job control OPTION statements and when set to 1, override standard options. The format of this byte is identical to the standard option byte (displacement 54) with one exception: Bit 7 in this byte is used to indicate to LIOCS that the rewind and unload option has been specified.

59

Job duration indicator byte

- Bit 0: 1 = Within a job condition  
0 = Outside a job condition
- 1: 1 = Dump on an abnormal end-of-job condition  
0 = No dump on abnormal EOJ
- 2: 1 = Pause at EOJ step } Set by Attention Routine for Job Control  
0 = No pause at EOJ }
- 3: 1 = Job control output on SYSLST  
0 = Output not on SYSLST
- 4: 1 = Job is being run out of sequence with a temporary assignment for SYSRDR  
0 = Conditions for 1 setting not met
- 5: 1 = PCIL is being condensed  
0 = PCIL is not being condensed
- 6: Reserved
- 7: 1 = Batch command just issued  
0 = Condition for 1 setting did not occur

Figure 10. Supervisor Communications Region (Part 4 of 5)

Key to Communications Region Displacements:

60	Binary disk address of the volume label area (label cylinder).
62	→ 76 As illustrated (Figures for information blocks, I/O tables, and pointers begin at Figure 21 which refers to more detailed Figures).
78	Set to the value nn specified in the LINES = nn parameter of the STDJC macro.
79	The format of the system date contained within this field is determined by the IPL program from information supplied in the date convention byte (displacement 53). Bytes 85-87 contain the day count.
88	Bytes reserved for use by LIOCS. Transient dump programs insert a key to indicate to the LIOCS end-of-volume routine, \$\$BCMT07, that it was called by a B-transient.
90	Address of the first part of the program information block (PIB) table. (See Figures 18 and 19).
92	ID number of the last checkpoint. Temporary indicator of file protected DASD. Used at IPL time, when DASDFP is specified.
94	Length of the LUBID queue (in bytes). This equals the number of channel queue entries. It can also be used to access the REQID, LUBDSP, and TKREQID queues: ( See GY24-5151).
96	Address of disk I/O position data. This is the starting address of the disk information block (DIB) table (See Figure 17).
98	Address of the beginning of the error recovery block. The error recovery block contains addresses of error recovery exits, error recovery queue information that can be used by physical transients routines, and defines storage for the error queue entries (See Figure 43 - GY24-5151).
100	→ 104 Option Tables. ( See GY24-5151).
106	Key of the program (BG, F2, or F1) that has timer support.
108	Address of LUBID queue. ( See GY24-5151).
110	Logical Transient Key (LTK) contains the same value as the PIK (PID) (Displacement 46) when the logical transient is requested. When the transient area is not in use, LTK is equal to zero. The SVC 2 routine sets the LTK. The SVC 11 routine resets the LTK.
112	Supervisor constants: DOLLARBO (4 bytes) = C'\$\$BO' SSKADR (5 bytes) = XL5'0' LTAREA (3 bytes) = Adcon of LTSVPT, logical transient save pointer
124	Address of second part of program information block (PIB) table (See Figure 20).
126	Address of PDTABB, table of DTF addresses for MICR support.
128	Address of QTAM vector table (IJLQTAD).
132	Address of background communications region.
134	Option Indicator Byte Bit 0: 1 = MCRR indicated for OBR writer 0 = No MCRR indicated for OBR writer 1: 1 = EU interface active 0 = EU interface not active 2: 1 = TP request 0 = No TP request 3: 1 = Supervisor support for only 9-track tape 0 = Supervisor does not support 9-track tape exclusively 4: Reserved 5: 1 = RETAIN/370 support generated 0 = RETAIN/370 support not generated 6-7: Reserved
135	System Configuration Byte 2 Bit 0: 1 = PCIL supported 0 = PCIL not supported 1-7: Reserved
136	Pointer to communications region extension (See Figure 11).

Figure 10. Supervisor Communications Region (Part 5 of 5)

Key to SDR Communications Region Displacements:

32 Address of SDR accumulator area which contains half-byte counters and accumulated error conditions.

36 Address of SDR unit switches.

SDR switch byte (1 for each PUB):

X'80' - Update operations complete

X'40' - Counters on external file overflowed

X'20' - I/O error during write

X'08' - SDR update half-byte counters routine required

X'04' - Update SDR record routine required

Other - Reserved

When entry contains X'01000000', indicates MCRR, no SDR supported.

40 Reserved.

44 SDR1 register save area.

72 Mask formats for interpretive error accumulator, SDR1:

X'FF' - End of update

X'FE' - Bypass counter

X'FD' - Set up 'OR' condition to previous counter

X'FC' - Ignore list item

Other - Test bit in error queue

76 Used by the interpretive error accumulator routine to process list passed by OBR/SDR A - transient.

96 Used by the interpretive error accumulator routine.

104 Used by the interpretive error accumulator routine for address alignment.

108 Executed by the interpretive error accumulator routine.

112 Loop counter for the SDR counter update.

116 Save area for pointers to entries in the SDR error queue.

118 Work area where half byte error counters are unpacked and updated.

136 List of devices passed to the SDR processor and \$\$ANERAD.

156 Used by SDR/OBR recorder phases to pass error message displacements and disk error addresses in event of an error.

160 Entry point from OBR/SDR A - transients. Branches to label SDRMM.

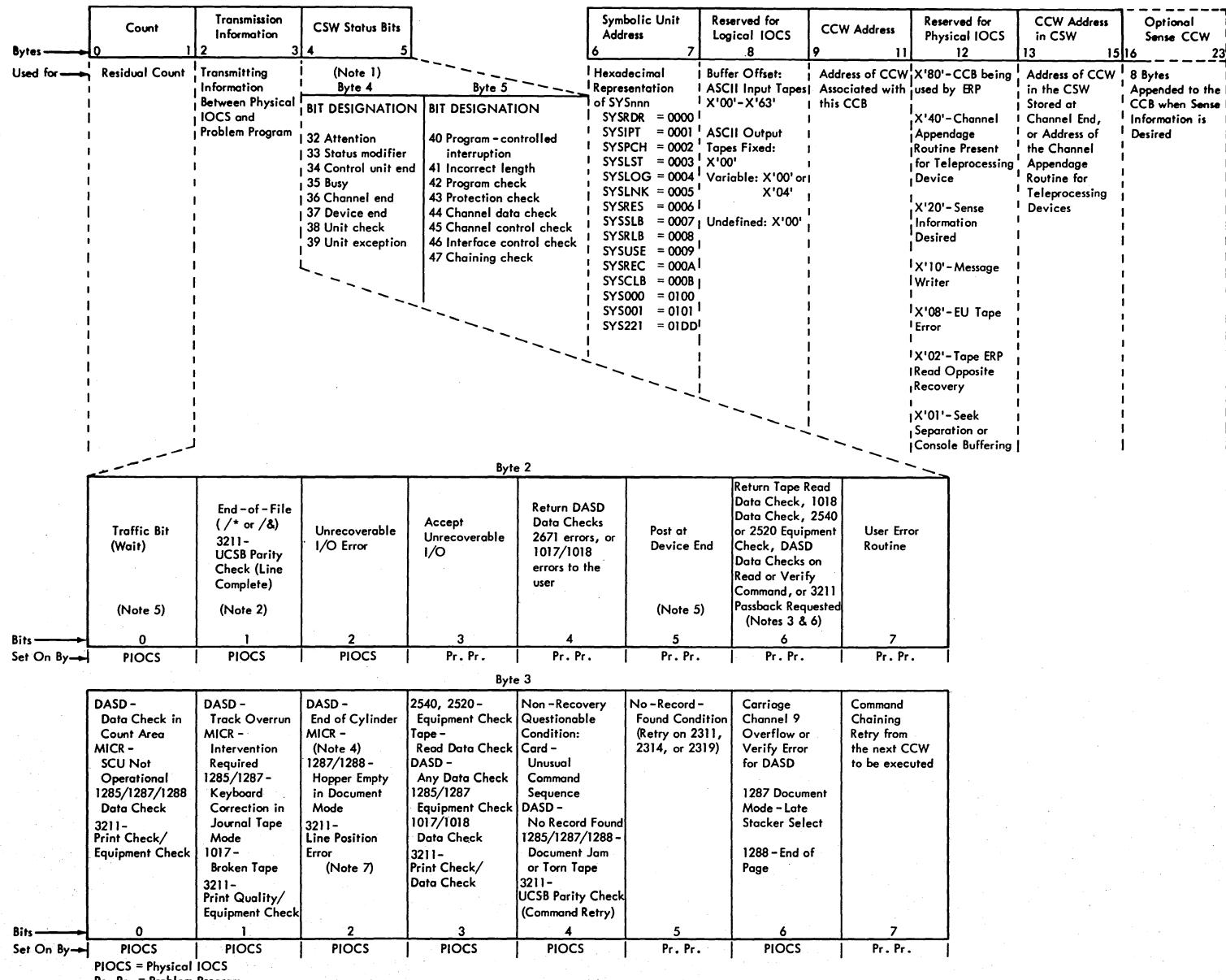
164 Pointer into the OBR/SDR unit switches. Status posted by recorder phases. (See byte 36).

168 OBR and SDR records formatted by the recorder phases.

Figure 12. SDR Communications Region - SDRTABLE (Part 2 of 2)

226 DOS IPL and Job Control

Figure 13. Command Control Block (CCB)



Note 1. Bytes 4 and 5 contain the status bytes of the Channel Status Word (Bits 32-47). If byte 2, bit 5 is on and device end results as a separate interrupt, device end will be ORed into CCB byte 4.

Note 2. Indicates /\* or \*/ statement encountered on SYSRDR or SYSIPT. Byte 4, bit 7 (unit exception) is also on.

Note 3. DASD data checks on count not returned.

Note 4. For 1255/1259/1270/1275/1412/1419, disengage. For 1275/1419D, I/O Error in external interrupt routine (channel data check or busout check).

Note 5. The traffic bit (Byte 2, bit 0) is normally set on at channel end to signify that the I/O was completed. If byte 2, bit 5 has been set on, the traffic bit and bits 2 and 6 in byte 3 will be set on at device end. Also see Note 1.

Note 6. 1018 ERP does not support the Error Correction Function.

Note 7. This error occurs as an equipment check, data check, or FCB parity check.

PIB TABLE

Byte Number		0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	= 16 Byte Length
All Bound PIB	Flag Byte See A *	Reserved	SP Prefix		Branch Instruction to the All Bound Routine								Reserved					
Problem Program PIB (Note 1)	Flag Byte See B *	Cancel Code **	SYSLOG ID (BG, F2, or F1)	NOP Instruction (CR)	Address of the Partition Save Area		Number of Core Blocks (Note 2)	Address of the Origin of the Partition		PIB Assign Flag See D *	User LUB Index	Number of Program LUBs	Flag Byte See C *					
Attention PIB	Flag Byte See E *	Cancel Code **	SYSLOG ID (AR)	Branch Code (BC)	Active=Address of Save Area Inactive=Remainder of BC Instruction		Switch Byte See F *	Logical Transient Bucket (contains save area address)		X'07' See D *	Reserved	Address of the Logical Transient						
Quiesce PIB	Flag Byte See A *	Cancel Code **	C' / &'		Branch Instruction to Quiesce I/O Routine		Scratch Byte X'00'	X'00'	X'04'	X'08'	X'OC'	X'10'	X'14'	X'18'				
Supervisor PIB	Flag Byte See A *	Cancel Code **	SP Prefix		Branch Instruction to General Exit Routine		Address of SYSRES PUB	Length of Error Queue Entry			Constants to Clear Bytes 2-5 of CCB							
Subtask PIB for AP (Note 3)	Flag Byte See B *	Cancel Code **	SYSLOG ID (BG, F2, or F1)	NOP Instruction	Address of the Save Area		Number of Core Blocks (Note 2)	Address of the Origin of the Main Task		PIB Assign Flag See D *	User LUB Index	Number of LUBs	Flag Byte See C *					

Note 1: Three problem program PIBs are built in this sequence when the MPS or BJF feature is selected as a generation option:

Background PIB  
Foreground 2 PIB  
Foreground 1 PIB

When a batch-only environment is established at generation time, the All Bound and Foreground PIBs are excluded from the table, and only one (BG) problem program PIB is built. However, the X'20' bytes that F2 and F1 PIBs normally occupy (between PIBBG and PIBAR) are filled with 32 bytes of DIBs data.

Note 2: Number is in multiples of 2K for F2 and F1. BG is always 10K (X'0A').

Note 3: Total of nine subtask PIBs are generated, and only when AP is specified at generation time.

\* See Figure 16 for flag byte expansions A, B, C, D, E and F.

\*\* See Supervisor PLM, GY24-5151

Bytes 90 and 91 (X'5A'-'5B') of the communications region contain the address of the first part of the PIB Table. Label PIBTAB identifies the first byte of the table.

Figure 18. First Part of PIB Table

**A** Supervisor, Quiesce, and ALL Bound PIB Flags:

Bit 0: 1 = Always one  
 1-4 : 0 = Always zero  
 5 : 1 = Always one  
 6 : 1 = Active  
 0 = Inactive  
 7 : 1 = Active  
 0 = Inactive

Note: If PTO=YES is specified, Bit 6 is a one in the Quiesce I/O PIB when attached by the supervisor. Otherwise it is always zero.

**B** Problem Program PIB Flag (First Byte in PIB):

Bit 0: 1 = Registers stored  
 0 = Registers not stored  
 1-3 : 0 = Always zero  
 4 : 1 = QTAM Wait active  
 0 = QTAM Wait inactive  
 5 : 0 = Normal execution  
 1 = Program has seized the system  
 6 : 1 = Unbound  
 0 = SVC 2-bound (B-transient in progress)  
 7 : 1 = Unbound  
 0 = SVC 7-bound (waiting for an I/O interrupt)

X'80' indicates the program is not present in the system  
 X'87' indicates the program is PTO bound  
 X'89' indicates the program is IDRA bound

**C** Problem Program PIB Flag (Last Byte in PIB):

Bit 0: 1 = Batched Job in Foreground  
 0 = No BJF  
 1: Cancel in LTA and Device not Assigned  
 2: 1 = /& on SYSIN if DASD  
 0 = No /& on SYSIN  
 3-4: Reserved  
 5: 1 = Task is cancelled  
 0 = Task not cancelling  
 6: 1 = Subtask (s) attached  
 0 = No subtasks attached  
 7: 1 = In AB Routine  
 0 = Not in AB Routine

**D** PIB Assign Flag

X'80' = SYSRES DASD file protect inhibited (allow write operation on SYSRES)  
 X'40' = Channel appendage exit allowed (BTAM)  
 X'20' = Cancel in progress (used in terminator function)  
 X'10' = Cancel control (set on a foreground cancel)  
 X'08' = Hold-Release flag for foreground assignments  
 X'07' = Supervisor or Attention routine PIB assign flag setting  
 X'04' = Background program PIB assign flag setting  
 X'02' = Foreground 1 program PIB assign flag setting  
 X'01' = Foreground 2 program PIB assign flag setting

**E** Attention PIB Flag

Bit 0: 1 = Registers stored  
 0 = Registers not stored  
 1-5 : 0 = Always zero  
 6 : 1 = Attention routine active  
 0 = Attention routine SVC 2-bound  
 7 : 1 = Active  
 0 = SVC 7-bound

X'80' indicates the attention routine is not present in the system.  
 X'89' indicates the program is IDRA bound

**F** Attention PIB Switch Byte

Bit 0-2: Reserved  
 3: 1 = PTAFTCH (Fetch SSANERRY, Z, or 0) Switch ON  
 0 = PTAFTCH (Fetch SSANERRY, Z, or 0) Switch OFF  
 4: 1 = Detach Logical Attention Routine (\$\$BATTNA) Switch ON  
 0 = Detach Logical Attention Routine (\$\$BATTNA) Switch OFF  
 5: 1 = Physical Attention Recall Switch ON  
 0 = Physical Attention Recall Switch OFF  
 6: 1 = Attention Request Switch ON  
 0 = Attention Request Switch OFF  
 7: 1 = External Interrupt Request Switch ON  
 0 = External Interrupt Request Switch OFF

Figure 19. PIB Flag Expansions

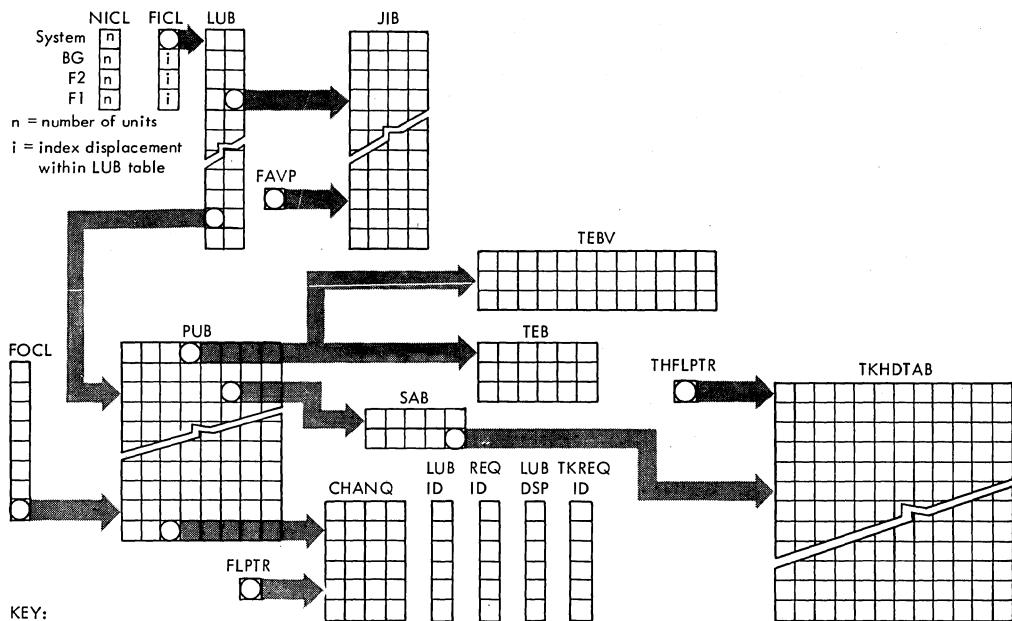
Note 1: The TEBV (Tape Error Block by Volume) Table is composed of one Status Block and (n) Error Blocks, and is addressed symbolically by label TEBVTAB.

Supervisor generation options in the FOPT macro determine the size of the TEBV Status Block at generation time:

- When EVA is chosen without ESTV, the TEBV Status Block is four bytes long (bytes 0-3), followed by TEBV Error Blocks, so that bytes 4-21 are omitted.
- When ESTV output is to SYSLOG, the TEBV Status Block is six bytes long (bytes 0-5), followed by TEBV Error Blocks, so that bytes 6-21 are omitted.
- When ESTV output is to DASD, the TEBV Status Block is 22 bytes long (bytes 0-21, such as shown in this Figure), followed by TEBV Error Blocks.

Note 2: The number of TEBV Error Blocks generated corresponds to the (n) parameter of the FOPT macro for TEB, TEBV, or EVA options. A TEBV Error Block always contains 18 bytes, as shown in bytes 22-39 of this Figure. Therefore, the TEBV Table is composed of one TEBV Status Block (with its byte length dependent on supervisor generation options, as described in Note 1), followed by (n) number of 18-byte TEBV Error Blocks.

Figure 21. TEBV Table Showing Status Block and Error Blocks  
(Part 2 of 2)



- KEY:**
- NICL (Number in Class) : The first byte contains the number of system class units. The second, third, and fourth bytes contain the number of programmer class units (BG, F2, F1) (Figure 15).
  - FICL (First in Class) : The first byte points to the first system class unit in the LUB table. (Always the first LUB table entry.) The second byte points to the first programmer class unit in the LUB table BG area. The third points to the first programmer class unit in the LUB table F2 area. The fourth points to the first programmer class unit in the LUB table F1 area (Figure 15).
  - LUB (Logical Unit Block) Table : The first byte points to a PUB table entry (if the logical unit is assigned) or contains X'FF'. The second byte points to a JIB table entry or contains X'FF' (Figure 15).
  - PUB (Physical Unit Block) Table : The first two bytes contain the channel and unit address of the physical device; the third a CHANQ pointer; the fourth a TEB pointer; the fifth device type codes; the sixth a device characteristic code or a SAB pointer; the seventh the channel scheduler flag; and the eighth has the job control flag (See Figure 14).
  - FOCL (First on Channel List) : The first byte points to the first PUB (highest priority) on channel zero. The next byte points to the first PUB (highest priority) on channel one, etc. A hexadecimal FF indicates the associated channel is not supported.
  - TEB (Tape Error Block by Unit) : One TEB is built for each tape unit at supervisor generation time if tape error statistics by unit are required (Figure 16).
  - TEBV (Tape Error Block by Volume) : One TEBV is built for each tape unit at supervisor generation time if tape error statistics by volume are required (Figure 21).
  - FAVP (First Available Pointer) : A one-byte pointer to the next available JIB entry.
  - JIB (Job Information Block) : The first two bytes contain extent or LUB information. The third contains ownership and JIB flags. The fourth contains JIB chaining information (See Figure 23).
  - CHANQ (Channel Queue) Table : The first byte contains the chain field (a pointer to the next in queue). The last three bytes contain the CCB address.\*
  - LUBID (LUB Identification) : A one-byte pointer to the LUB making the I/O request.\*
  - REQID (Requestor Identification) : A one-byte pointer to the program containing the CCB.\*
  - LUBDSP (LUB Displacement) : A one-byte value equal to the absolute LUB number (CCB byte 7)\*.
  - FLPTR (Free List Pointer) : A one-byte pointer to the next free entry in the channel queue.\*
  - SAB (Seek Address Block) : A four-byte (BCCH) address that is the current disk address of the device plus a fifth byte that contains a Track Hold Table pointer of X'FF'. If the Track Hold function is not supported, the fifth byte contains X'00'.
  - TKHDTAB (Track Hold Table) : The first byte contains a pointer to the next available entry (or X'FF'); bytes 2 - 4 have CCB address of the requesting task; bytes 5 - 10 have a disk address (BBCCHH) of track being held; byte 11 has key of owning track; and byte 12 has two uses: bit 0=1 means a task is waiting for the track, and bits 4 - 7 count the number of holds on the track.\*  
Note: The number of holds is one more than the value of bits 4 - 7 of the last byte.
  - THFLPTR (Track Hold Free List Pointer) : A one-byte pointer to the next free entry in the Track Hold Table.\*
  - TKREQID (Track Requestor Identification) : A one-byte pointer to the PIB of the task requesting I/O.\*

\*See Supervisor PLM, GY24-5151

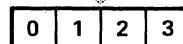
Figure 22. I/O Table Interrelationship

### JIB Table

JIB 1
JIB 2
JIB 3
JIB 4
JIB 5
JIB 6

Number (length of JIB table)  
determined at supervisor generation

Note: Two JIBs are required for a 2321 extent; one for lower limit and one for upper limit. The lower limit defining JIB must be chained to the upper limit defining JIB. Byte 1 of this type JIB contains the subcell number times 10 plus the strip number in binary.



### Type of Entry

Stored standard assignment	LUB entry of stored standard assignment (PUB and JIB pointers)
Alternate assignment	PUB pointer of alternate X'00' assignment
① 2311 Extent	C <sub>L</sub> C <sub>L</sub> C <sub>H</sub> C <sub>H</sub> ②
① 2321 Extent	or B <sub>L</sub> B <sub>L</sub> C <sub>L</sub> C <sub>L</sub> B <sub>H</sub> B <sub>H</sub> C <sub>H</sub> C <sub>H</sub> ③

Flag Type	Bit	Meaning if Bit = 1
Contents	0	Stored standard assignment
	1	Alternate assignment
	2	2311 Extent
	3	2321 Extent
Ownership	4	Standard assignment for DASD extent
	5	Background
	6	Foreground 1
	7	Foreground 2

Chain Byte.  
Contains the displacement index of the next JIB.  
A hexadecimal 'FF' defines the end of the chain.

① Only when file-protect on DASD

② Lower Cylinder  
Upper Cylinder

③ Cell or combined subcell and strip

Bytes 68 - 69 (X'44' - '45') of the communications region contain the address of the JIB table entry. Label JIBTAB identifies the first byte of the table.

Figure 23. Job Information Block (JIB) Table

Displacement	Label	Description
0-3	ACCTWK1* (ACCTABLE)*	Work area used in SIO update.
4-7	ACCTWK2	Work area used with ACCTWK1 in start/stop time routine
8-11	ACCTSVPT	Job card pointer; address of job card field following jobname.
12	ACCTPART	ID of partition in charge (partition switch name).
13	ACCTRES2	Reserved.
14-15	ACCTLEN	Length of SIO area = $6n + 1$ , where n = number of devices for this partition in SYSGEN option JA = (n1, n2, n3).
16-21	ACCTLOAD	Label area instruction; moves JAI label area address to OPEN/CLOSE transients.
22-23	ACCTRES3	Reserved.
24-27	ACCTLADD	Address of alternate label area.
28-31	ACCTCPU	Counter for CPU time elapsed in a jobstep, counted in 300ths of a second.
32-35	ACTOVHT	Counter for overhead time; time not charged to any partition.
36-39	ACCTBNDT	Counter for all-bound time; system wait state time divided between running partitions.
40-47	ACCTSVJN	Save area for job name during simulated EOJ.
JOB ACCOUNTING TABLE (user's portion of Partition Table)		
48-55	ACCTJBNM	Job name; taken from job card.
56-71	ACCTUSR	User information; 16 bytes from Job card.
72-73	ACCTPTID	Partition ID; 'BG', 'F2', or 'F1' in EBCDIC format.
74	ACCTCNCL	Cancel code; see Cancel Codes and Messages (in <u>Supervisor and Related Transients</u> , GY24-5151).
75	ACCTYPER	Type of record: 'S'=job step, 'L'=last step of job.
76-83	ACCTDATE	Date in format specified at SYSGEN (MM/DD/YY or DD/MM/YY).
84-87	ACCTSTRT	Start time of job, in packed decimal (OHHMMSS; F=sign).
88-91	ACCTSTOP	Stop time of job, in same format as ACCTSTRT
92-95	ACCTRES	Reserved.
96-103	ACCTEXEC	Phase name; taken from execute card.
104-107	ACCTHICR	High core address of active program phase, from COMREG.
108-111	ACCTIMES	CPU time elapsed in a job step; counted in 300ths of a second.
112-115	-----	Overhead time; elapsed time not charged to any partition, in 300ths of a second.
116-119	-----	All-bound time; system wait state time divided between running partitions, in 300ths of a second.
120	ACCTSIO	SIO tables: 6 bytes for each device specified by SYSGEN options, as follows: 2 bytes for device address (Ocuu), 4 bytes for count of SIOs in current jobstep.
-----	-----	Overflow byte: normally X'20', but is X'30' if more devices are used within a partition than specified by SYSGEN options.

\*Note: DSECT ACCTABLE symbolically addresses the JAI Partition Tables with labels as shown. Each partition in which JAI is supported has its own JAI Partition Table, labeled ACCTBG, ACCTF2, ACCTF1, for active partitions BG, F2, and F1 respectively.

Figure 24. Job Accounting Interface Partition Table

GY24-5086-4

This sheet is for comments and suggestions about this manual. We would appreciate *your* views, favorable or unfavorable, in order to aid us in improving *this* publication. This form will be sent directly to the author's department. Please include your name and address if you wish a reply. Contact your IBM branch office for answers to technical questions about the system or when requesting additional publications. Thank you.

Name

Address

How did you use this manual?

As a reference source

As a classroom text

As a self-study text

What is your occupation?

Your comments\* and suggestions:

---

\* We would especially appreciate your comments on any of the following topics:

Clarity of the text

Organization of the text

Accuracy

Cross-references

Index

Tables

Illustrations

Examples

Appearance

Printing

Paper

Binding

**YOUR COMMENTS, PLEASE . . .**

This manual is part of a library that serves as a reference source for systems analysts, programmers and operators of IBM systems. Your answers to the questions on the back of this form, together with your comments, will help us produce better publications for your use. Each reply will be carefully reviewed by the persons responsible for writing and publishing this material. All comments and suggestions become the property of IBM.

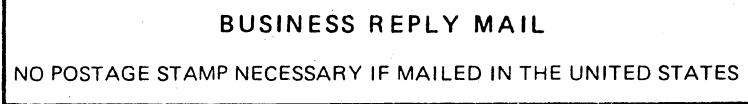
Please note: Requests for copies of publications and for assistance in utilizing your IBM system should be directed to your IBM representative or to the IBM sales office serving your locality.

CUT ALONG THIS LINE

Fold

Fold

FIRST CLASS  
PERMIT NO. 1359  
WHITE PLAINS, N. Y.



POSTAGE WILL BE PAID BY . . .

IBM Corporation  
112 East Post Road  
White Plains, N.Y. 10601

Attention: Department 813 U

Fold

Fold

International Business Machines Corporation  
Data Processing Division  
1133 Westchester Avenue, White Plains, New York 10604  
[U.S.A. only]

IBM World Trade Corporation  
821 United Nations Plaza, New York, New York 10017  
[International]

**IBM**

**International Business Machines Corporation  
Data Processing Division  
1133 Westchester Avenue, White Plains, New York 10604  
[U.S.A. only]**

**IBM World Trade Corporation  
821 United Nations Plaza, New York, New York 10017  
[International]**