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File No. S370-37

**Systems**

**DOS/VS System Serviceability  
Aids Logic**

**Program Numbers 5745-SC-PDA  
5745-SC-UTL  
5745-SC-TPE  
5745-SC-ERP**

**Release 29**

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This edition applies to Version 5, Release 29, of the IBM Disk Operating System/Virtual Storage, DOS/VS, and to all subsequent versions and editions until otherwise indicated in new editions or Technical Newsletters. Changes are continually made to the information herein. A change to the text or to an illustration is indicated by a vertical bar to the left of the change. Before using this publication in connection with the operation of IBM systems, consult the IBM System/360 and System/370 Bibliography, GA22-6822, for the editions that are applicable and current.

This edition documents DOS/VS support of the following new systems and devices:

IBM System/370 Model 115  
IBM System/370 Model 155-II  
IBM System/370 Model 158  
IBM 3203/5203  
IBM 3340  
IBM 3540

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This PLM (Program Logic Manual) is a detailed guide to the serviceability aid programs provided with the DCS/VS system, supplementing various supervisor and transient functions. It documents internal logic and data flow through descriptive text, flowcharts, and figures.

For the complete logic of the DCS control and service operations, this manual is to be used with the following six companion PLMs.

- DOS/VS Error Recovery and Recording Transients, SY33-8552
- DOS/VS IPL and Job Control, SY33-8555
- DOS/VS Librarian, SY33-8557
- DOS/VS Linkage Editor, SY33-8556
- DOS/VS Supervisor Logic, SY33-8551
- DOS/VS Logical Transients, SY33-8553.

Prerequisite to the effective use of the seven PLMs are the following publications.

- IBM System/370 Principles of Operation, GA22-7000
- Introduction to DOS/VS, GC33-5370
- DOS/VS System Management Guide, GC33-5371
- IBM System/360 Disk and Tape Operating Systems, Assembler Language, GC24-3414.

Publications related in subject matter to the seven system control PLMs are:

- DOS/VS Serviceability Aids and Debugging Procedures, GC33-5380
- DOS/VS System Generation, GC33-5377

- DCS/VS Messages, GC33-5379
- DOS/VS Supervisor and I/O Macros, GC33-5373
- DOS/VS Operating Procedures, GC33-5378
- DCS/VS Data Management Guide, GC33-5372.

Titles and abstracts of other related publications are listed in the IBM System/360 and System/370 Bibliography, GA22-6822.

This manual consists of nine major sections:

- PDAIDS (Problem Determination Aids)
- SCAID (System Debugging Aid)
- DUMPGEN (Dump Generator)
- PDSDM (Page Data Set Dump)
- LSERV (Label Information Cylinder Display)
- ESTVUT (Error Statistics by Tape Volume Utility)
- FREP (Environmental Recording, Editing, and Printing)
- Printer support programs
- Appendixes containing various cross-reference lists, sample printouts, and supervisor tables.

The flowchart symbols used in this manual conform to the flowcharting standards of the American National Standards Institute, Inc. Numerals, such as C0, identify the program or general level flowcharts. The detailed flowcharts are identified by letters AA through ZZ. See Appendix D.



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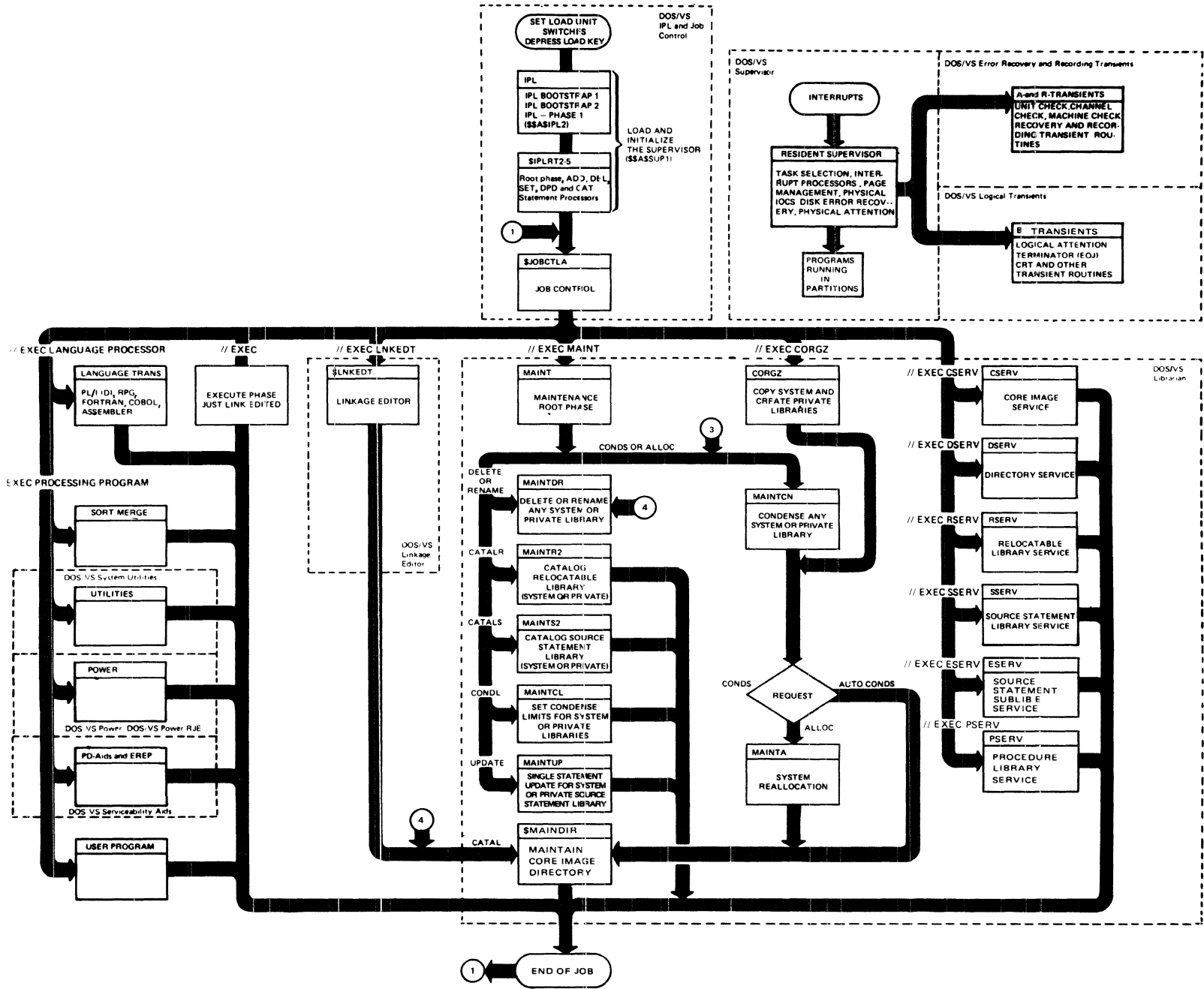
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INTRODUCTION

PDAIDS (Problem Determination Aids) provide the option to trace specified events during the operation of a program. Tracing may record the events of the problem program, the supervisor, or both. PDAIDS also provide the possibility to dump transient areas.

INPUT/OUTPUT TRACE: The I/O (Input/Output) trace records I/O device activity. The data recorded by the I/O trace function may be for all or a selected group of I/O devices. When an I/O interrupt occurs, the data recorded is:

- I/O old PSW
- CSW.

When an SIO instruction is issued by the DOS/VS supervisor, the data recorded is:

- Condition code
- Device address
- CCB address
- CSW.

FETCH/LOAD TRACE: The F/L (Fetch/Load) trace records the order in which phases and transients are called from the core image library under the control of DOS/VS. When a fetch or a load is issued that causes an SVC 1, 2, or 4, the data recorded is:

- Phase or transient name
- SVC number
- Location of the SVC
- Partition identifier
- Load address of the phase
- Entry address of the phase.

At times, SVCs 5, 6, 11, and 14 branch directly into the supervisor fetch or load routine. The fetch or load (SVCs 1, 2, and 4) is recorded; however, the calling address and the SVC values for SVCs 5, 6, 11, and 14 are not indicated in the actual fetch or load trace record.

GENERALIZED SUPERVISOR CALL TRACE: The GSVIC (Generalized Supervisor Call) trace records SVC interrupts as they occur. All SVCs, or a selected group of SVCs, may be traced. The data recorded by the GSVIC trace is:

- SVC old PSW
- Task identification
- Contents of register 0
- Last three bytes of register 1
- SVC number
- Partition identifier.

If PTO is included in the system, the SVCs issued when the physical transient area is busy are not traced.

QTAM TRACE: The QTAM trace records the input/output activity of QTAM in three areas:

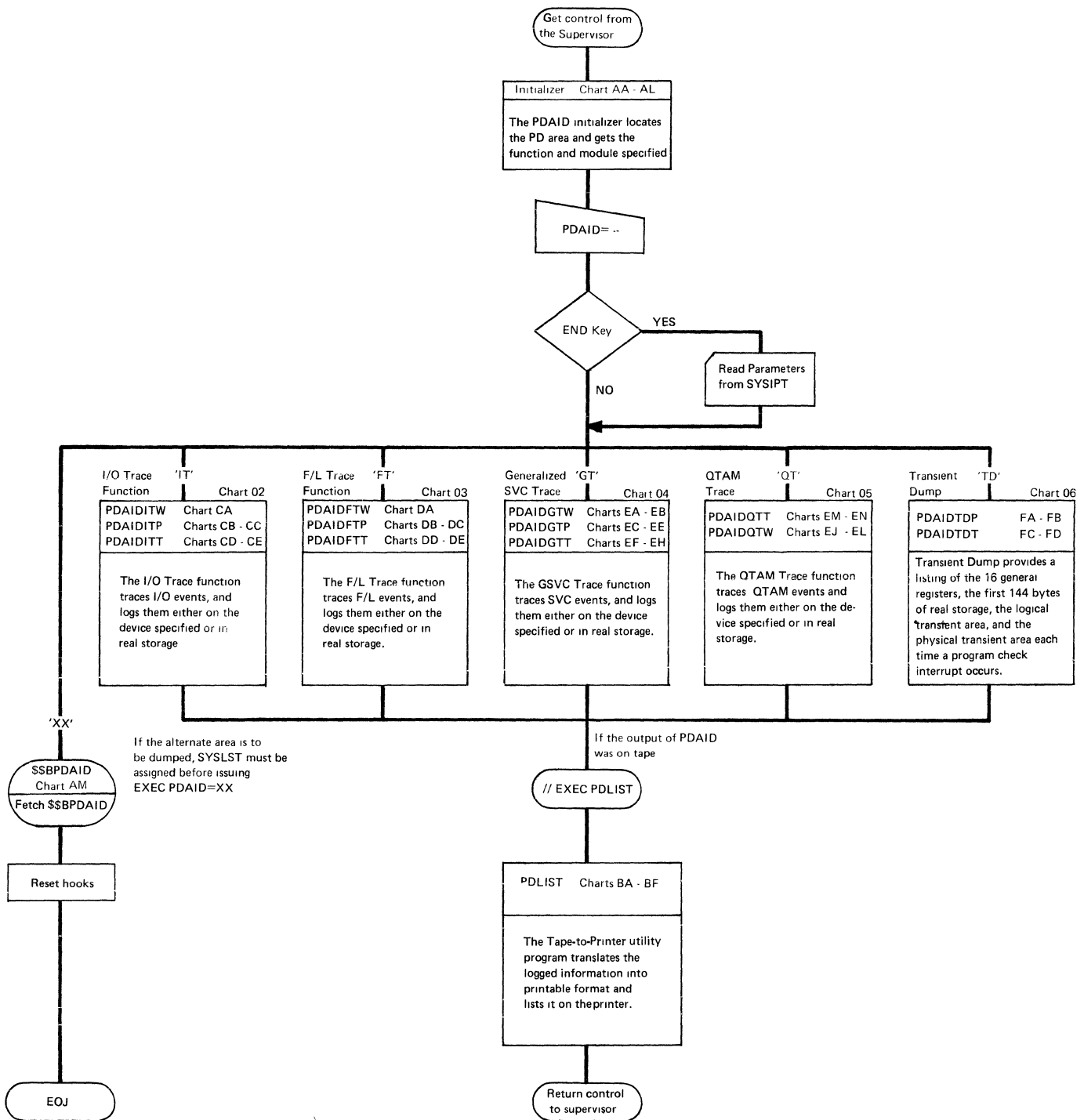
- SVCs 0 and 31
- A supervisor-issued SIO
- An I/O interrupt.

The I/O old PSW, the CSW, and the device address are recorded when an I/O interrupt occurs. The condition code, device address, CCB address, and CSW are recorded when a supervisor issues an SIO. The SVC old PSW, the SVC number, the contents of registers 0 and 1, and the partition identifier are recorded when an SVC 0 or 31 is issued.

TRANSIENT DUMP: The transient dump allows certain areas of the supervisor to be displayed when a program check occurs. These areas are:

- The 16 general registers used at the time of failure
- The first 144 bytes of main storage
- The physical transient area
- The logical transient area.

Chart 01. General Logic Flow of the PDAID Programs



## System Consideration for PDAIDs

If the use of PDAIDs is desired, then the following must be performed prior to their execution:

- During system generation, specify PD=YES or PD≥1400 in the FOPT macro of the installation tailored supervisor. (See Figure 55 in Appendix B for supervisor storage allocations.)

Note: Up to 10,240 may be specified to increase the size of the PD area for the core-wrap mode.

- Catalog the main phase (PDAID) to the core image library prior to its execution.

All the PDAID programs are self-relocating, and can be run in any partition. If data provided by PDAIDs is recorded on magnetic tape, the PDLIST program must be used to obtain a listing. See the DCS/VS System Generation manual listed in the Preface for supervisor specifications and cataloging procedures.

## Initiation of PDAID

You can execute PDAIDs by using standard DOS/VS job control language from either SYSLOG or SYSIPT. The statement

```
// EXEC PDAID
```

causes the main phase, PDAID, to load at the address of the initiating partition. Control is given to PDAID, which issues the following message to the operator on SYSLOG:

```
4C10D PDAID=
```

The operator must respond to this message with one of the following:

IT Specifies an I/O Trace. See Note 1.

FT Specifies an F/L Trace. See Note 1.

GT Specifies a GSVC Trace. See Note 1.

QT Specifies a QTAM Trace. See Note 1.

TD Specifies transient dump. See Note 1.

XX Terminates the PDAID presently running. See Note 2.

END Indicates PDAID control statements are entered through SYSIPT.

Note 1: When IT, FT, GT, QT, or TD is specified, the operator must give additional PDAID control statements through SYSLOG.

Note 2: If a msg of the alternate area is desired, SYSLST must be assigned before XX is specified.

Figure 2 illustrates the PDAID control statements in the sequence in which they must be used.

- The END response is valid only for SYSLOG and cannot be used as a SYSIPT operand.
- Multiple operands or operator responses to PDAID control statements for traces with a variable number of functions (such as ignoring SVCs) are not allowed. Repeat each parameter with each variable. Repeat each message until either the maximum number of variables is reached or an END response is given.
- GO terminates the PDAID control input, and the default is taken for any PDAID options that are not specified. When you use SYSLOG, GO is a valid response (see Figure 2). When you use SYSIPT, GO should be the last parameter, and it has no operand associated with it.

Selection of an Output Device: PDAID message/parameter OUTPUT DEVICE= permits the selection of an output device. Specify the device by channel and unit, not by symbolic unit. If an output device is specified, PDAID checks the address against the supervisor PUB and selects the appropriate phase for the unit type (tape or printer).

Selection of Core-Wrap Mode: If an output device is not specified, core-wrap mode is assumed. The event trace tables (see Figures 3, 6, 7, and 12) are kept in the PD area. The number of events contained in this area depends on the size of that area generated at system generation time with the PD option of the FOPT macro. PD=1400 (or PD=YES) is the minimum that can be selected. Figure 1 shows the maximum number of entries for each trace type when PD=1400 (minimum PD area), and when PD=10,240 (maximum PD area).

supervisor, but resides in the main page pcc1, can be used for the trace tables (see Figures 3, 6, 7, and 12). The size of the alternate area is specified through the message/parameter AAA=nK. AAA= and OUTPUT DEVICES= are mutually exclusive; when one is specified, the other cannot be used. If the alternate save area is specified, the PD save area is not used. Neither can the PD save area be used when PDAID and SDAID are used concurrently.

If core-wrap mode is selected, an alternate area, which belongs to the

Neither core wrap ncr AAA are valid for transient dump.

Trace Type	Length of Entry	Maximum Number of Entries when PD = 1400	Maximum Number of Entries when PD = 10,240
I/O	18	42	544
F/L	22	42	453
GSVC	18	50	552
QTAM	21	35	465

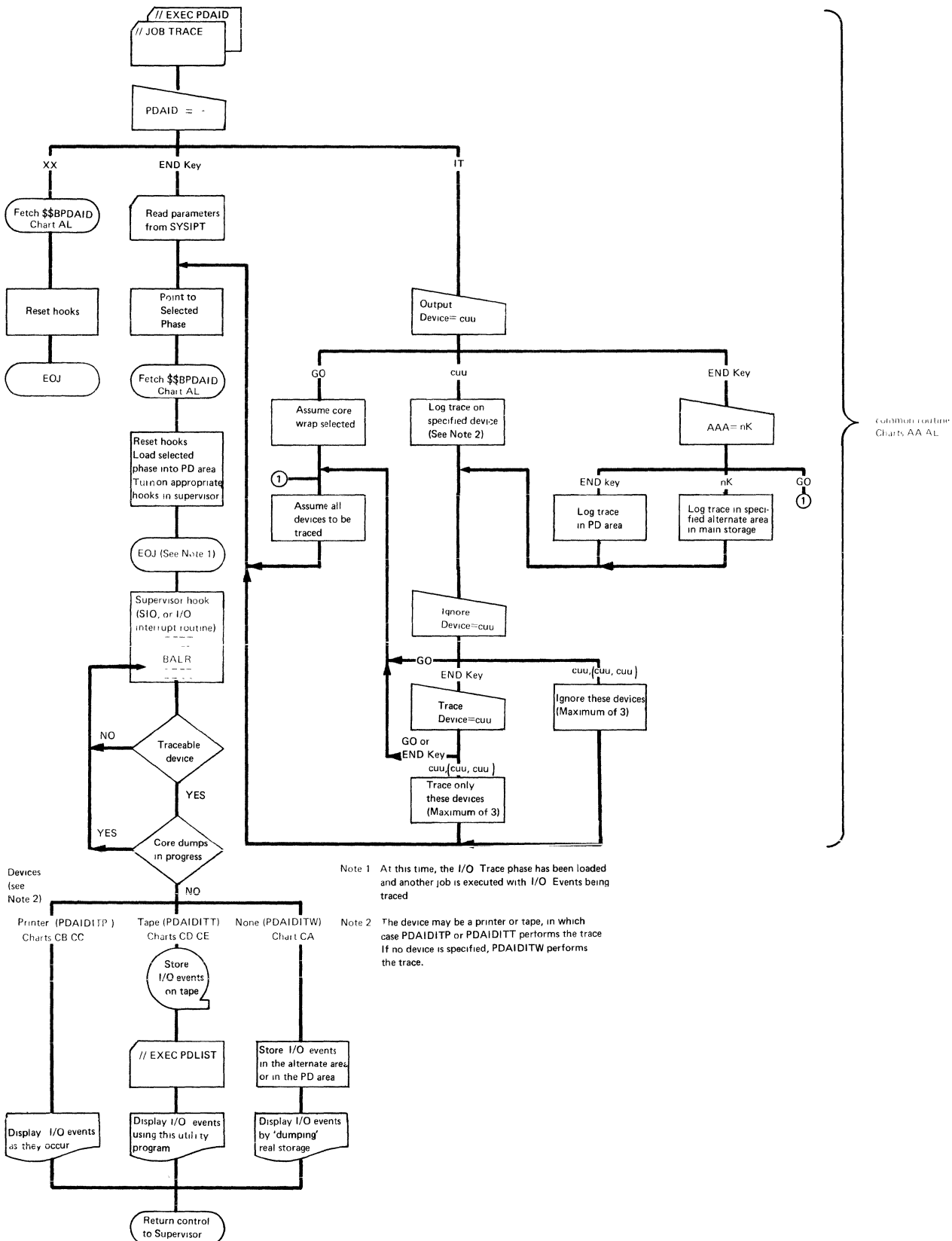
Figure 1. Maximum Number of PD Area Entries

SYSLOG / SYSIPT Message / Parameter	SYSLOG / SYSIPT Response / Operand	Meaning	Default
PDAID =	$\left\{ \begin{array}{l} FT \\ GT \\ IT \\ QT \\ TD \\ XX \\ \text{END Key} \end{array} \right\}$	FT Fetch/Load Trace GT GSVK Trace IT I/O Trace QT QTAM Trace TD Transient dump XX Terminate present PDAID function END Key Additional PDAID control input through SYSIPT	None, the function continues.
OUTPUT DEVICE = (Note 1)	$\left\{ \begin{array}{l} \text{cuu} \\ \text{'X'cuu'} \\ \text{END Key} \\ \text{GO} \end{array} \right\}$	Specify the hexadecimal channel and unit number of either a magnetic tape unit or a printer for the output device of the PDAID. A printer is invalid for QTAM Trace. A device must be specified for transient dump.	Core - Wrap mode; none for transient dump
AAA= (Notes 1, 3, and 4)	$\left[ \begin{array}{l} \text{nk} \\ \text{END Key} \\ \text{GO} \end{array} \right]$	Specify the size of an alternate area for Core - Wrap Mode. Minimum is 2K, maximum 99K.	Core - Wrap Mode using PD Save Area.
TRACE PARTITION=  (Valid only for Fetch/Load, GSVK and QTAM trace)	$\left[ \begin{array}{l} \text{SP} \\ \text{BG} \\ \text{F4} \\ \text{F3} \\ \text{F2} \\ \text{F1} \\ \text{END Key} \\ \text{GO} \end{array} \right]$	SP Supervisor BG Background F4 Foreground 4 F3 Foreground 3 F2 Foreground 2 F1 Foreground 1  For the QTAM trace only SVCs 0 and 31 are recorded per partition.	Trace all partitions and the supervisor.
IGNORE DEVICE= (Notes 2 and 3)	$\left[ \begin{array}{l} \text{cuu} \\ \text{'X'cuu'} \\ \text{END Key} \\ \text{GO} \end{array} \right]$	Specify the hexadecimal channel and unit number of the device to be ignored by the I/O or QTAM trace. A maximum of 3 may be specified.	Trace all devices.
TRACE DEVICE= (Notes 2 and 3)	$\left[ \begin{array}{l} \text{cuu} \\ \text{'X'cuu'} \\ \text{END Key} \\ \text{GO} \end{array} \right]$	Specify the hexadecimal channel and unit number of the device to be traced by the I/O or QTAM trace. A maximum of 3 may be specified.	Trace all devices.
IGNORE SVC= (Notes 2 and 3)	$\left[ \begin{array}{l} \text{nn} \\ \text{END Key} \\ \text{GO} \end{array} \right]$	Specify the hexadecimal SVC number to be ignored by the GSVK trace. A maximum of 6 may be specified.	Trace all SVC s.
TRACE SVC= (Notes 2 and 3)	$\left[ \begin{array}{l} \text{nn} \\ \text{END Key} \\ \text{GO} \end{array} \right]$	Specify the hexadecimal SVC number to be traced by the GSVK trace. A maximum of 6 may be specified.	Trace all SVC s.
GO (Valid SYSIPT parameter)	GO (Valid SYSLOG response)	GO terminates the PDAID control input and uses the default for those options that are not specified.	None.

- Note 1. OUTPUT DEVICE and AAA are mutually exclusive. Neither can be shared with any other program.  
 Note 2. TRACE and IGNORE are mutually exclusive.  
 Note 3. Not valid for transient dump.  
 Note 4. When PDAID is used concurrently with SDAID the alternate PD save area cannot be used.

Figure 2. PDAID Control Statements

Chart 02. Logic Flow of the I/O Trace Function





## I/O TRACE FUNCTION

The I/O trace function (Chart 02) provides trace tables for input/output devices. (See Figures 3 and 4.)

I/O trace allows the I/O activity of programs run under DOS/VS to be traced. Tracing consists of:

- Recording the device address, the I/O old PSW, and the CSW when an I/O interruption occurs and
- Recording the SIO prefix, the condition code, the device address, the CCB address, and the CSW (when the CSW is stored in response to an SIO instruction issued by the DOS/VS supervisor).

Either of these is referred to as an I/O event. The events may be preserved in a rotating buffer (see Figure 6) in storage (first entry overwritten when the area is full, etc.), printed, or recorded on tape. When a tape is used, it must be processed by the PDLIST utility program to provide readable output data.

If the program is running in virtual mode, the real CCB and CSW addresses are translated to virtual addresses before printing.

Sample for I/O interrupt:				
Device address	I/O old PSW		CSW	
2	8		8	

Sample trace for a supervisor issued SIO:				
SIO bit	Dev. Addr.	CCB Address	CSW	
2	2	2	4	8

You can detect the SIO issued by the supervisor (CSW stored condition) by checking the PSW portion of an entry in the storage dump and determining if the system mask byte contains an X'00'. If the system mask byte is not X'00', the I/O event was an interrupt.

Figure 3. I/O Trace Table Entry

## Tracing Options

The I/O trace function provides the following options.

- Trace all I/O activity on the system
- Eliminate a maximum of three devices
- Limit trace to a maximum of three devices.

The trace-limiting options (see Figure 2) are specified by the initializer keywords IGNORE DEVICE= or TRACE DEVICE=. All I/O activity is traced if one of these option keywords is not specified. The two keywords are mutually exclusive. When either is specified, the other becomes invalid.

The three limiting options are invoked by specifying the channel and unit addresses (X'cuu' or cuu) of the appropriate devices. Symbolic device references (SYSxxx) are invalid.

## Data Collection

I/O trace resides in the PD area and performs the actual tracing of I/O events. The first entry to the phase causes some initialization to occur before the I/O event is acted upon. At each entry, the phase tests the logical transient area for a dump transient; normally, it does not trace any I/O activity when a dump is in progress. (If it is necessary to trace I/O events during a dump routine, the exit branch following the compare instruction labeled DUMPCHK should be altered to a NOP.) This prevents a dump either from overflowing the trace table when core wrap is used (see Figure 6), or from causing excess output in output mode.

If no dump is in progress, the device address is matched against either IGN or TRC entries to determine if the event should be entered into the trace tables.

Note: It is not necessary to ignore the PD output device. I/O events from this device are handled internally by the module. Events from the PD output device are traced if the I/O activity originates outside the PD module (that is, the device is being shared).

If the event is not to be traced, control is returned to the supervisor routine from which the trace was entered. If the event involves a device to be

traced, an entry is made in the trace table. At this point, the core-wrap returns to the supervisor.

### Output

The I/O output phase tests for a full table before returning to the supervisor, and attempts to output the table when it contains enough entries. If the output device cannot be accessed, control is returned to the supervisor, and output is retried at each subsequent entry to the trace phase. Limited overflow buffers are available in each module:

Type Output	Full Table	Overflow Entries	Maximum Capacity
Printer (PDAIDITP)	3 Entries	5 Entries	8 Entries
Tape (PDAIDITT)	13 Entries	7 Entries	20 Entries

When the I/O output device must share a selector channel or a control unit, the overflow capacity can be exceeded, and I/O events can be lost. The trace phase tests for such losses, and indicates such with an \* when they occur. (It is recommended that output options be selected to avoid such losses, and that one or more of the most active devices be ignored if an overflow occurs.) Figure 4 illustrates output after it has been printed.

### PDAIDITW (CORE-WRAP MODE)

PDAIDITW preserves a fixed number of I/O events in the PD save area, or in the alternate save area of the supervisor (when AAA= is specified).

Since PDAIDITW executes in the PD area, the actual save area - when the PD save area is used - is equal to the PD area (generated during system generation) minus the length of PDAIDITW.

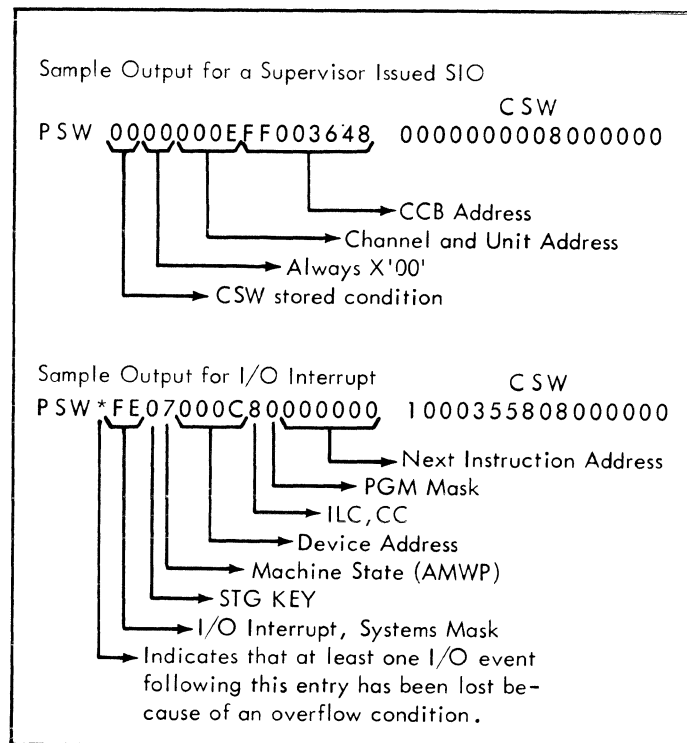


Figure 4. Sample Output for I/O Trace

The maximum number of entries which can be stored in the PD area is shown in Figure 1. When the area is full, the oldest entry is overlaid by each new entry. (Figure 6 illustrates the method of updating the trace table for core-wrap mode).

To retrieve the trace tables, the PD area must be dumped. This can be done by using the PDAREA operand of the DUMP command. Of course, any dump that includes the supervisor will also include the PD area.

If the alternative area is used, it is dumped automatically on the device assigned to SYSLST, when the PDAID program is terminated.

PDAIDITW sets up the following pointers (Figure 6).

- SLOT1 -- address of the beginning of the save area.
- NEXT -- address of the next available slot in the save area. (Because NEXT is filled with unchecked new information, it may contain either the oldest entry in the table, or the most recent activity of a device not being traced. If the latter is the case, ignore the entry.)
- WRAPADR -- address of the end of the save area.

Trace Type	Address of SLOT1 is at
Input/output	PDAREA+X'194'
Fetch/load	PDAREA+X'E6'
GSVC	PDAREA+X'114'
QTAM	PDAREA+X'1BC'

Figure 5. Trace Entry Locations in the PD Save Area

#### PDAIDITP (PRINTER OUTPUT)

PDAIDITP is selected when a printer is specified as the output device. It collects three I/O events, then formats and prints them, using a line printer.

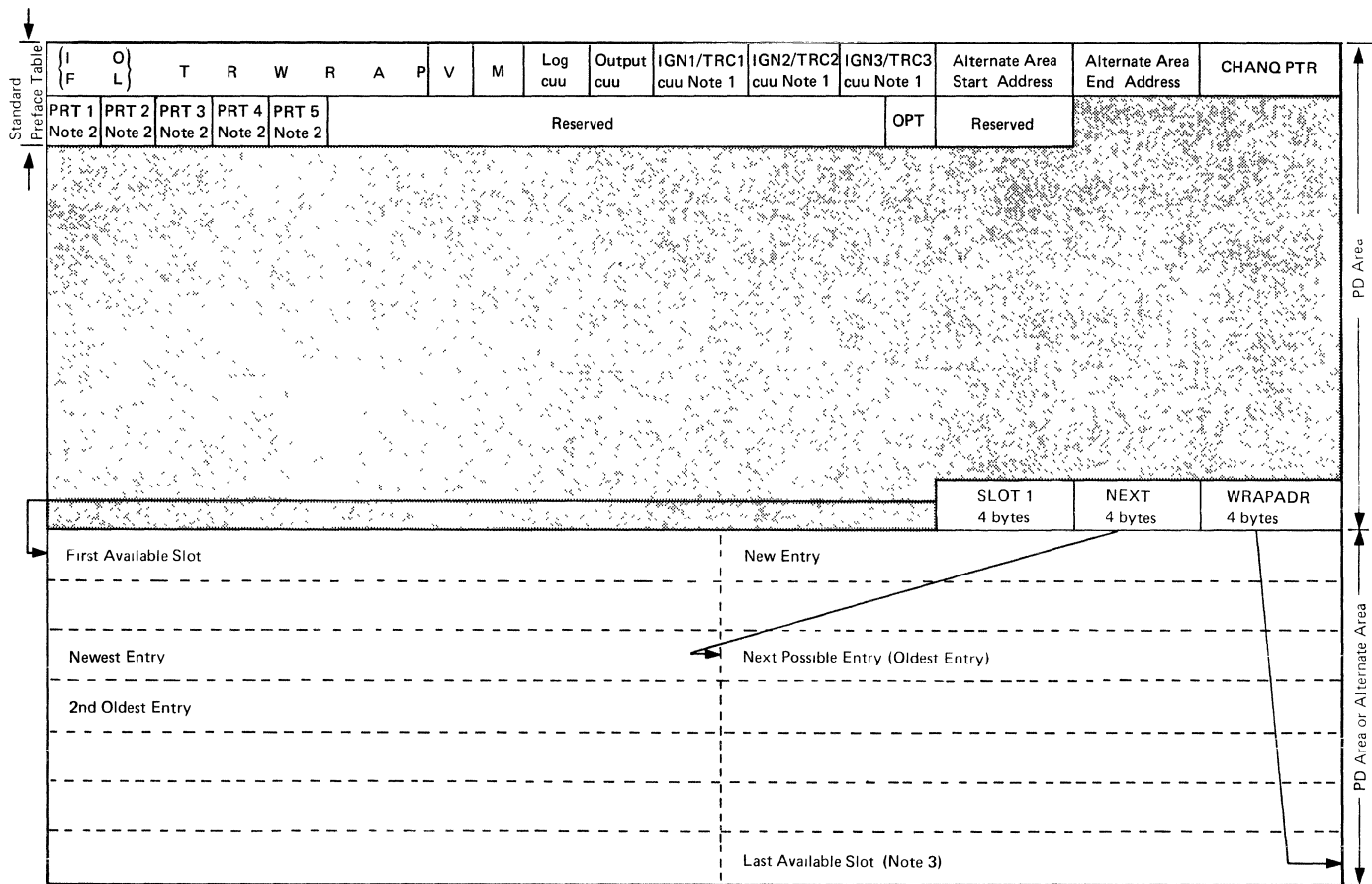
If the printer cannot be accessed, control is returned to the supervisor. Subsequent events are preserved by 'pushing up' the table slot, and entering each new event at the bottom (Figure 7). When an

unreported event is 'pushed out' of the top, an asterisk is set into the I/O area to indicate the overflow. Printing is attempted at each entry to the phase, until successful. If the printer is not ready, or indicates errors, the message 4024A is issued on SYSLOG and the system waits for an END response when the device is made ready.

#### PDAIDITT (TAPE OUTPUT)

PDAIDITT collects and writes on an unlabeled tape the I/O events that occur during execution of the problem program. The events are written on tape in core image (unprintable) format. PDAIDITT requires a tape-to-printer operation using the PDLIST utility program to retrieve the traced events. The tape must be processed with the tape drive temporarily assigned as SYS005 and SYSLST assigned to a printer to obtain readable listings of the traced events.

I/O events are collected in an area that may contain a maximum of 20 entries. An attempt is made to write the entire area as a single record when 13 events are present in the table. If the channel is busy on the attempted output, control is returned to the supervisor SIO routine or interrupt handler routine. When the next I/O event is received, it is entered in one of the five overflow entries, and another attempt to output is made. Thus, the records on tape contain between 13 and 20 I/O events per block. The PDLIST utility program takes this into account and prints only the valid I/O events.



Note 1: Used by Input/Output and QTAM Trace only.

Note 2: These bytes are used by Fetch/Load, GSVc, and QTAM Trace functions.

Note 3: When "Last Available Slot" is filled, "NEXT" is reset to "SLOT 1", and the table is overlaid with new entries.

The layout of the PD Area is shown in Figure 16.

Figure 6. Entering New Events in the Trace Table for I/C, F/L, GSVc, and QTAM Core-Wrap Modes

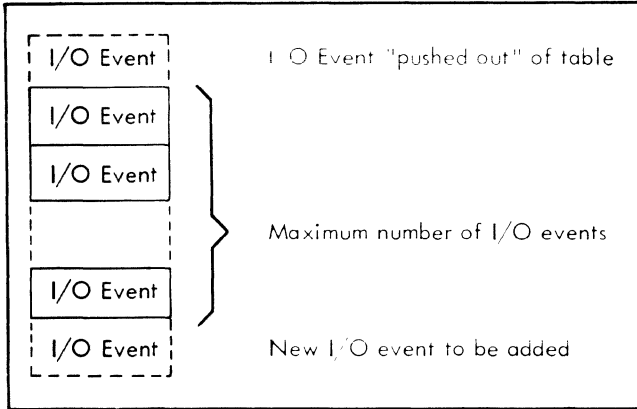


Figure 7. Entering New I/O Events in the Trace Table

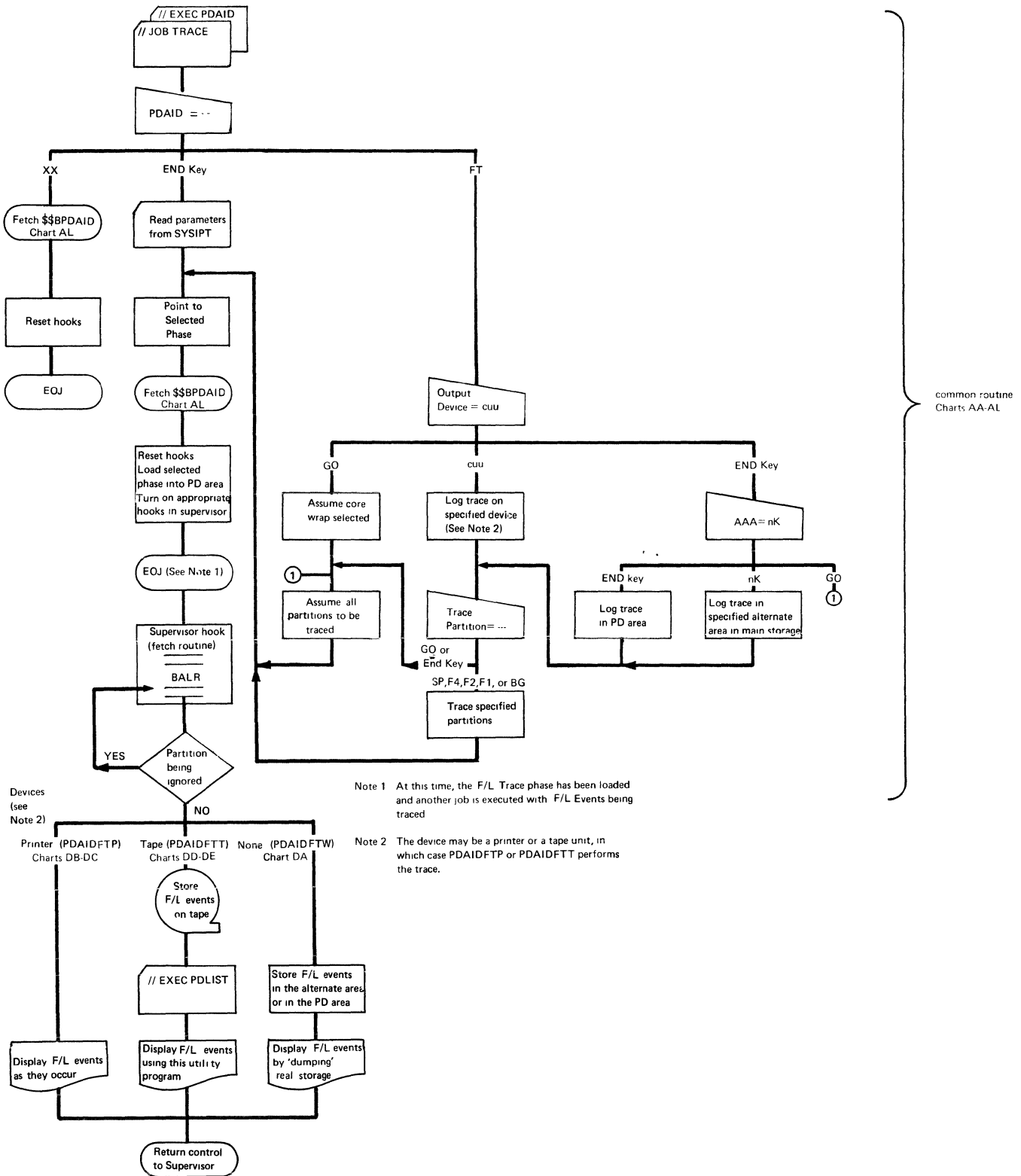
Because 20 is the maximum number of I/O events that can be recorded, an overflow will occur when the 21st I/O event is entered in the table. The tape module

recognizes this condition by checking the 20th entry location in the table for an unreported I/O event. If one is found, a flag is set in the preceding (19th) entry location. As the 20th entry is 'pushed out' (see Figure 7) of the table, the 19th entry replaces the 20th, and thus the flag is available in the last entry.

The PDLIST utility program checks for this flag when printing, and sets an \*, indicating that an overflow has occurred and an I/O event(s) was lost. The \* indicator is printed in the entry that precedes the missed I/O event(s).

PDAIDIT makes no provision to handle error conditions during output. If the tape unit is busy, an immediate return to the supervisor is made. If the tape unit is not busy, but a CSW stored condition is indicated or the tape unit is inoperable, the message 4C24A is issued on SYSLOG and the system waits for an END response when the device is ready.

Chart 03. Logic Flow of the Fetch/Load Trace Function



## FETCH/LOAD TRACE FUNCTION

The F/L (fetch/load) trace (Chart 03) allows tracing the order in which phases and transients are under control of DOS/VS. See Figure 8 for the format of F/L trace entries. Tracing consists of recording (for SVC 1, SVC 2, and SVC 4):

- The name of the phase or transient being called
- The supervisor call number
- The location of the supervisor call
- The partition identifier
- The load address of the phase
- The entry address of the phase.

Note: At times, SVC 5, 6, 11 and 14 branch directly into the supervisor fetch or load routine. These are traced whenever they occur, and appear in the output of the trace; however, the calling address and SVC values do not indicate the actual fetch or load.

Each collection of data is referred to as an F/L event. The events may be preserved in a rotating buffer (first entry overwritten when the area is full), or may be printed or recorded on tape. When a tape is used, the tape must be processed by the PDLIST utility program to provide readable output data.

Use of the request key during the operation of the F/L trace may result in apparently erroneous data due to the supervisor action required to handle the request. In particular, supervisor calls that have already been recorded may not be completed, and part of the data put out by the specific phase (PDAIDFTW, PDAIDFTP, or PDAIDFTT) may pertain to these incomplete SVCs.

### Tracing Options

The F/L trace:

- Traces all SVC 1, 2, 4, and certain SVC 5, 6, 11 and 14 interruptions, and
- Limits the trace by partition (multiprogramming systems only).

Trace limiting options are specified by the initializer keyword TRACE PARTITION= (see

Figure 2). These options are useful only when the user runs several partitions at once, and does not wish to trace all of them. Normally, only one partition would be operating at a given time, and the default (trace all partitions) would allow both the single partition and the supervisor to be traced.

### Data Collection

F/L trace phases reside in the PD area, and perform the actual tracing of F/L events. All events are recorded after the phase is physically loaded into storage. The first entry to the phase causes some initialization to occur before the F/L event is acted upon.

The value in the partition identification key (PIK) is matched against the (partition) entries to determine whether or not the event should be entered into the tables. To conserve storage, the phase tests for partitions to ignore, rather than partitions to trace. PDAID accepts parameters and converts them to ignore parameters for the F/L trace modules. For example, if F1, F2, F3, F4, and SP is specified, the initializer converts this information to an 'ignore BG' parameter for the F/L trace phase.

If the event is to be traced, an entry is made in the trace table. If not, control returns to the supervisor routine from which the trace was entered.

### Output

F/L phases test for a full table before returning to the supervisor, and attempt to output the table when it contains enough entries. If the output device cannot be accessed, control returns to the supervisor, and output is retried at each subsequent entry to the trace function. Limited overflow buffers are available in each phase as shown in the following table:

Type	Full Table	Overflow Entries	Maximum Capacity
Printer (PDAIDFTP)	2 Entries	4 Entries	6 Entries
Tape (PDAIDFTT)	9 Entries	6 Entries	15 Entries

Phase Name	SVC	Calling Address	Load Address	Entry* Address	Partition ID
8	1	3	4	4	2

\*If a phase not found condition is detected, the load and entry addresses are set to X'FFFFFF'.

Figure 8. F/L Trace Table Entry

PDAIDFTW (CORE-WRAP MODE)

PDAIDFTW preserves a fixed number of F/L events in the PD save area, or in the alternate save area of the supervisor (when AAA= is specified).

Since PDAIDFTW executes in the PD area, the actual save area - when the PD save area is used - is equal to the PD area (generated during system generation) minus the length of PDAIDFTW.

The maximum number of entries that can be stored in the PD area is shown in Figure 1. When the PD area is full, the oldest entry is overlaid by each new entry. (Figure 6 illustrates the method for updating the trace table for core-wrap mode.)

To retrieve the trace tables, the PD area must be dumped. This can be done by using the PDAREA operand of the DUMP command. Of course any dump that includes the supervisor will also include the PD area.

If the alternate area is used, it is dumped automatically on the device assigned to SYSLST, when the PDAID program is terminated.

PDAIDFTW sets up the following pointers:

- SLOT1 -- address of the beginning of the save area.
- NEXT -- address of the next available slot in the save area. (Because NEXT is filled with unchecked new information, it may contain either the oldest entry in the table, or the SVC number and

calling address from a partition not being traced. If the latter is the case, ignore the entry.)

- WRAPADR -- address of the end of the save area.

Note: The location of SLOT1 is shown in Figure 5.

PDAIDFTP (PRINTER OUTPUT)

PDAIDFTP is selected when a printer is specified as the output device. It collects two F/L events, formats them for output, and prints them on a line printer. See Figure 9 for sample output.

If the printer cannot be accessed, control returns to the supervisor. If the printer is off-line or is not ready, the message 4C24A is issued on SYSLOG and the system waits for an END response when the printer is made ready.

When two entries have been saved, they are formatted for output, and the save area is cleared. Two more entries may be saved before output is achieved, without an overflow condition occurring. However, when the I/O area and save area are full and an entry must be made, the oldest entry in the save area is lost, and an \* indicator is set in the I/O area. Thus, an \* in the output indicates that the next chronological entry (or entries) was lost due to an overflow.



PDAIDFTT (TAPE OUTPUT)

PDAIDFTT collects and writes, on an unlabeled tape, the F/L events that occur during execution of a job stream. The events are written on tape in core image format. Output from PDAIDFTT is formatted into printable characters by the PDLIST utility program. The input tape drive must be temporarily assigned as SYS005 before execution of PDLIST, and SYSLST must be assigned to a printer.

F/L events are collected in a save area inside the PD area. When nine entries have been made in the save area, the phase attempts to output. If the tape drive cannot be accessed, control returns to the supervisor. If the tape drive is not ready, or indicates errors, the message 4C24A is issued on SYSLOG and the system waits for an END response when the device is made ready. At each entry to the phase, the top slot in the save area is checked for an entry. If an entry is present, the save area is full, and an overflow occurs when the current entry is saved. If an overflow occurs, a flag is set in the oldest entry remaining after the current entry is saved. The PDLIST utility program checks for this flag when printing, and sets an \* indicator when the flag is found, signifying that at least one F/L event was lost due to an overflow.

Because PDAIDFTT attempts to output when it receives the ninth entry and has a buffer of six entry slots, there are between 9 and 15 entries per 330-byte block on the tape. The PDLIST utility program therefore checks for a valid entry before formatting.

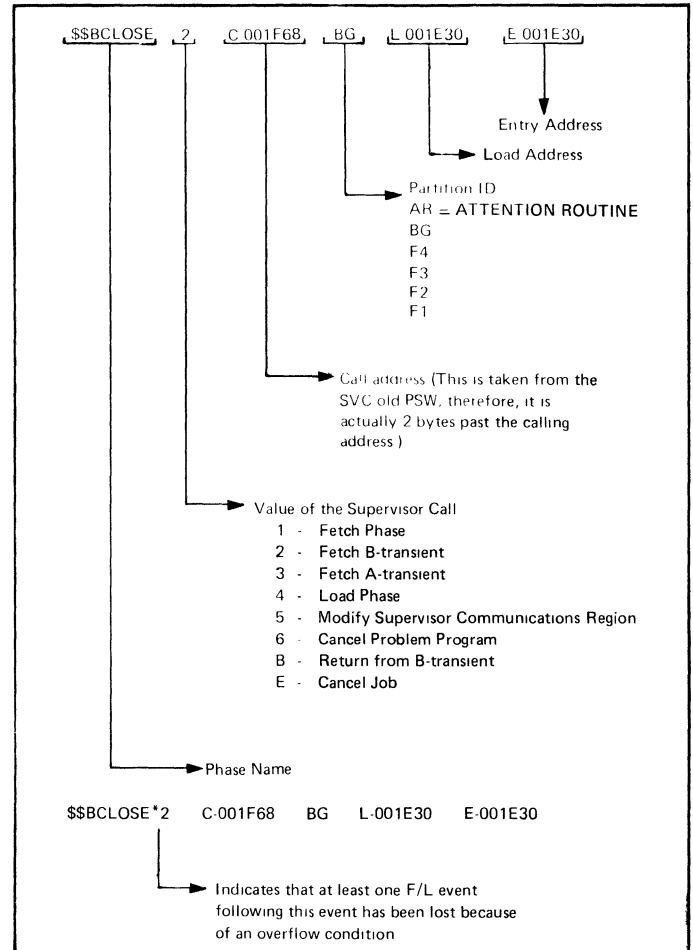
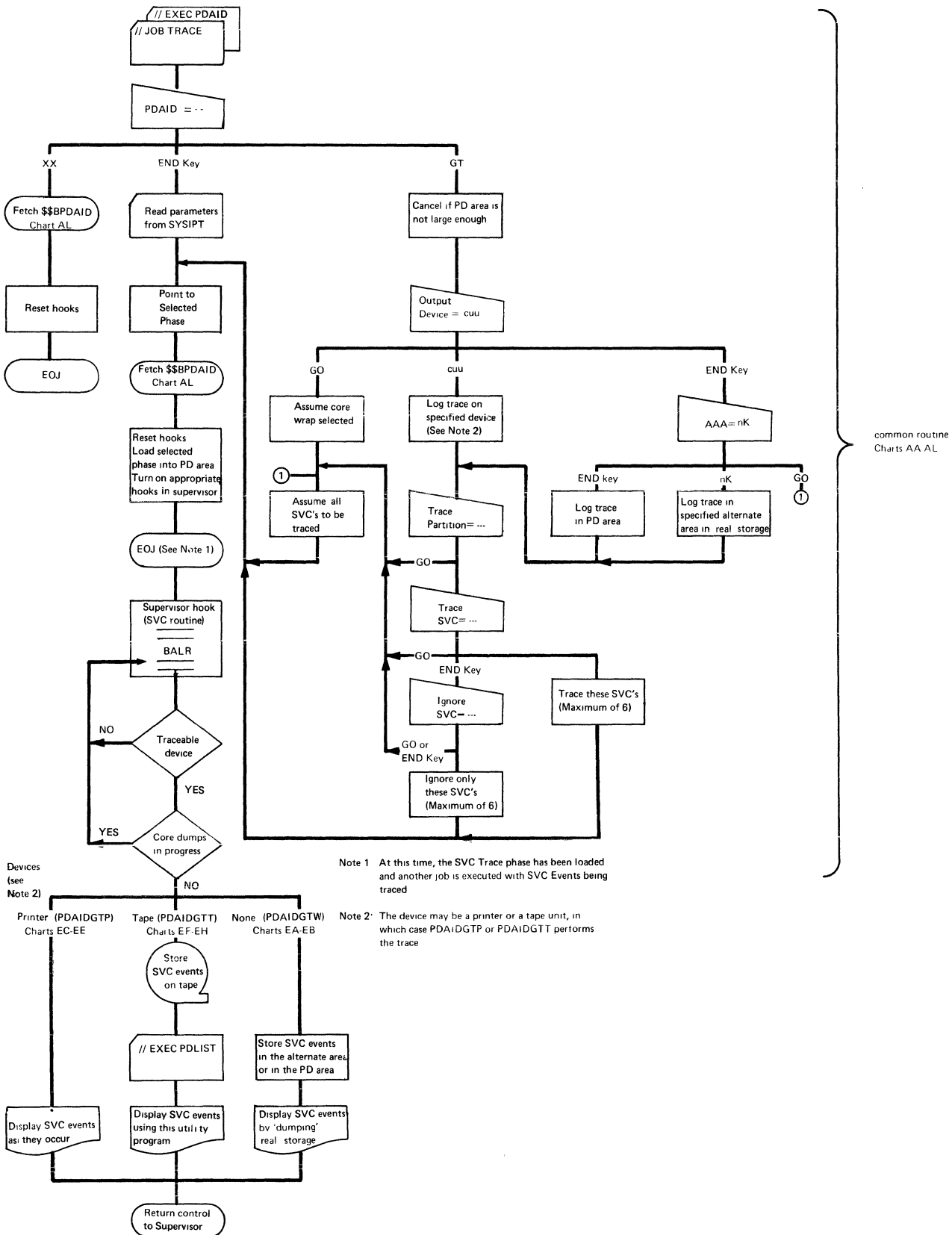


Figure 9. Sample Output for F/L Trace

Chart 04. Logic Flow of the GSVVC Trace Function



## GSVC TRACE FUNCTION

The GSVC trace function (Chart 04) builds a trace table as SVC interrupts occur. The trace table (Figure 10) consists of:

- The SVC old PSW
- The contents of register 0
- The last three bytes of register 1
- The SVC number
- The partition identifier.

The trace table entries may be stored in a rotating buffer in core (core-wrap mode), or output to a printer or tape unit. When tape output is used, the tape must be processed by the PDLIST utility program to provide readable output data.

SVC old PSW	Reg. 0	SVC no.	Last 3 bytes reg. 1	Partition ID
8	4	1	3	2

Figure 10. GSVC Trace Table Entry

## Tracing Options

The GSVC function provides the following options:

- Trace all SVCs
- Selectively trace up to six SVCs
- Selectively eliminate up to six SVCs and trace all others
- Trace in all partitions
- Selectively trace up to five partitions.

Trace limiting options (see Figure 2) are specified by the initializer keywords IGNORE SVC= or TRACE SVC=. All SVC activity is traced if one of these option keywords is not specified. The two keywords are mutually exclusive: when either is specified, the other becomes invalid.

The six SVC limiting options are invoked by specifying the SVCs to be traced or ignored. The partition limiting options are specified by the initializer keyword TRACE PARTITION=. This is useful only when the user must run several partitions at

once, and does not wish to trace all of them.

**Note:** If PTO=YES in the FOPT macro then SVCs issued when the physical transient area is busy are not traced.

## Data Collection

GSVC trace resides in the PD area and performs the actual tracing of SVCs. The first entry to the phase causes some initialization to occur before the SVC is acted upon.

The value in the program interrupt key (PIK) is matched against the PRT (partition) entries to determine whether or not the event should be entered into the tables. To conserve storage, the phases test for partitions to ignore, rather than partitions to trace. The initializer program (PDAID) accepts TRACE PARTITION= parameters and converts them to ignore parameters for the SVC trace phases. For example, if F1, F2, F3, F4, and SP is specified, the initializer converts this information to an 'ignore BG' parameter for the SVC trace phase.

If the event is to be traced, an entry is made in the trace table. If not, control returns to the supervisor routine from which the trace was entered.

## Output

If the SVC event was caused by an SVC being issued, the phases store the necessary information in the table but do not attempt to move data to the I/O area. If, however, the SVC event was caused by an SIO or I/O interrupt, the phases first store the information in the table and then test for a full table. If the table is full, the phases attempt an output before returning control to the supervisor.

Type	Full Table	Overflow Entries	Maximum Capacity
Printer	2 Entries	4 Entries	6 Entries
Tape	9 Entries	5 Entries	14 Entries

When the output device must share a selector channel or a control unit, the trace tables overflow capacity can be exceeded and SVCs can be lost. The trace phase tests for such losses and indicates

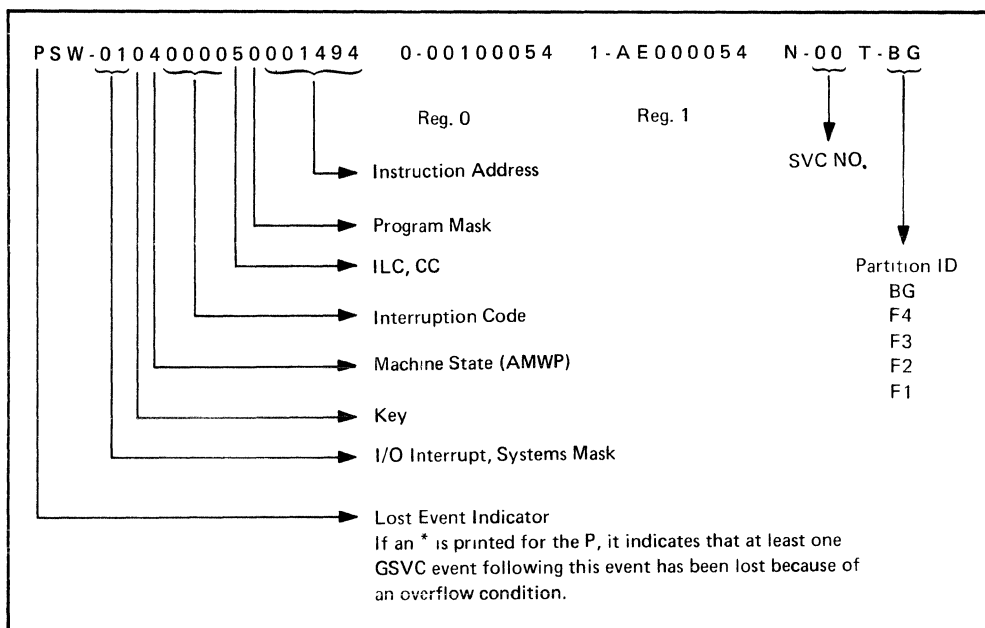


Figure 11. Sample Output for GSV Trace

them with an \* when they occur. (It is recommended that output options be selected to avoid such losses, and that one or more of the most active devices be ignored if an overflow occurs.) Figure 11 illustrates output after it has been printed.

PDAIDGTW (CORE-WRAP MODE)

PDAIDGTW preserves a fixed number of I/O events in the PD save area, or in the alternate save area of the supervisor (when AAA= is specified).

Since PDAIDGTW executes in the PD area, the actual save area - when the PD save area is used - is equal to the PD area (generated during system generation) minus the length of PDAIDGTW.

The maximum number of entries that can be stored in the PD area is shown in Figure 1. When the PD area is full, the oldest entry is overlaid by each new entry. (Figure 6 illustrates the method for updating the trace table for core-wrap mode.)

To retrieve the trace tables, the PD area must be dumped. This can be done by using the PDAREA operand of the DUMP command. Of course any dump that includes the supervisor will also include the PD area.

If the alternate area is used, it is dumped automatically on the device assigned to SYSLST, when the PDAID program is terminated.

PDAIDGTW sets up the following pointers:

- SLOT1 -- address of the beginning of the save area.
- NEXT -- address of the next available slot in the save area. (Because NEXT is filled with unchecked new information, it may contain either the oldest entry in the table, or the SVC number and calling address from a partition not being traced. If the latter is the case, the entry should be ignored.)
- WRAPADR -- address of the end of the save area.

Note: The location of SLOT1 is shown in Figure 5.

PDAIDGTP (PRINTER OUTPUT)

PDAIDGTP is selected when a printer is specified as the output device. It collects the two GSV events, formats them for output, and prints them using a line printer.

If the printer cannot be accessed, control returns to the supervisor. If the printer is not ready or indicates errors, the message 4C24A is printed on SYSLOG and the system waits for an END response when the printer is made ready.

When two entries have been saved, they are formatted for output and the save area is cleared. Four more entries may be saved before output is achieved, without an overflow condition occurring. However, when the I/O area and save area are full and an additional entry must be made, this entry causes the save area to overflow (Figure 12). The oldest entry in the save area is lost, and an \* indicator is set in the I/O area. Thus, an \* in the output indicates that the next chronological entry (or entries) was lost due to an overflow. Entries are double-spaced and printed two per line.

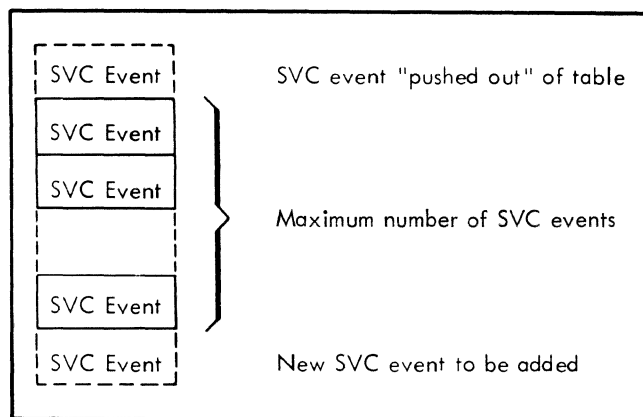


Figure 12. Entering New SVC Events in the Trace Table for Output Devices

PDAIDGTT (TAPE OUTPUT)

PDAIDGTT collects and writes on an unlabeled tape the SVC events that occur during execution of the problem program. The events are written on tape in core image (unprintable) format. PDAIDGTT

requires a tape-to-printer operation using the PDLIST utility program to retrieve the traced events. The tape must be processed with the tape drive temporarily assigned as SYS005 and SYSLST assigned to a printer to obtain readable listings of the traced events.

GSVC events are collected in an area that may contain a maximum of 14 entries. An attempt is made to write the entire area as a single record when 9 events are present in the table. If the channel is busy on the attempted output, control is returned to the supervisor SIO routine or interrupt handler routine. When the next SVC event is received, it is entered in one of the five overflow entries, and another attempt to output is made. Thus, the records on tape contain between 9 and 14 SVC events per block. The PDLIST utility program takes this into account and prints only the valid SVC events.

Because 14 is the maximum number of SVC events that can be recorded, an overflow will occur when the 15th SVC event is entered in the table. PDAIDGTT recognizes this condition by checking the 14th entry location in the table for an unreported SVC event. If one is found, a flag is set in the preceding (13th) entry location. As the 14th entry is 'pushed out' of the table, (see Figure 12) the 13th entry replaces the 14th, and thus the flag is available in the last entry.

The PDLIST utility program checks for the flag when printing, and sets an \* indicator, indicating that an overflow has occurred and an SVC event(s) was lost. The \* indicator is printed in the entry that precedes the missed SVC event(s).

PDAIDGTT makes no provision to handle error conditions during output. If the tape unit is busy, an immediate return to the supervisor is made. If the tape unit is not busy, but a CSW stored condition is indicated or the tape unit is inoperable, the message 4C24A is issued on SYSLOG and the system waits for an END response when the device is made ready.



## QTAM TRACE FUNCTION

### Description and Operation

The QTAM trace function builds a trace table as interrupts occur. There are three types of trace events, each having a prefix that defines the event type (Figure 13).

A V-type event (Figure 13) is created when an SVC interrupt occurs and consists of:

- V-prefix
- SVC number
- SVC old PSW
- Contents of register 0
- Contents of register 1
- Partition identifier.

An S-type event (Figure 13) is created when an SIO interrupt occurs and consists of:

- S-prefix
- Condition code
- Device address
- CCB address
- CSW.

An I-type event (Figure 13) is created when an I/O interrupt occurs and consists of:

- I-prefix
- Device address
- I/O old PSW
- CSW.

The trace events may be stored in a rotating buffer in core (core wrap) or sent to a tape unit. When tape mode is used, the tape must be processed by the PDLIST utility program to provide readable output data.

<u>SVC Interrupt:</u>					
V	SVC No	SVC OLD PSW	Reg.0	Reg.1	Part id
1	2	8	4	4	2

<u>SIO Interrupt:</u>					
S	C	Device Address	CCB Address	CSW	Not used
1	1	2	4	8	5

<u>I/O Interrupt:</u>				
I	Device address	I/O old PSW	CSW	Not used
1	2	8	8	2

Figure 13. QTAM Trace Table Entries

### Tracing Options

The QTAM trace function provides the following options:

- Trace all SVC 0 and 31, SIO, and I/O interrupts
- Selectively trace SVC 0 and 31, SIO, and I/O interrupts from any three devices
- Ignore SVC 0 and 31, SIO, and I/O interrupts from any three devices
- Limit the trace by partition.

Trace-limiting options (see Figure 2) are specified by the initializer message/parameters IGNORE DEVICE= or TRACE DEVICE=. (The device options are invoked by specifying the three devices to be traced or ignored.) All SVC 0 and 31, SIO, and I/O interrupt activity is traced in all partitions if one of these options is not specified. They are mutually exclusive: when either is specified, the other becomes invalid.

The partition limiting options are specified by the initializer keyword TRACE PARTITION=. This is useful only if the user runs several partitions at the same time, but does not wish to trace all of them.

## Data Collection

The QTAM trace phases reside in the PD area and perform the actual tracing of QTAM events. The first QTAM event causes some initialization to occur before it is handled. At each entry, the trace tests the logical transient area for a dump transient; normally, it does not trace any interrupt activity when a dump is in progress. (If it is necessary to trace QTAM events during a dump routine, the exit branch following the compare instruction should be altered to a NOP.) This prevents the dump from either causing the trace table to overflow when core wrap is used (see Figure 3), or from causing excess output.

If no dump is in progress, the device address is matched against either IGN or TRC entries to determine if the event should be entered into the trace tables.

If the event is not to be traced, control is returned to the supervisor routine from which the trace was entered. If the event involves a device to be traced, an entry is made in the trace table. At this point, the core-wrap module returns control to the supervisor.

## Output

The QTAM output phases test for a full table before returning to the supervisor, and attempt to output the table when it contains enough entries. If the output device cannot be accessed, control is returned to the supervisor, and output is retried at each subsequent entry to the trace module. Limited overflow buffers are available in each module:

Type	Full	Overflow	Maximum
Output	Table	Entries	Capacity
Tape	8 Entries	2 Entries	10 Entries

When the output device must share a selector channel or a control unit, the overflow capacity can be exceeded, and QTAM events can be lost. The trace tests for such losses, and indicates such with an \* when they occur. (It is recommended that

output options be selected to avoid such losses, and that one or more of the most active devices be ignored if an overflow occurs.) Figure 14 illustrates output after it has been printed.

## PDAIDQTW (CORE-WRAP MODE)

PDAIDQTW preserves a fixed number of events in the PD save area, or in the alternate save area of the supervisor (when AAA= is specified).

Since PDAIDQTW executes in the PD area, the actual save area - when the PD save area is used - is equal to the PD area (generated during system generation) minus the length of PDAIDQTW.

The number of entries that can be stored in the PD area is shown in Figure 1. When the PD area is full, the oldest entry is overlaid by each new entry. (Figure 6 illustrates the method for updating the trace table for core-wrap mode.)

To retrieve the trace tables, the PD area must be dumped. This can be done by using the PDAREA operand of the DUMP command. Of course any dump that includes the supervisor will also include the PD area.

If the alternate area is used, it is dumped automatically on the device assigned to SYSLST, when the PDAID program is terminated.

PDAIDQTW sets up the following pointers.

- SLOT 1 -- address of the beginning of the save area.
- NEXT -- address of the next available slot in the save area. (Because NEXT is filled with unchecked new data, it may contain either the oldest entry in the table, or the SVC number and calling address from the partition not being traced. If the latter is the case, the entry should be ignored.)
- WRAPADR -- address of the end of the save area.

Note: The location of SLOT1 is shown in Figure 5.



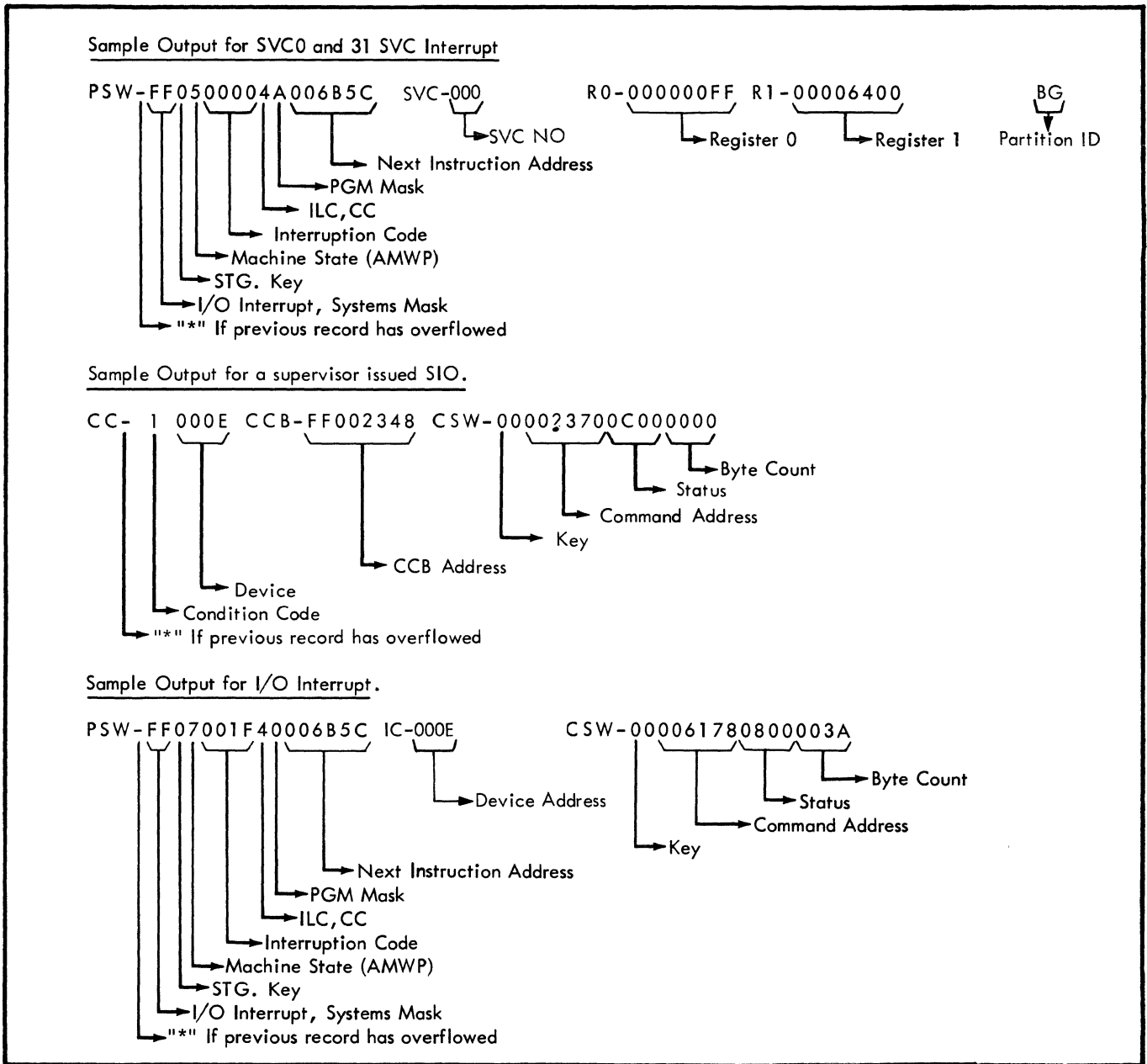


Figure 14. Sample Output for QTAM Trace

PDAIDQTT (TAPE OUTPUT)

PDAIDQTT collects and writes on an unlabeled tape the QTAM events that occur during execution of the problem program. The events are written on tape in core image (unprintable) format. Thus, this requires a tape-to-printer operation using the PDLIST utility program to retrieve the traced events. The tape must be processed with the tape drive temporarily assigned as SYS005, and SYSLST assigned to a printer to obtain readable listings of the traced events.

QTAM events are collected in an area that may contain a maximum of 10 entries. An attempt is made to write the entire area as a single record when 8 events are present in the table. If the channel is busy on the attempted output, control is returned to the supervisor SIO routine or interrupt handler routine. When the next QTAM event is received, it is entered in one of the two overflow entries, and another attempt to output is made. Thus, the records on tape contain between 8 and 10 QTAM events per block. PDLIST utility program takes this into account and prints only the valid QTAM events.

Because 10 is the maximum number of QTAM events that can be recorded, an overflow occurs when the 11th QTAM event is entered in the table. PDAIDQTT recognizes this condition by checking the 10th entry location in the table for an unreported QTAM event. If one is found, a flag is set in the preceding (9th) entry location. As the 10th entry is "pushed out" of the table (Figure 15), the 9th entry replaces the 10th, and thus the flag is available in the

last entry. The PDLIST utility program checks for this flag when printing, and sets an \* indicator, noting that an overflow has occurred and a QTAM event(s) was lost. The \* indicator is printed in the entry that precedes the missed QTAM event(s).

PDAIDQTT makes no provision to handle error conditions during output. If the tape unit is busy, an immediate return to the supervisor is made. If the tape unit is not busy, but a CSW stored condition is indicated or the tape unit is inoperable, the message 4C24A is issued on SYSLOG and the system waits for an END response when the device is made ready.

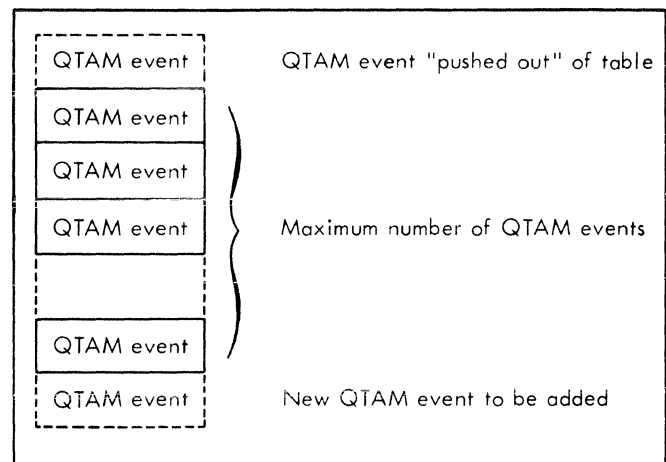
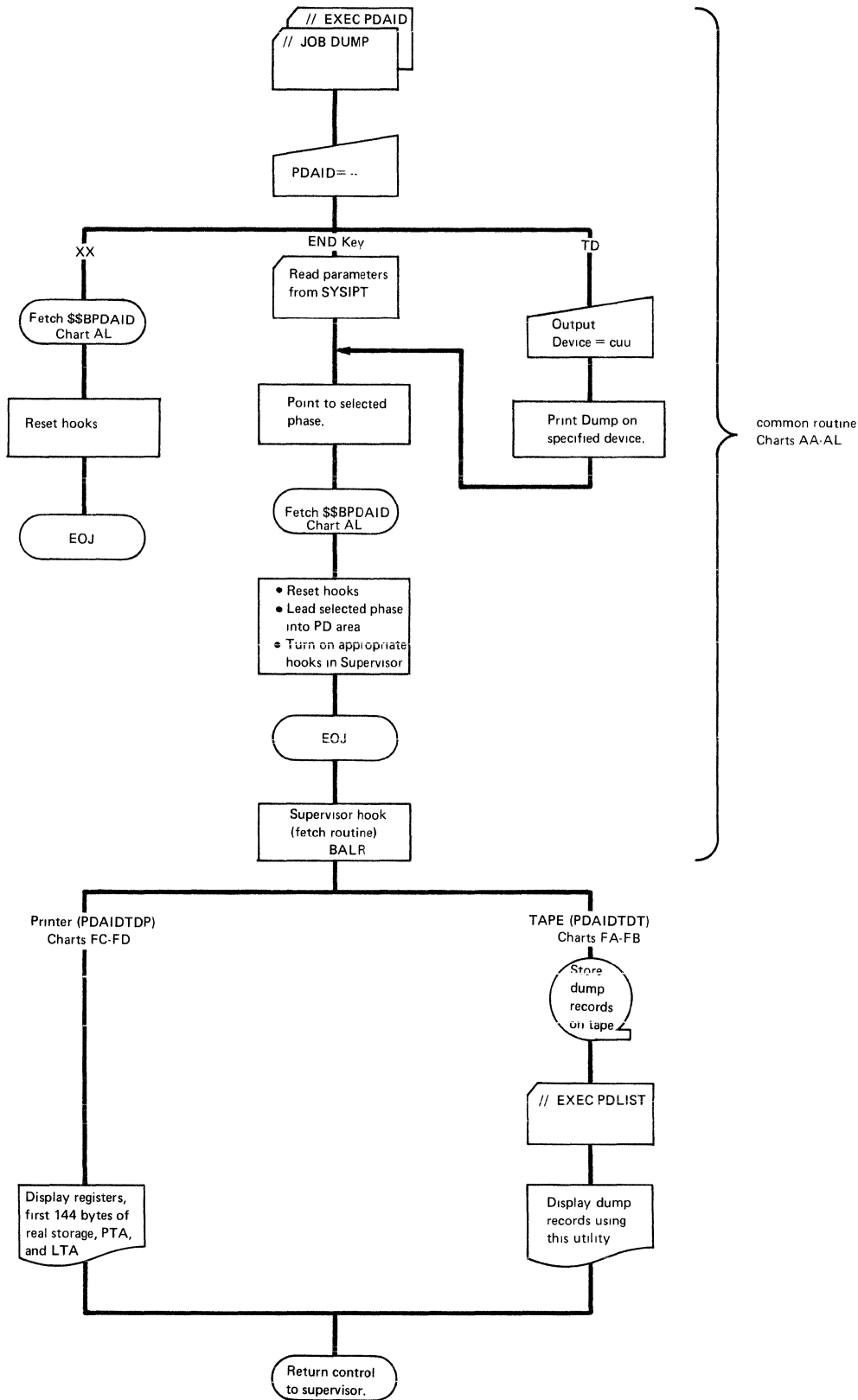


Figure 15. Entering New QTAM Events in the Trace Table for Tape Output Devices



Chart 06. Logic Flow of Transient Dump



## TRANSIENT DUMP

Transient dump (Chart 06) allows certain areas of the supervisor to be displayed when a program check occurs. These areas are displayed before they are altered by any supervisor routines. The areas displayed are:

- The sixteen general registers being used at the time of failure
- The first 144 bytes of real storage
- The PTA (physical transient area)
- The LTA (logical transient area).

The output device of the transient dump program must be either a printer or a magnetic tape unit. If a tape is used, the tape must be processed by the PDLIST utility program to provide readable output data.

## Tracing Options

None.

## Data Collection

Transient dump phases reside in the PD area. The transient areas are displayed before they can be altered by any supervisor routine; the registers and first 144 bytes of real storage are restored to their values at the time of failure before they are displayed.

## Output

Transient dump takes control of the system when a program check occurs. If the output device fails, message

4C24A NO I/O TO OD

is issued. The exit from this message is either to the output routine if the failure is corrected, or to the supervisor.

## PDAIDTDP

PDAIDTDP is selected when a printer is specified as the output device. Control is obtained from the supervisor and all areas are printed. If the channel program for the printer cannot be activated, control returns to the supervisor. If the printer is off-line or not ready, the message 4C24A is issued on SYSLOG and the system waits for an END key response when the printer is made ready.

Each area is blocked, formatted for output, and printed. There is no loss of data due to overflow since the I/O new PSW is altered so that control is always returned to PDAIDTDP until all data is printed.

## PDAIDTDT

PDAIDTDT collects and writes, on an unlabeled tape, the contents of the general registers, the first 144 bytes of real storage, the PTA, and the LTA. Control is obtained from the supervisor and the records containing all areas are written on tape. Output from PDAIDTDT is formatted into printable characters by the PDLIST utility program.

If the channel program for the magnetic tape unit cannot be activated, control returns to the supervisor. If the tape unit is off-line or not ready, the message 4C24A is issued on SYSLOG and the system waits for an END key response when the tape unit is made ready.

Each area is blocked, formatted for output, and written. There is no loss of data due to overflow since the I/O new PSW is altered so that control is always returned to PDAIDTDT until all data is written.

TABLES USED BY PDAID

Byte						
PDAREA	0	PDAREAND			PDINTRHK	
	8	PDSIOHK			PDTRANHK	
	16	PDFLTHK1			PDFLTHK2	
	24	Reserved			Register 9	
	32	Register 10			Register 11	
	40	Phase Name				
	48	VER	MOD	LOG cuu	Output cuu	IGN1/TRC1 cuu
	56	IGN2/TRC2 cuu	IGN3/TRC3 cuu	Alternate Area Start		
	64	Alternate Area End			CHANQ PTR	
	72	PRT1	PRT2	PRT3	PRT4	PRT5   Reserved
	80	Reserved				
	88	Reserved		OPT	Reserved	
	96	Entries .....				

Figure 16. Standard Preface in the PD Area (Part 1 of 2)

<u>Displacement</u>	<u>Label</u>	<u>Description</u>
0-3	PDAREAND	Address of end of PD Area
4-7	PDINTRHK	Address of interrupt trace hook *
8-11	PDSIOHK	Address of START I/O hook *
12-15	PDTRANHK	Address of transient dump hook *
16-19	PDFLTHK1	Address of F/L trace hook 1 *
20-23	PDFLTHK2	Address of F/L trace hook 2 *
24-27		Reserved
28-39		Save area for registers 9, 10, and 11
40-47	Phase Name	Phase being run
48	VER	Version number in hexadecimal
49	MOD	Modification level in hexadecimal
50-51	LOG	Address of system log device
52-53	Output	Address of output device
54-55	IGN1/TRC1	Address(es) of devices to ignore or trace
56-57	IGN2/TRC2	
58-59	IGN3/TRC3	
60-63	Alternate Area Start	Start address of alternate area
64-67	Alternate Area End	Ending address of alternate area
68-71	CHANQ PTR	Address of channel queue pointer for output device
72-76	PRT1-PRT5	Partition(s) to be ignored **
77-90		Reserved
91	OPT	Option byte X'00' = TRC device X'80' = IGN device
92-95		Reserved
96-n		Entries

**Note:** The address of the PD Area is found at SYSCOM + X'48'

\* A hook is coding introduced at supervisor generation. The coding normally branches around itself. The initialization makes the branch instruction a NCP to allow a PDAID function to be performed.

\*\* The initializer inverts the logic. When the user specifies a partition(s) to be traced, PDAID enters the partition(s) to be ignored in the standard preface table.

Figure 16. Standard Preface in the PD Area (Part 2 of 2)

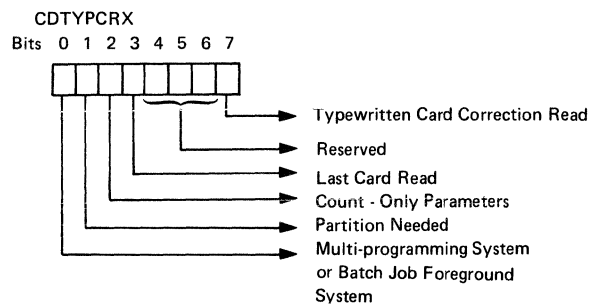
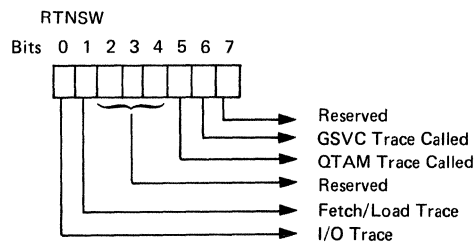
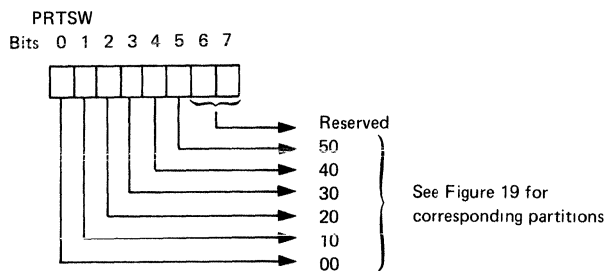
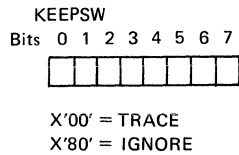
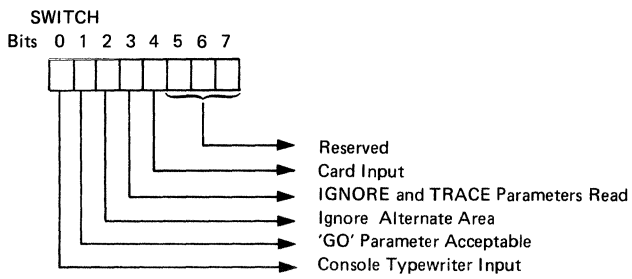


Figure 17. Switches Used by PDAID

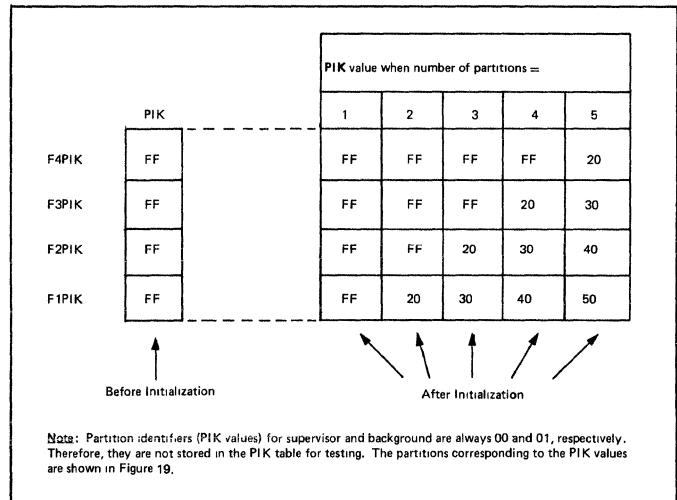


Figure 18. PIK Table in PDAID Phase

PIK Value	Partitions ignored when number of partitions =				
	1	2	3	4	5
50	-	-	-	-	F1
40	-	-	-	F1	F2
30	-	-	F1	F2	F3
20	-	F1	F2	F3	F4
10	BG	BG	BG	BG	BG
00	SP	SP	SP	SP	SP

Figure 19. Partition Identifiers



Chart AA. PDAID Initializer: Determining the Function

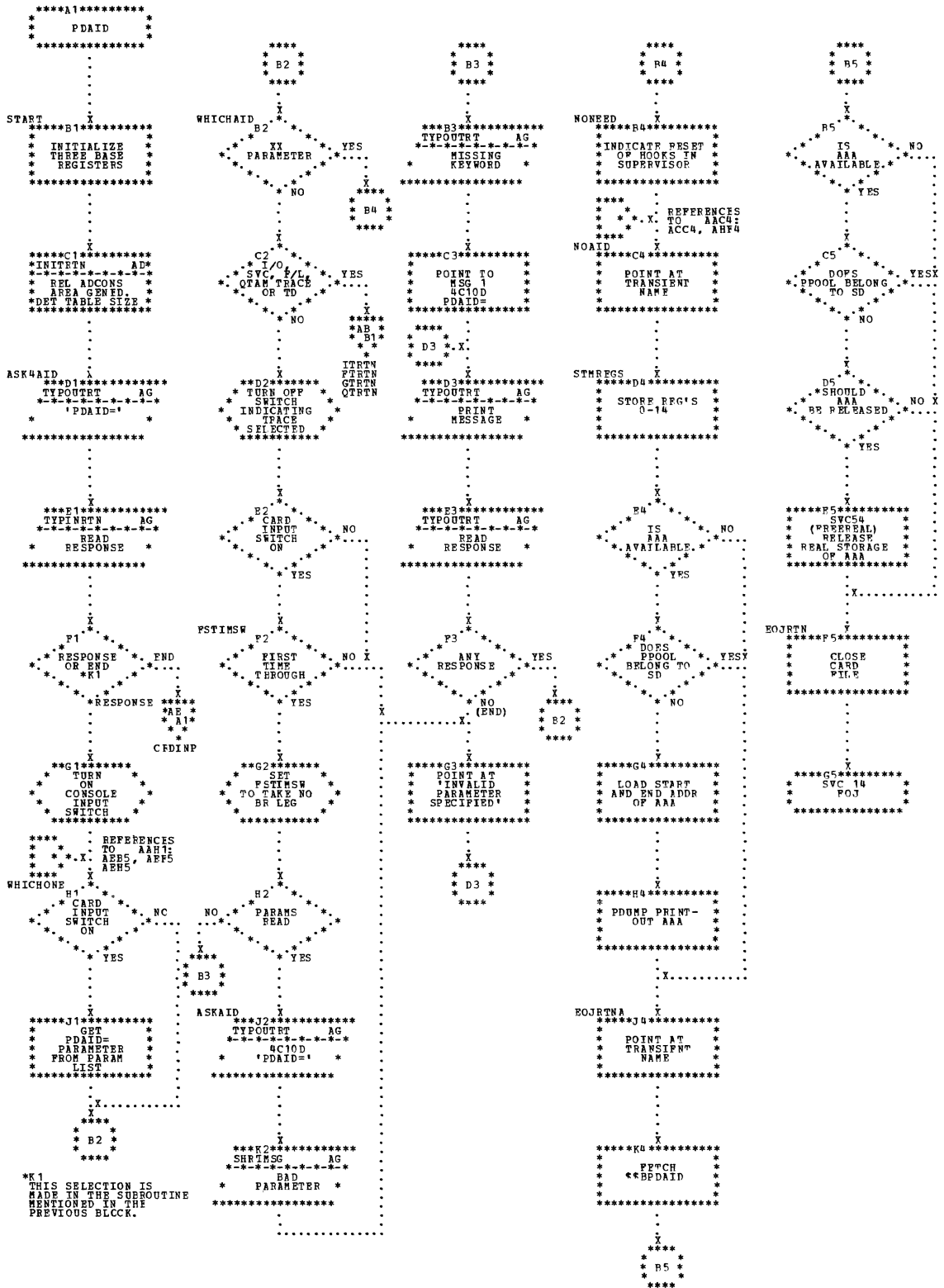
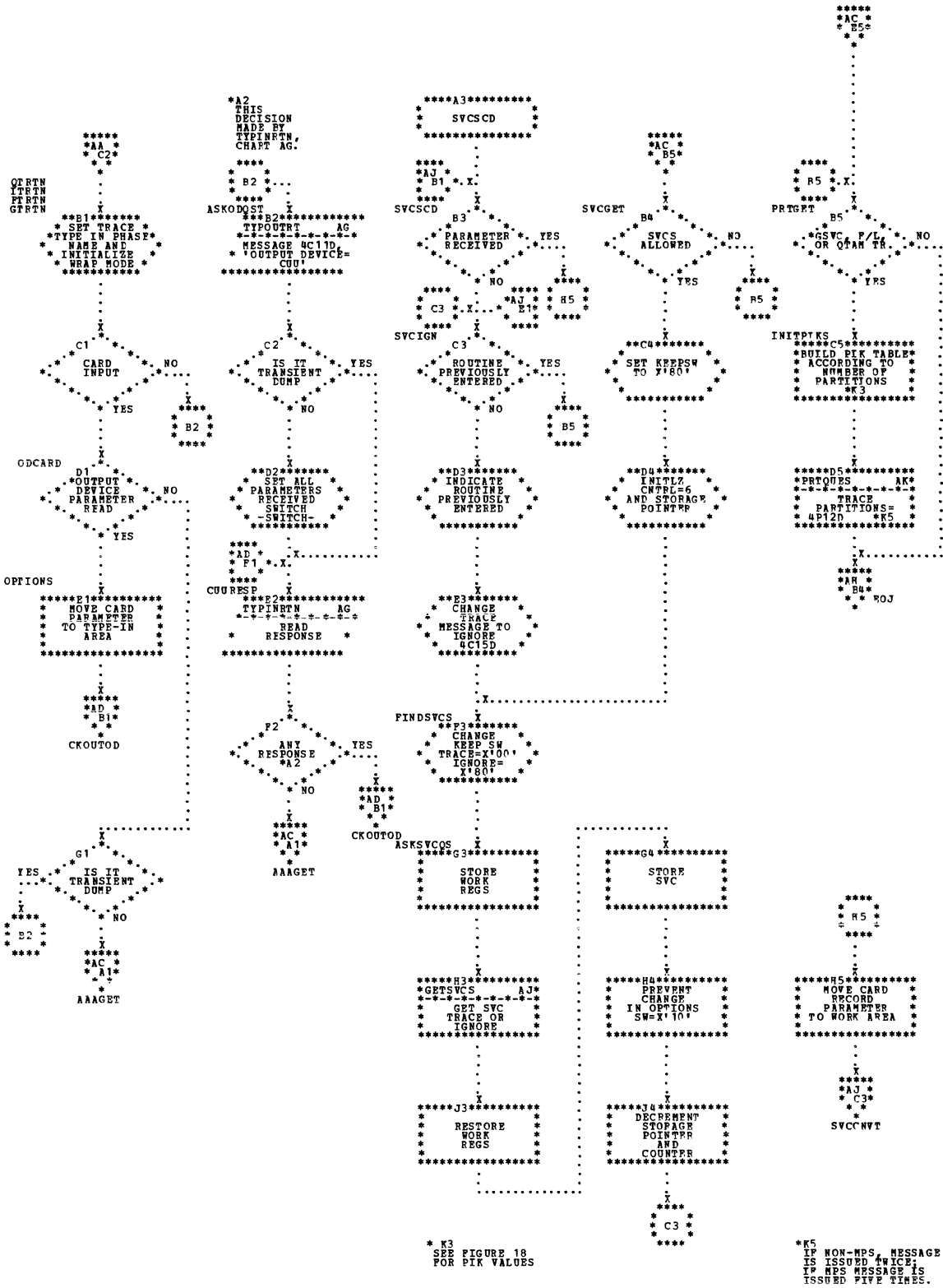


Chart AB. PDAID Initializer: Initializing the Function (Part 1 of 3)



\* K3  
SEE FIGURE 18  
FOR PK VALUES

\* R5  
IF NON-RPS MESSAGE  
IS ISSUED TWICE;  
IF RPS MESSAGE IS  
ISSUED FIVE TIMES.

Chart AC. PDAID Initializer: Initializing the Function (Part 2 of 3)

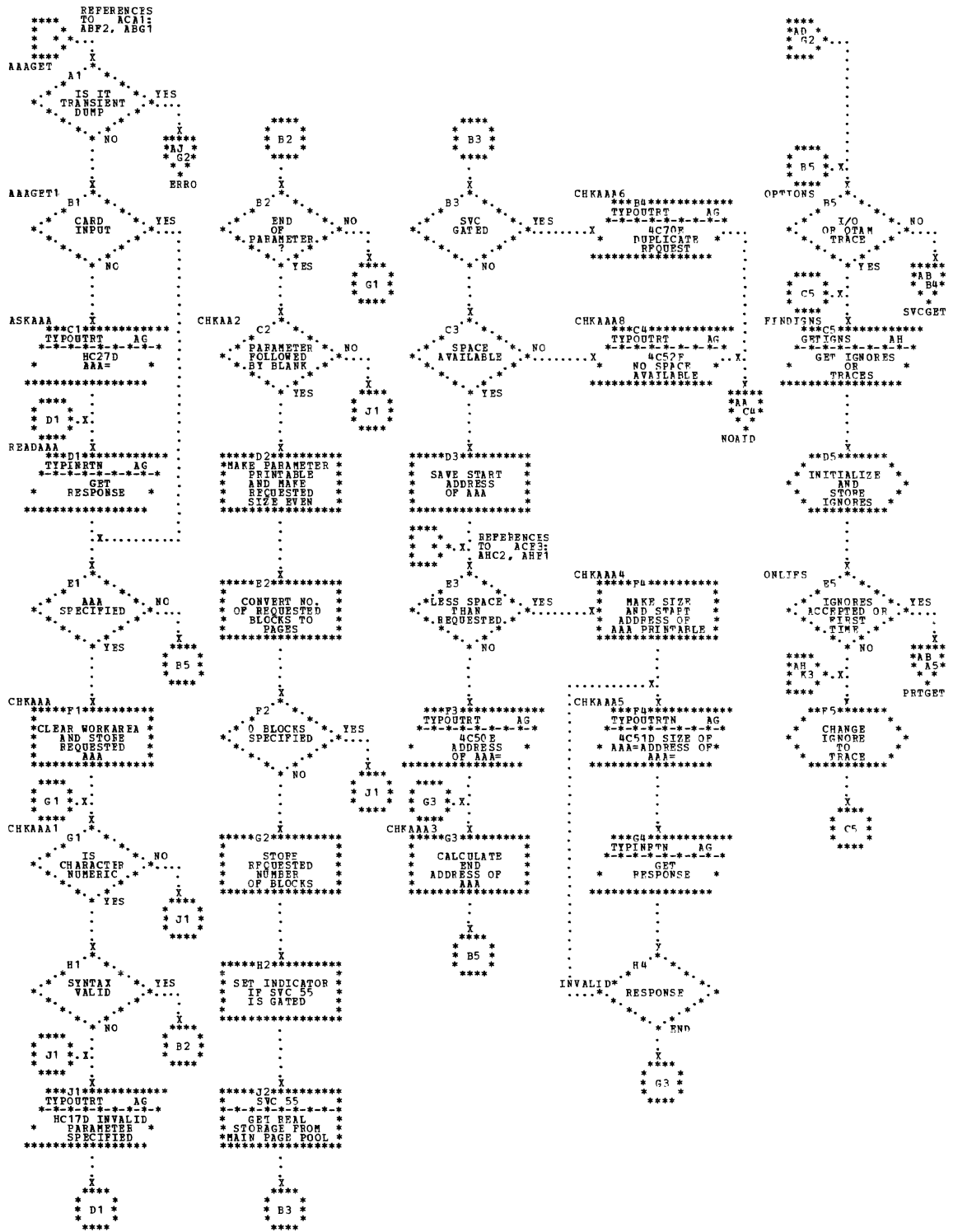
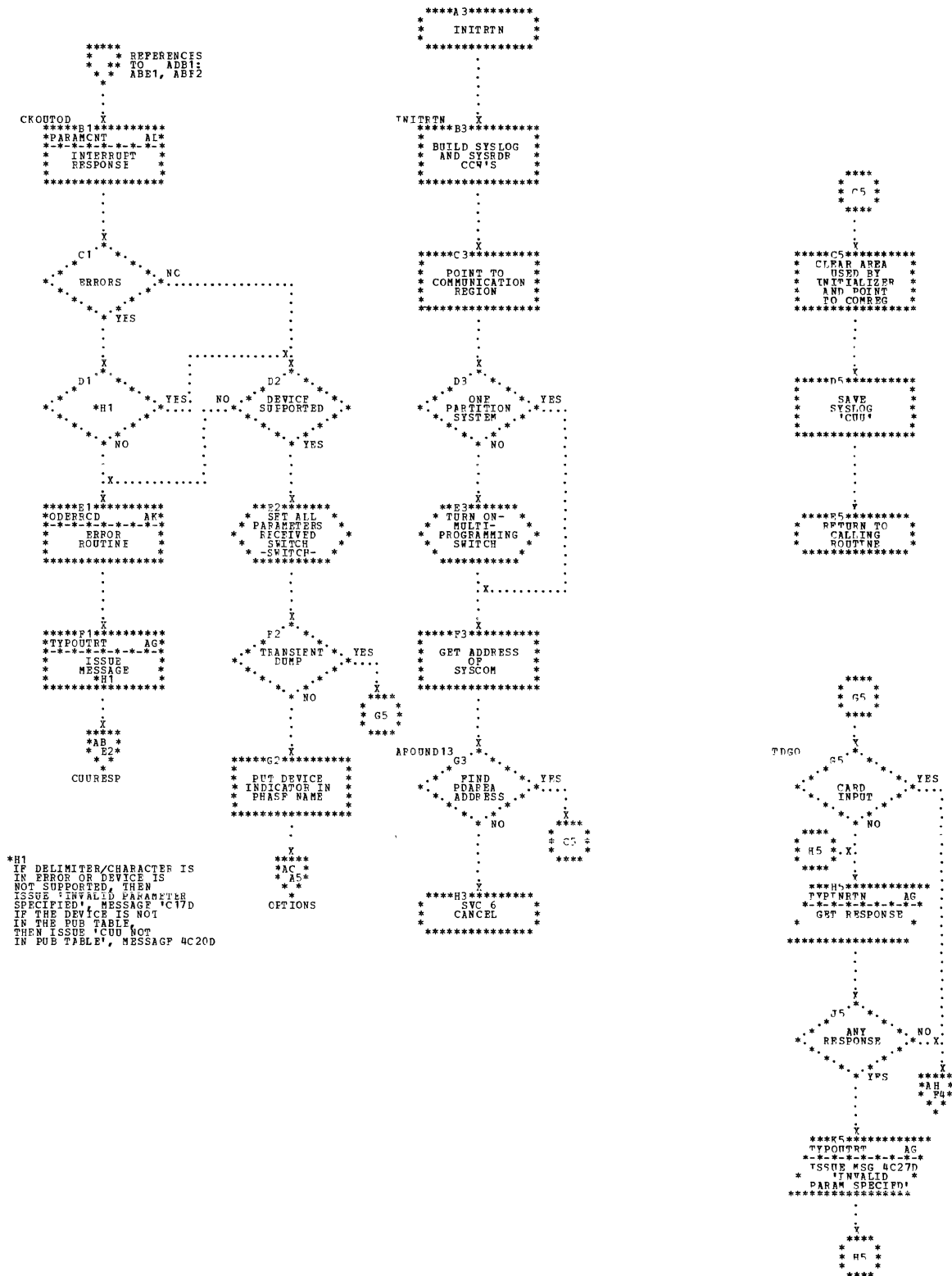


Chart AD. PDAID Initializer: Initializing the Function (Part 3 of 3)



\*H1  
IF DELIMITER/CHARACTER IS  
IN ERROR OR DEVICE IS  
NOT SUPPORTED THEN  
ISSUE 'INVALID PARAMETER  
SPECIFIED', MESSAGE 'C17D'  
IF THE DEVICE IS NOT  
IN THE PUB TABLE  
THEN ISSUE 'CUU NOT  
IN PUB TABLE', MESSAGE '4C20D'

# Chart AE. PDAID Initializer: Card Input Routine

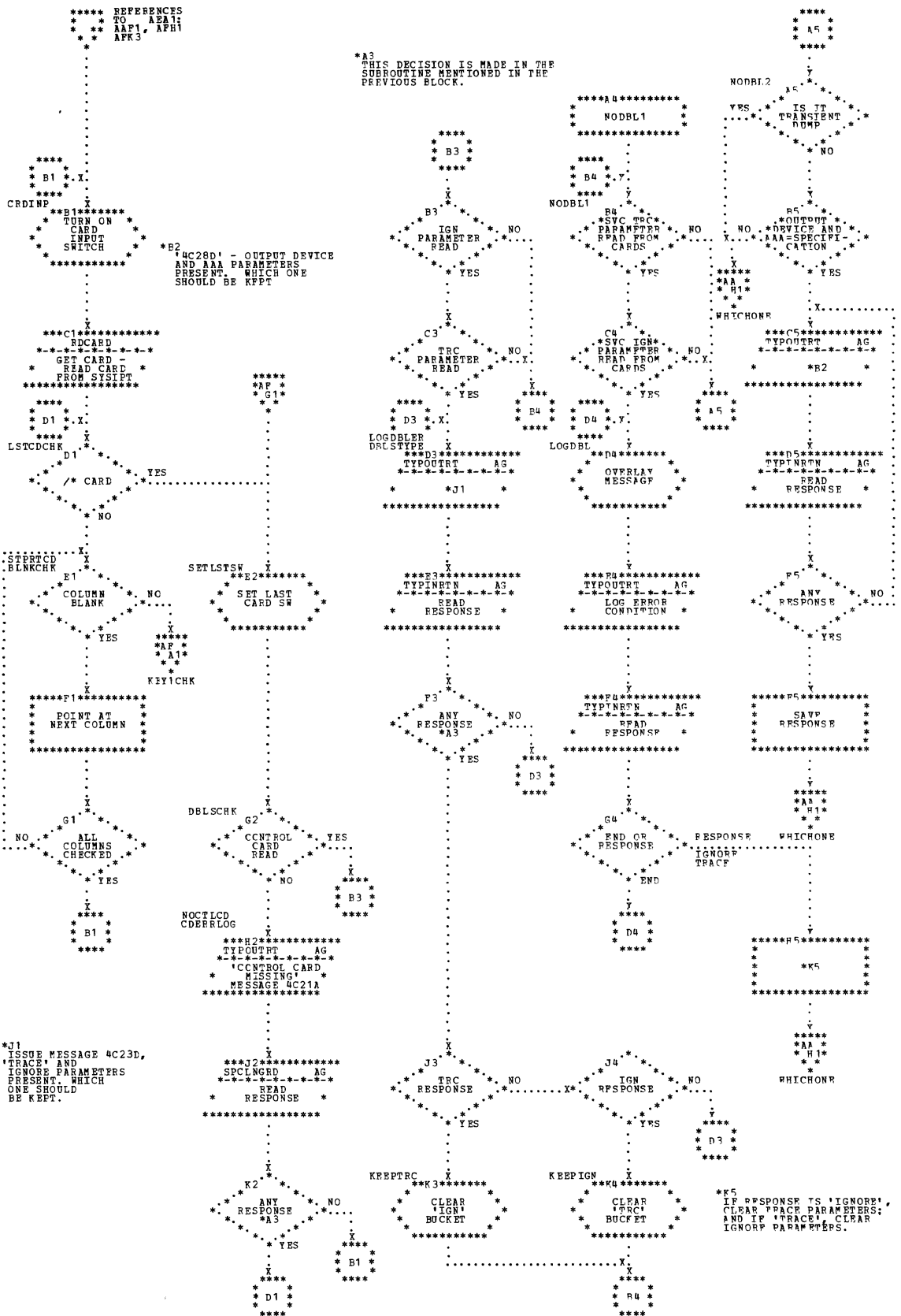


Chart AF. PDAID Initializer: Keyword Verification

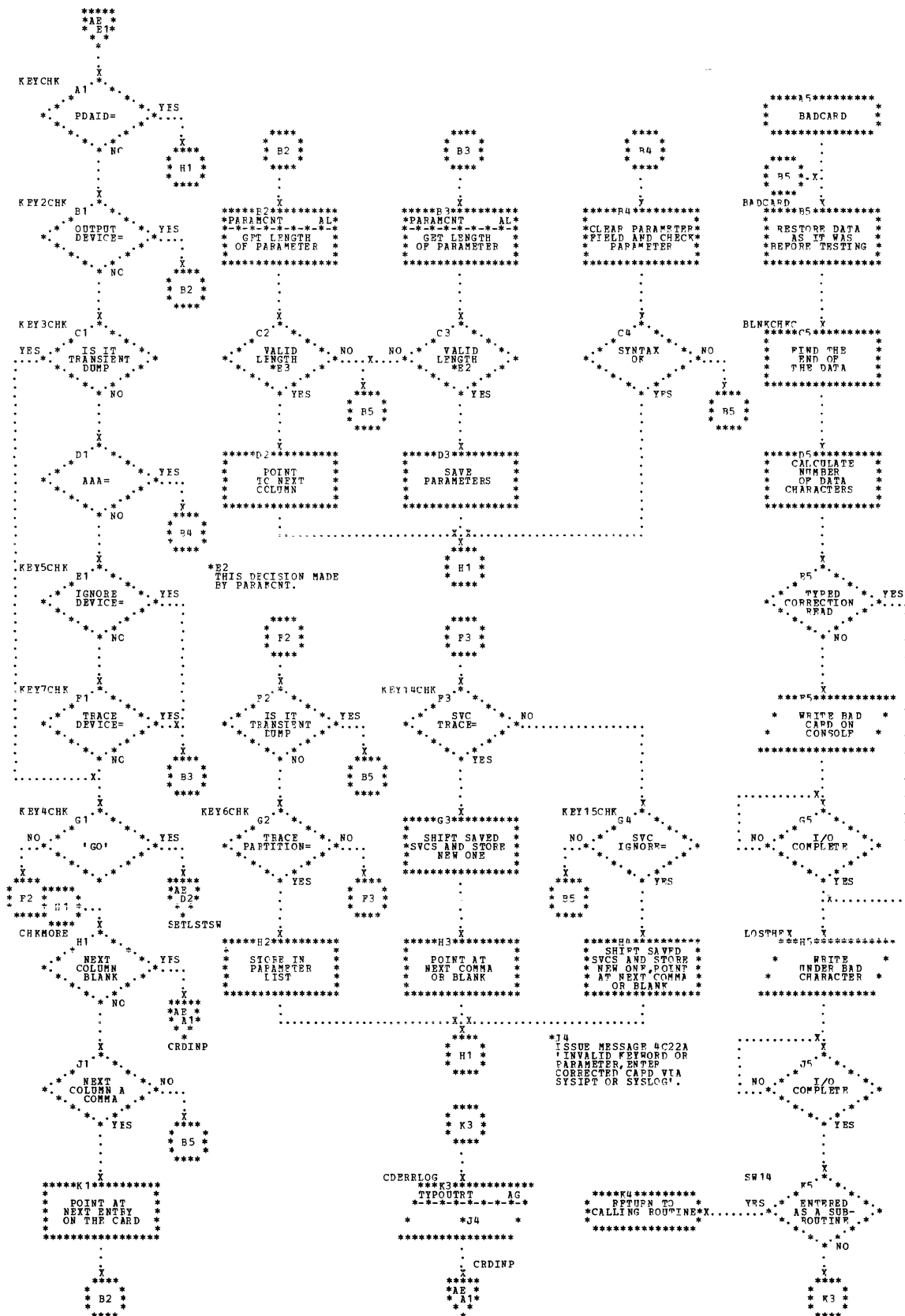


Chart AG. PDAID Initializer: Console Input/Output Routines

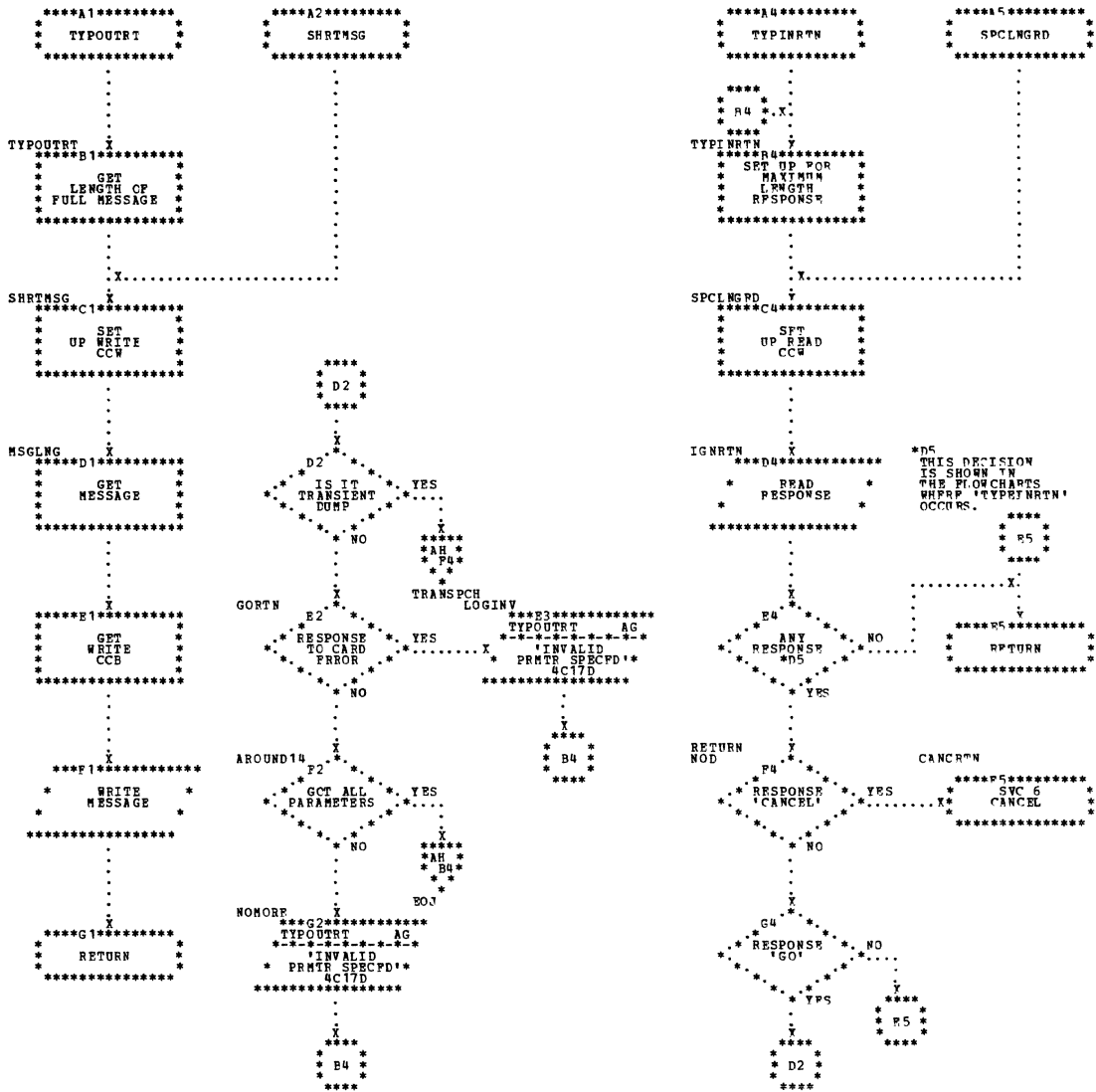


Chart AH. PDAID Initializer: Get IGN/TRC Parameters ECJ Routine; Check Display Limits  
(Part 1 of 2)

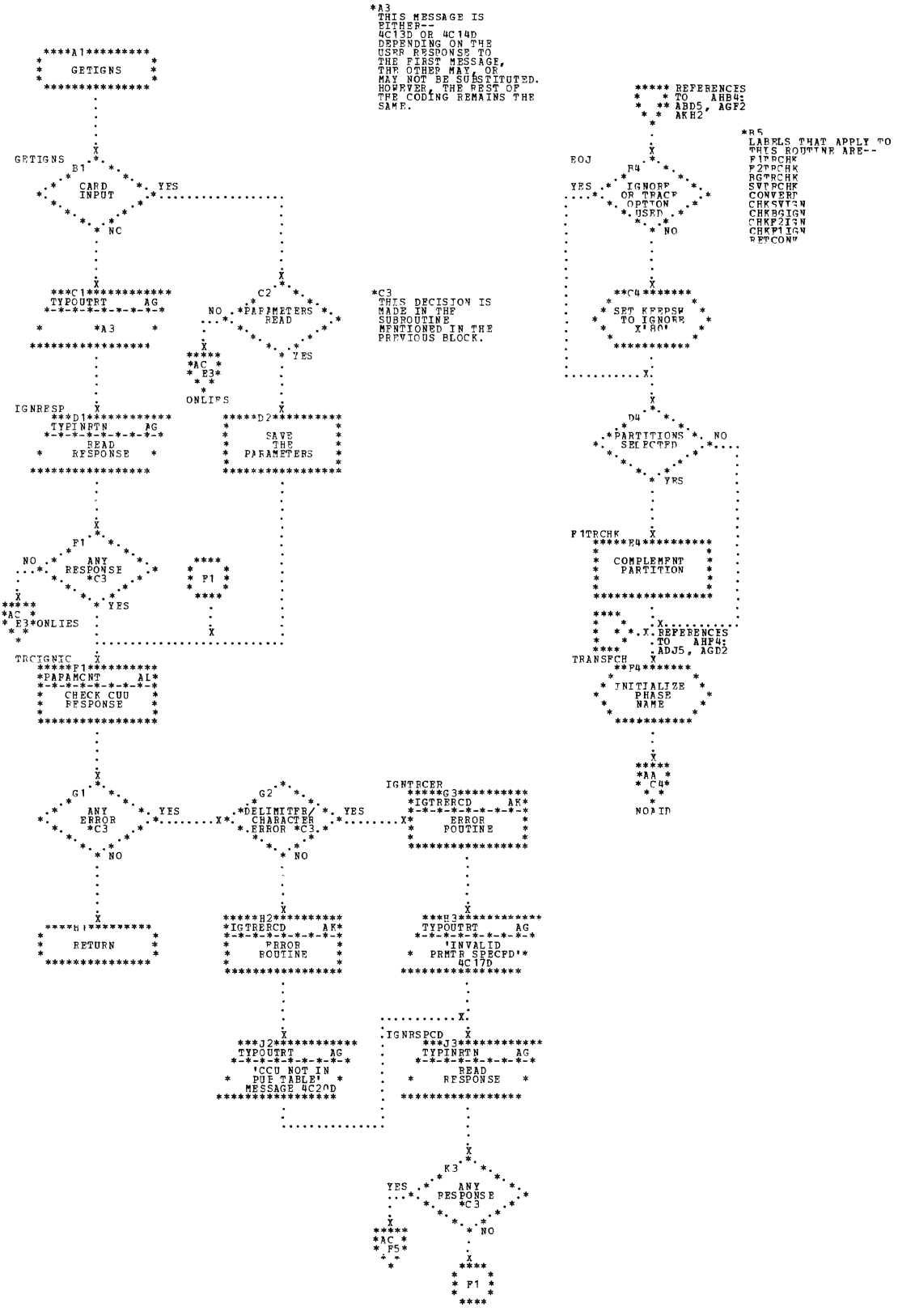






Chart AK. PDAID Initializer: PHASE and PARTITION Parameters; Error Routines

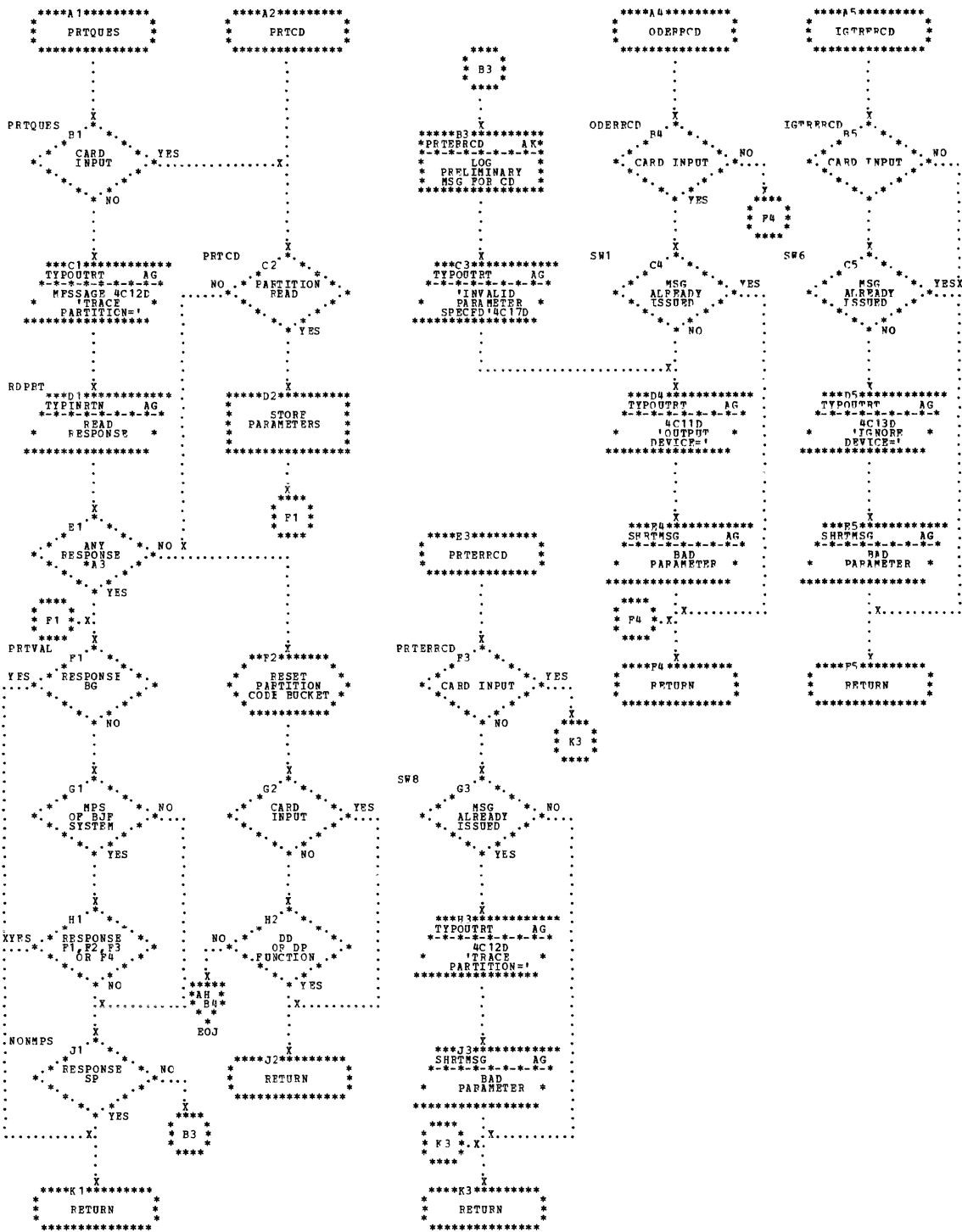


Chart AL. PDAID Initializer: Check Parameters, Devices, and Convert to Binary

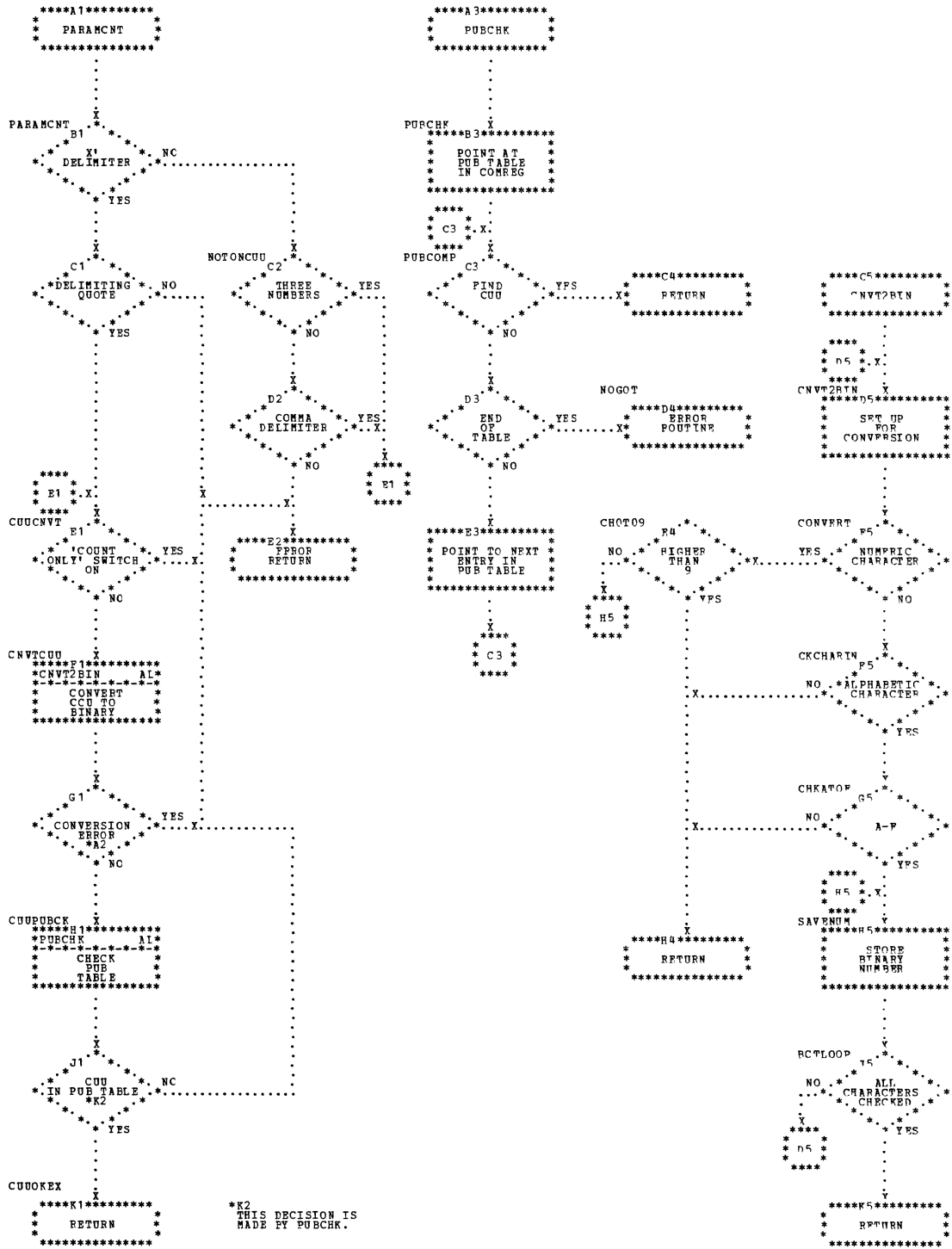




Chart BA. PDLIST - Tape-to-Printer Program (Part 1 of 3)

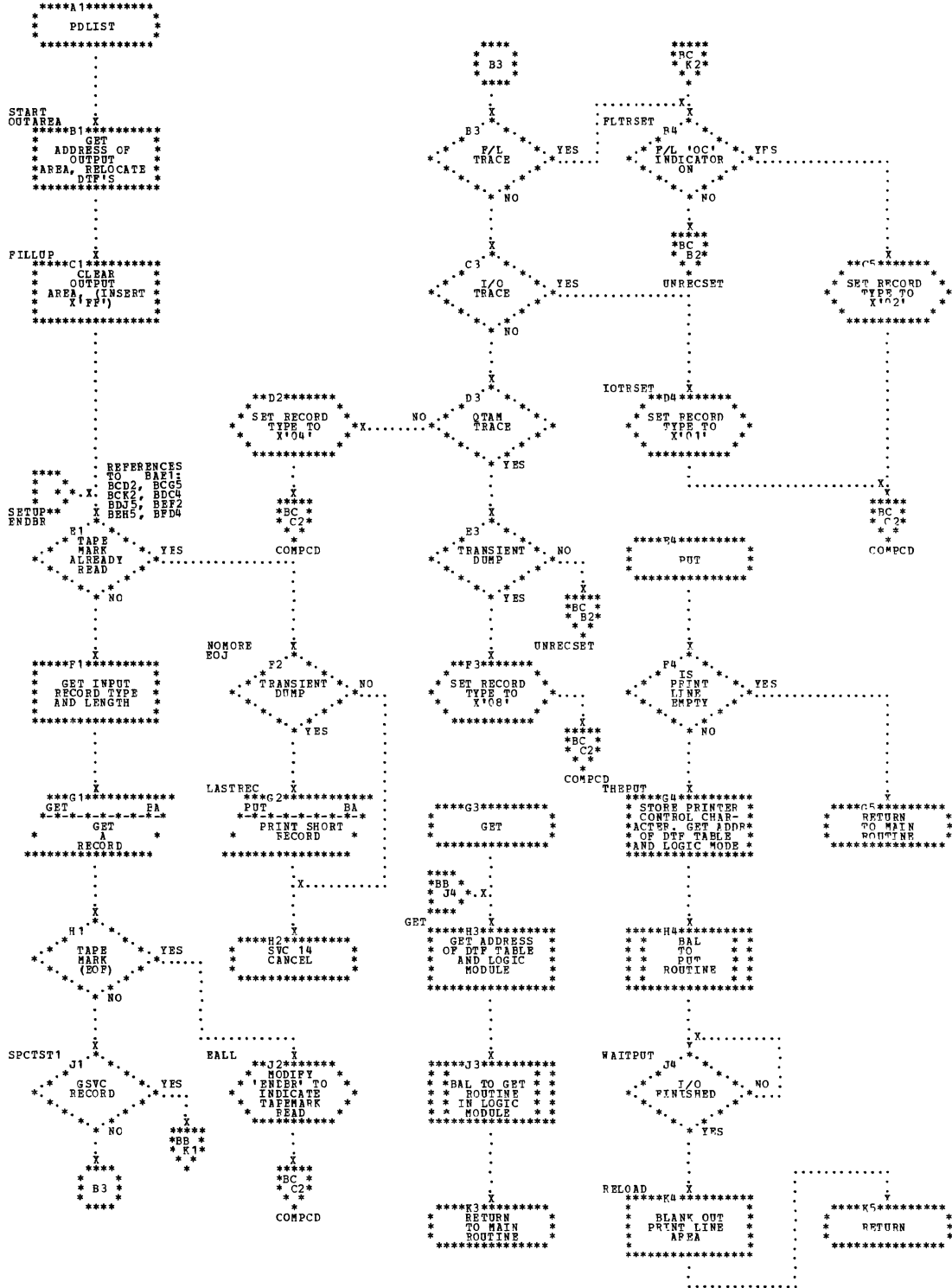


Chart BB. PDLIST - Tape-to-Printer Program (Part 2 of 3)

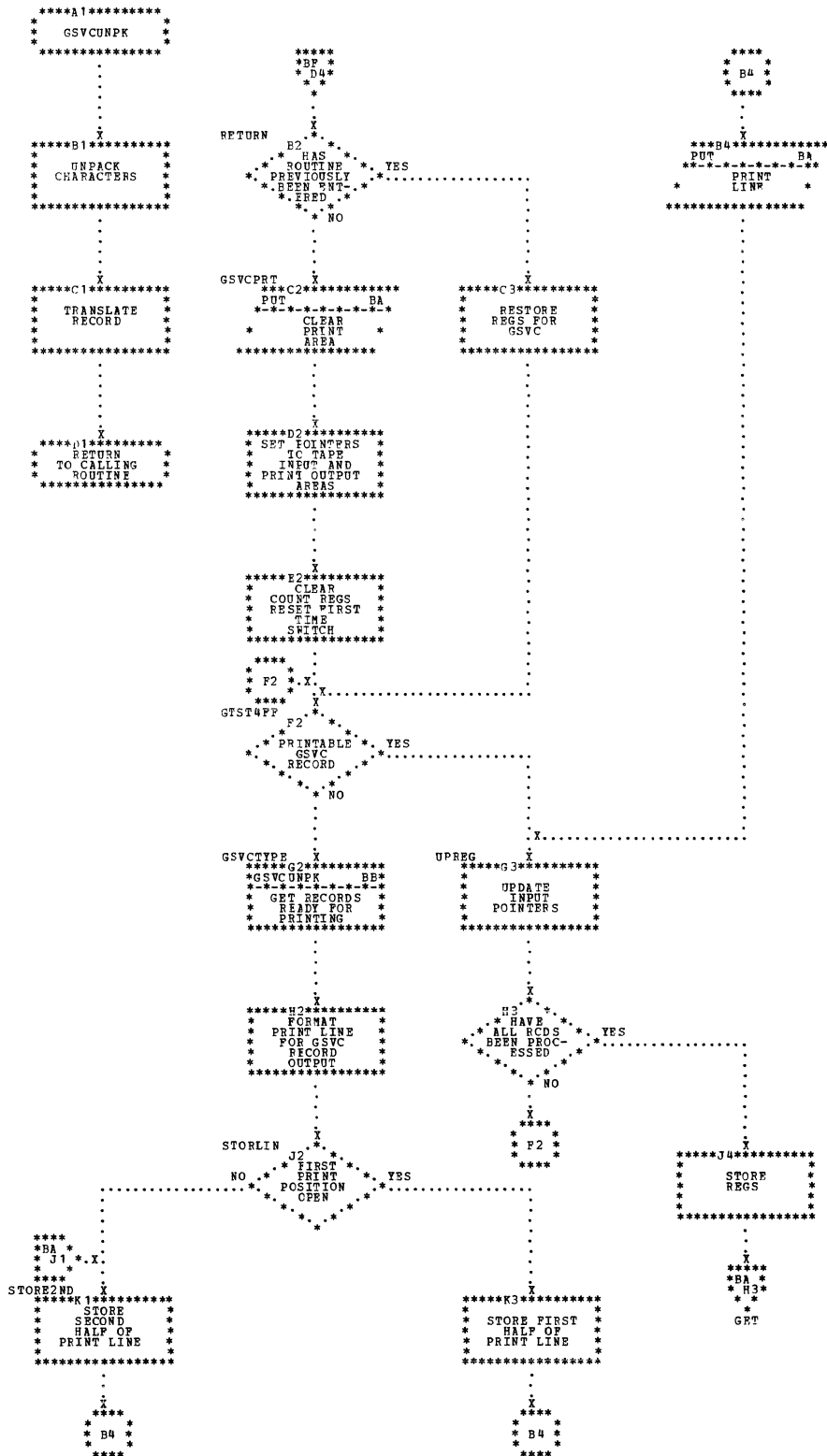




Chart BD. PDLIST - I/O Trace Records

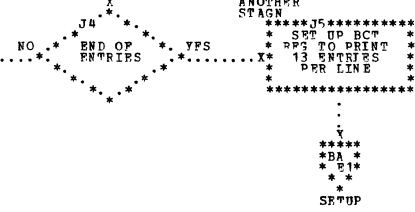
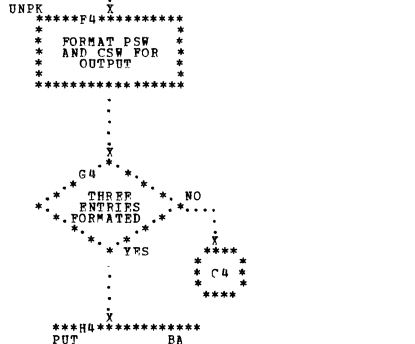
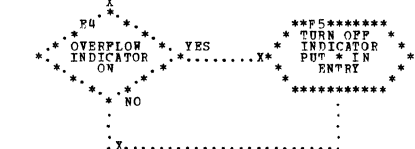
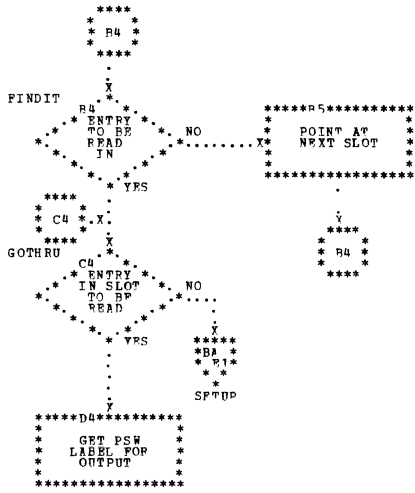
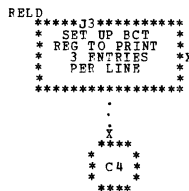
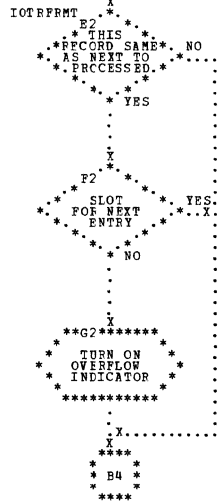
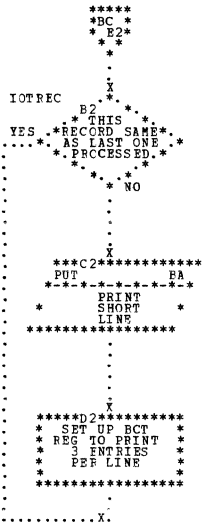








Chart CA. I/O Trace: Core-Wrap Mode (PDAIDITW)

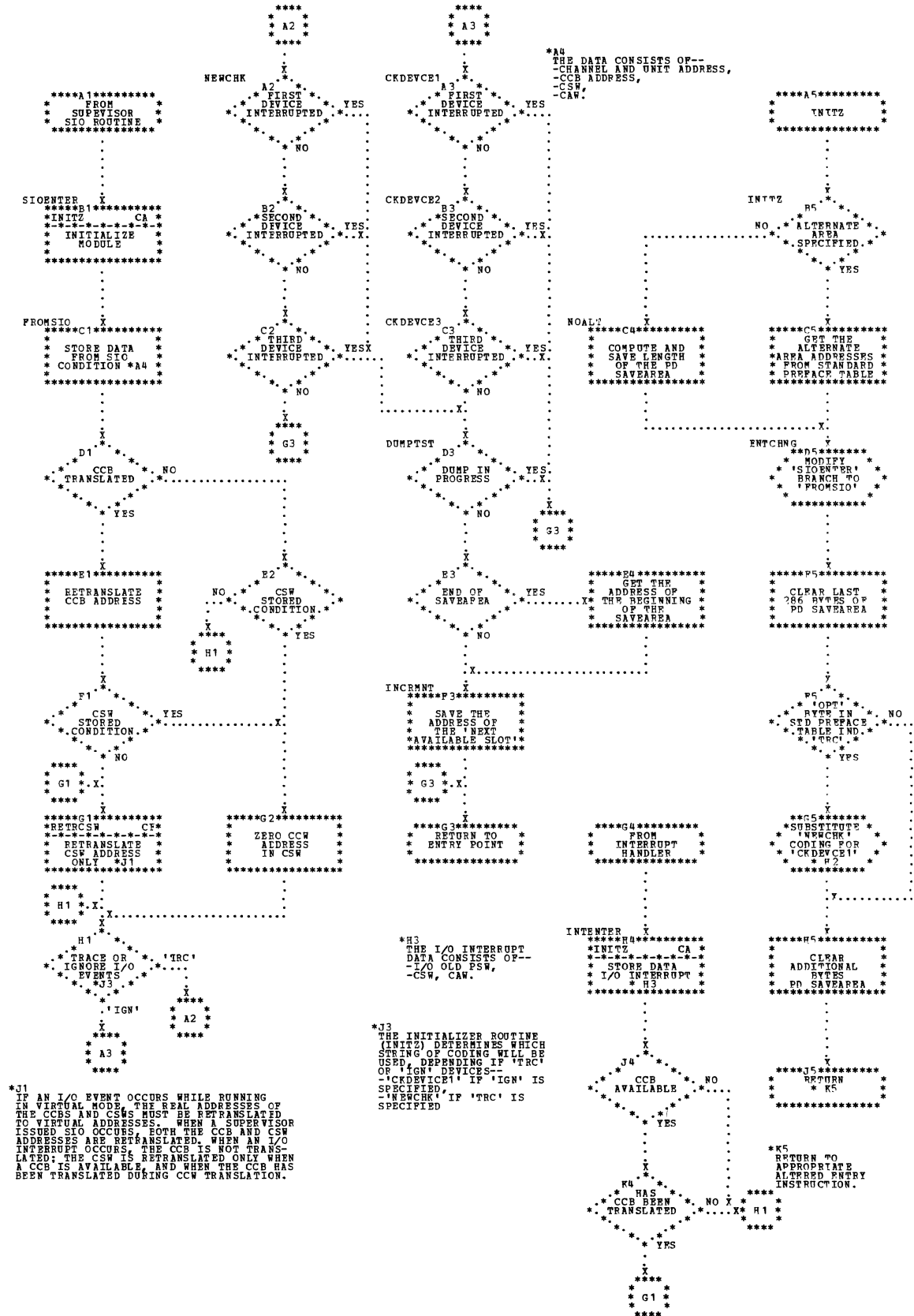


Chart CB. I/O Trace: Print Mode (PDAIDITP) (Part 1 of 2)

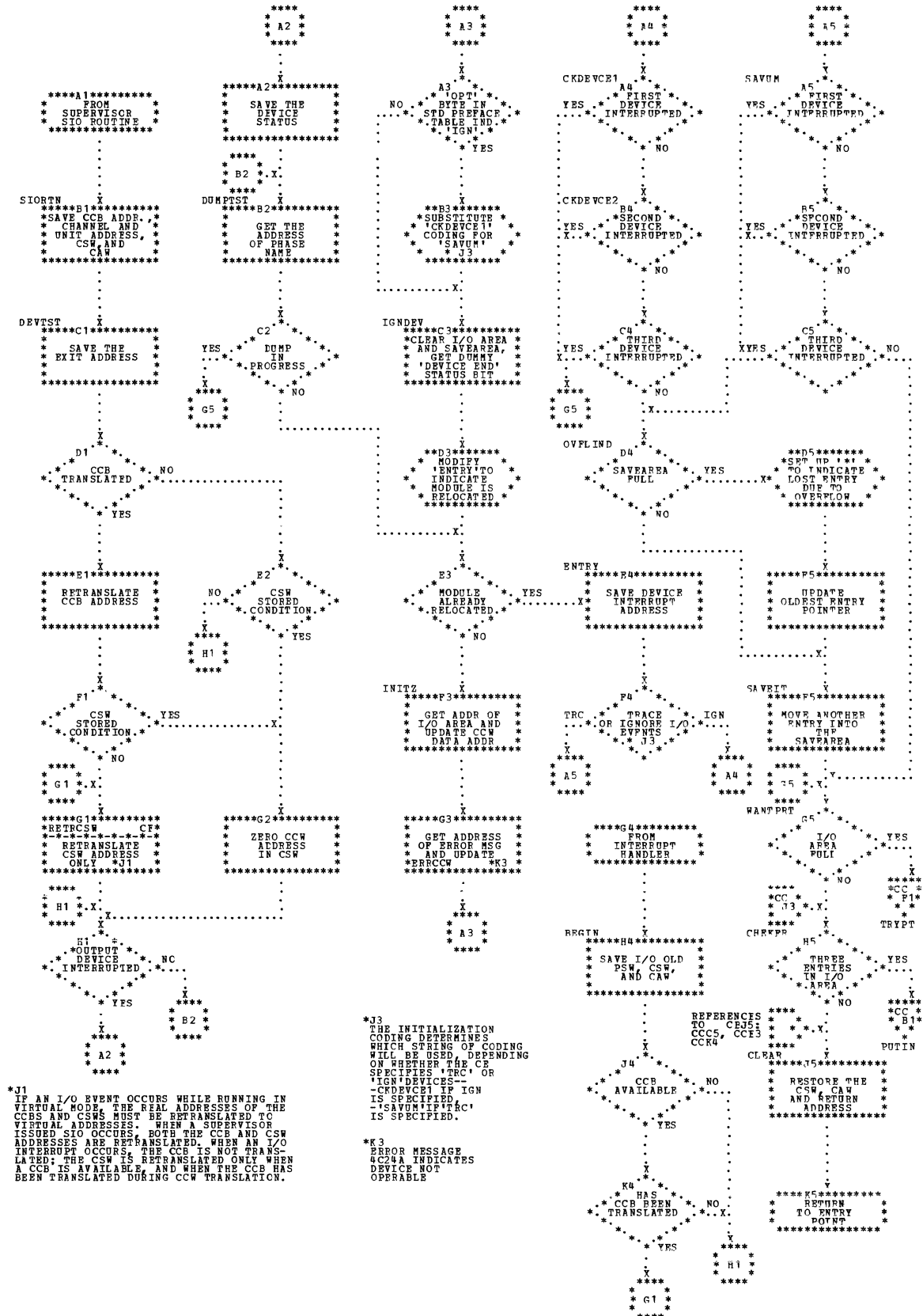


Chart CC. I/O Trace: Print Mode (PDAIDITP) (Part 2 of 2)

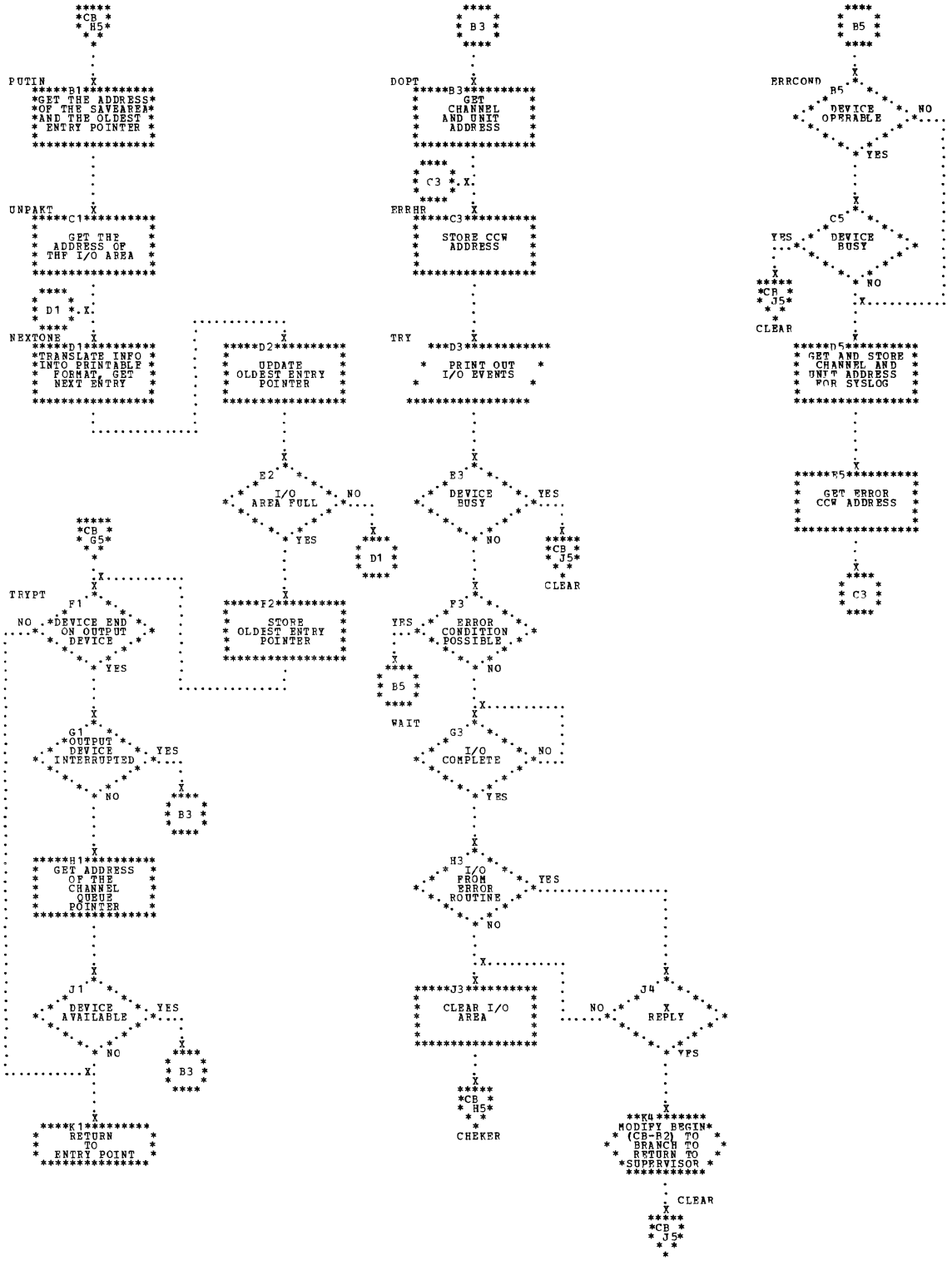


Chart CD. I/O Trace: Tape Mode (PDAIDITT) (Part 1 of 2)

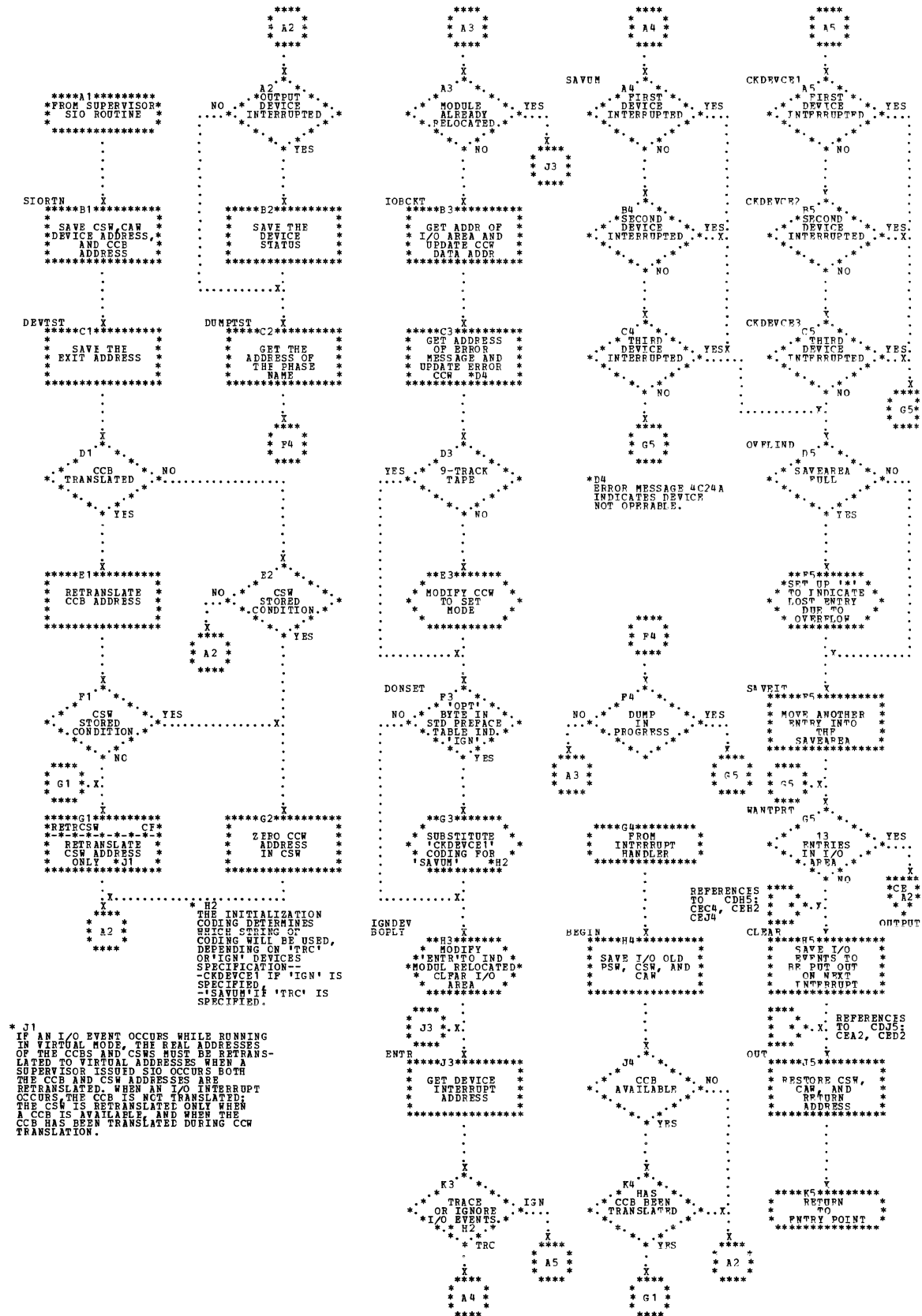
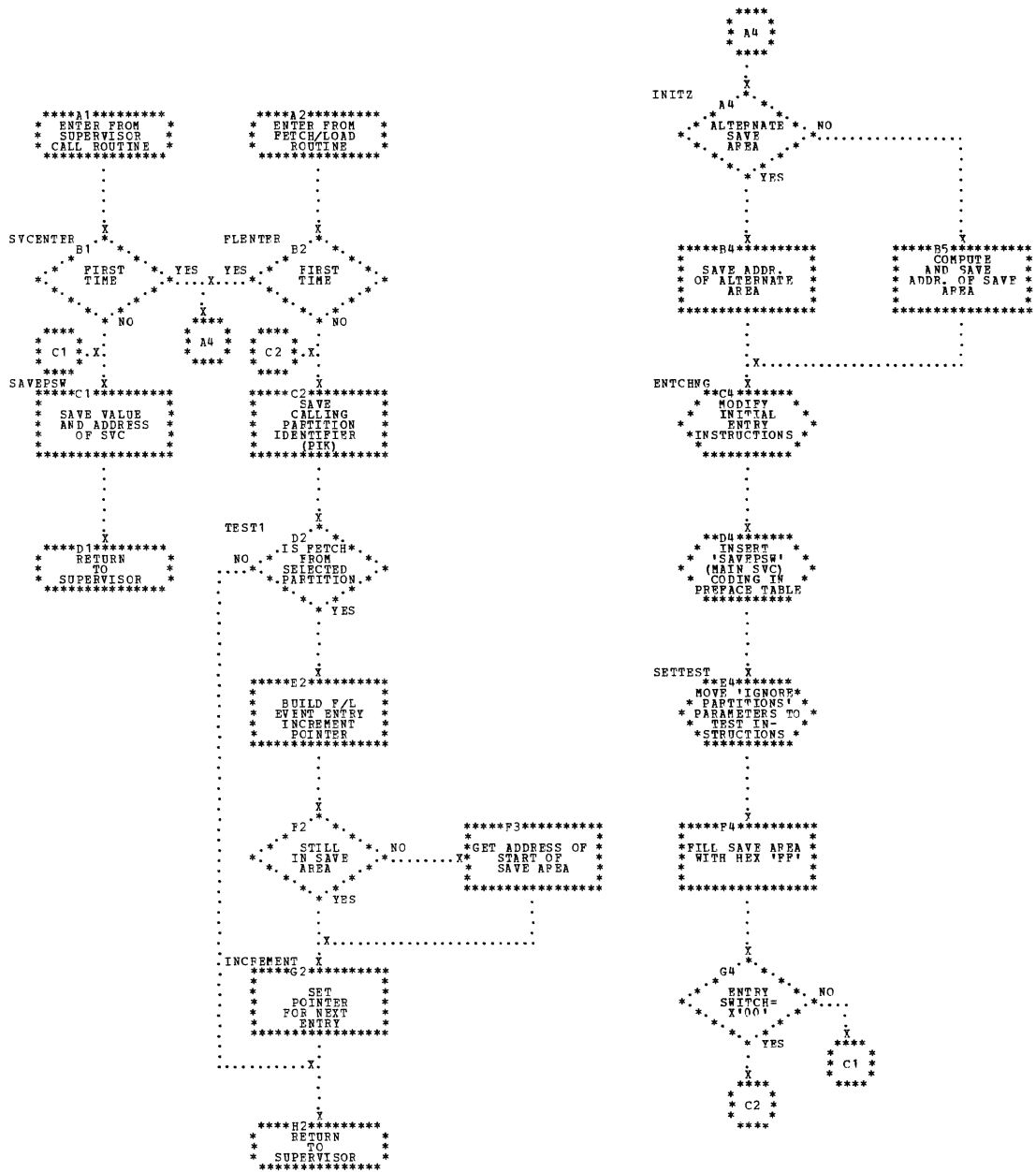








Chart DA. F/L Trace: Core-Wrap Mode (PDAIDFTW)



\*K5  
THE BRANCH ADDRESS IS  
DETERMINED AT THE  
INITIAL ENTRY THIS  
DECISION IS SHOWN ONLY  
AS AN AID IN FOLLOWING  
THE LOGIC - IT DOES NOT  
APPEAR IN THE LISTING.

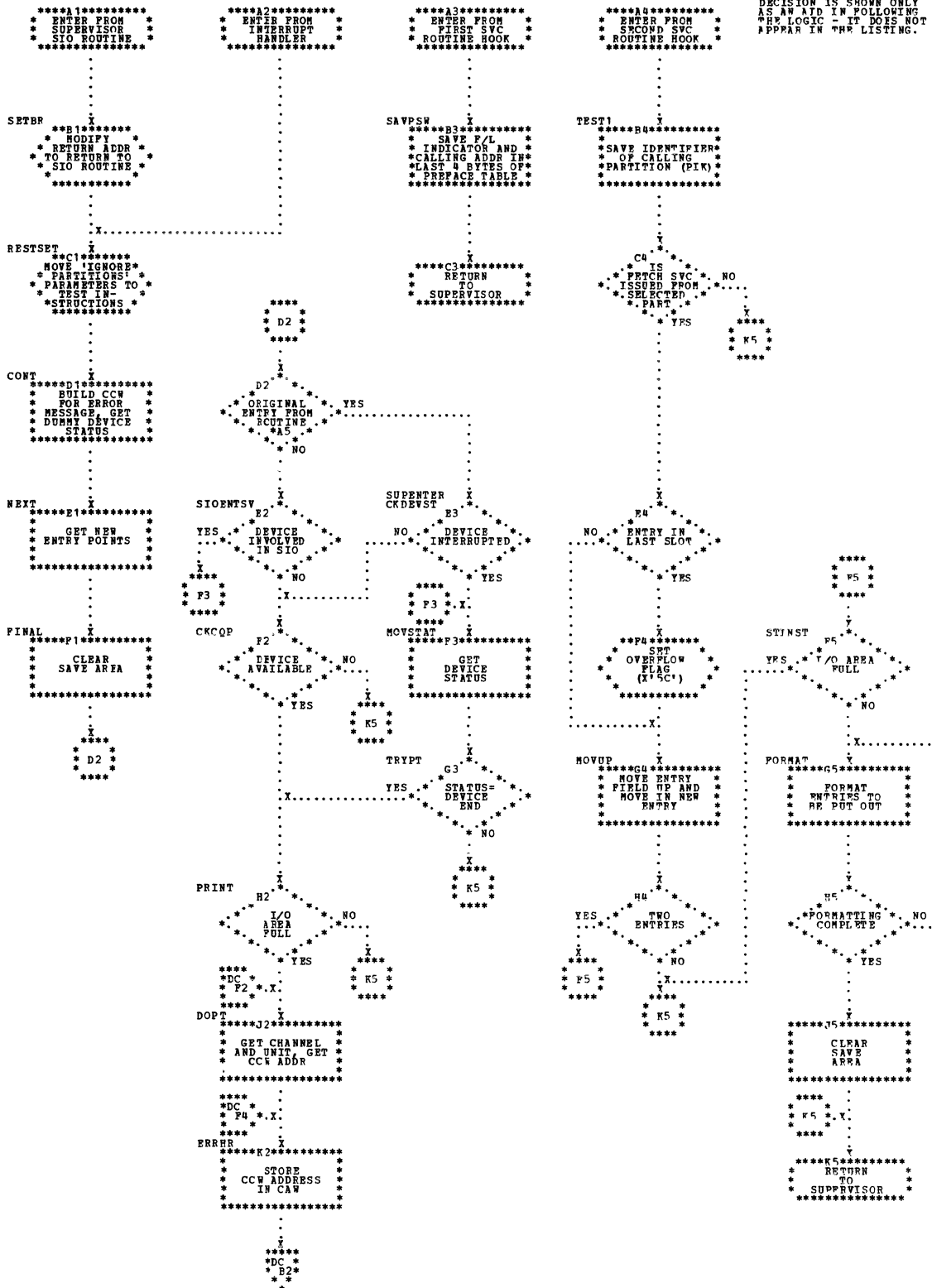


Chart DC. F/L Trace: Print Mode (PDAIDFTP) (Part 2 of 2)

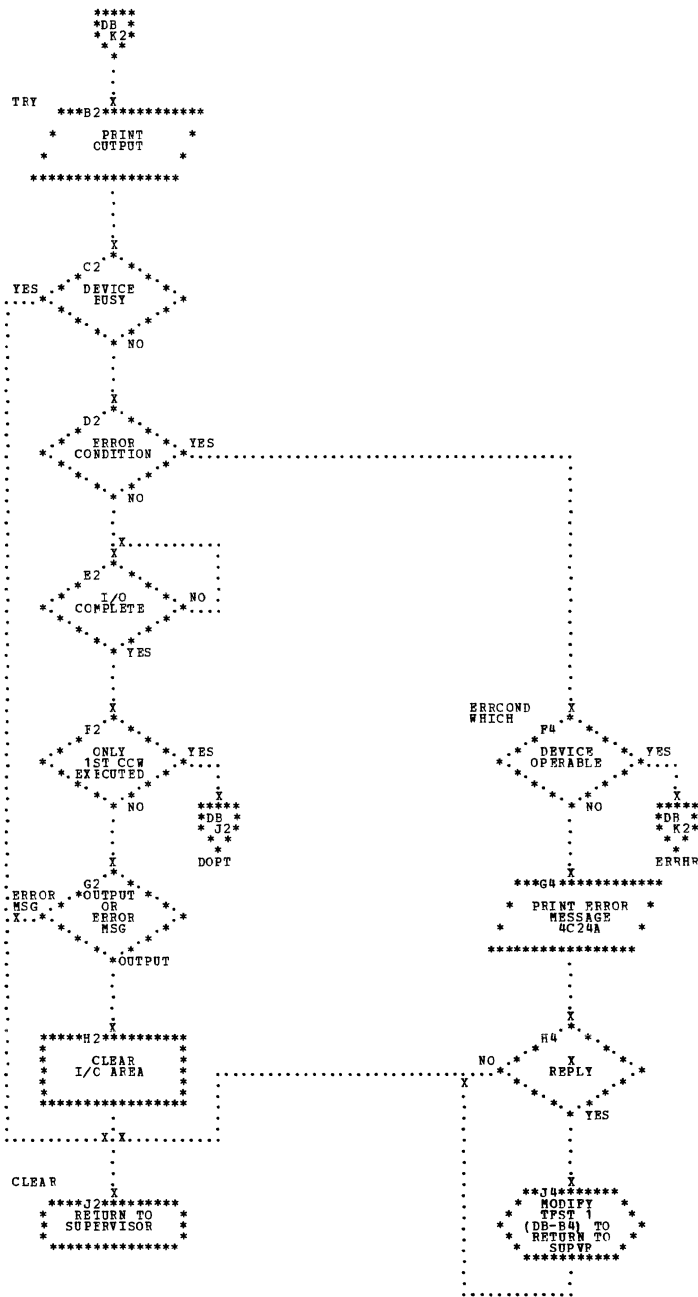


Chart DD. F/L Trace: Tape Mode (FDAIDFTT) (Part 1 of 2)

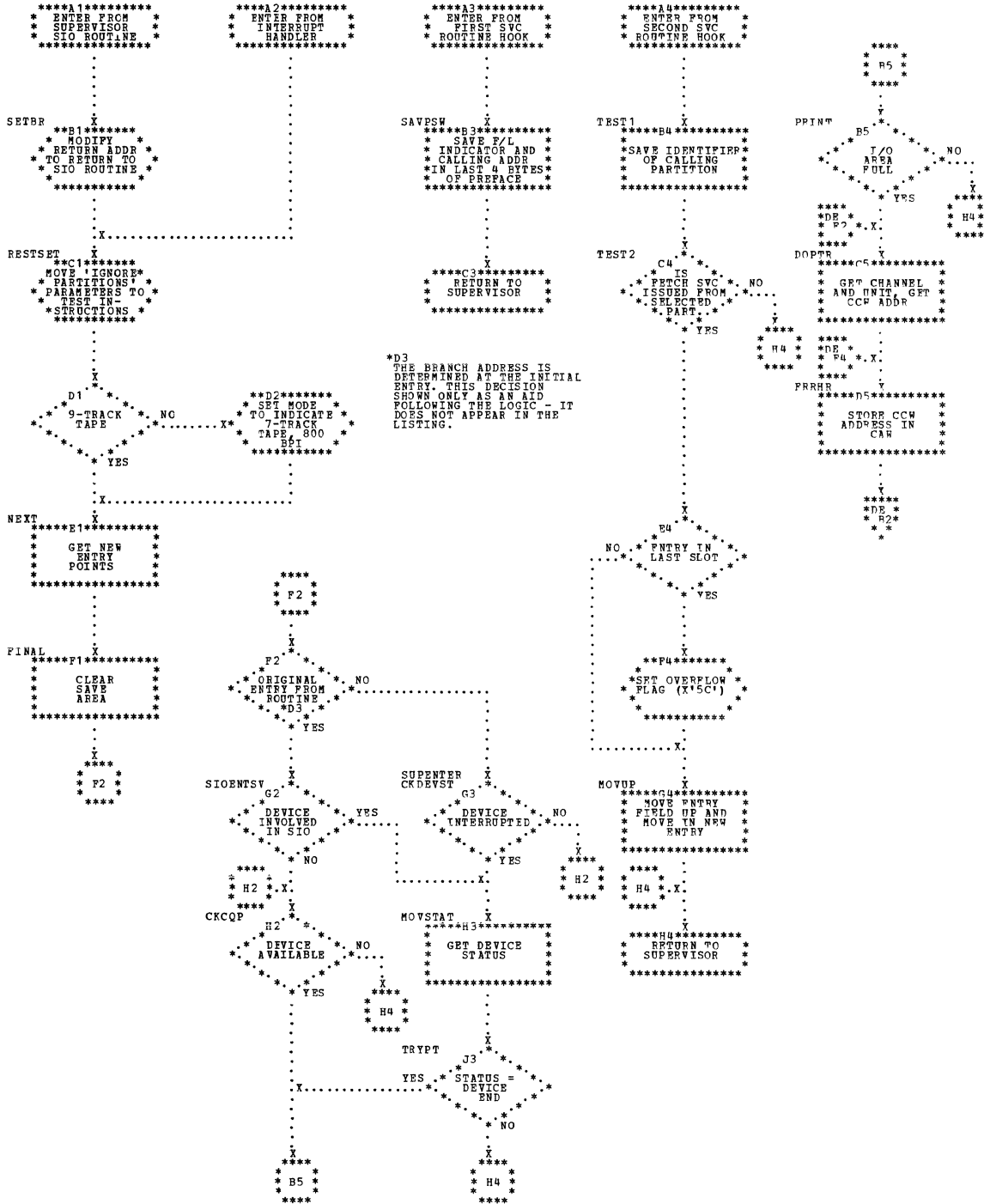




Chart EA. GSVVC Trace: Core-Wrap Mode (PDAIDGTW) (Part 1 of 2)

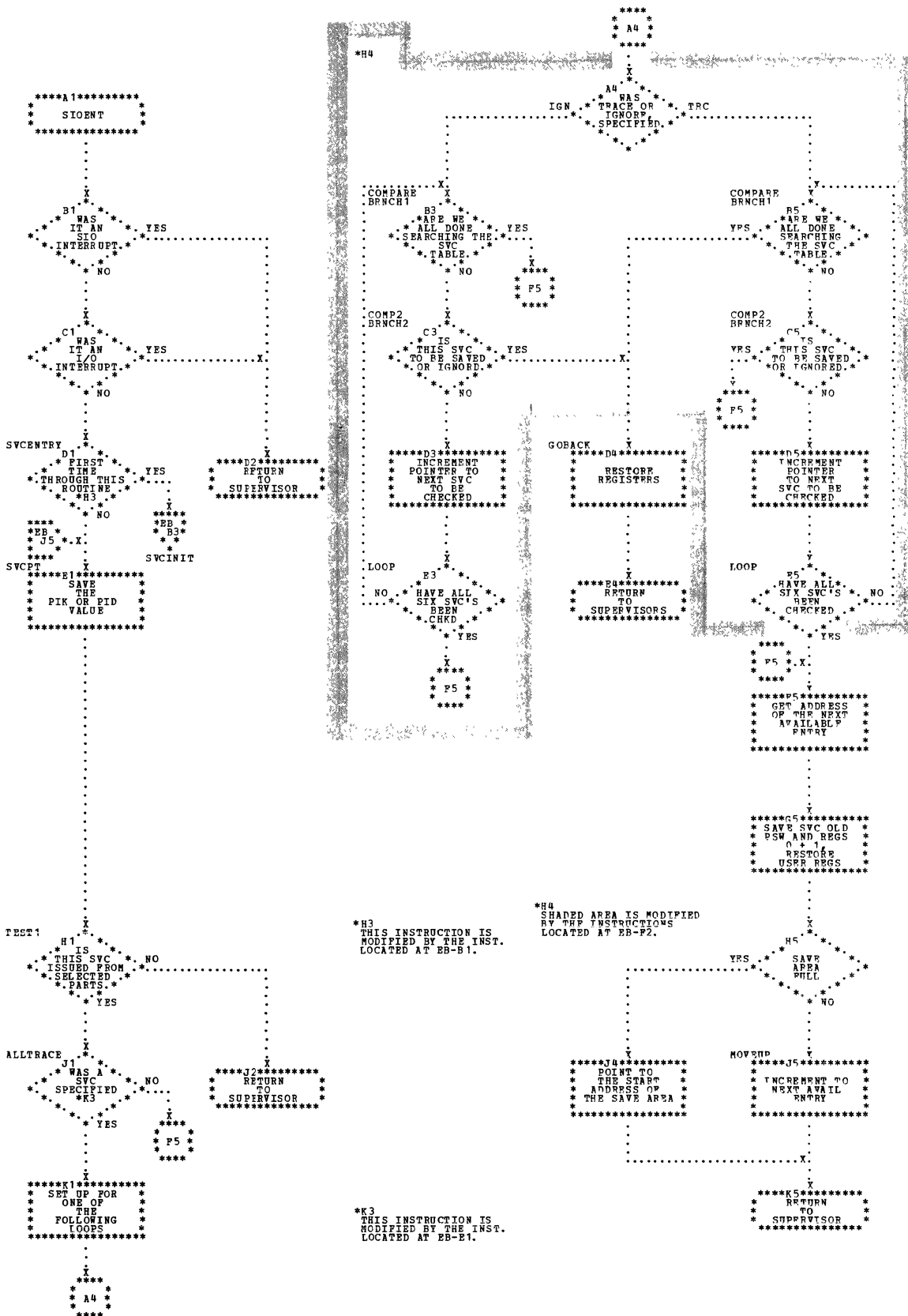
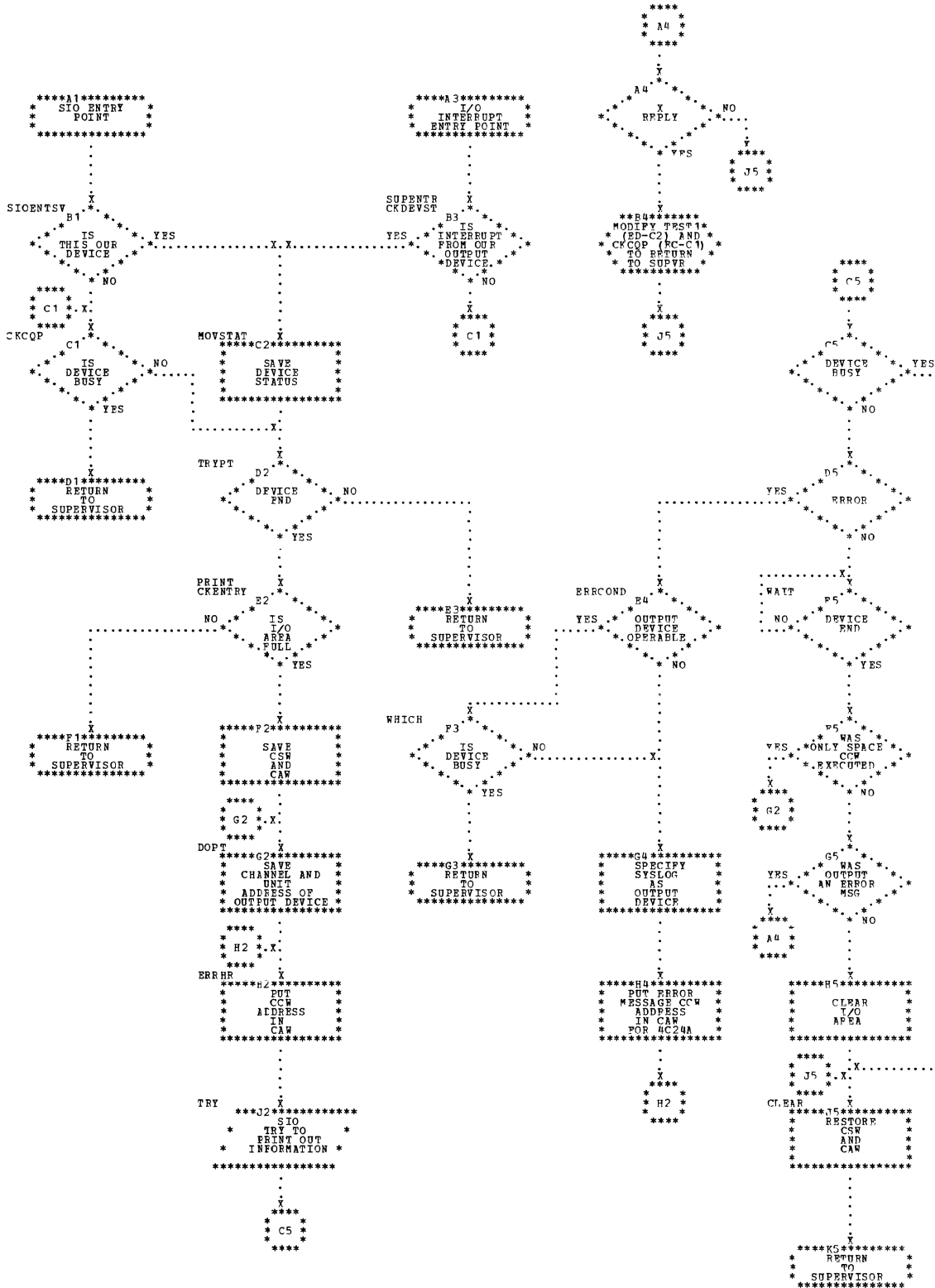




Chart EC. GSVG Trace: Print Mode (PDAIDGTP) (Part 1 of 3)





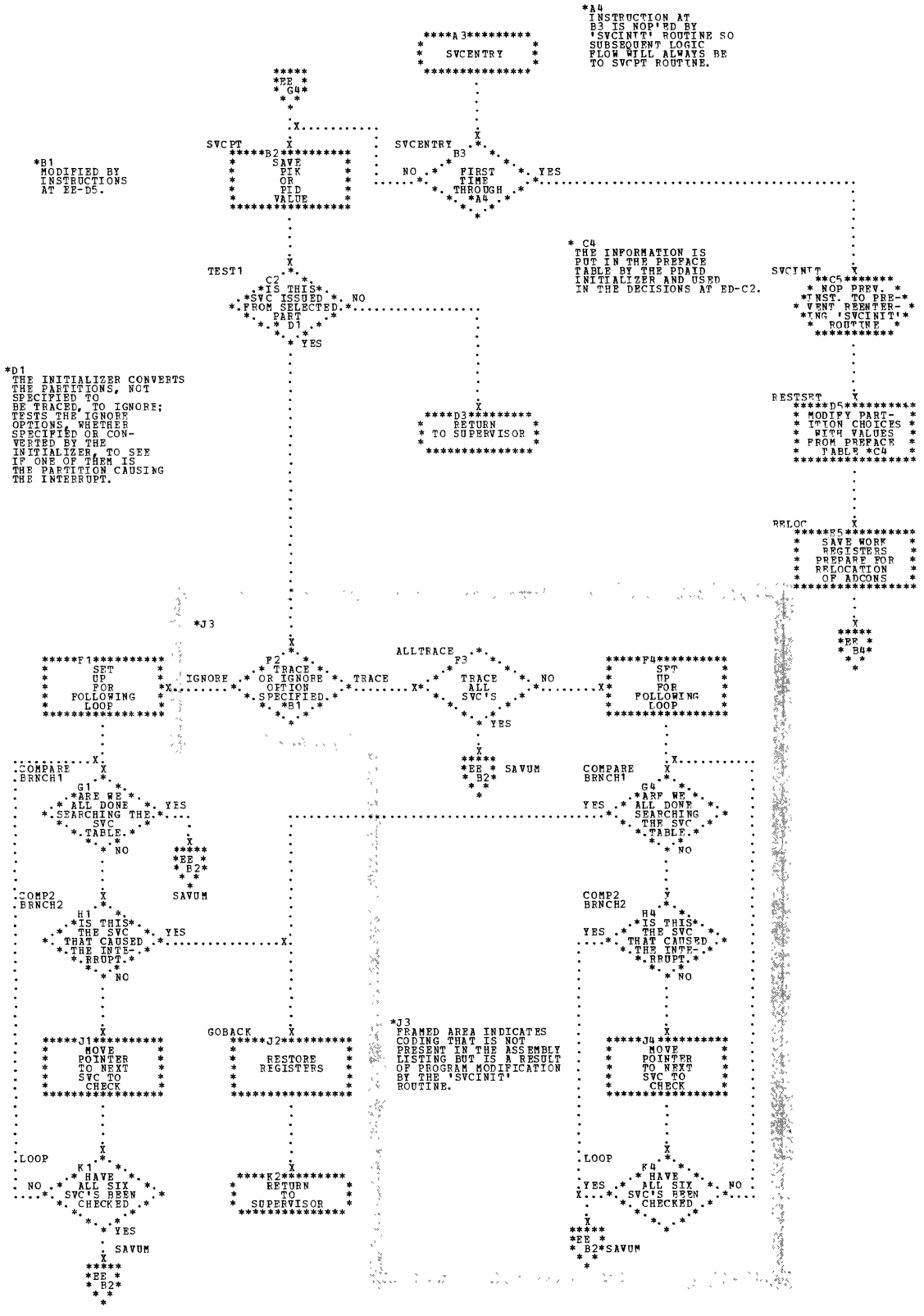




Chart EF. GSV C Trace: Tape Mode (PDAIDGTT) (Part 1 of 3)

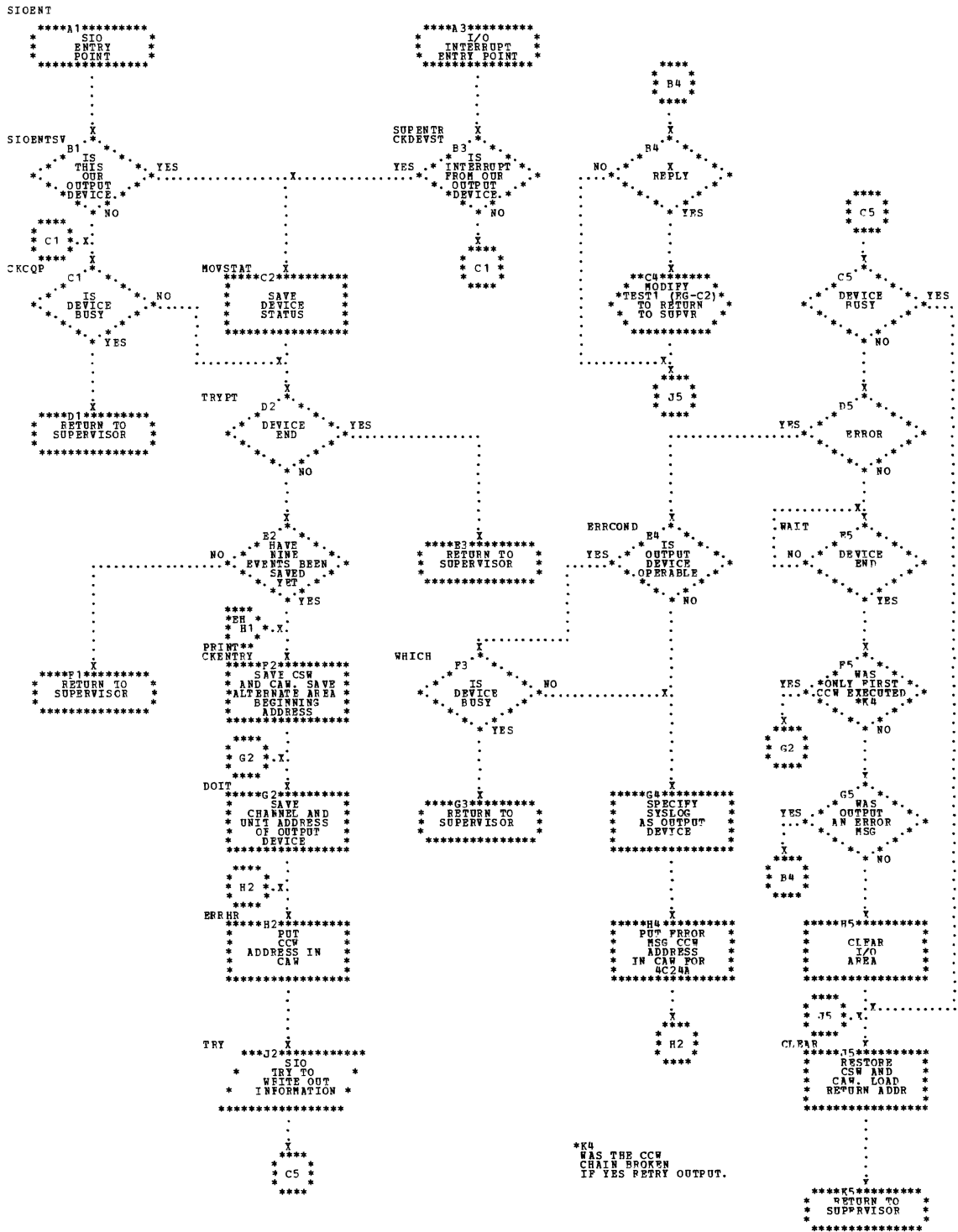




Chart EH. GSVVC Trace: Tape Mode (PDAIDGTT) (Part 3 of 3)

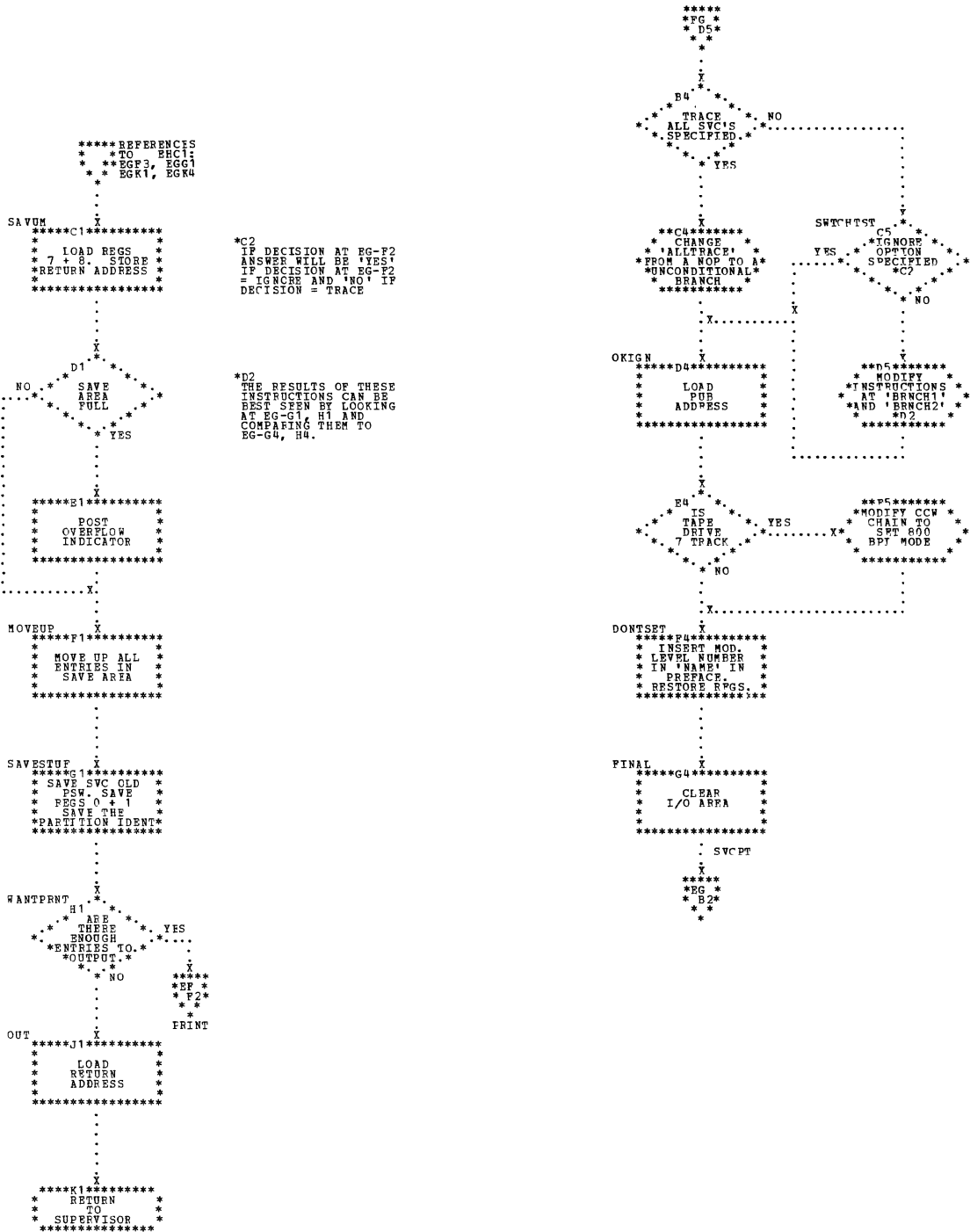


Chart EJ. QTAM Trace: Core-Wrap Mode (PDAIDQIW) (Part 1 of 3)

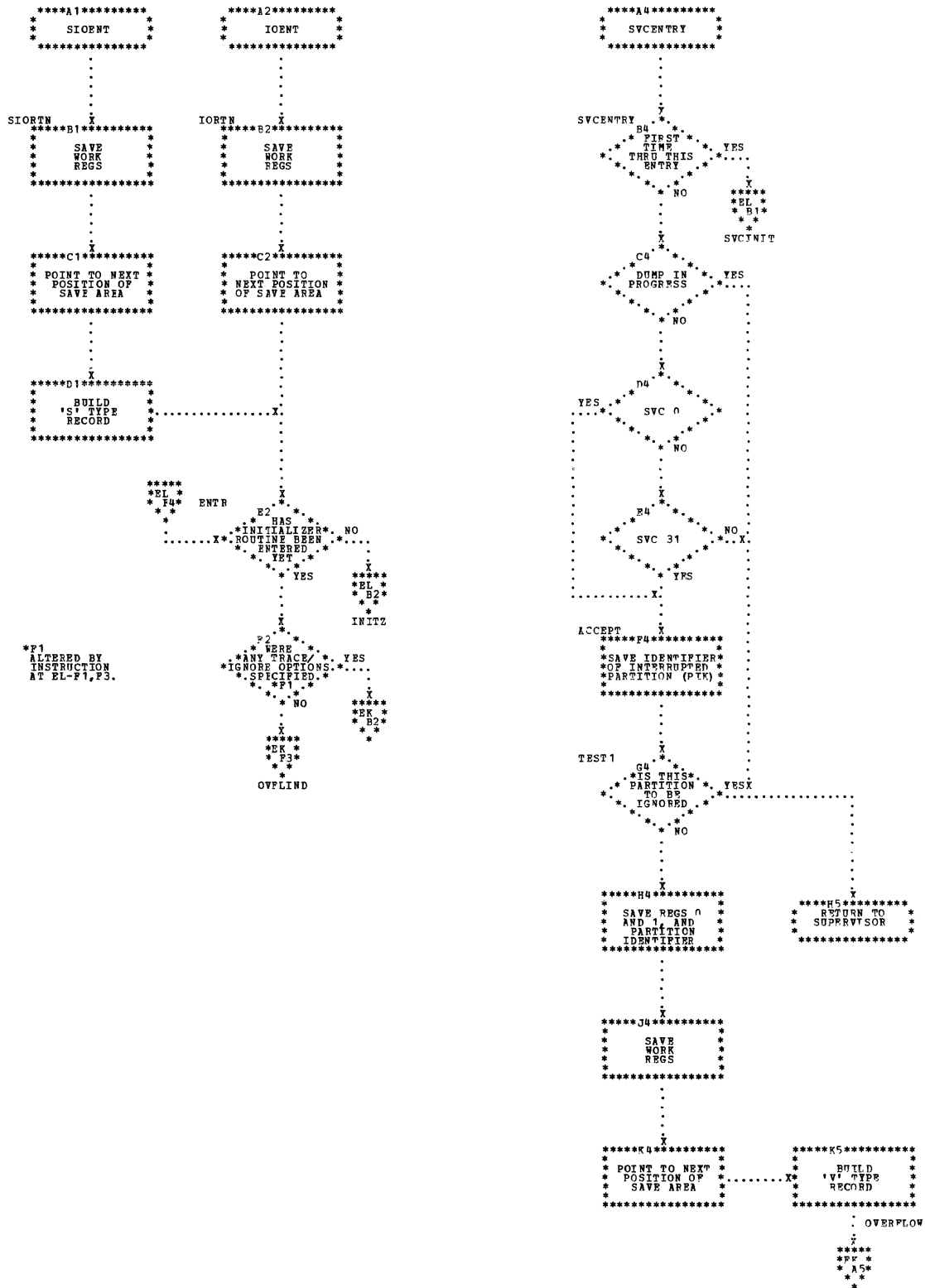
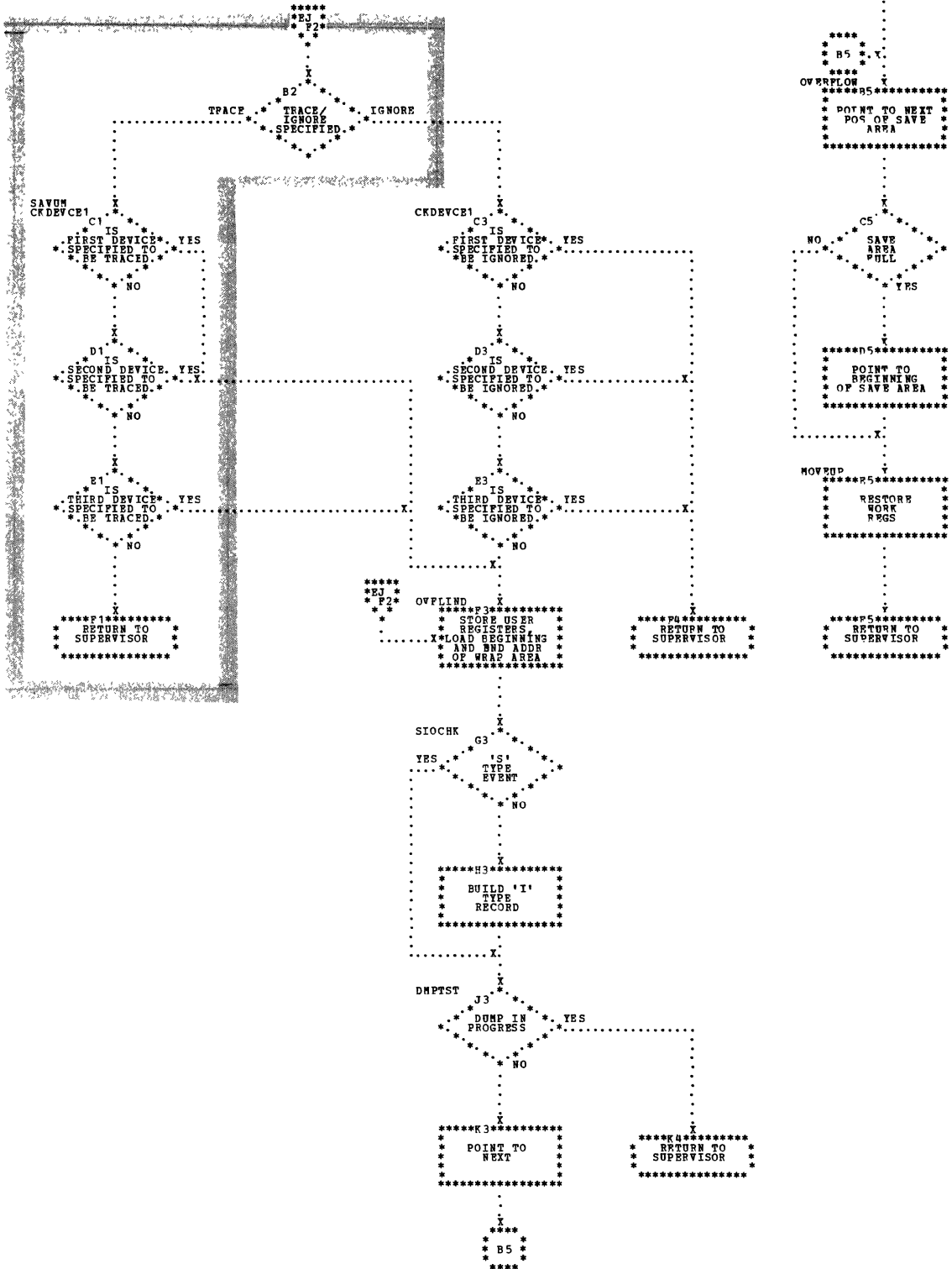


chart EK. QTAM Trace: Core-Wrap Mode (PDAIDQIW) (Part 2 of 3)

\*\*1  
 FRAMED AREA  
 INDICATES CODING  
 THAT HAS BEEN  
 ALTERED BY IMITZ  
 ON CHART EL.







rt EM. QTAM Trace: Tape Output Phase (PDAIDQTT) (Part 1 of 2)

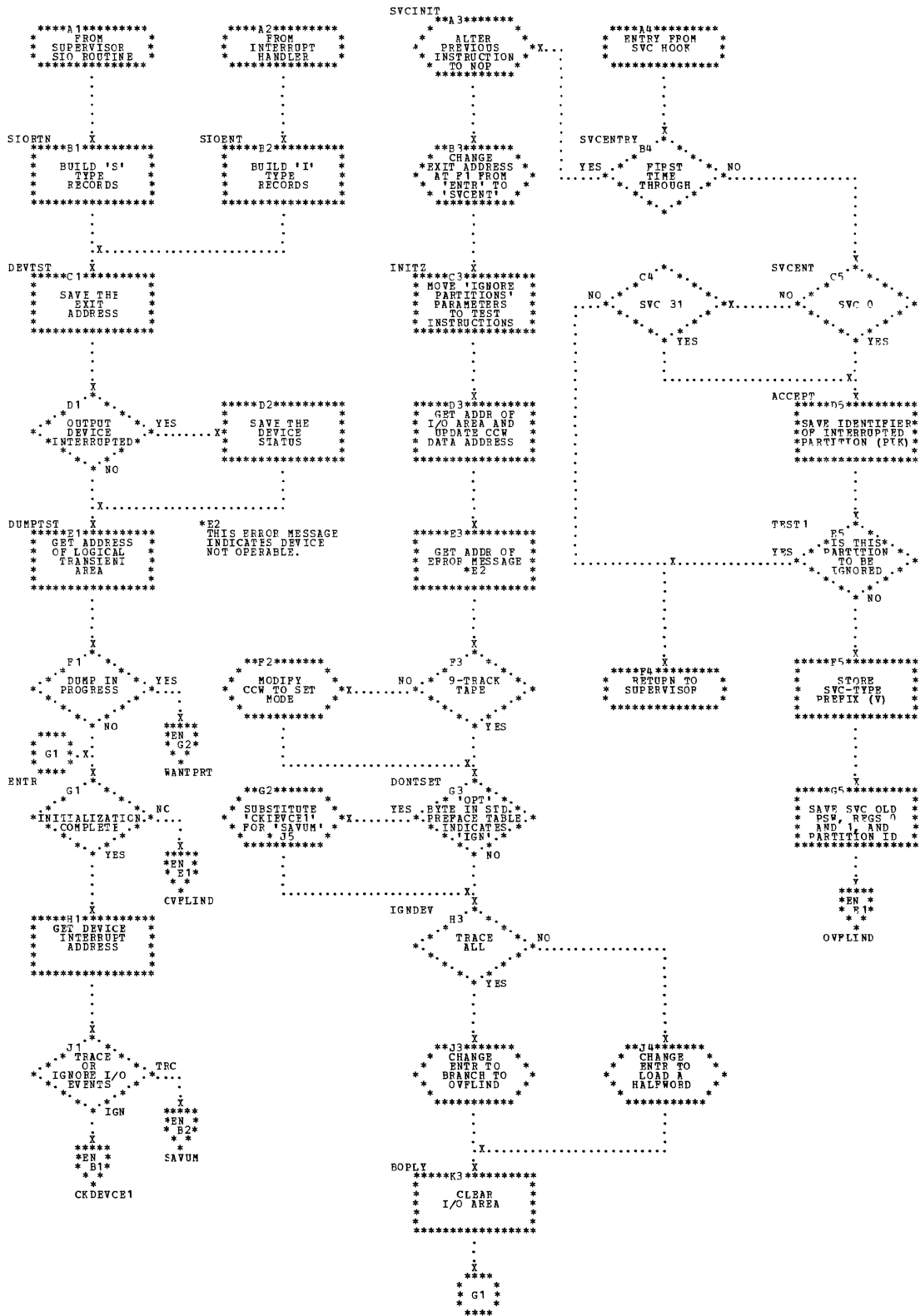


Chart EN. QTAM Trace: Tape Output Phase (PDAIDQTT) (Part 2 of 2)

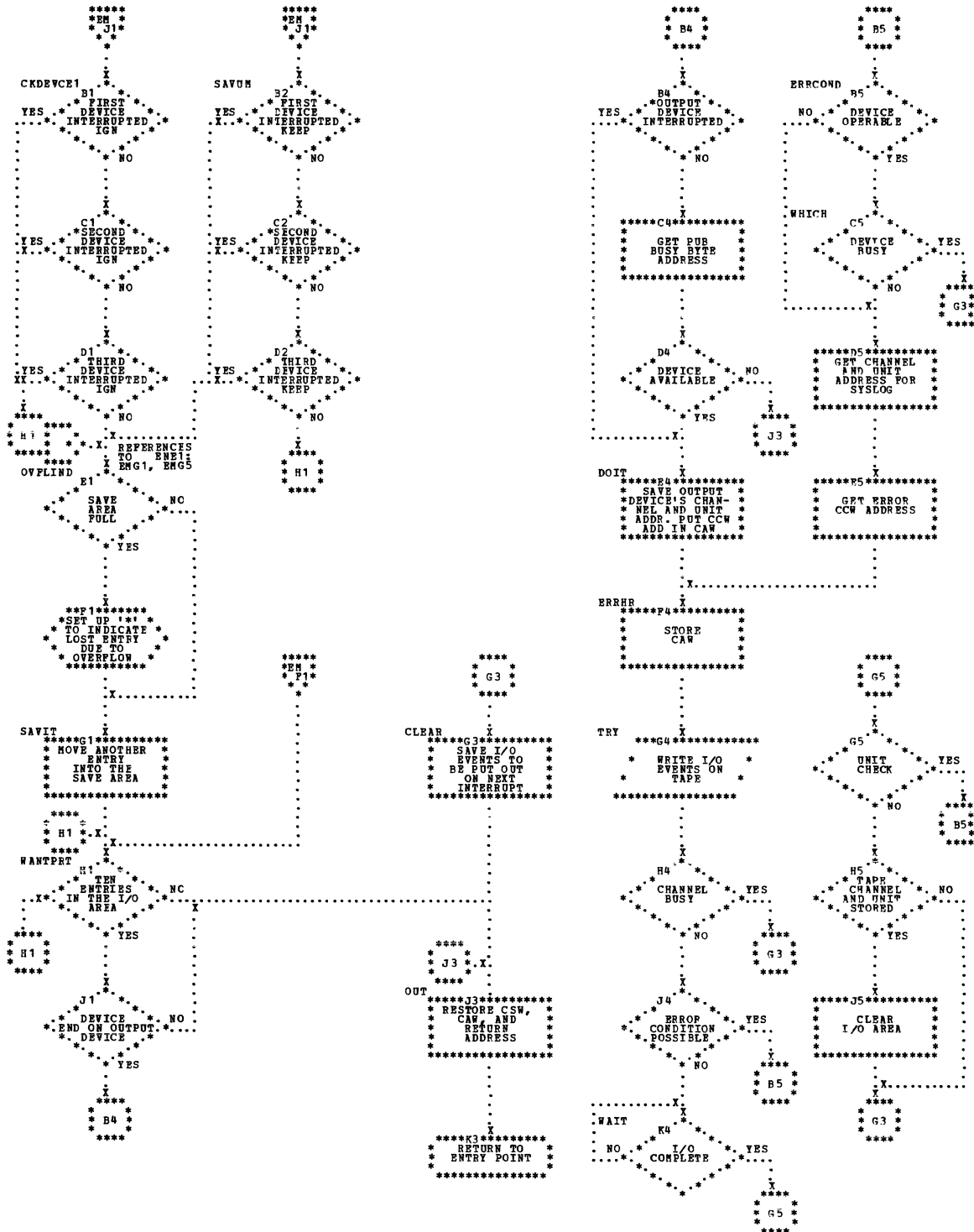


Chart FA. Transient Dump: Tape Mode (PDAIDTDT) (Part 1 of 2)

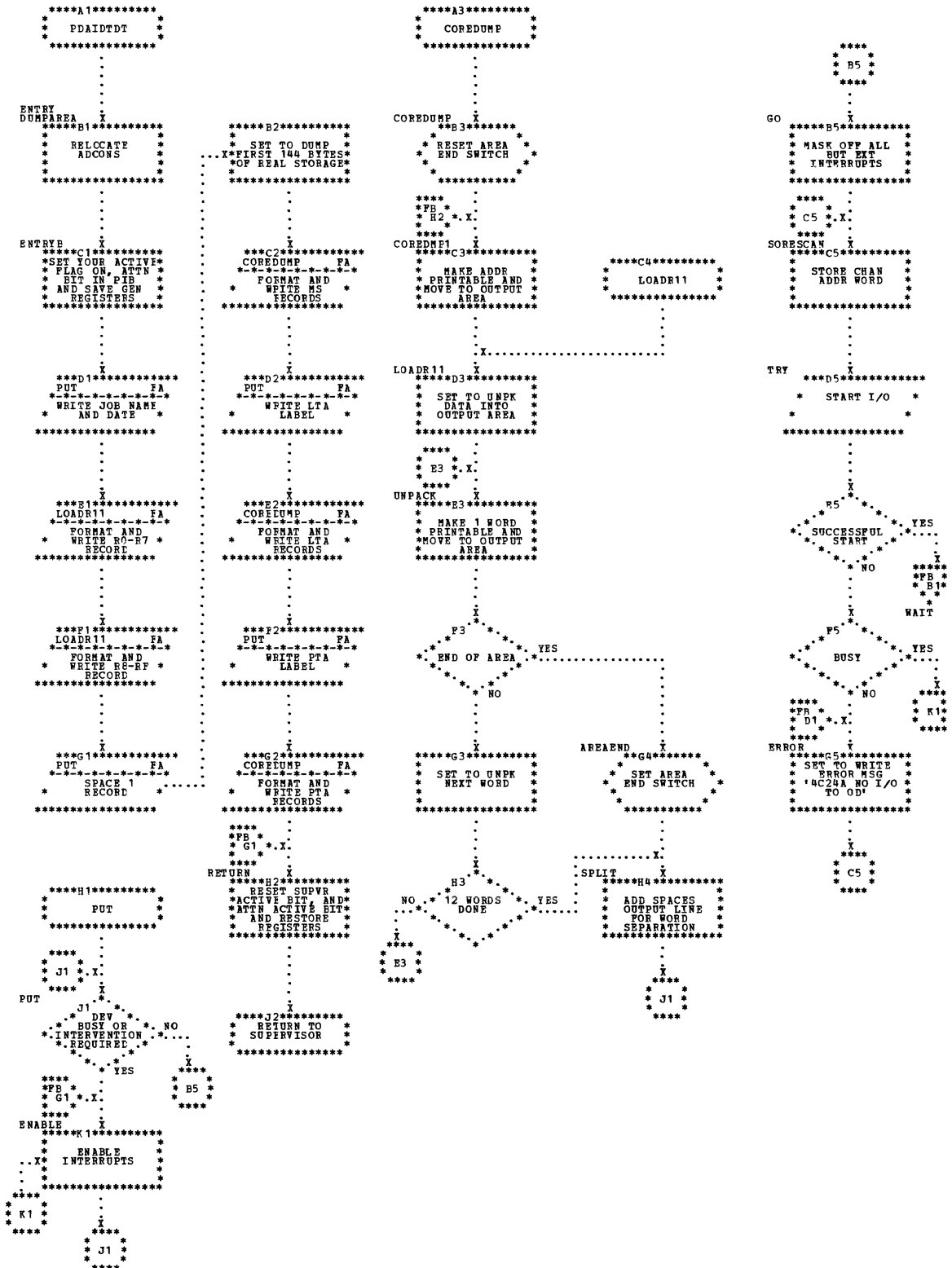


Chart FB. Transient Dump: Tape Mode (PDAIDTDT) (Part 2 of 2)

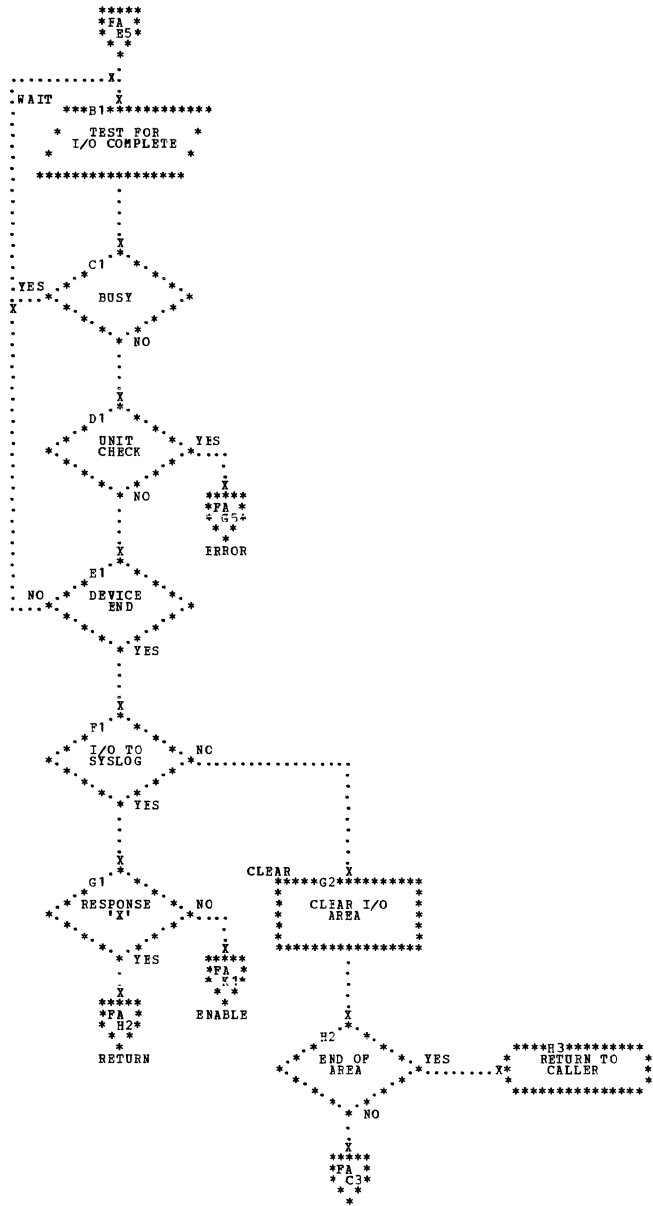


Chart FC. Transient Dump: Printer Mode (PDAIDTDP) (Part 1 of 2)

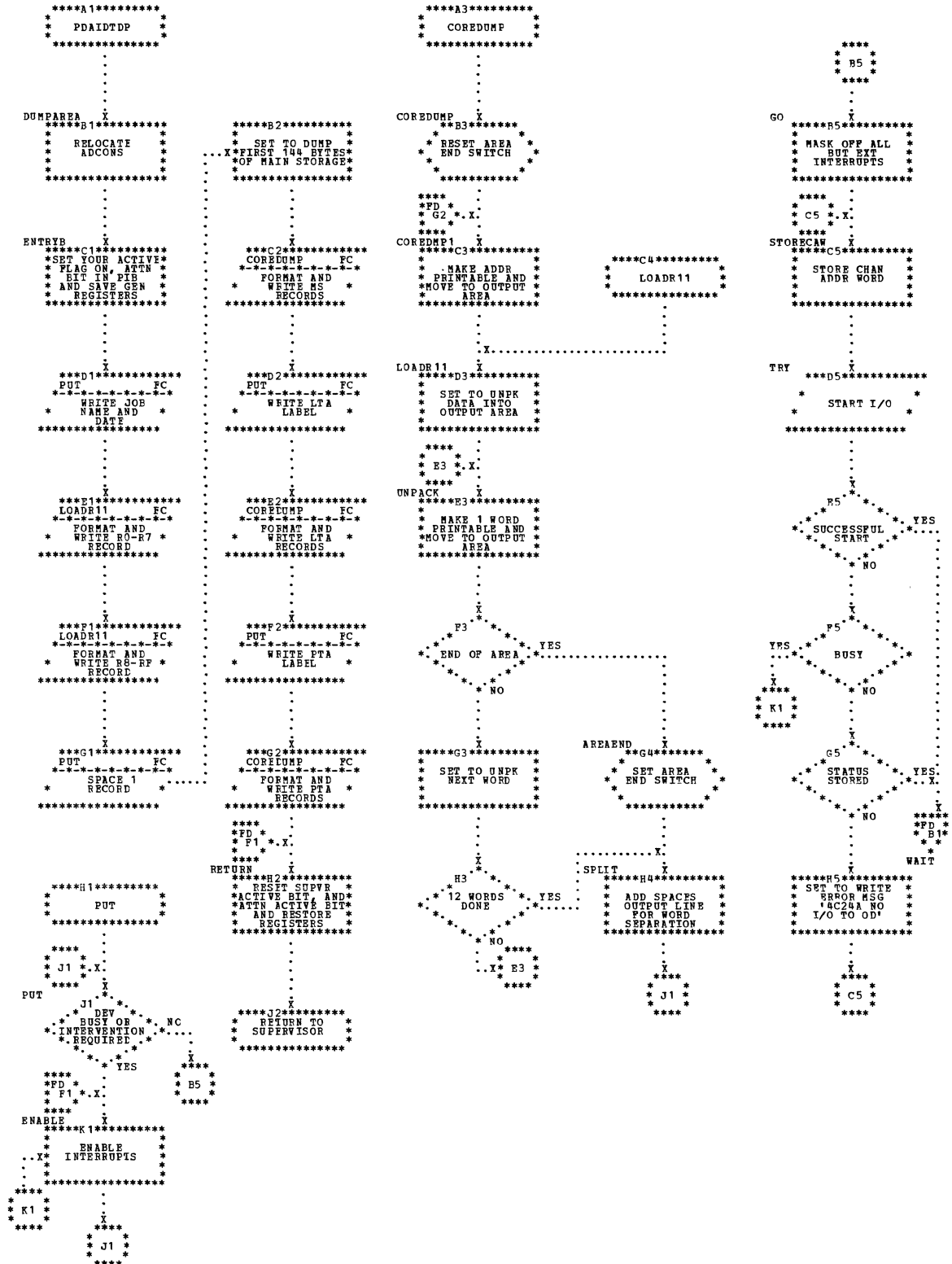
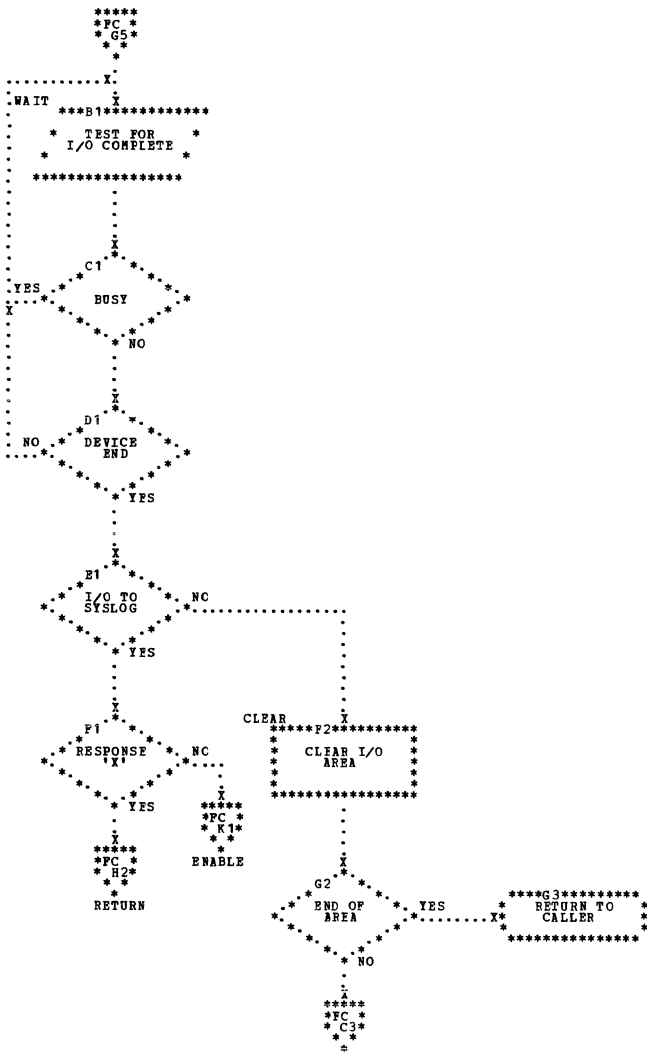


Chart FD. Transient Dump: Printer Mode (PDAIDTDP) (Part 2 of 2)



INTRODUCTION

SDAID provides software debugging facilities:

- For tracing the occurrence of user-selected program or monitor call events
- For dumping parts or the whole real storage at the time one or more of the selected events occurs.

SDAID tracing and dumping facilities are provided by the utilization of the two following System/370 features:

- Program Event Recording (PER)
- Monitor Call Event.

The occurrence of a designated program event or monitor call event is signaled to the program by a program interruption if the CPU is enabled for this condition. Control registers 9, 10, 11 and PSW bit 1 (PER bit) are dedicated to control program events interruptions.

The SDAID program can be executed in any real or virtual partition and is initiated by entering // EXEC SDAID via SYSLOG or SYSRDR. An operator/system dialog follows, beginning with the message

4C55D GIVE SPACE FOR SDAID =

The messages issued by SDAID are listed in Appendix A of this manual.

More information on the usage of the SDAID program can be found in Section 2-B, Part 2 of DOS/VS Serviceability Aids and Debugging Procedures, GC33-5380.

To get control as soon as a program interruption occurs, the tracing and dumping routines must reside permanently in real storage.

SDAID uses the two following DOS/VS inner functions to get the number of page frames from the page pool necessary to accommodate the tracing and dumping routines and to return them to the page pool when SDAID is no longer desired:

- GETREAL     Get real storage
- FREERREAL   Free real storage.

The tracing and dumping routines operate with events occurring both in the DOS/VS supervisor resident or transient area and in the problem program area.

SDAID uses the following DOS/VS inner functions to temporarily fix all pages in which fields must be initialized for subsequent tracing and dumping and to free them after initialization:

- TFIX        Temporary fix a page frame
- TFREE       Free a previously temporarily fixed page frame.

This chapter provides information to familiarize the reader with the external characteristics of the two System/370 features and the four DOS/VS inner functions. It will also facilitate understanding of SDAID internal program logic.

PROGRAM EVENT RECORDING (PER)

The Program Event Recording feature permits the program to be alerted of the following events:

- Successful execution of a branch instruction
- Successful fetching of an instruction between predefined limits of a logical storage area
- Alteration of the contents of storage between predefined limits of a logical storage area
- Alteration of the contents of one or more designated general registers.

The information concerning a program event is provided to the program by means of a program interruption, whose cause is identified in the interruption code.

CONTROL REGISTER ALLOCATION

The information for controlling program event recording resides in control registers 9, 10, and 11 (see Figure 20).

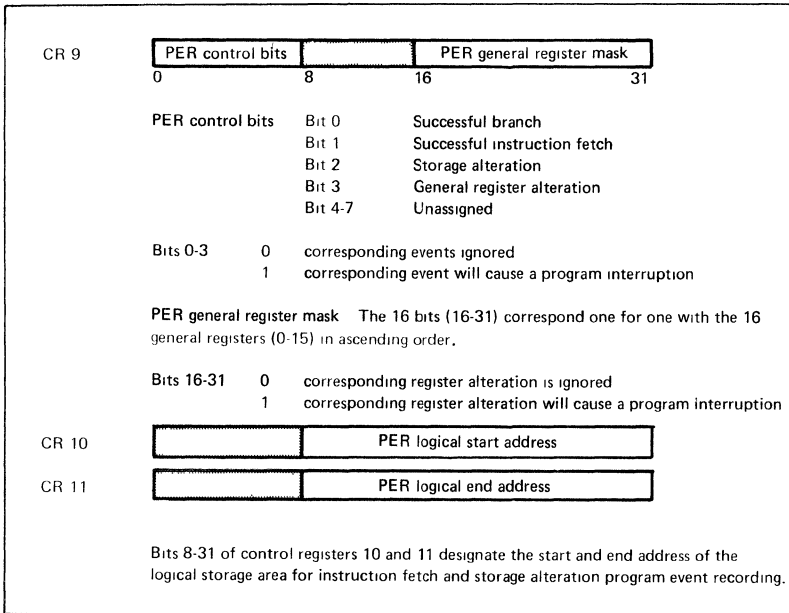


Figure 20. PER Control Information in Control Registers 9, 10, and 11

OPERATION

Operation of program event recording is controlled by PSW bit 1 (PER bit).

PSW Bit 1 = 0 : Program events are ignored

= 1 : Program events are permitted subject to the setting of PER control bits in control register 9.

When a designated program event occurs, with the CPU enabled for this condition, a program interruption will occur. The events causing the interruption are identified by the PER code in the program interruption code extension. When one or more bits in the PER code are on, bit 8 of the interruption code in the program interruption code extension is set to one. That is to say, bit 8 of the interruption code indicates whether or not PER action is required (see Figure 21).

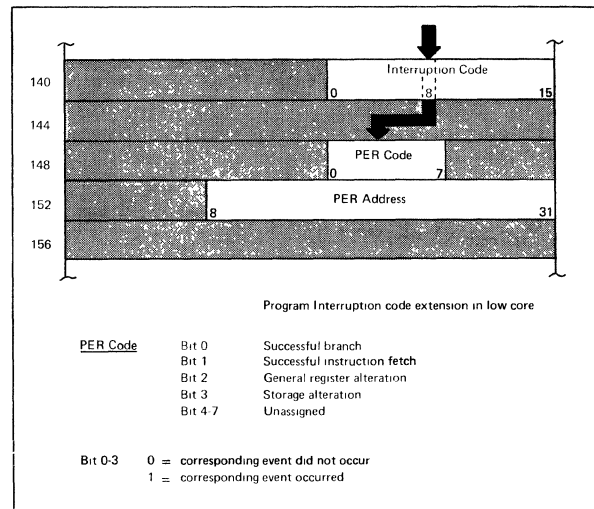


Figure 21. Layout of Low Real Storage Used for Checking Whether or Not a Specific Event Occurred

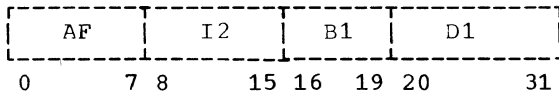
MONITOR CALL EVENT

The location of the instruction associated with the event is identified by the PER address in the program interruption extension in low real storage.

The monitor call event is defined by the execution of a monitor call (MC) instruction, the format of which is:



MC D1 (B1), I2 (SI)

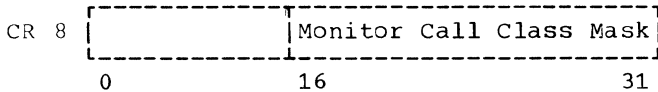


Bits 12-15 of the monitor call instruction identify one of the sixteen possible monitor call classes and are set up from the immediate operand value (I<sub>2</sub>).

The first operand field (B1) + D1 is used to identify the monitor call code for the specified class.

**CONTROL REGISTER ALLOCATION**

The information for controlling monitor call events is contained in control register 8.



Monitor call class mask: The 16 bits (16-31) correspond one for one with the 16 possible monitor call classes (0-15) in ascending order.

- Bits 16-31 = 0 : Execution of a monitor call instruction for the specified class will be treated as a NOP instruction.
- = 1 : Execution of a monitor call instruction for the specified class will cause a program interruption.

**OPERATION**

When a designated monitor call instruction is executed, with the monitor call class mask bit enabled for the specified class, a program interruption will occur.

The monitor call class (I2 field) causing the interruption is identified by the monitor class field in the program interruption code extension.

Bit 9 of the interruption code, in the program interruption code extension, is set to one.

The monitor code ((B1) + D1 field) associated with the instruction is identified in the monitor code field in the program interruption code extension.

The program interruption code extension is shown in Figure 22.

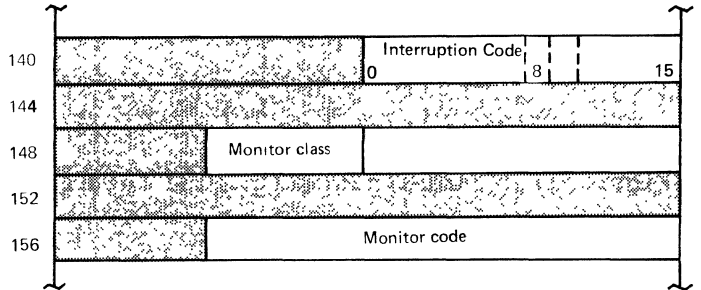


Figure 22. Program Interruption Code Extension

GETREAL - FREERREAL

Access to DOS/VS GETREAL and FREERREAL functions is provided by the two following SVCs:

- SVC 55 Allocates page frames from the main page pool at the end of real storage for the PDAID alternate area or for the SDAID area.
- SVC 54 Gives back to the page pool all the page frames allocated by a preceding SVC 55.

Figure 23 shows the parameters passed and returned by SVC 55 and SVC 54.

TFIX - TFREE

Access to DOS/VS TFIX and TFREE functions is provided by an offset table in low real storage.

- TFIX Attaches a page frame to a specified page and temporarily fixes that page frame so that it can no longer be swapped out.
- TFREE Frees a temporarily fixed page frame.

Figure 24 shows the parameters passed by TFIX and TFREE.

SVC	Macro	Parameter passed		Parameter returned	
		R0	R1	R0	R1
54	--	--	--	--	--
55	--	Number of page frames asked.	--	Number of page frames allocated.	Beginning address of allocated area.  Zero if duplicate request.

Figure 23. Parameters Passed and Returned by SVCs 55 and 54.

Function	SVC	Macro	Parameter passed	Parameter returned
TFIX	-	-	R2 - Any logical address within the page to be fixed	-
TFREE	-	-	R3 - No. of page frame to be freed	-

Figure 24. Parameters Passed by TFIX and TFREE

#### GENERAL DESCRIPTION

The SDAID tracing/dumping facilities are performed by the SDAID event handling routines (phase SDEHR). The SDEHR phase takes control as soon as a program check occurs and operates in supervisor state with a key of zero. For this reason it must permanently reside in real storage as long as the tracing/dumping facilities are desired.

To meet these requirements a number of page frames, which can be specified by the user and cannot be less than three (6K bytes), is taken from the page pool at the end of real storage. These pages are then

assigned to an area called SDAREA in which the SDEHR phase resides along with a core wrap recording buffer (SDEUF).

The set of SDAID routines can be divided into three parts distinguished by the time of their execution (see Figure 25).

- Initialization
- Operation
- Termination.

This section gives a general description of these three parts. In addition, it describes the inter-phase communication and the layout of the SD area in the supervisor area.

Part of SDAID	Phase	Module	CSECT	State	Key	Execution Time	Execution Area	Function
INITIALIZATION	SDAID	IJBSDAD1	SDAID1 PRTMOD CSLMOD	P	Partition	Initialization	Partition	Get real storage for SDAREA LIOCS for SYSST LIOCS for SYSLOG
	SDPAR	IJBSDPAR	SDPAR	P	Partition	Initialization	Partition	User option acceptance routine
	SDAID2	IJBSDAD1 IJBSDAD2	SDAID2 SDEHR	P	Partition	Initialization	Partition	Print out selected options  This phase will be moved to SDAREA
	\$\$BSDAID			P/SV	0	Initialization	L T A	Move SDEHR to SDAREA. Activate PSWs and Control Registers to perform selected options
OPERATION	SDEHR	IJBSDAD2	SDEHR	SV	0	Operation	SDAREA	Analyze program check and perform tracing/dumping
TERMINATION	ENDSD	IJBENDSD	ENDSD	P	Partition	Termination	Partition	Call \$\$BATTN3
	\$\$BATTN3			P/SV	0	Termination	L T A	Printout SDBUF Return SDAREA to page pool Deactivate PSWs and control registers

Figure 25. Summary of SDAID Program

#### COMMUNICATION BETWEEN PHASES

Communication between phases takes place by means of a communication region referred to as parameter list (MONPARM). This parameter list is divided into two sections. The first section (see Figure 26) contains parameters to control the

desired tracing and dumping facilities of the operation phase SDEHR. It is transferred to SDAREA at the end of SDEHR.

The second section (see Figure 27) contains parameters to control the initialization process of the operation phase SDEHR.

MONWRAP	Start address of core wrap buffer		(SDBUF)	
MONWRAP	End address of core wrap buffer		(SDBUF)	
MONWPNT	Next available entry in core wrap buffer		(SDBUF)	
MONPRTR	Printer address	Ocuu	Reserved	
MONTE	(1)	Reserved	MONCHK	(1) Reserved
MONENQ	(1)	Reserved	MONHDL	(1) Reserved
MONHALT	(2)	Reserved	NONDD	(3) Reserved
MONOUTCL	Output class		Reserved	
MONDSTRT	Reserved			
MONDEND	Reserved			

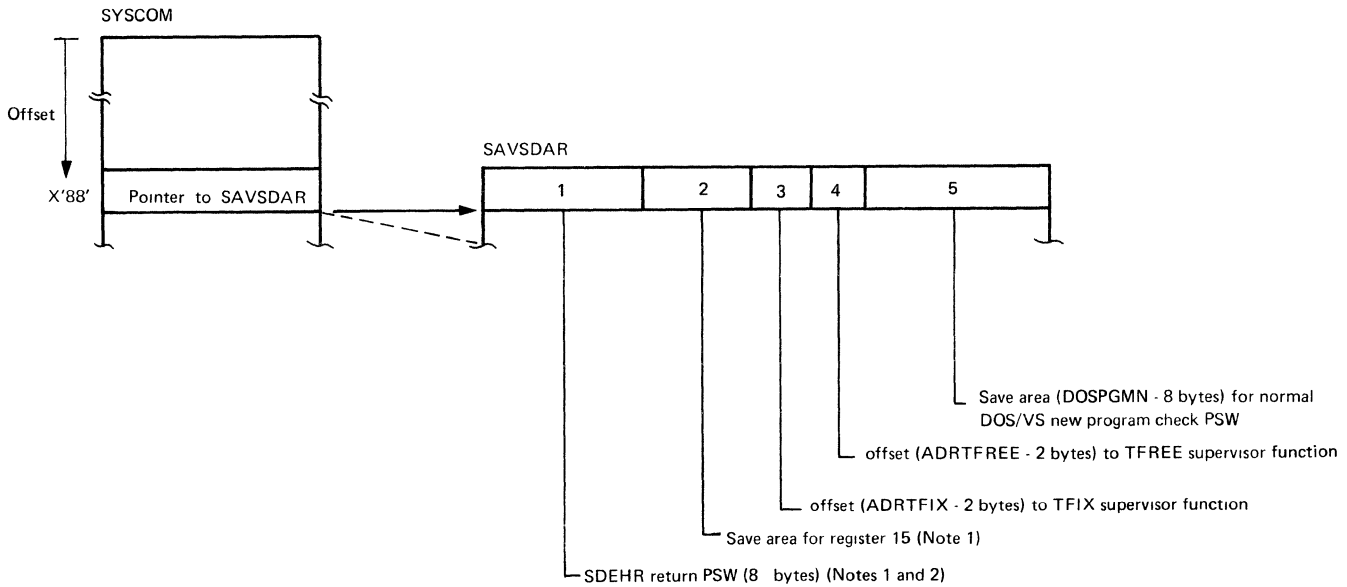
  

(1)	MONTE	Translation exception event switch (Interruption code X'11')
	MONCHK	Program check event switch (interruption code X'01' to X'12' except X'11')
	MONENQ	Page fault enqueue event switch (Monitor Call class 5, code X'000010')
	MONHDL	Page fault handling event switch (Monitor Call class 5, code X'000011')
		Switch X'00'            Event is ignored X'FF'                Event is enabled - recording takes place
(2)	MONHALT	Stop on event switch
		Switch X'00'            Resume processing after event recording X'FF'                Enter wait state after event recording
(3)	NONDD	Non-destroying dump switch. This switch is only tested if MONHALT switch ON. It is set by the operator when system enters wait state and is reset to X'00' by the program after real storage dump is ended.
		Switch X'00'            No real storage dump X'FF'                Dump whole real storage

Figure 26. Section 1 of Parameter List (MCNPARM)

MONCR08	Control Register 8		
MONCR09	Control Register 9		
MONCR10	Control Register 10		
MONCR11	Control Register 11		
MONREAL	Start address of SDAREA		
MONRELAD	Start address of SDEHR CSECT in phase SDAID2		
MONDIFF	Length of SDEHR CSECT	Reserved	
MONCPU	CPU identification		
MONDUB	Address in SAVSDAR of return PSW	MONDUBX address in SAVSDAR for R15 Save	
	Reserved	MONMPEX X'40'	Reserved
MONLDUB	Addresses in SDEHR CSECT where to move the address of the return PSW from MONDUB field (See SDAID area in supervisor)		
MONLDUBX	Addresses in SDEHR CSECT where to move the address of the save area for R15 from MONDUBX field. (See SDAID area in supervisor)		
NPSDOSAD	Address in SDEHR where to move address of DOSPGMN (SAVSDAR field 5)		
MONFIX	Start address of 1st Section of Parameter list		
	End address of 1st Section of Parameter list		
	Start address of SDEHR CSECT		
	End address of SDEHR CSECT		
	Reserved		

Figure 27. Section 2 of Parameter List (MONPARM)



Note 1: Fields 1 and 2 may be inverted (if SAVSDAR is not aligned on a doubleword boundary) to allow PSW load to return from SDEHR routines.

Note 2: If there is an interruption due to a monitor call for a class different from 0, 1, and 2, or due to a PER event without another program check interruption, this field contains the old program check PSW at interruption time. Otherwise the contents of field 5 are moved into this field.

Figure 28. Layout and Contents of SAVSDAR

#### SDAID AREA IN SUPERVISOR

An area (SAVSDAR) is provided in the supervisor area for SDAID (see Figure 28). It is mainly used for saving, but contains also a table that gives the offsets to TFIX and TFREE supervisor functions.

#### INITIALIZATION ROUTINES

A real partition of at least 12K or any virtual partition is needed to initialize SDAREA.

The following functions are performed by the three initialization phases and the initialization transient (see also Figure 29):

SDAID Get page frames from the page pool and allocate them to SDAREA.

SDPAR Initialize an operator-to-system communication to set up the operator-selected tracing and dumping options in the parameter list (MONPARM).

SDAID2 Print on SYSLST the selected or default tracing/dumping options and give control to the initialization transient. Upon return from the transient, post the successful initialization and release the partition.

\$\$ESDAID Initialize the event handling routines (SDEHR) and move them to the SDAREA. Initialize PSWs and control registers to perform selected tracing/dumping options.

The storage layout during initialization is shown in Figure 30.

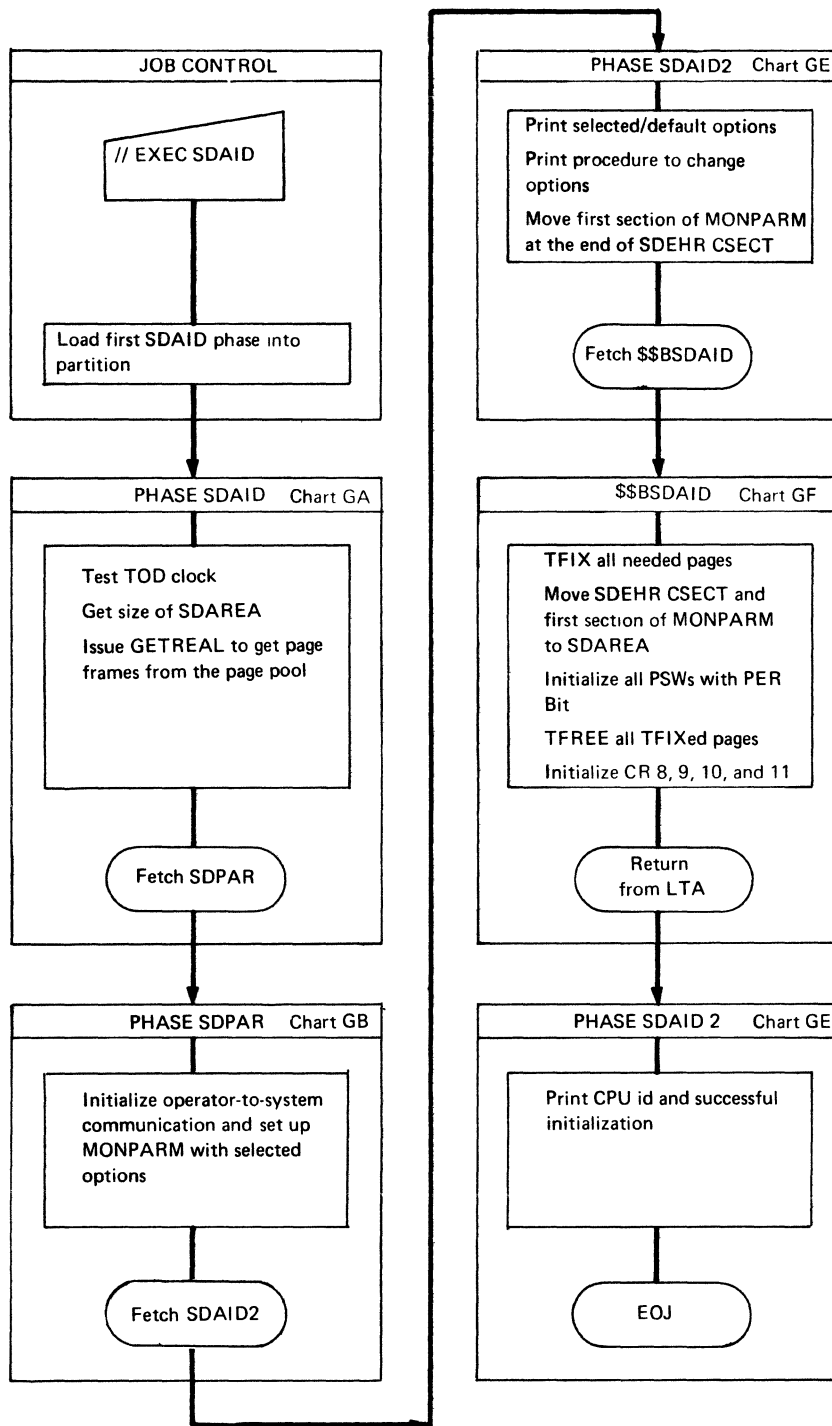


Figure 29. General Logic Flow of SDAID Initialization Routines

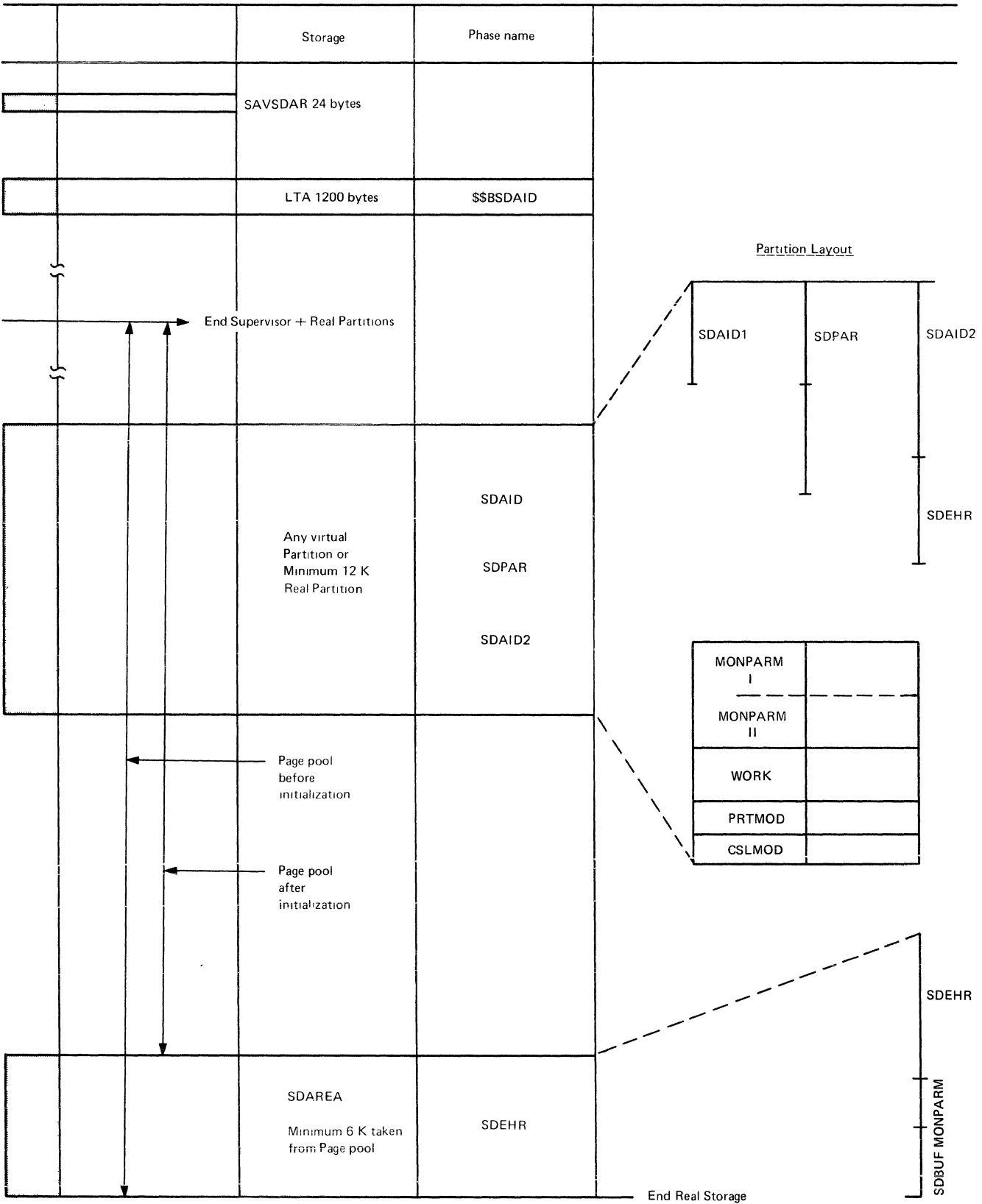


Figure 30. Storage Layout During Initialization



PHASE SDAID

Figure 31 is a tabular summary of the logic flow of phase SDAID (compare Chart GA).

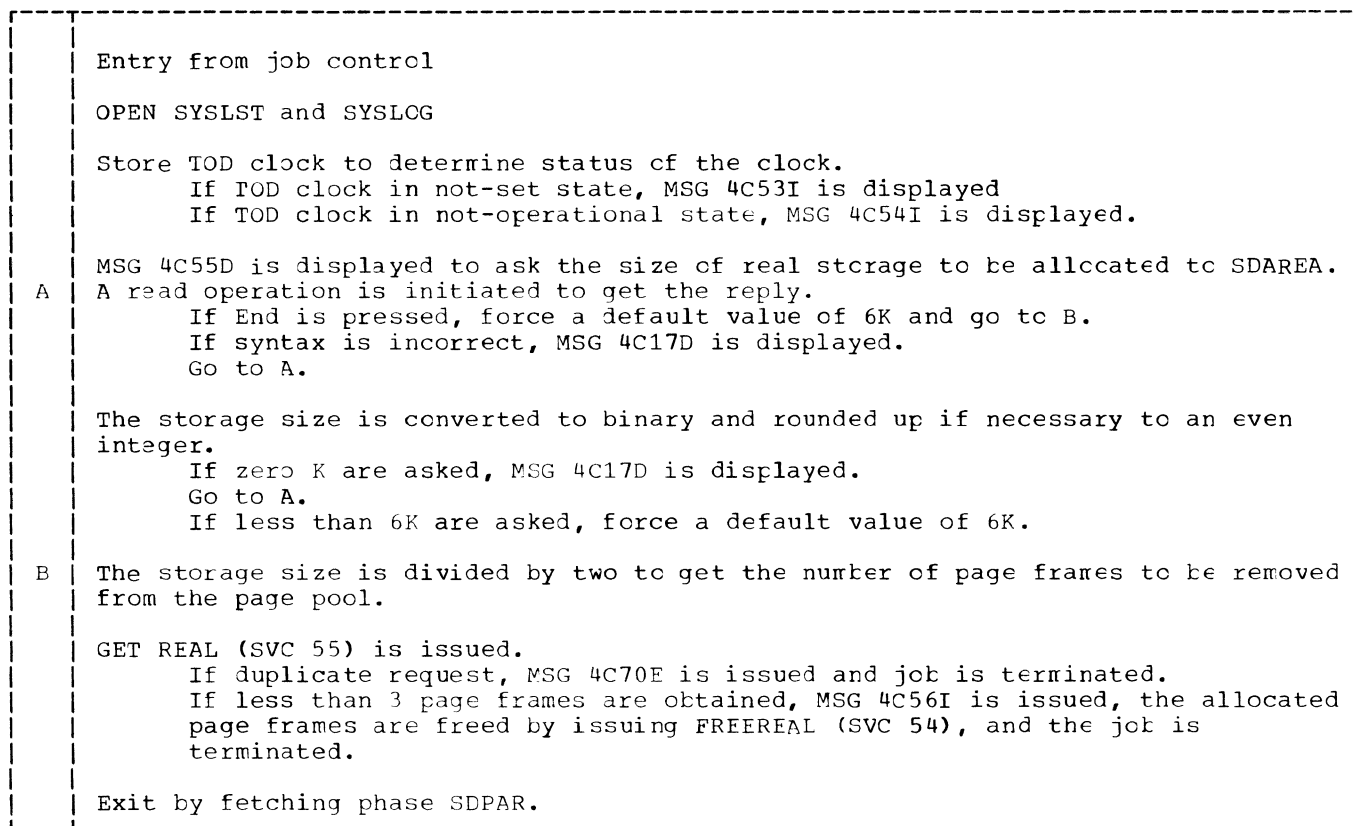


Figure 31. Logic Flow of Phase SDAID

PHASE SDPAR

Processing of SDPAR routines is under control of the parameter description table included in this phase.

For each option, which the operator can select to set up the designed tracing and dumping facilities, the parameter description table contains one keyword entry.

For each keyword displayed the operator can either choose the default values for this option by pressing the END key or alter the default values by entering, depending on the keyword, one or more parameters separated by commas.

For each parameter entered by the operator for a specified keyword, the parameter description table contains, depending on the parameter, a subset of one or more alternatives.

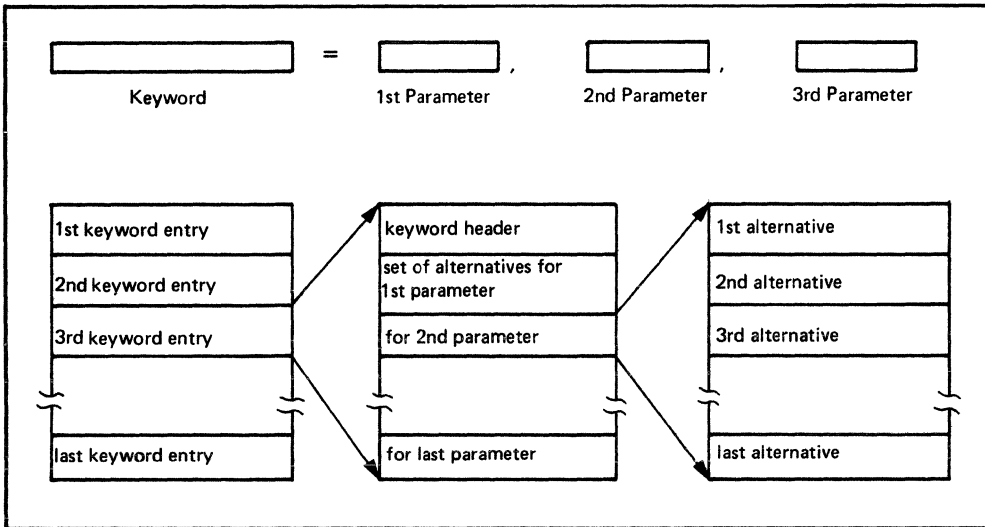


Figure 32. Logical Structure of the Parameter Description Table

The parameter description table thus consists of several keyword entries. Each keyword entry is composed of one header field followed by a set of one or more alternatives fields. The set of alternatives fields is divided into as many subsets of alternatives as there are

possible parameters in the reply for the specified keyword. This logical structure of the parameter description table is shown in Figure 32. The physical structure of this table is shown and explained in Figure 33.

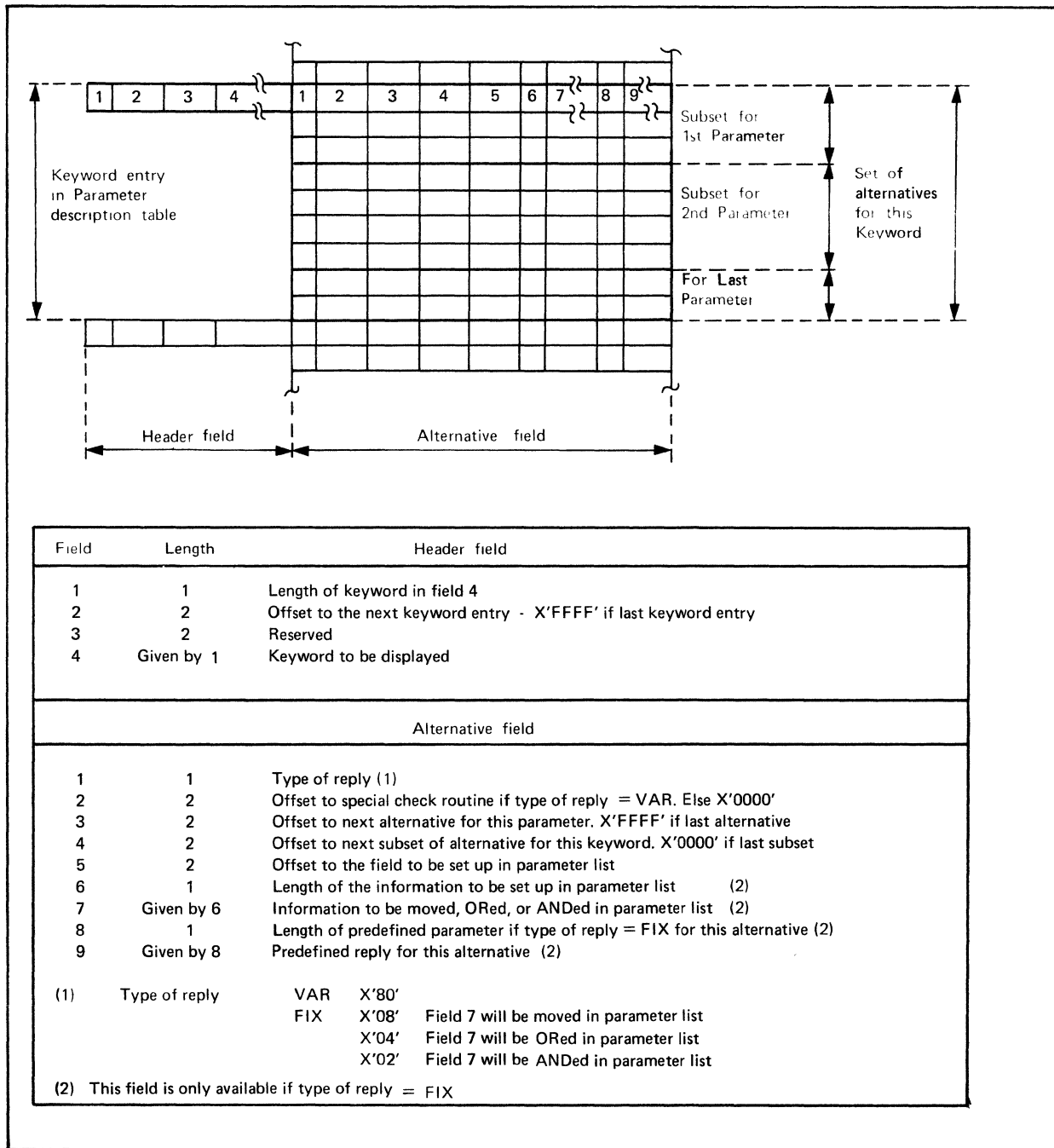


Figure 33. Physical Structure of the Parameter Description Table

Figure 34 is a tabular summary of the logic flow of phase SDPAR (compare flowcharts GB-GD).

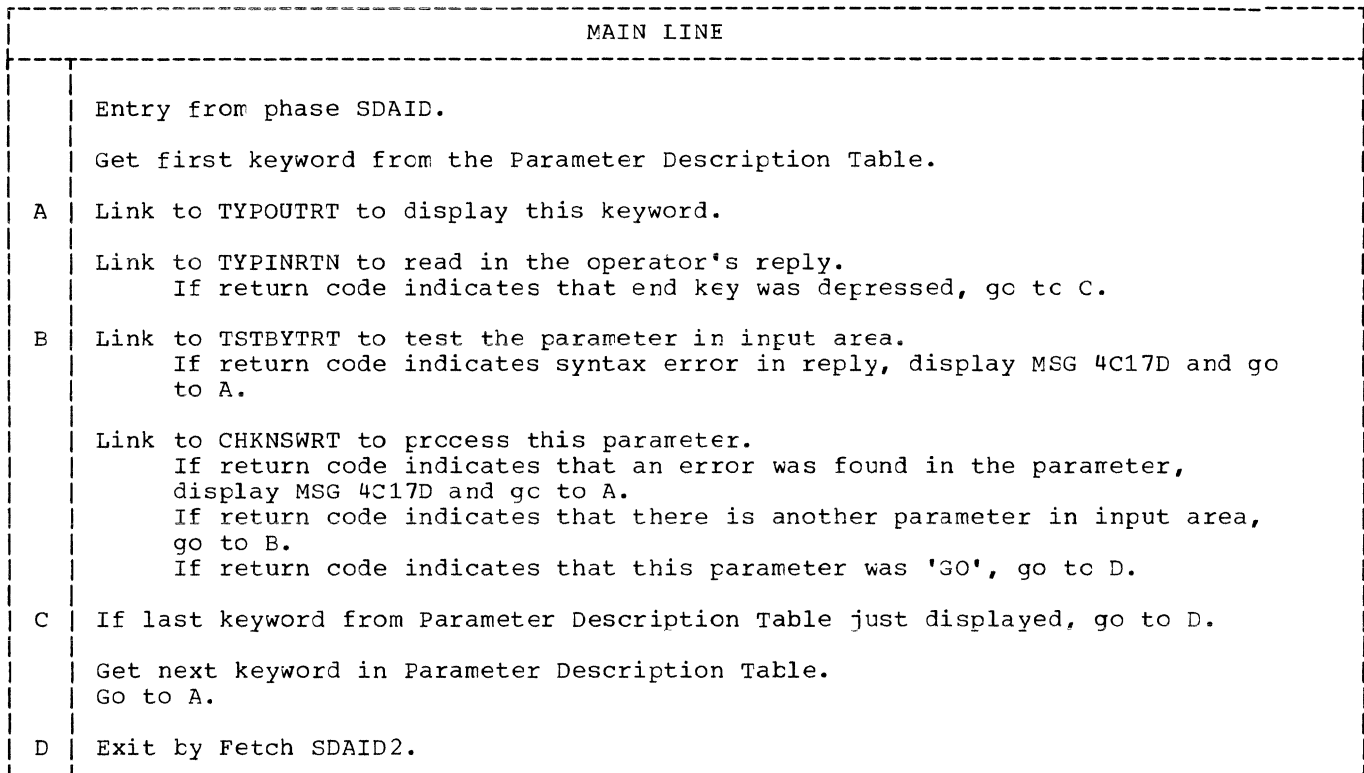


Figure 34. Logic Flow of Phase SDPAR (Part 1 of 3)

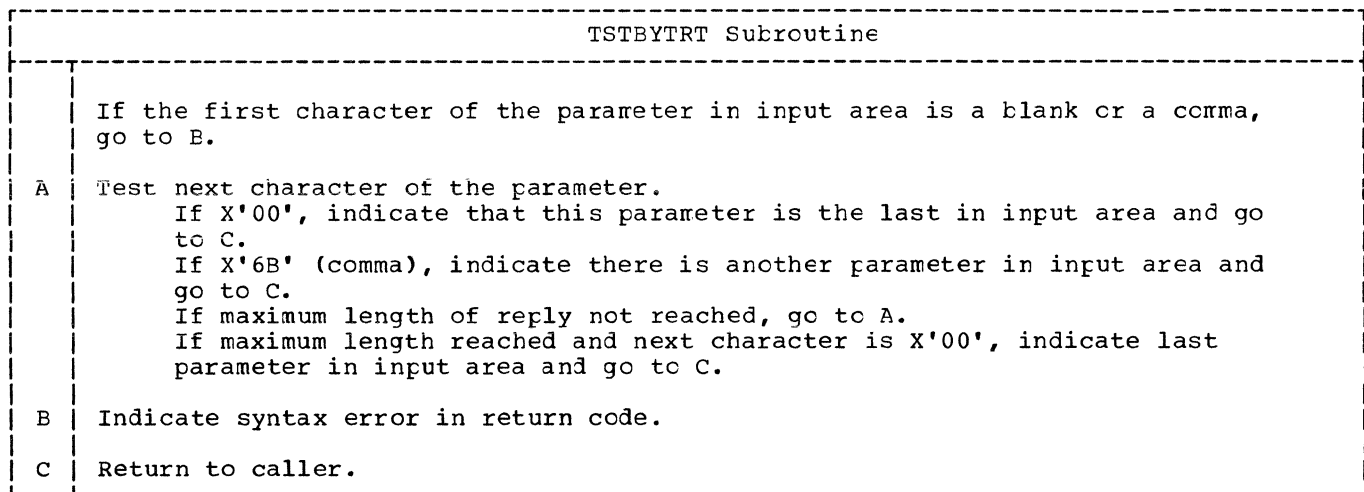


Figure 34. Logic Flow of Phase SDPAR (Part 2 of 3)

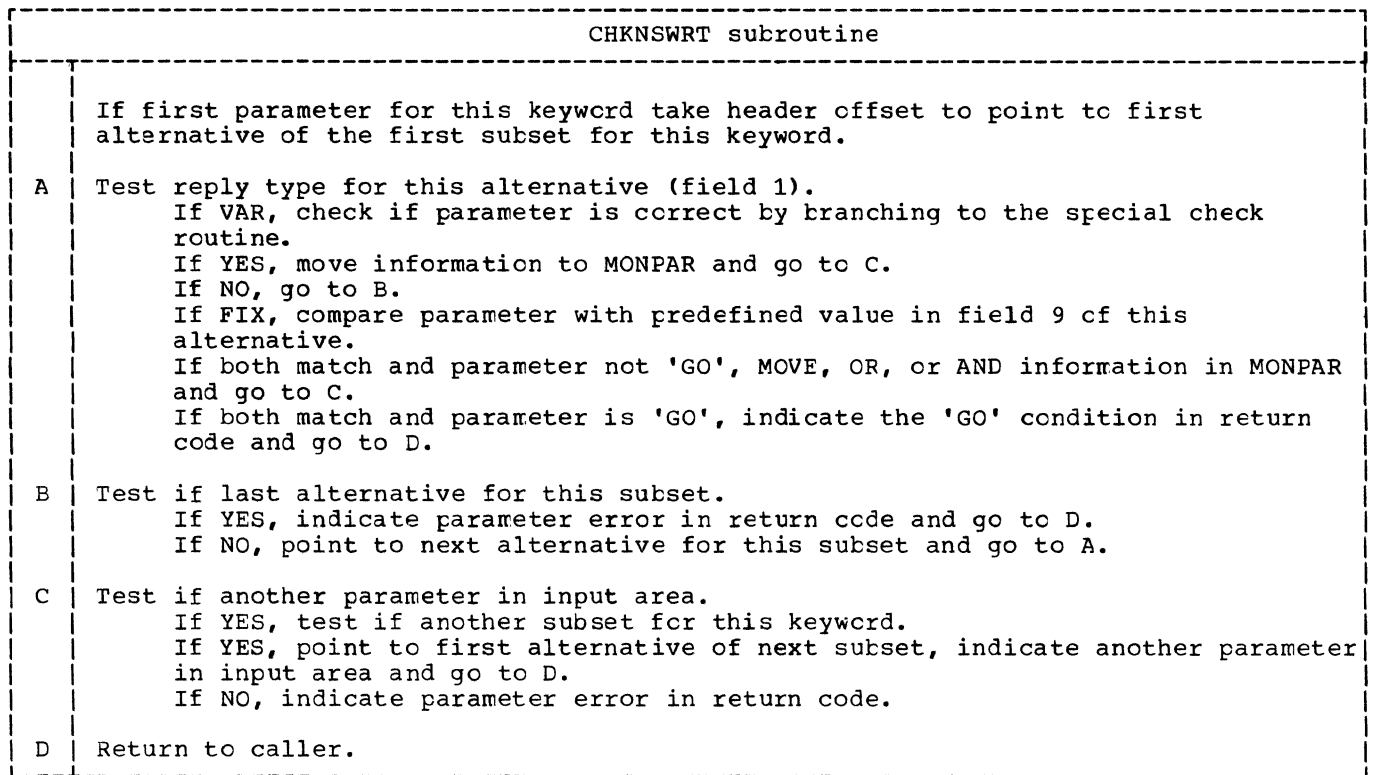


Figure 34. Logic Flow of Phase SDPAR (Part 3 of 3)

Figure 35 is a tabular summary of the logic flow of phase SDAID2 (compare flowchart GE).

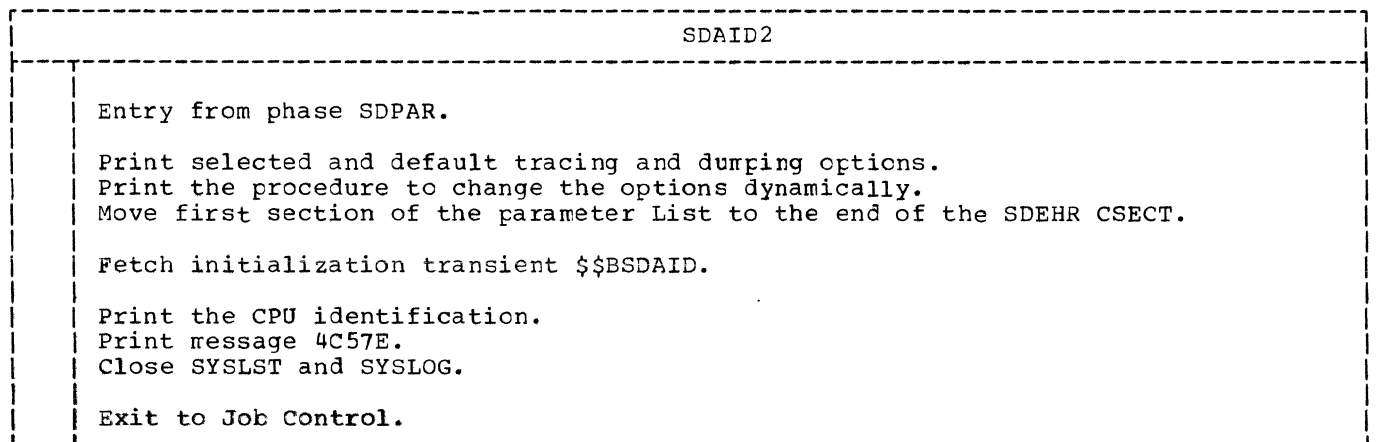


Figure 35. Logic Flow of Phase SDAID2

## TRANSIENT \$\$BSDAID

Processing of \$\$BSDAID is under control of the three tables described below. The first two tables are built by \$\$BSDAID, the last one is partially built by \$\$BSDAID.

- Control List Table

This table has 15 entries. Each entry corresponds one to one to the PIB of the 15 possibly running tasks or subtasks. Each entry contains the address of the corresponding PIB if the task is active, or zero if it is inactive. This table is used to build the other two tables.

- Fix List Table

This table has 21 entries. Each entry contains, after initialization, the logical address within a page to be temporarily fixed.

If SDAID2 runs in a virtual partition, the first five entries are used to accommodate the five addresses from the parameter list field MONFIX (see Figure 27). The sixth entry contains the address of the PSW in the partition save area. If SDAID2 runs in real mode, the first six entries of the Fix List table remain zero.

Each of the 15 remaining entries contains the address of the PSW in the save area of each task or subtask when they run in virtual mode, or zero if they run in real mode. The entry that corresponds to the SDAID2 partition is always zero.

- PER List Table

This table has 56 entries. Each entry contains, after initialization, the address of a PSW in which the PER bit must be set to 1.

The first 16 entries overlap with the last 16 entries of the Fix List table. Entry 17 contains the address of the PSW in the ITA save area. If SDAID2 runs in real mode, entry 18 contains the address of the PSW in the partition save area.

Each of the next 15 entries (entries 34 through 48) contains the address of the PSW in the system save area of each task or subtask if active.

Entry 49 contains the address in SAVSDAR where the normal DOS/VS new program PSW has been saved.

The last seven entries contain the addresses of all DOS/VS new and old PSWs in low real storage in which the PER bit must be set.

Figure 36 is a tabular summary of the logic flow of the transient \$\$B\$DAID (compare flowchart GF)

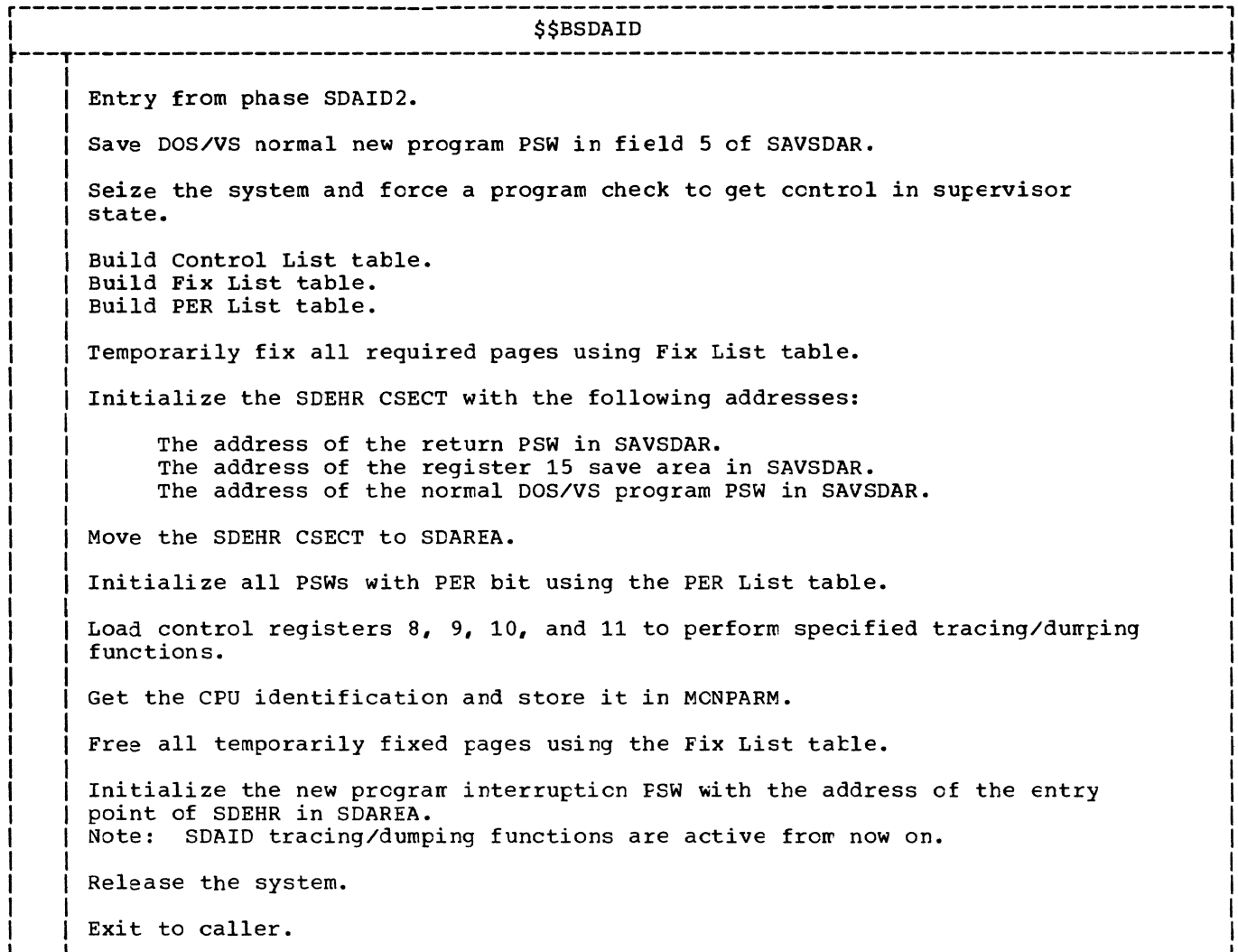


Figure 36. Logic Flow of Transient \$\$B\$DAID

## EVENT HANDLING ROUTINES

The event handling routines (phase SDEHR) perform all the tracing and dumping functions of the SDAID program. They reside permanently in SDAREA where they are moved by the initialization routines. Phase SDEHR gets control as soon as a program interruption occurs and operates in supervisor state with a key of zero and disabled for I/O interrupts, external interrupts, and program check interrupts due to PER events.

Two ways of recording may be selected:

- Core mode (output class 00)  
The recording of all specified events takes place in a core wrap buffer (SDBUF). The number of events which can be kept in this buffer depends on the size of storage allocated to SDAREA during initialization. If the output mode is automatic, the buffer is printed out on the specified printer as soon as it is full. Otherwise the buffer is overlaid by the newest one.
- Print mode (output classes 01-09)  
All specified events are recorded on the

specified printer at the time the event occurs. The amount of information given to the user depends on the output class selected.

To perform the I/O operations an integrated printer IOCS takes control of the specified printer so that it is possible to trace events along with customer's output on the same printer.

Phase SDEHR uses (1) the 24-byte SAVSDAR in the supervisor and (2) a minimum of 6K at the high end of real storage. The latter area (called SDAREA) is organized as follows:

1. The phase SDEHR
2. The parameter list MCNPARM (see section 2)
3. The core-wrap buffer SDBUF

The set of event handling routines can be divided into the five groups shown in Figure 37. The logic flow from groups of routines and from individual routines to one another is illustrated in Figure 38.

SDEHR Event Handling Routines		
Group	Name	Function
Event Handler Routines	DISPATCHER PEREVENT MCEVENT DATEVENT PGMEVENT STOPONEV	Analyze the interruption code and transfer control to the appropriate routines Process PER events Process MC events, page handling and page enqueue Process page translation exception event (code X'11') Process program check events (codes X'01' to X'12' except X'11') Receive control if Stop-on-event switch (MONHALT) is on
Recording Routines	FASTRECX PRINTHD PRINTHDMC PRINTIT PRINTBUF	Record the event in the core wrap buffer (SDBUF) Print heading information Print heading information for page enqueue and handling Print output as specified by output class option Print the core wrap buffer (SDBUF)
Dumping and Editing Routines	DUMPSUB SPECDUMP EDITXBYT EDITREGX TRANSTD INSTxxx	Edit and print storage Edit and print storage Translate one or more bytes from HEX to EBCDIC Translate one or more groups of four bytes from HEX to EBCDIC Translate TOD clock Translate six bytes instruction from HEX to EBCDIC
Printer IOCS Routines	MONOPEN MONPUT MONCLOSE CSWxxxx NOTOPO ERRSIO	Take control of the specified printer Issue SIO and test condition code Return printer to DOS/VS Analyze the CSW Process not operational status Issue sense SIO or SIO in case of an error condition
Miscellaneous Routines	TESTLIM SAVEXT RESTEXT BLANKRTN	Test whether event between logical storage address limits Save previous external conditions Restore previous external conditions Get a work area

Figure 37. Summary of Routines Contained in SDEHR



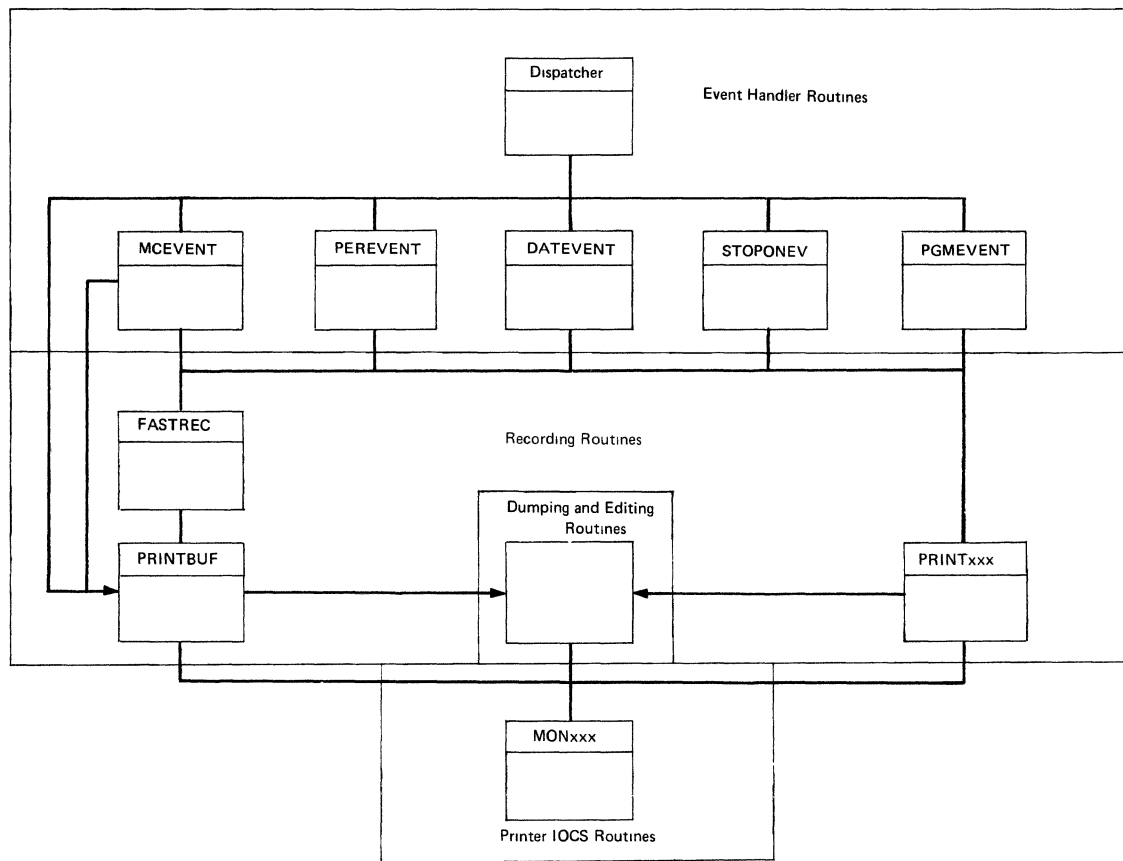


Figure 38. Flow of Logic Between Routines and Groups of Routines

CORE WRAP BUFFER (SDBUF)

OPERATION

When output class equals FASTREC, the recording of the specified events takes place in the core wrap buffer (SDBUF). The size of this buffer depends on the amount of storage allocated to SDAREA during initialization. Each entry in SDBUF is 64 bytes long. The information stored by the recording routine is shown in Figure 39.

At initialization time, MONWRAP in the parameter list contains the SDBUF start address, MCNEWRAP contains the SDBUF end address, and MONWPNT points to the first entry in SDBUF.

Each time recording is performed, MONWPNT is updated with the address of the next entry. When the last entry is filled, the SDBUF is printed out if recording mode automatic is specified, and MONWPNT is updated to point again to the first entry. Otherwise MONWPNT is updated to point to the first entry so that the oldest event is overlaid by the new one.

The logic flow of the individual routines of phase SDEHR is described in tabular form in the remainder of this section.

Event MNEMO or PER code	MC class	PER event address or MC code
or page translation exception address		PGM old PSW
		TOD clock
		TE mask
		Reserved
Control Register 9		
Control Register 10		
Control Register 11		
General Register 13		
General Register 14		
General Register 15		
General Register 0		
General Register 1		
General Register 2		

Figure 39. Format of Core-Wrap Buffer (SDBUF)

## EVENT HANDLING ROUTINES

Figure 40 is a tabular summary of the logic flow of the Event Handling Routines.

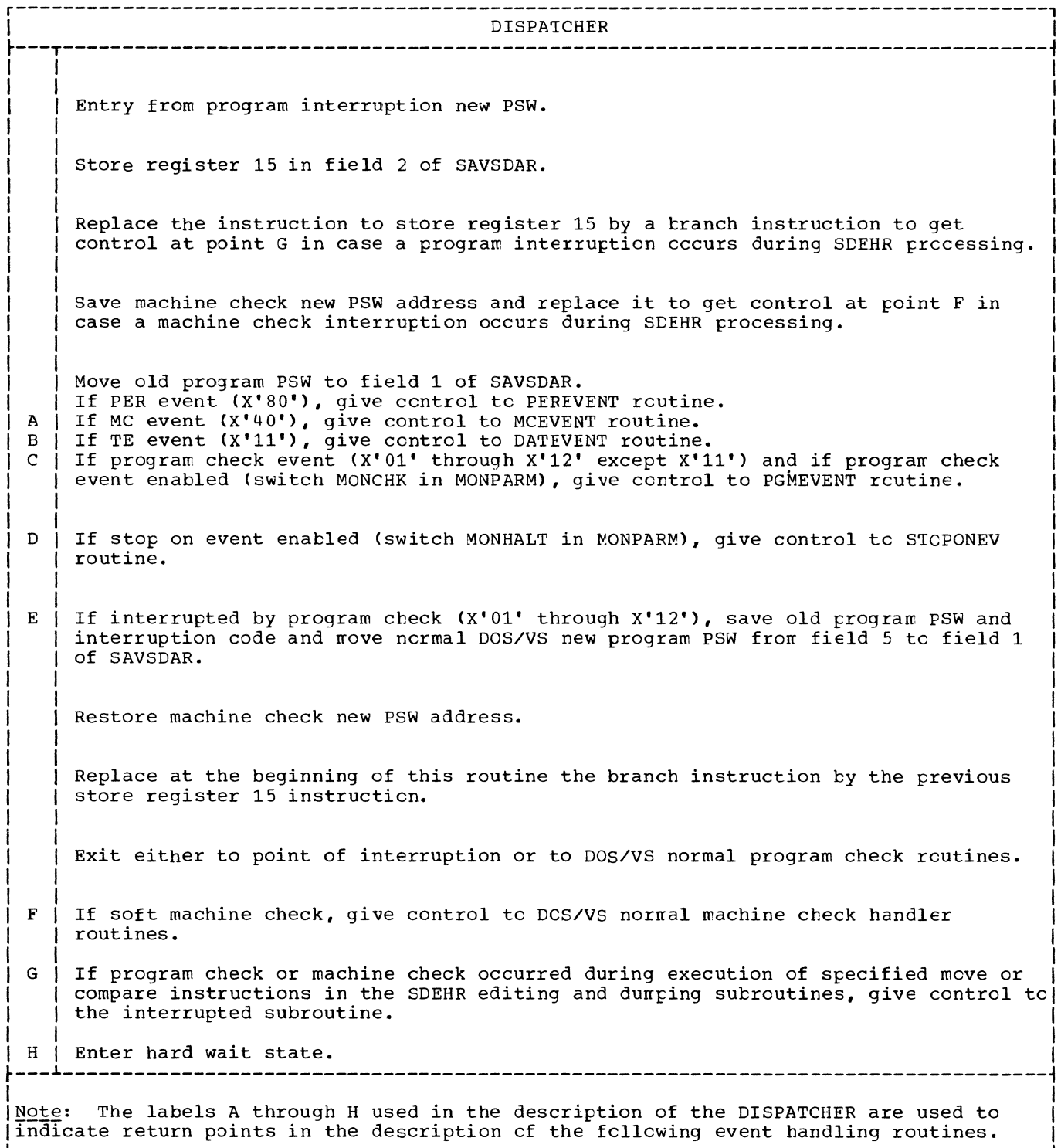


Figure 40. Logic Flow of the Event Handling Routines (Part 1 of 6)

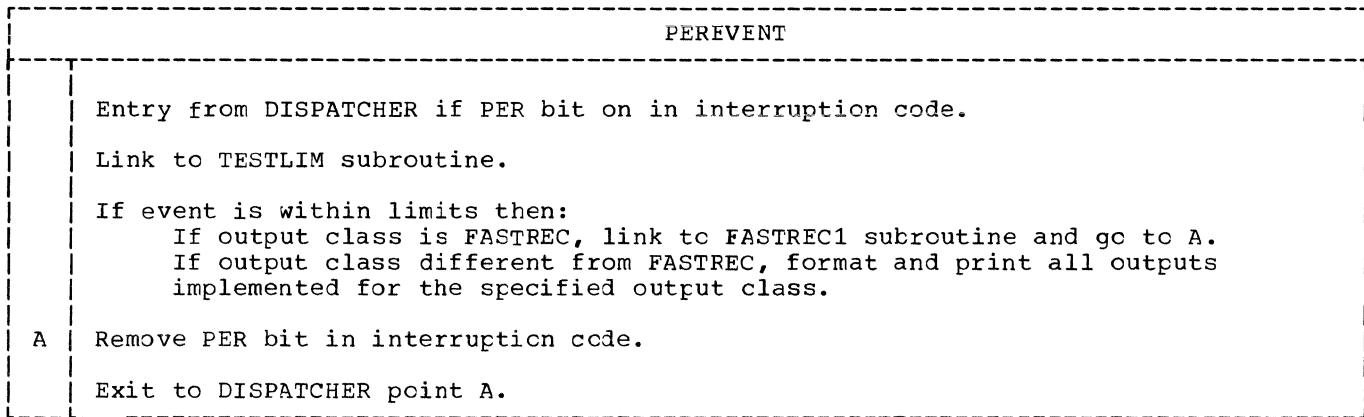


Figure 40. Logic Flow of the Event Handling Routines (Part 2 of 6)

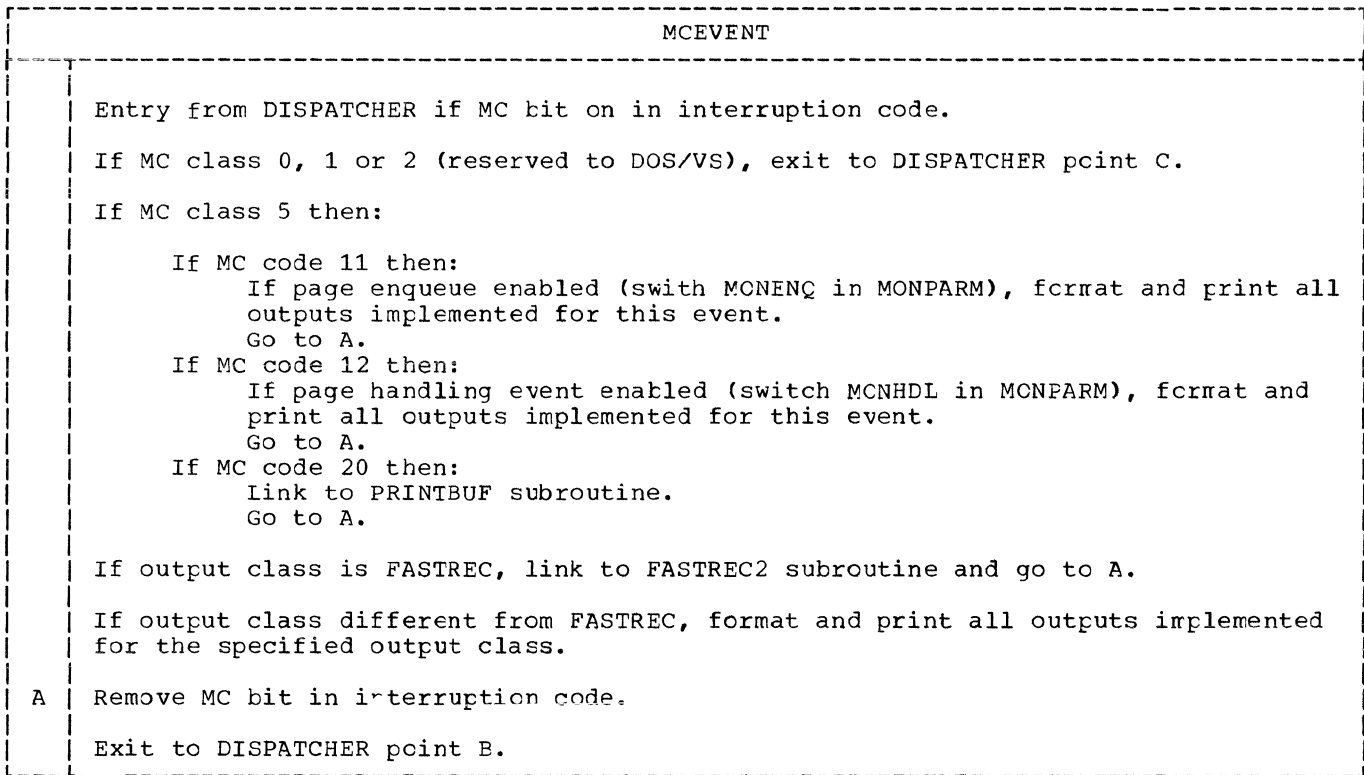


Figure 40. Logic Flow of the Event Handling Routines (Part 3 of 6)

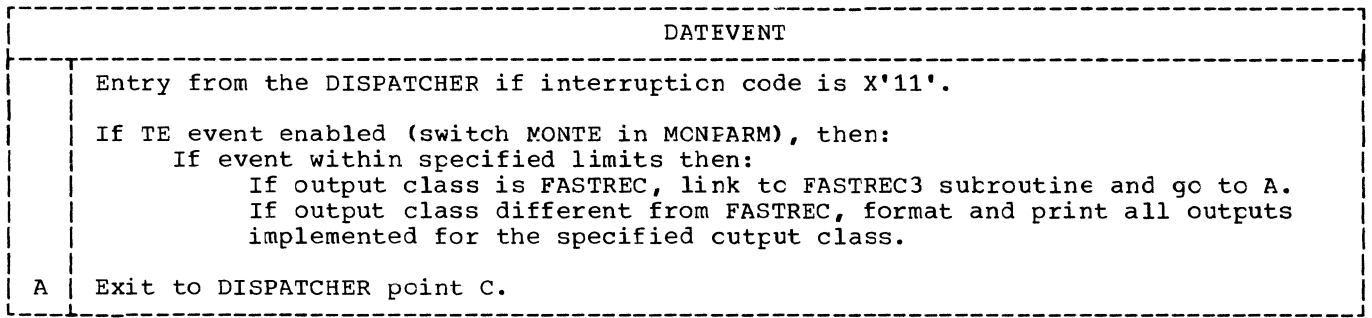


Figure 40. Logic Flow of the Event Handling Routines (Part 4 of 6)

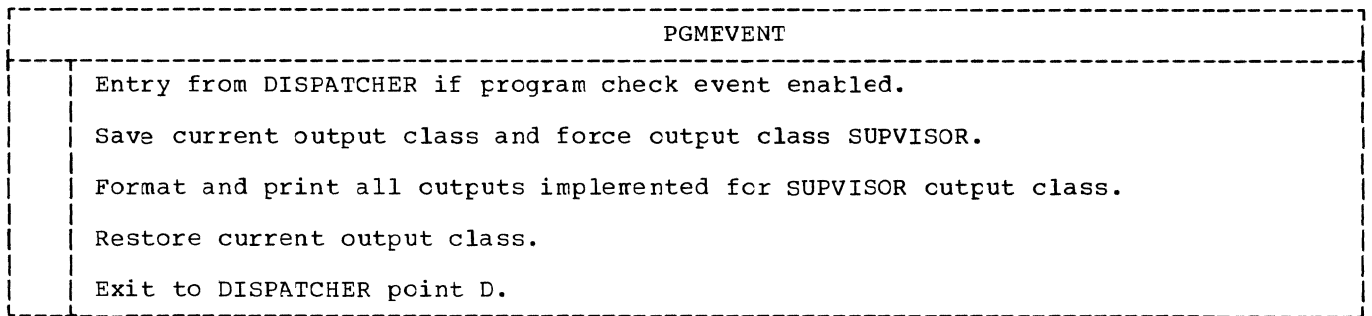


Figure 40. Logic Flow of the Event Handling Routines (Part 5 of 6)

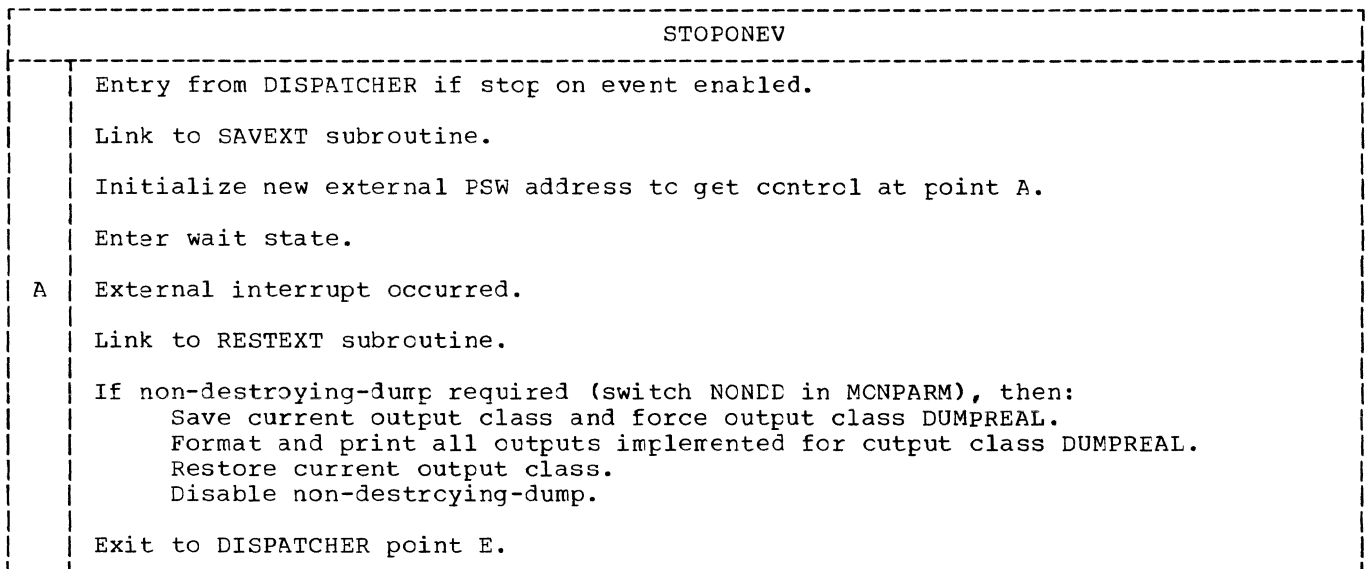


Figure 40. Logic Flow of the Event Handling Routines (Part 6 of 6)

RECORDING ROUTINES

Figure 41 is a tabular summary of the logic flow of the Recording Routines.

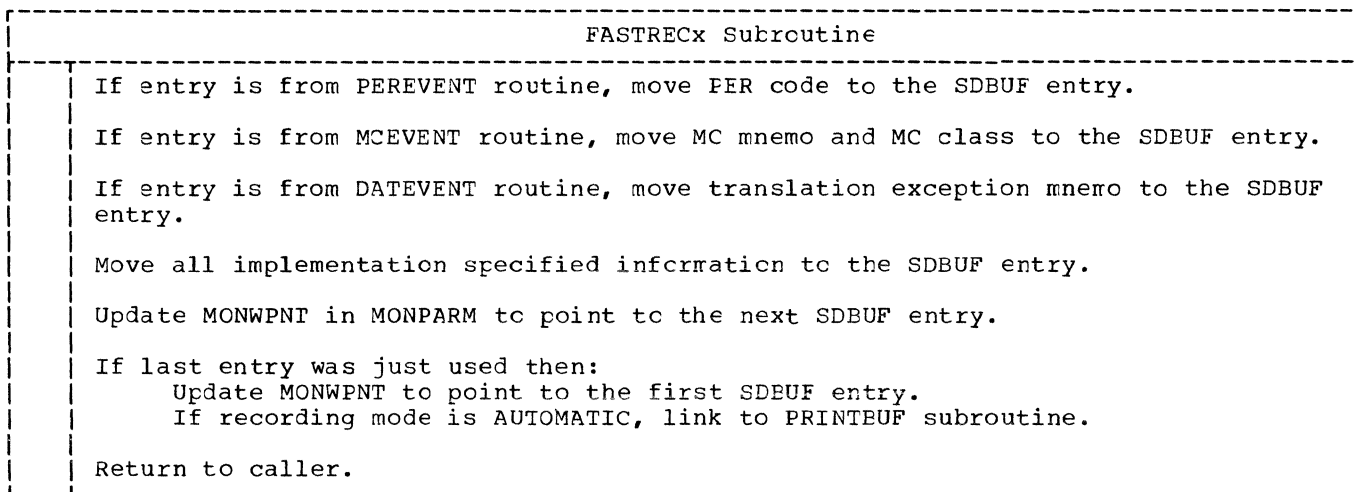


Figure 41. Logic Flow of Recording Routines (Part 1 of 5)

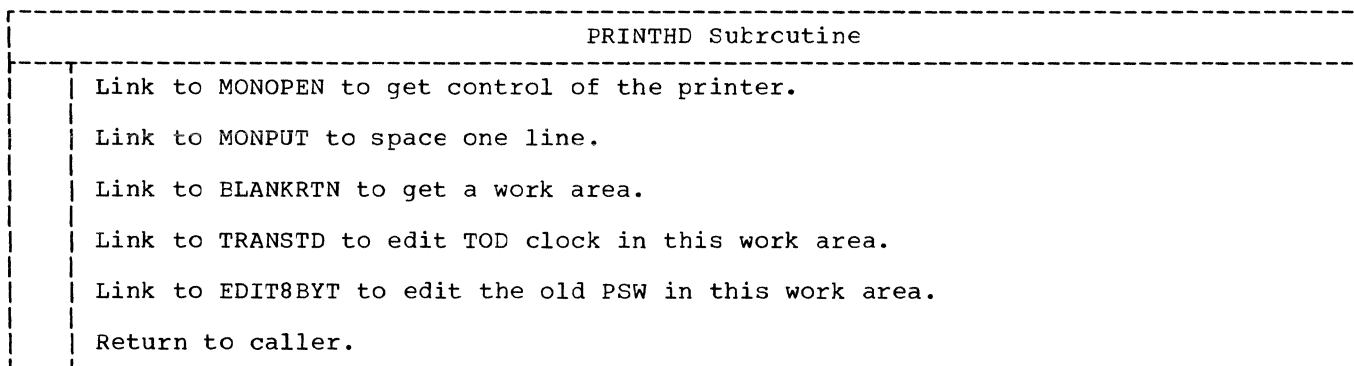


Figure 41. Logic Flow of Recording Routines (Part 2 of 5)

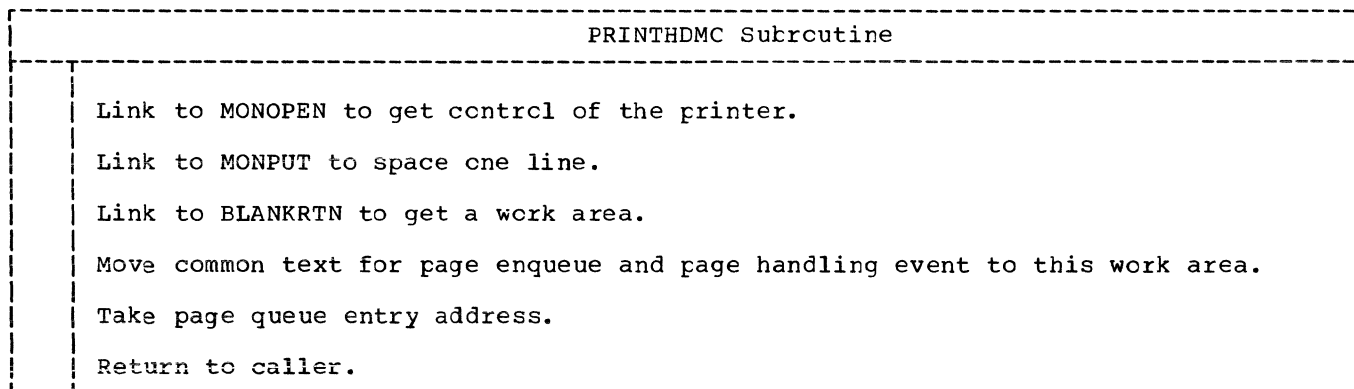


Figure 41. Logic Flow of Recording Routines (Part 3 of 5)

PRINTIT Subroutine

Link to MONPUT to print the heading line.

If output class is FASTREC or PSW, go to B.

Edit and print general registers 0 through 15.

If output class is GPR, go to B.

Edit and print control registers 0 through 15.  
Dump low storage from address 0 to 11F.

If output class is LOCORE, go to A.

If output class is COMREG then:  
Link to SPECDDUMP to dump COMRG and SYSCOM.  
Go to A.

If output class is PAGETAB then:  
Link to SPECDDUMP to dump segment table, page table and page frame table.  
Go to A.

If output class is SUPERVISOR then:  
Link to DUMPSUB to dump the entire supervisor.  
Go to A.

If output class is DUMPREAL then:  
Link to DUMPSUB to dump the entire real storage.

A | Link to MONPUT to space one line.

B | Link to MONCLOSE to return the printer to DOS/VS.

Return to the caller.

Figure 41. Logic Flow of Recording Routines (Part 4 of 5)

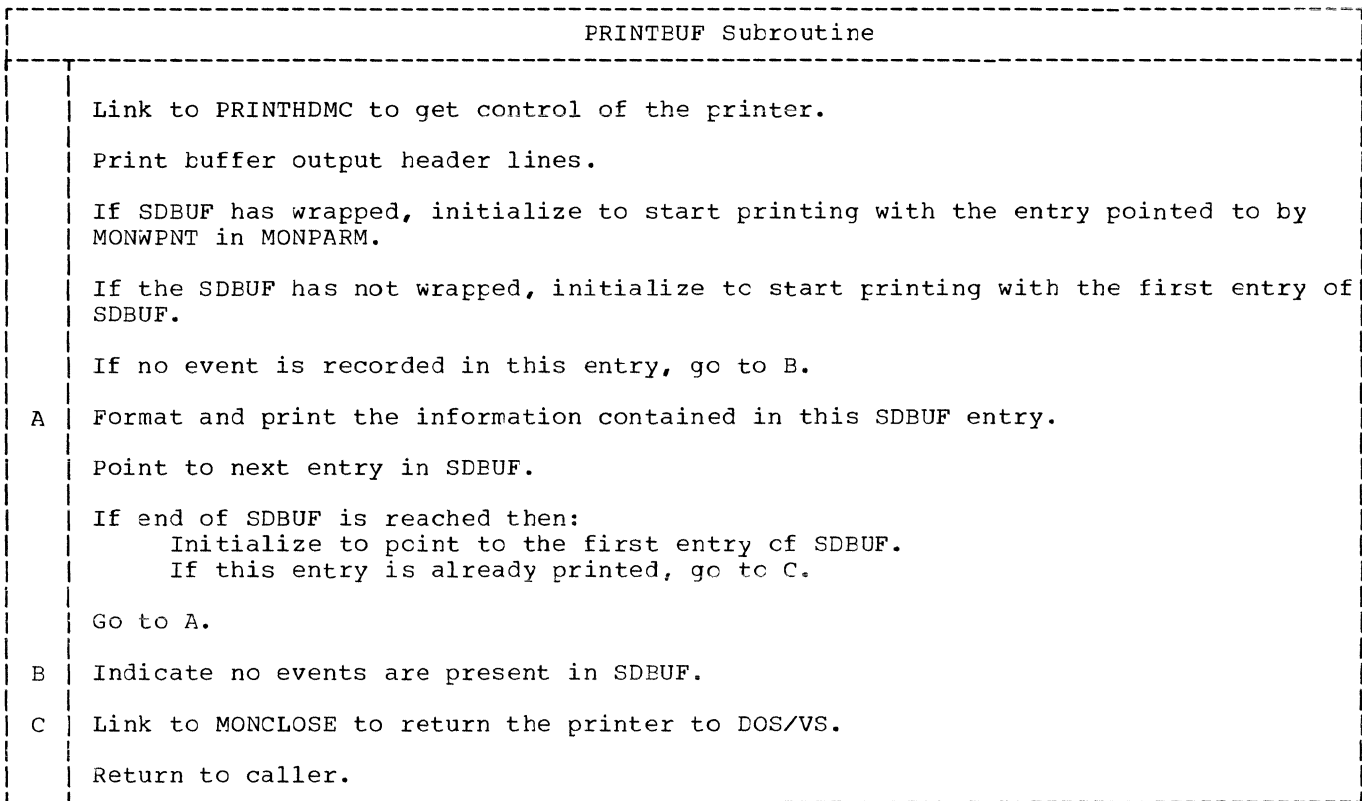


Figure 41. Logic Flow of Recording Routines (Part 5 of 5)



## DUMPING AND EDITING SUBROUTINES

The SDEHR routines run with the DAT feature off, and the storage area limits to be dumped or edited by the following subroutines are not checked for validity.

The program check due to addressing exception which may occur while executing predefined move or compare instructions are checked by the DISPATCHER, and control is returned to the appropriate subroutine entry point.

Furthermore, the machine check which may occur while executing these same predefined move or compare instructions are checked by the DISPATCHER, and control is returned to the appropriate subroutine entry point if a soft machine check occurred.

In these cases, the subroutine returns a character 'X' for each storage location outside real storage.

Figure 42 is a tabular summary of Dumping and Editing subroutines.

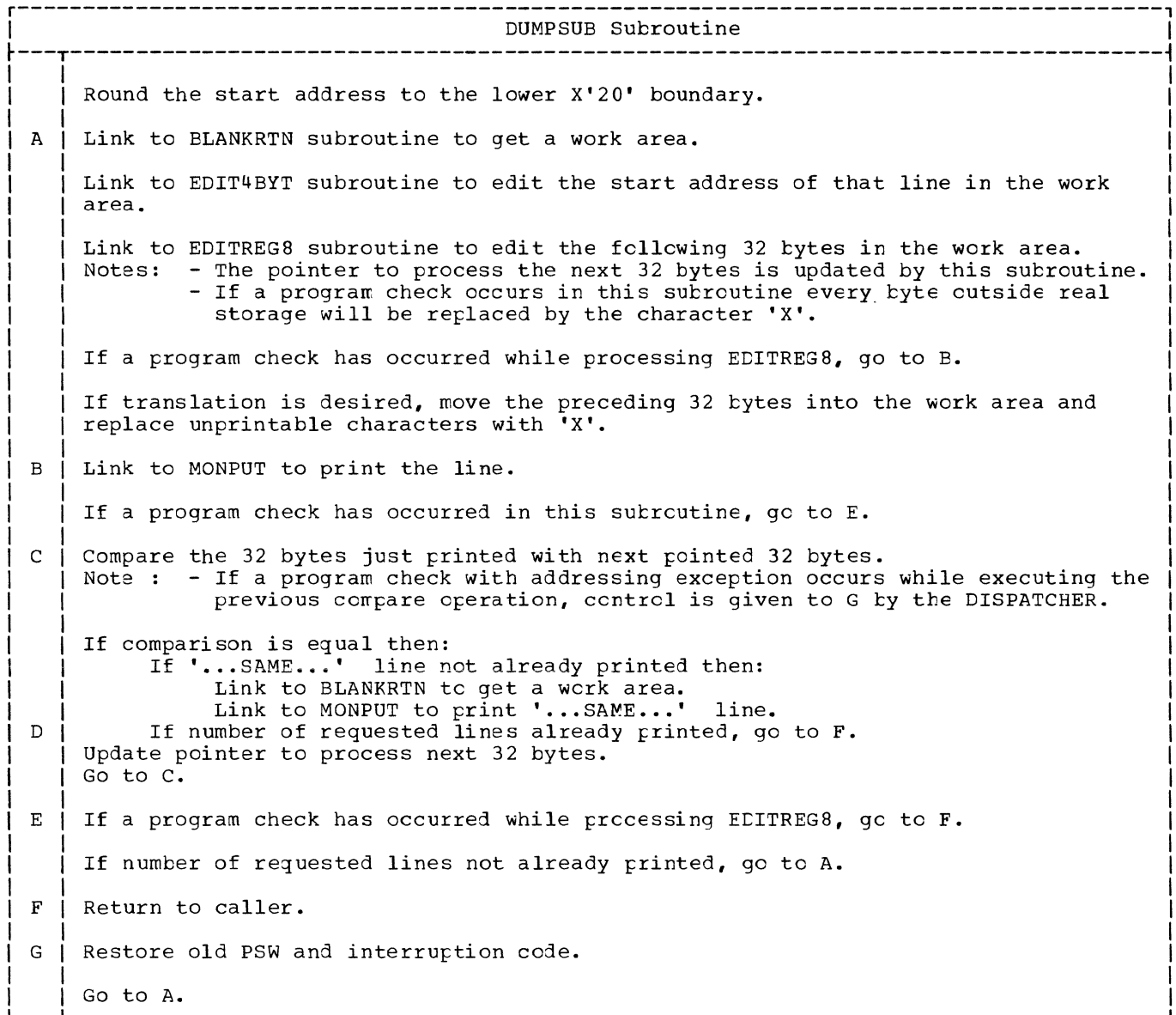


Figure 42. Logic Flow of Editing and Dumping Subroutine (Part 1 of 6)

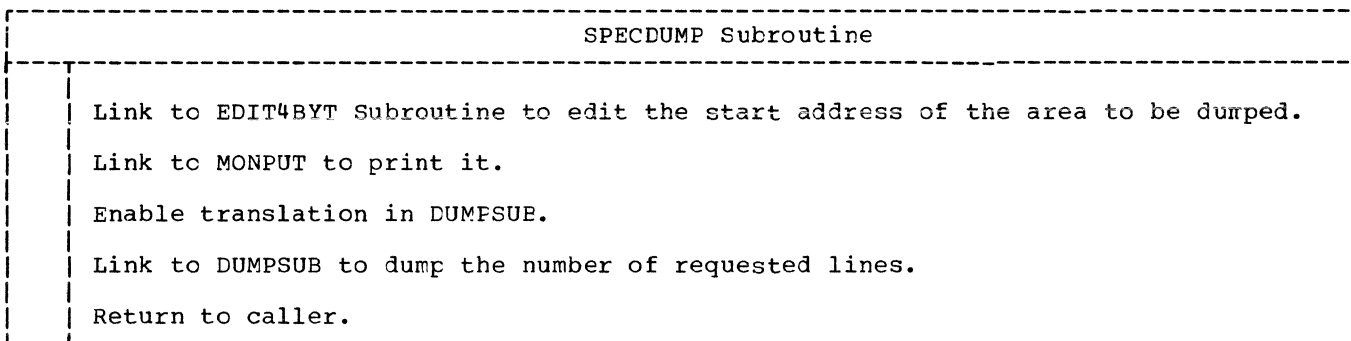


Figure 42. Logic Flow of Editing and Dumping Subroutine (Part 2 of 6)

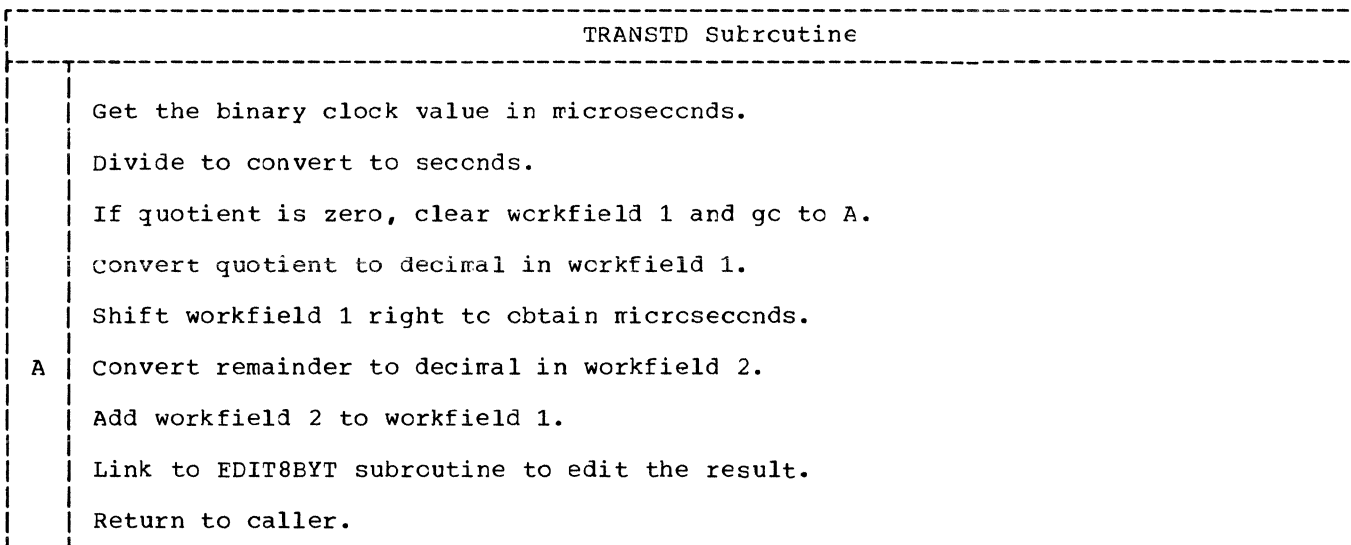


Figure 42. Logic Flow of Editing and Dumping Subroutine (Part 3 of 6)

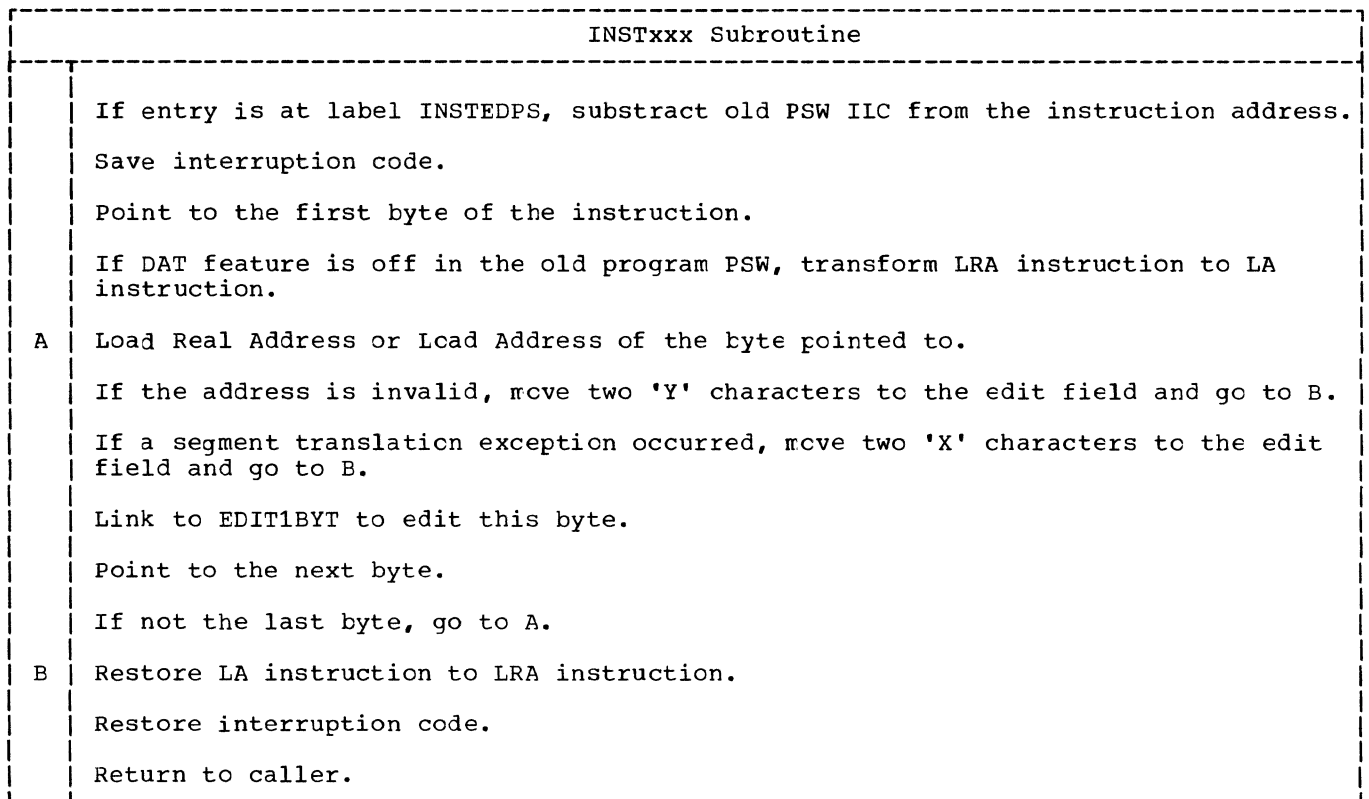


Figure 42. Logic Flow of Editing and Dumping Subroutine (Part 4 of 6)

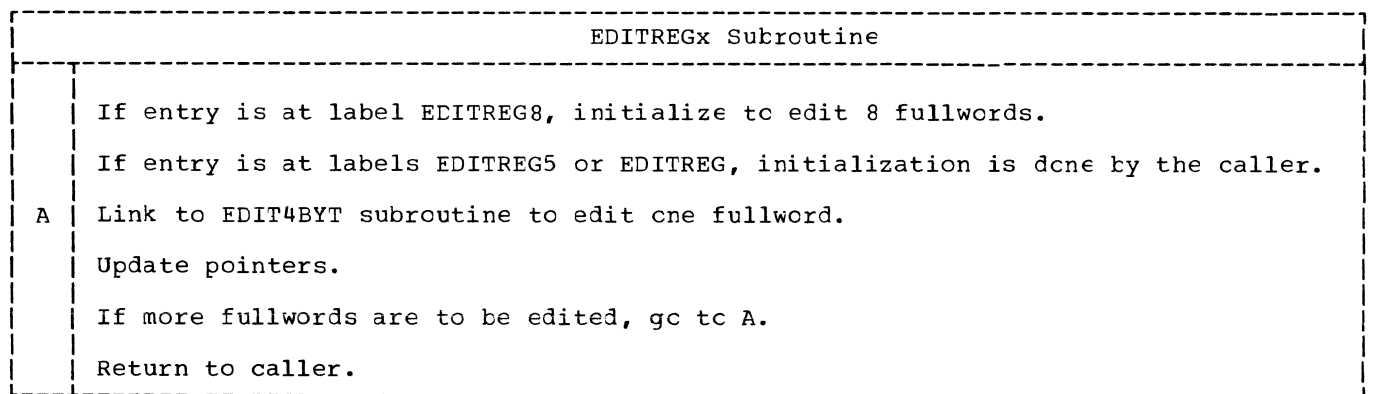


Figure 42. Logic Flow of Editing and Dumping Subroutine (Part 5 of 6)

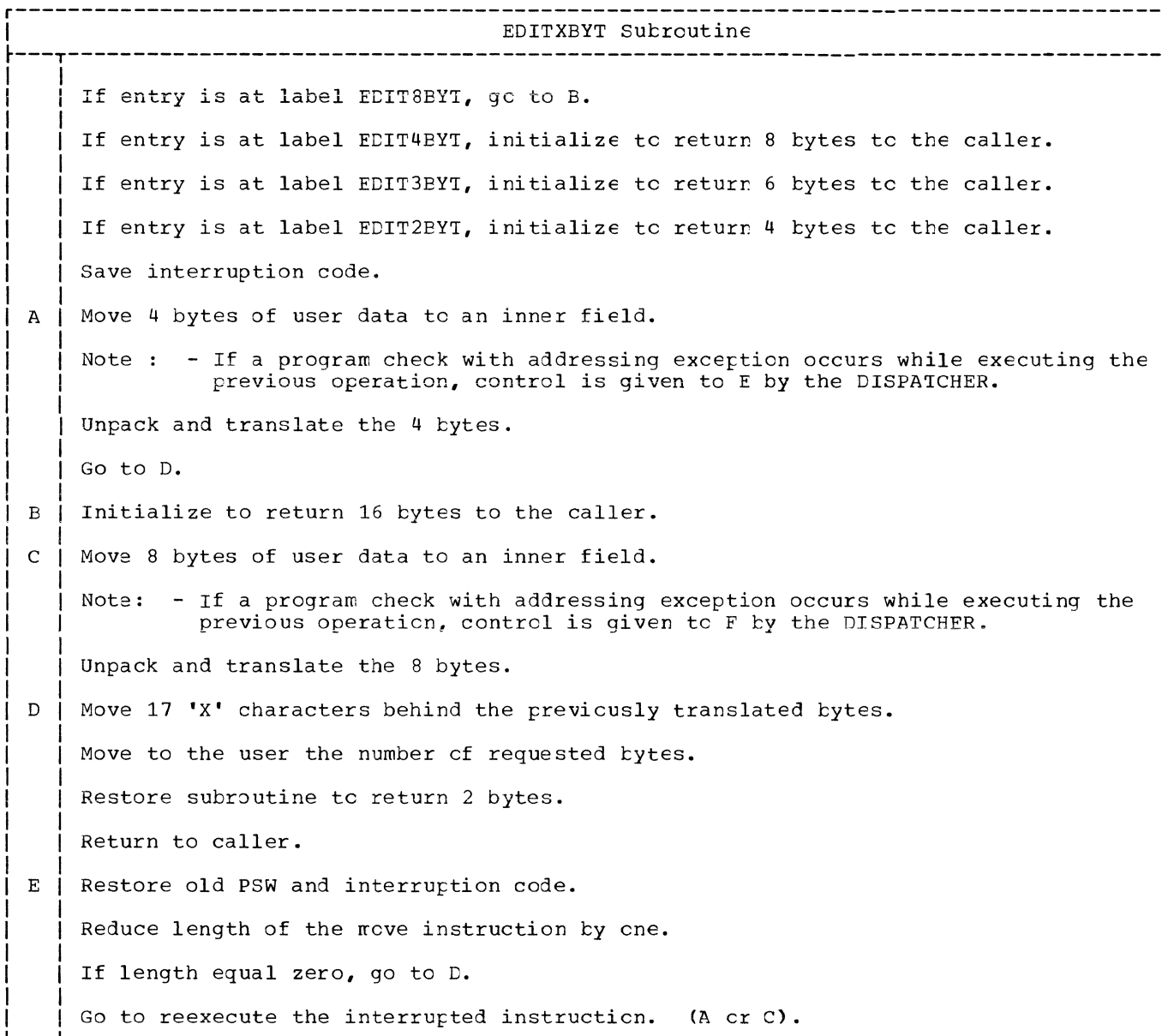


Figure 42. Logic Flow of Editing and Dumping Subroutine (Part 6 of 6)

#### PRINTER IOCS

When the output class is other than FASTREC, tracing and dumping takes place on the specified printer at the time the event occurs. SDEHR does not use the DOS/VS channel scheduler to perform its I/O operations; it has its own printer IOCS. At the time SDEHR has to perform output, the specified printer may be executing an operation previously initiated either by the system or by the user.

Before taking control of the printer the MONOPEN routine tests the status of the printer:

- If the specified channel, subchannel, and device are available, the SDEHR printer IOCS gets control of the printer immediately.
- If the channel is still working in burst mode, the MONOPEN routine enters a hard wait state.

- If the subchannel is still working with the device, MONOPEN enters a TIO loop until the channel-end condition is stored.
  - If the device is still working, the TIO loop is entered until the DEVICE-END condition is stored
  - In both preceding cases, the interruption conditions are saved to be simulated at the time all the desired output is performed.
  - If the status received at channel-end or device-end time indicates abnormal conditions, the CSW and, if a unit check occurred, the sense byte are printed to make the user aware that the output may be incorrect.
- Before returning control of the printer, the MONCLOSE routine tests whether interruption conditions have to be simulated:
- If a channel-end condition is to be simulated, the last user-executed CCW is replaced by a write-no-space CCW. The operation is initiated and the user CCW is restored before control of the printer is returned.
  - If a device-end condition is to be simulated, a write-nc-space operation is initiated. A TIO loop is entered until the channel-end interruption occurs and control of the printer is returned.

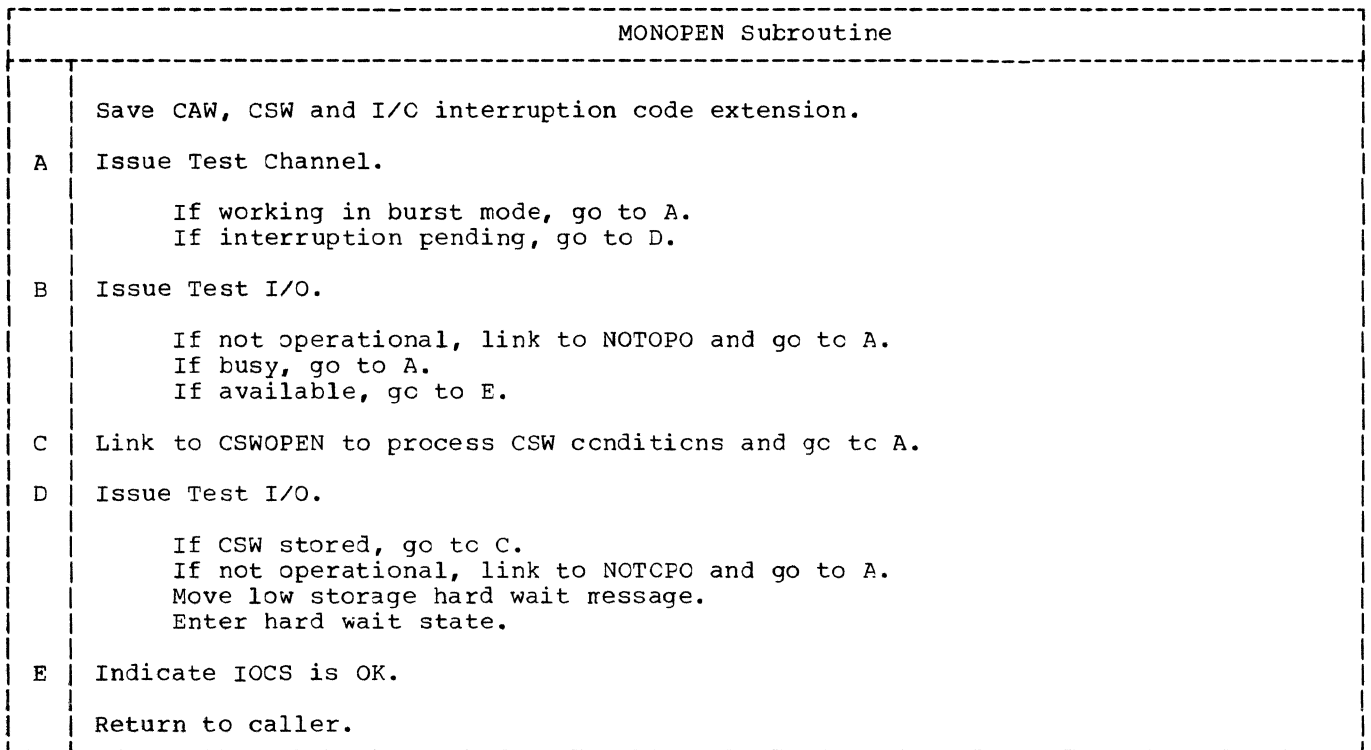


Figure 43. Logic Flow of Printer IOCS Subroutines (Part 1 of 6)

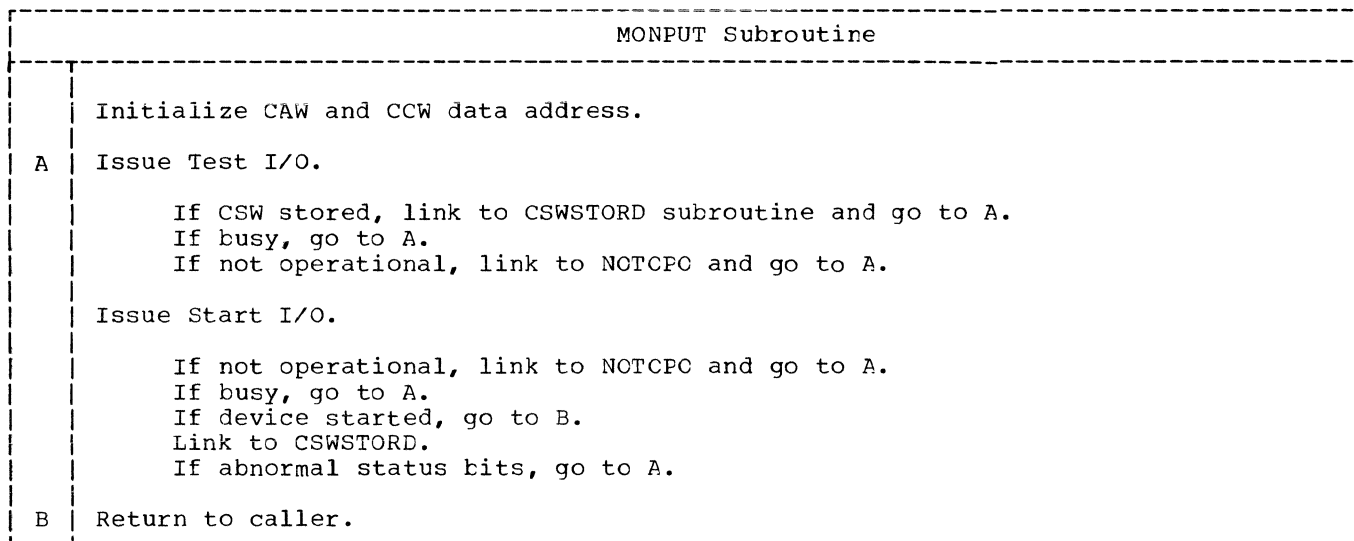


Figure 43. Logic Flow of Printer IOCS Subroutines (Part 2 of 6)

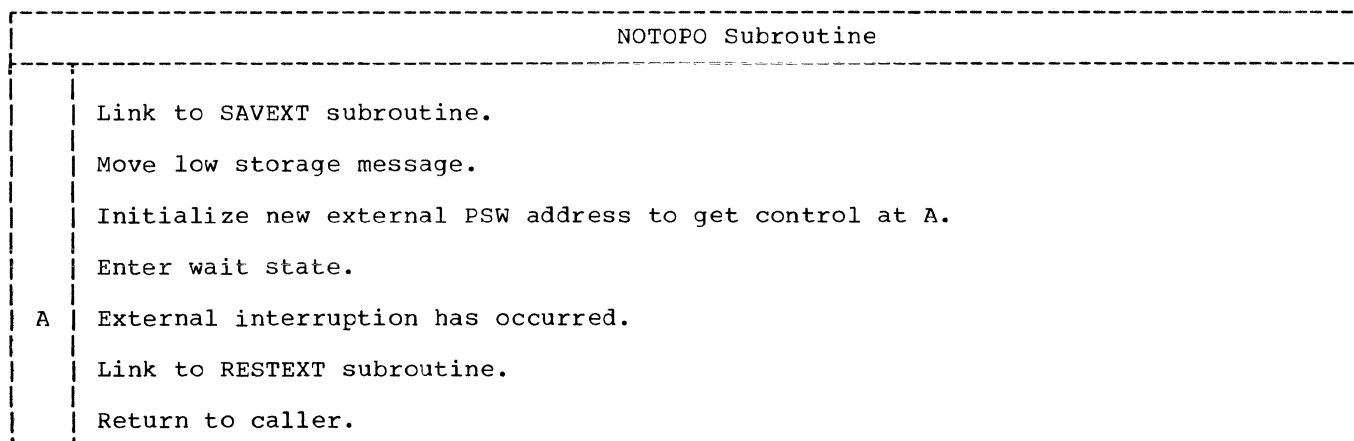


Figure 43. Logic Flow of Printer IOCS Subroutines (Part 3 of 6)

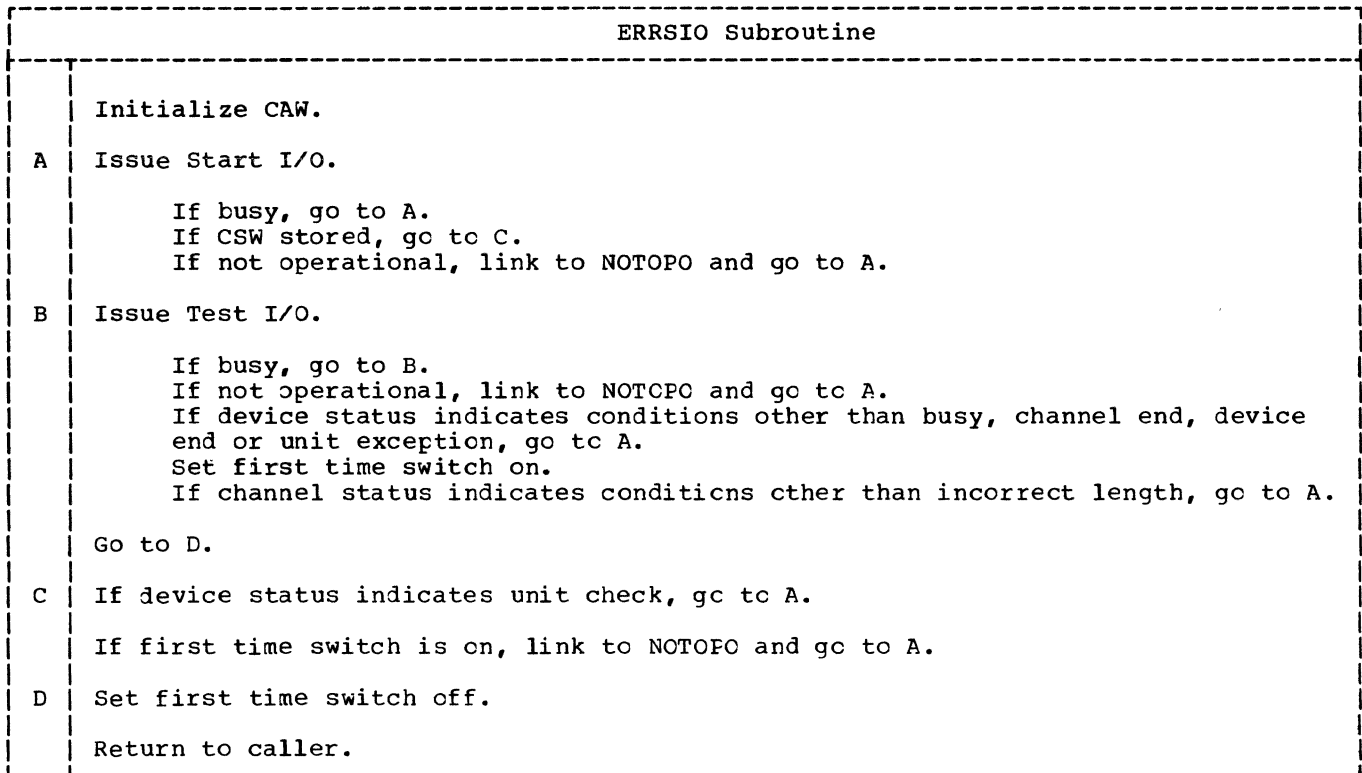


Figure 43. Logic Flow of Printer IOCS Subroutines (Part 4 of 6)

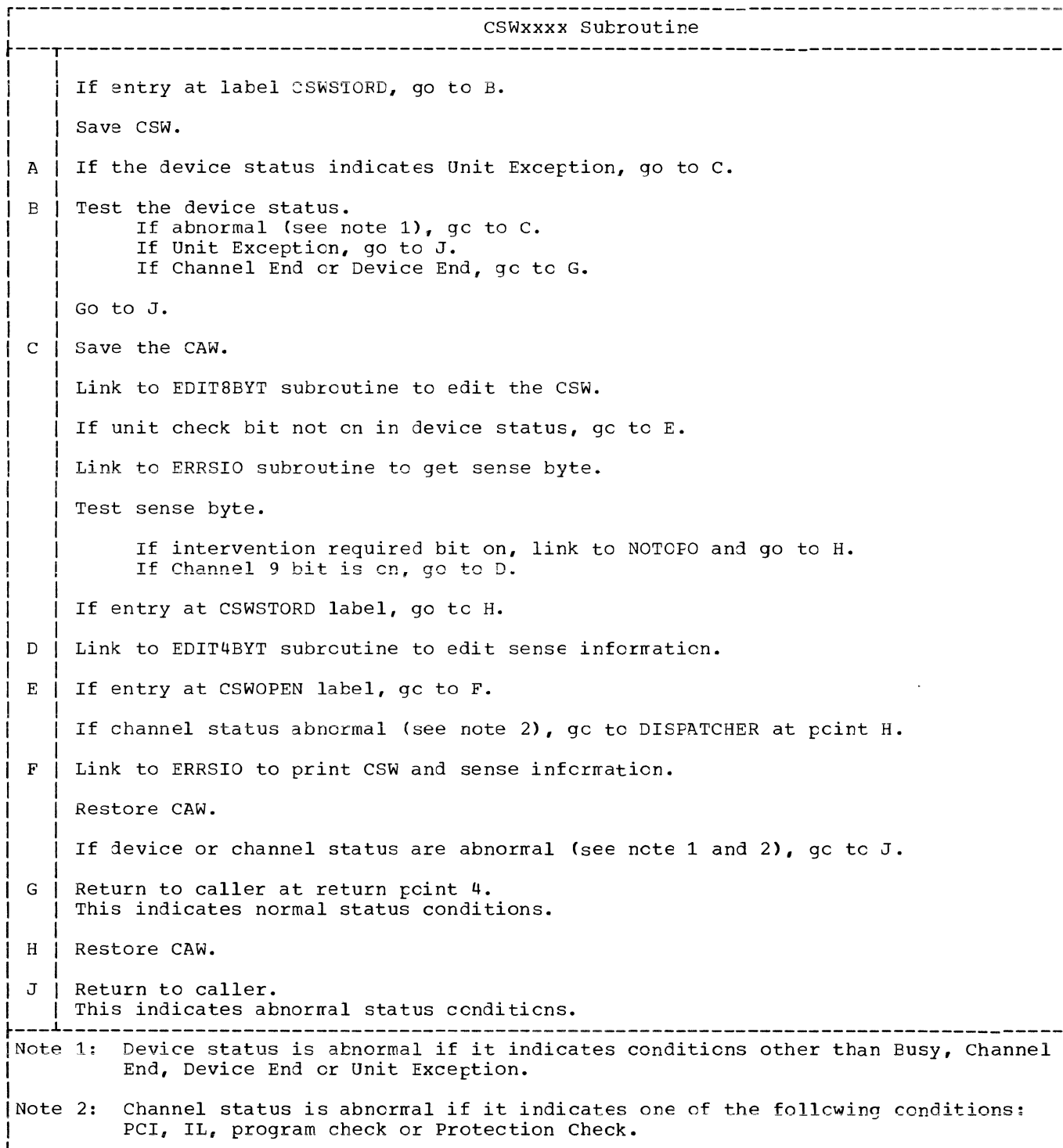


Figure 43. Logic Flow of IOCS Subroutines (Part 5 of 6)



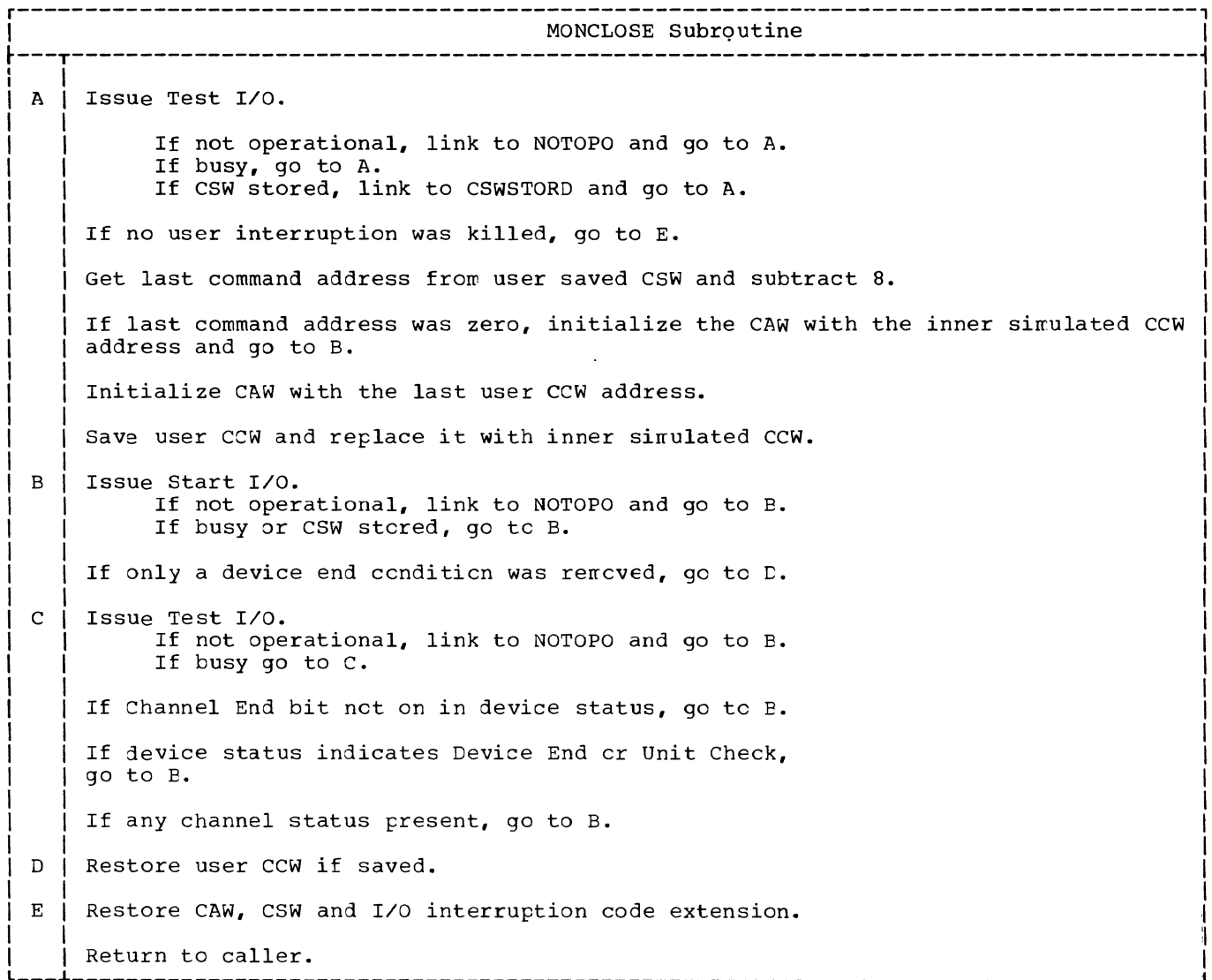


Figure 43. Logic Flow of IOCS Subroutines (Part 6 of 6)

MISCELLANEOUS

Figure 44 is a tabular summary of the logic flow of the Miscellaneous Routines.

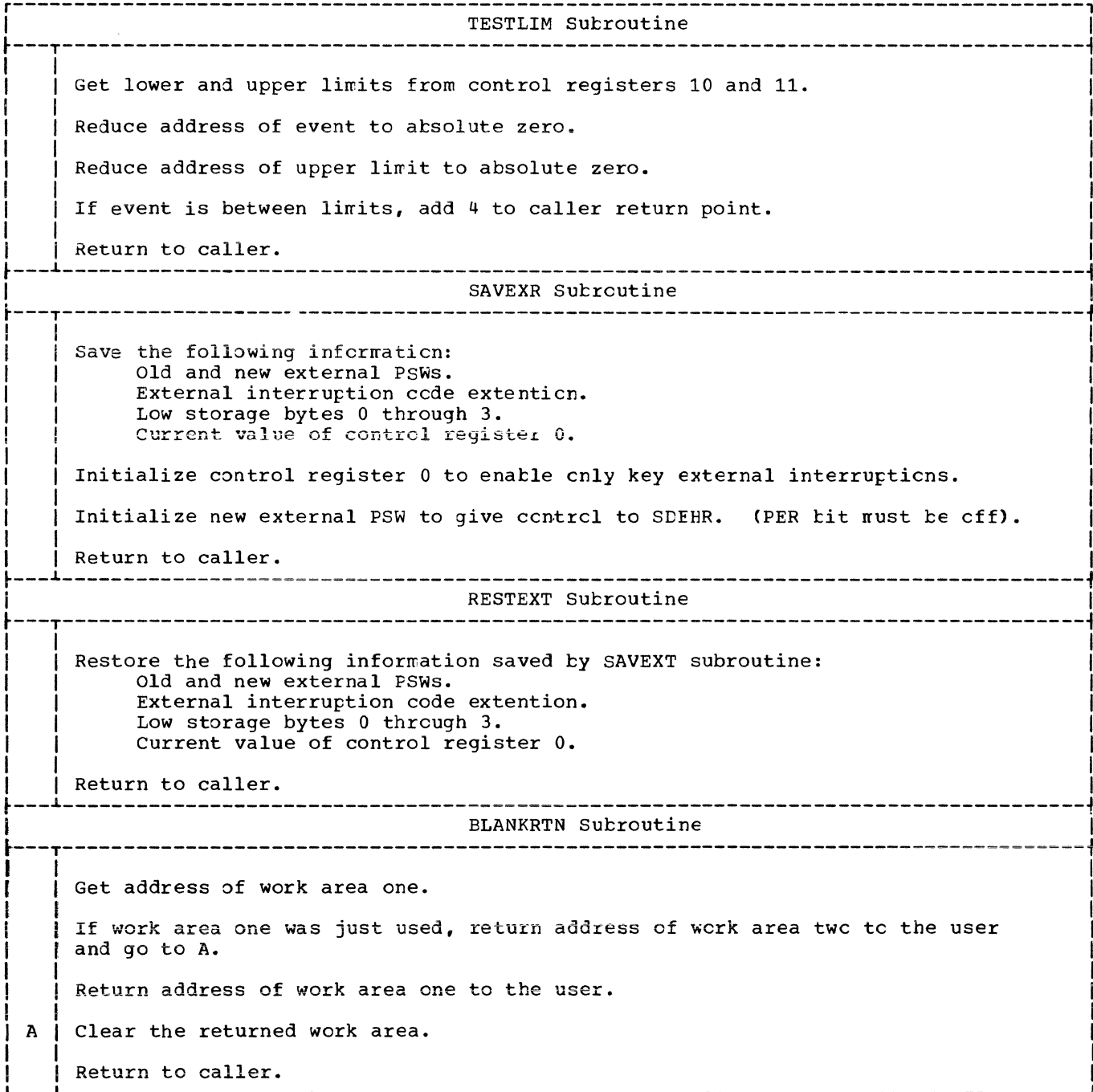


Figure 44. Logic Flow of Miscellaneous Routines

TERMINATION ROUTINES

The SDAID tracing and dumping functions are terminated either by issuing the ENDSO attention command or by executing the phase ENDSO in a virtual or real partition.

The Termination phase and the termination transient perform the following functions:

ENDSO Call \$\$BATTN3 termination transient and release the partition upon return.

\$\$BATTN3 Print the SDBUF buffer. Switch off in control registers 8, 9, 10, and 11 all fields set up for the tracing/dumping options. Switch the PER bit off in all PSWs and release the page frames allocated to SDAREA.

The general logic flow of the SDAID termination routines is shown in Figure 45.

During termination, the SDAID program uses the following storage areas:

- SAVSDAR in the supervisor
- The logical transient area (LTA) for \$\$BATTN3
- A virtual or real partition (only if termination is via // EXEC ENDSO)
- SDAREA at the high end of real storage

Figures 46 and 47 show the logic flow of phase ENDSO and of the transient \$\$BATTN3, respectively, in tabular form.

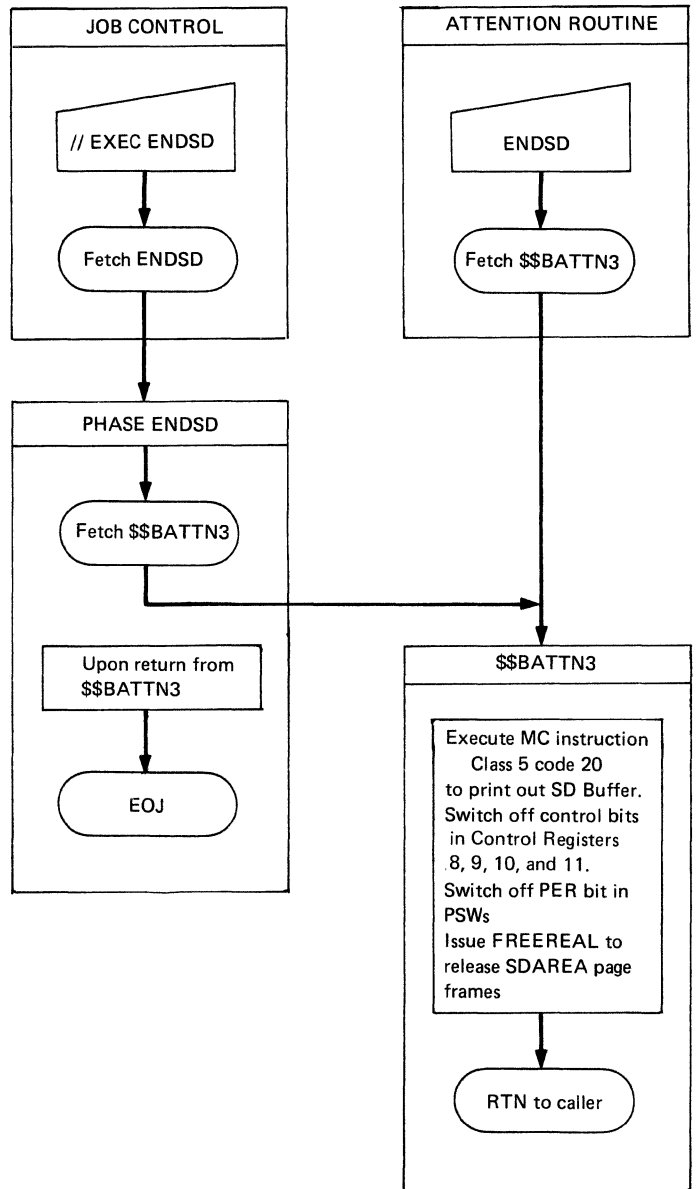


Figure 45. General Logic Flow of Termination Routines

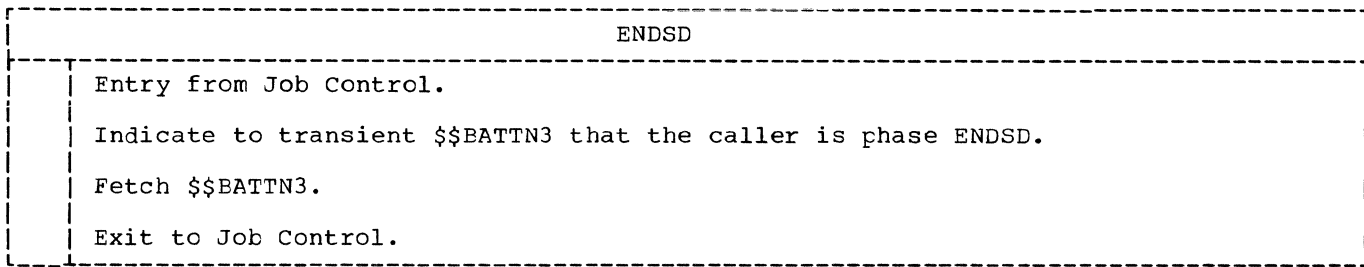


Figure 46. Logic Flow of Phase ENSD

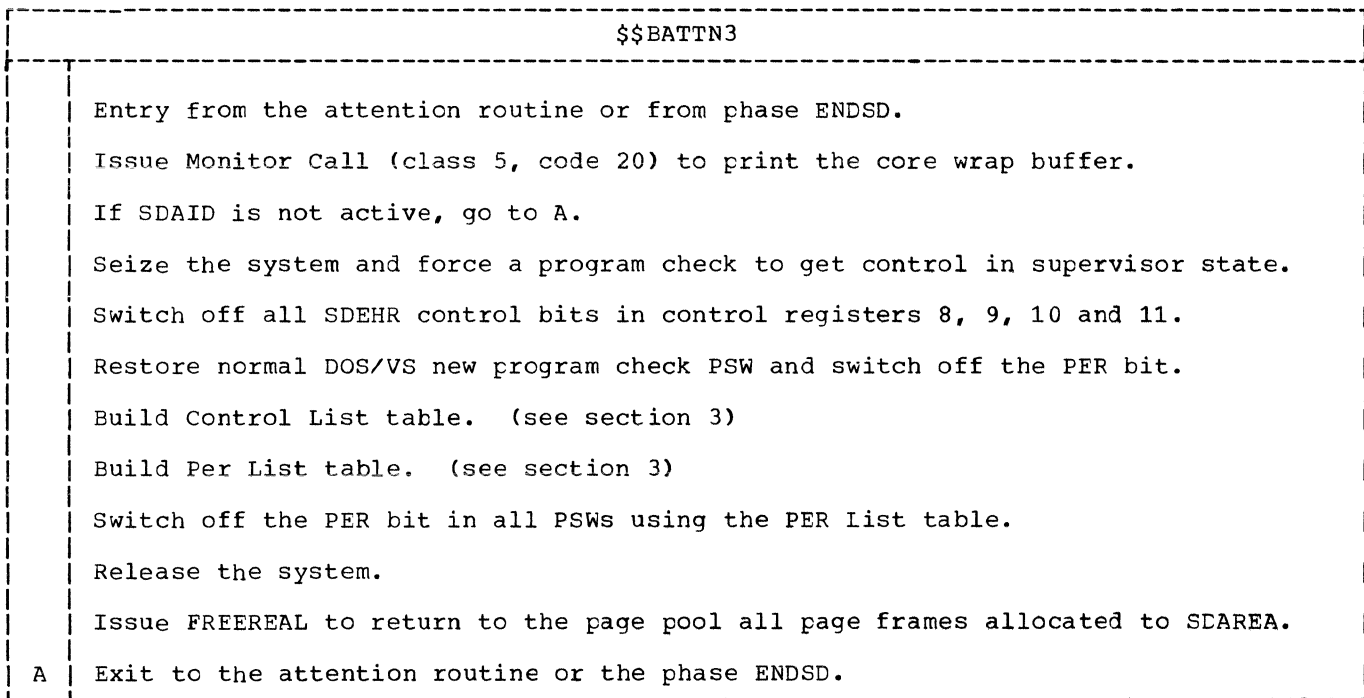


Figure 47. Logic Flow of Transient \$\$BATN3



Chart GB. SDPAR - Initialize an Operator-to-System Communication (Part 1 of 3)  
 Refer to Figure 29.

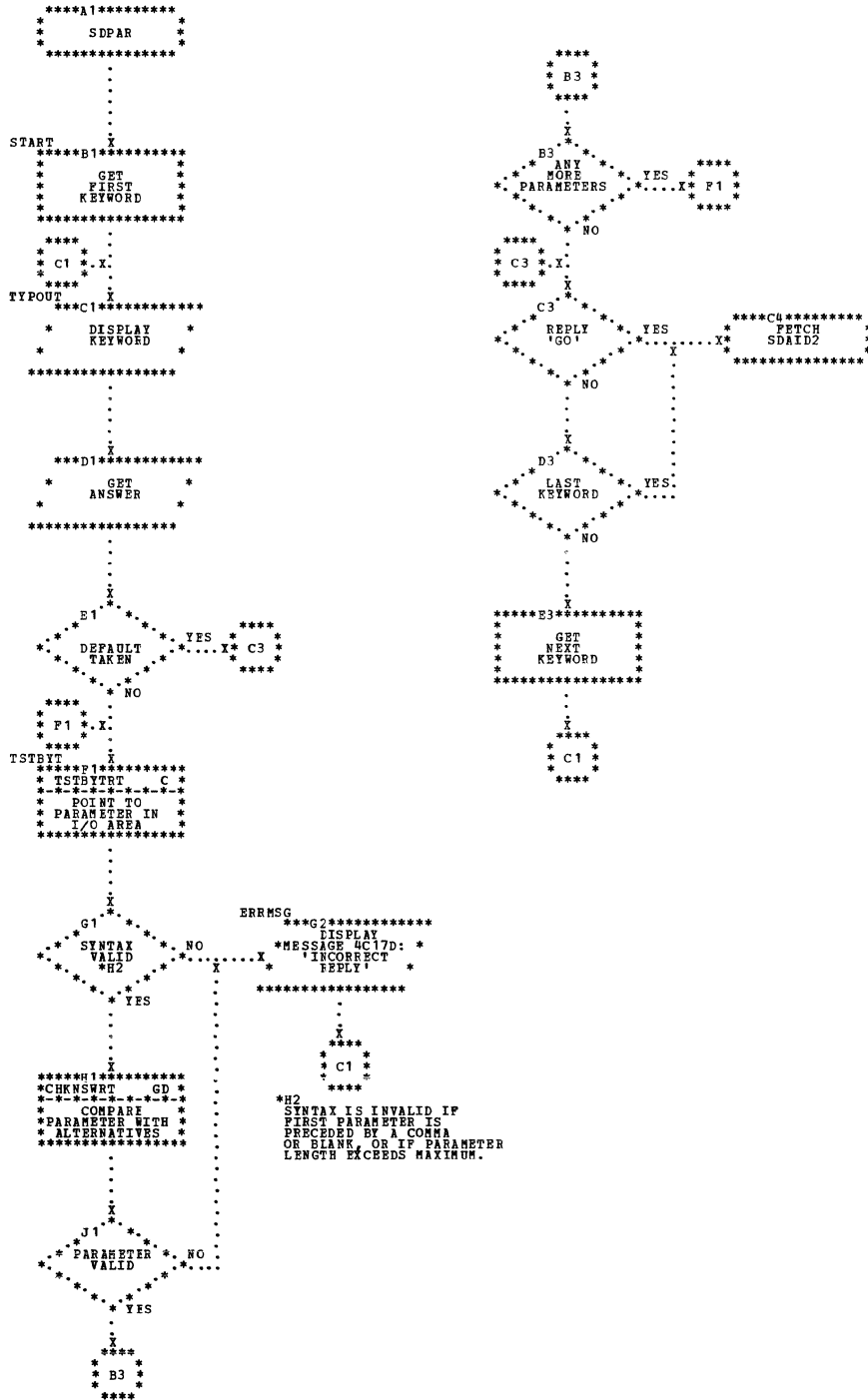


Chart GC. SDPAR - Initialize an Operator-to-System Communication (Part 2 of 3)  
 Refer to Figure 29.

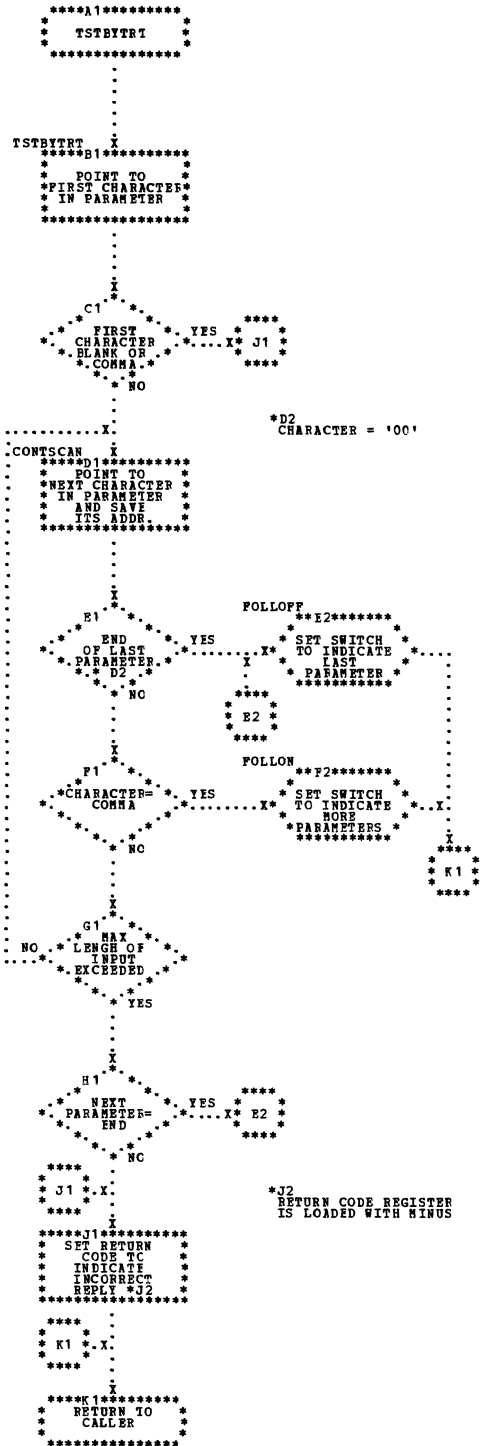


Chart GD. SDPAR - Initialize an Operator-to-System Communication (Part 3 of 3)  
Refer to Figure 29.

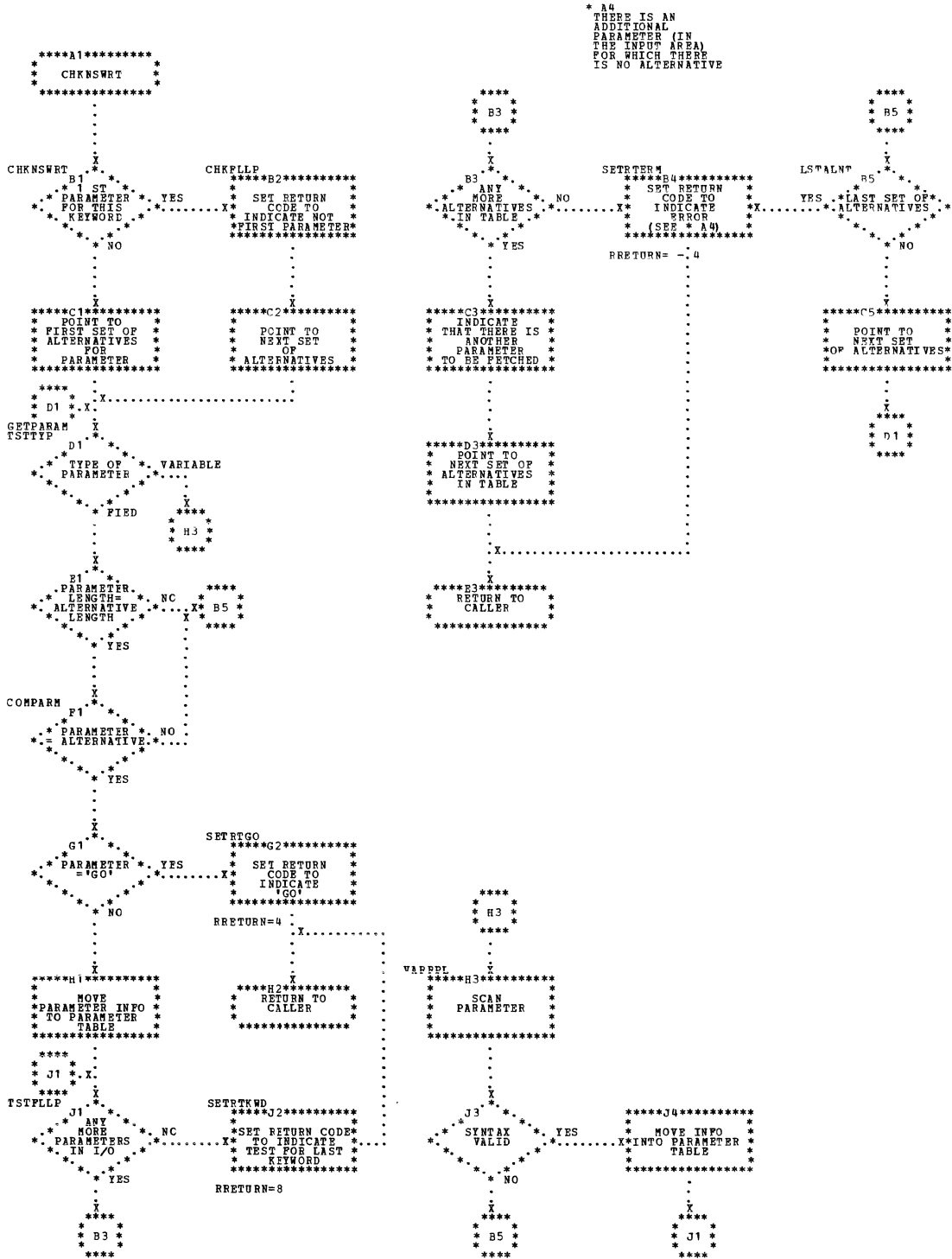




Chart GE. SDAID2 - Print Selected or Default Tracing/Dumping Options  
 Refer to Figure 29.

```

*****A1*****
* SDAID2 *
* ***** *
.
.
.
X
*****B1*****
* PRINT DEFAULT *
* OPTIONS AND *
* ***** *
* SPECIFIED *
.
.
.
X
*****C1*****
* STORE START *
* AND END ADDRESS *
* OF SD CORE *
* BUFFER IN *
* PARAMETER LIST *
* ***** *
.
.
.
X
*****D1*****
* PRINT *
* PROCEDURES *
* FOR *
* CHANGING *
* PARAMETERS *
* ***** *
.
.
.
X
*****E1*****
* MOVE FIRST PART *
* OF PARAMETER *
* LIST TO END OF *
* EVENT HANDLING *
* ROUTINE *(E2) *
* ***** *
.
.
.
X
*****F1*****
* $$$SDAID GF *
* - - - - - *
* MOVE AREAS *
* TO SDAREA *
* ***** *
.
.
.
X
*****G1*****
* PRINT *
* CPU *
* ID *
* ***** *
.
.
.
X
*****H1*****
* PRINT *
* MESSAGE *
* 4C57E *
* ***** *
* (SEE *H2) *
.
.
.
X
*****J1*****
* * * * *
* * CLOSE * *
* * SYSLOG * *
* * AND * *
* * SYSLIST * *
* * * * *
.
.
.
X
*****K1*****
* * * * *
* * EOI * *
* * * * *

```

\* E2  
 FIRST PART OF  
 PARAMETER LIST  
 INCLUDES PARAMETERS  
 UP TO HOWEND THIS  
 PART OF THE LIST IS  
 REQUIRED BY THE EVENT  
 HANDLING ROUTINES FOR  
 COMMUNICATION BETWEEN  
 THE SDAID MODULES

\* H2  
 MESSAGE:  
 "SDAID  
 SUCCESSFULLY  
 INITIATED"

Chart GF. \$\$\$BSDAID - Initialize Event Handling Routines, PSWs, and Control Registers  
Refer to Figure 29.

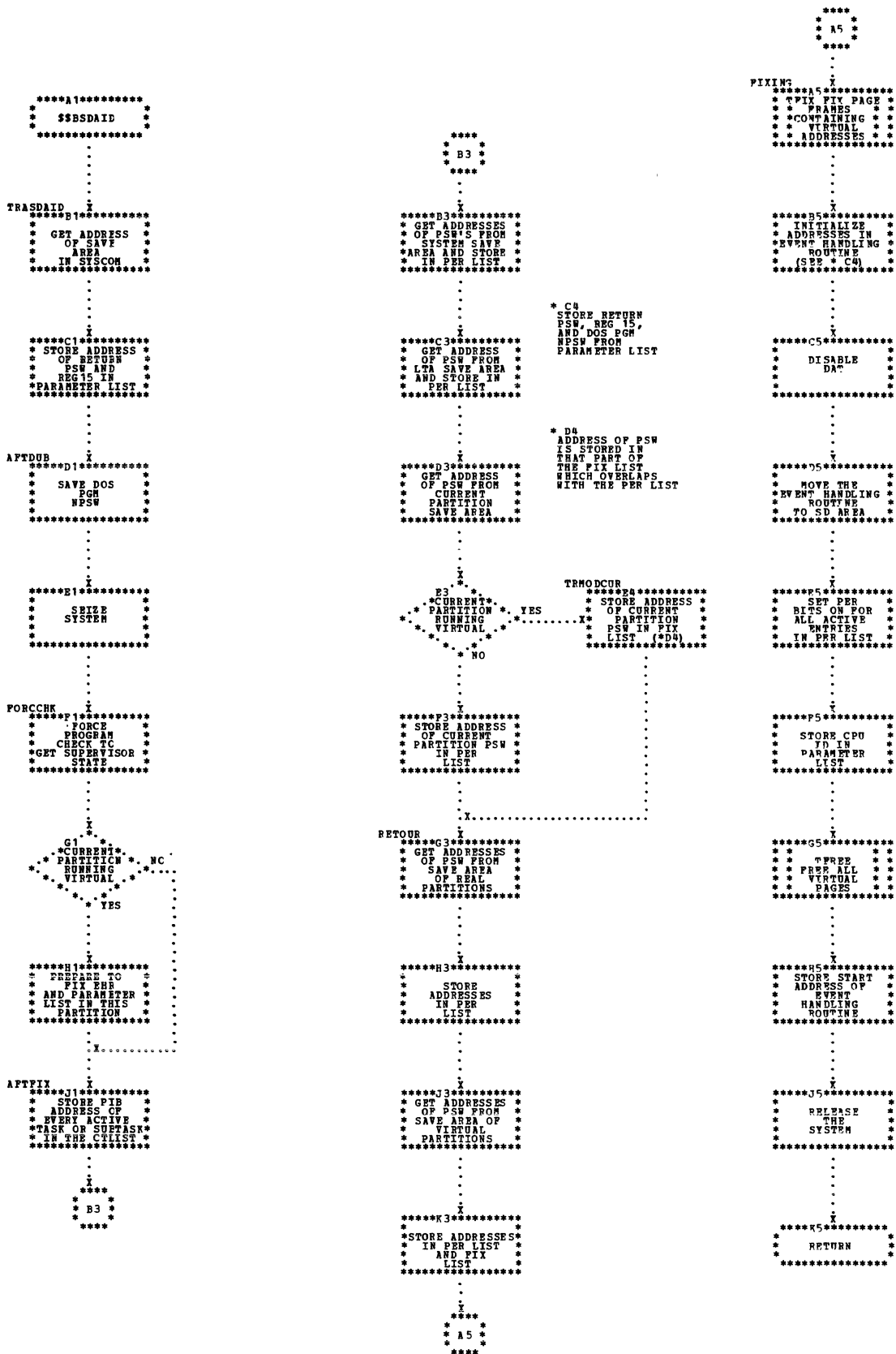


Chart GG. MONENTPG - Event Handling Routine (Part 1 of 2)

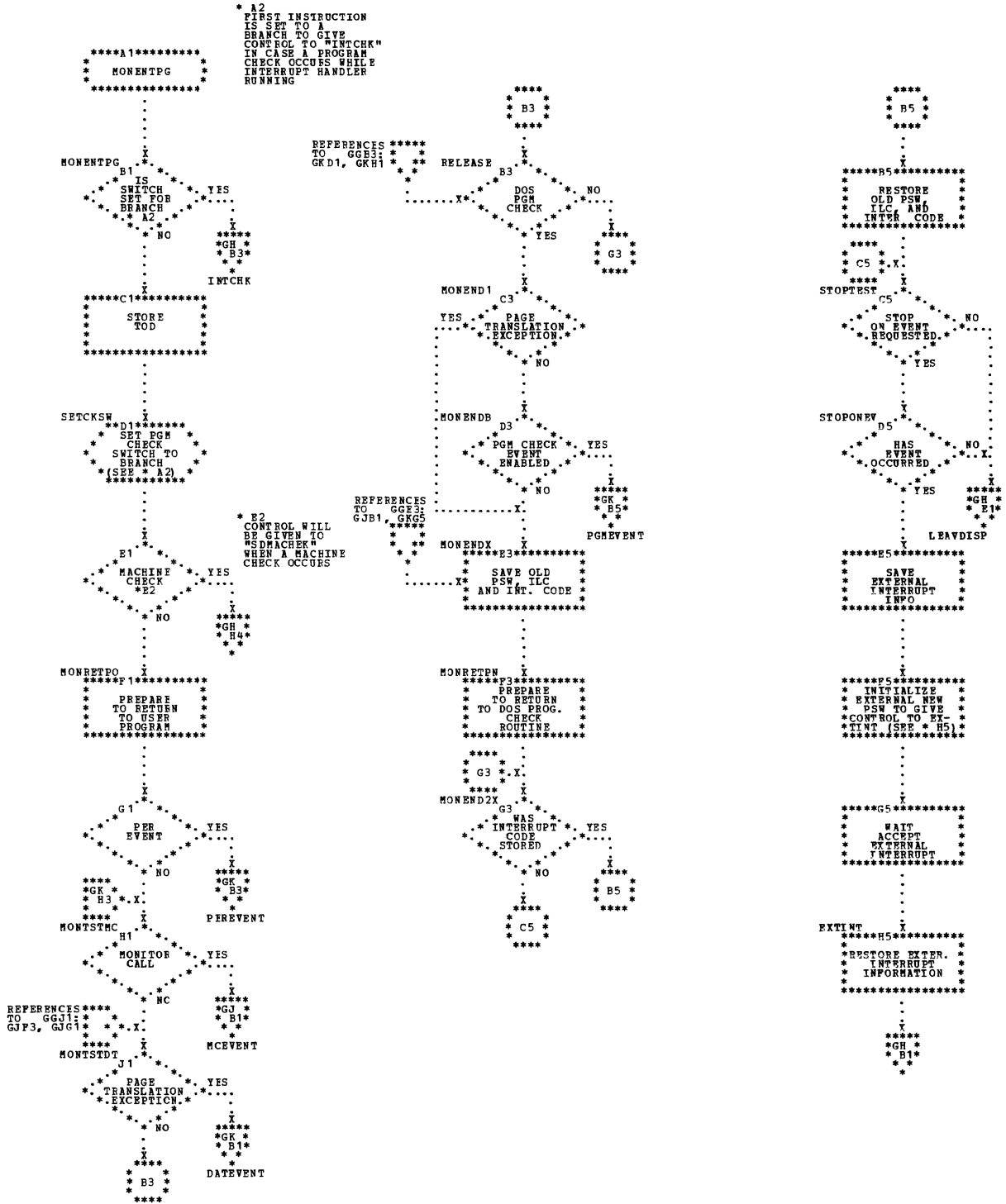




Chart GJ. MCEVENT

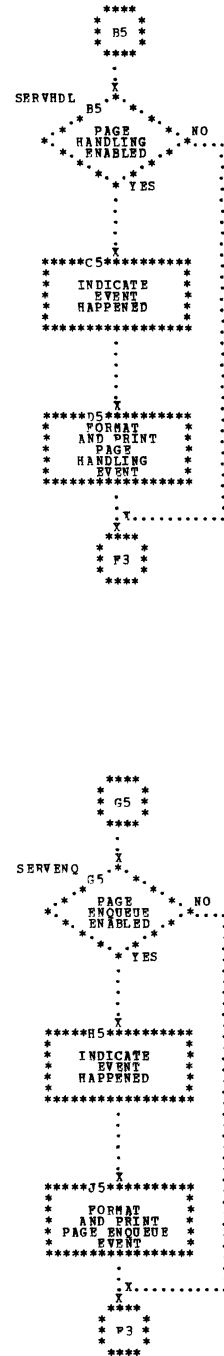
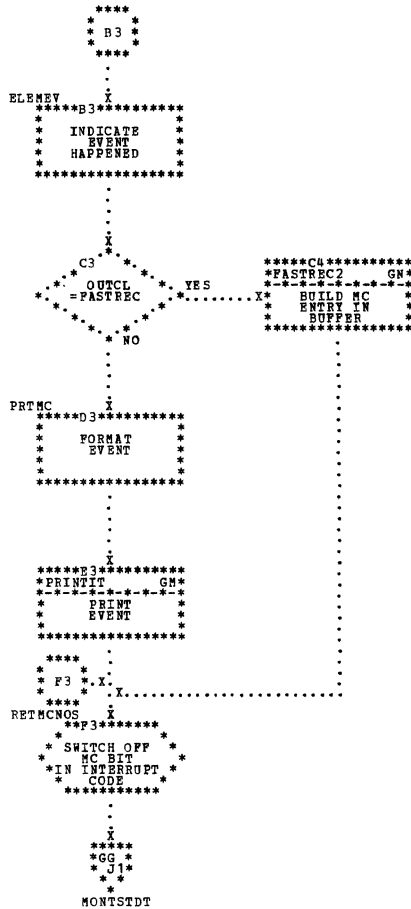
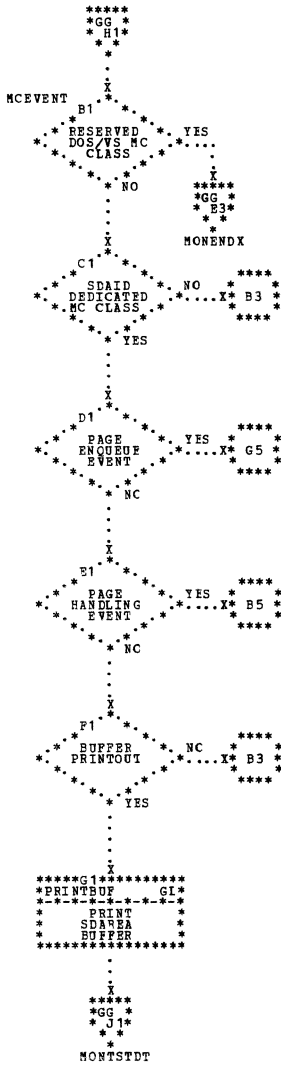


Chart GK. DATEVENT, PGMEVENT, and PEREVENT

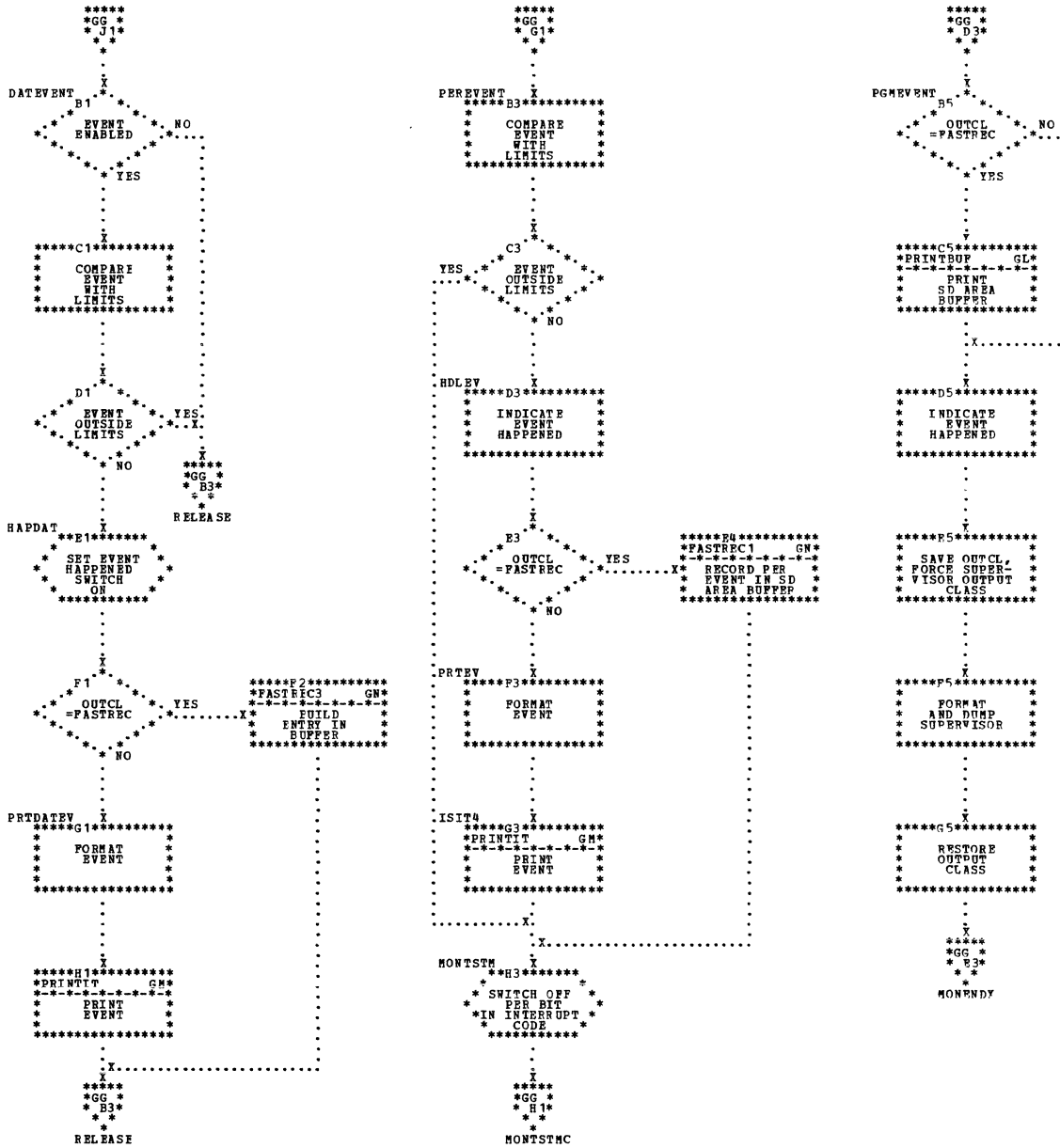




Chart GM. PRINTIT

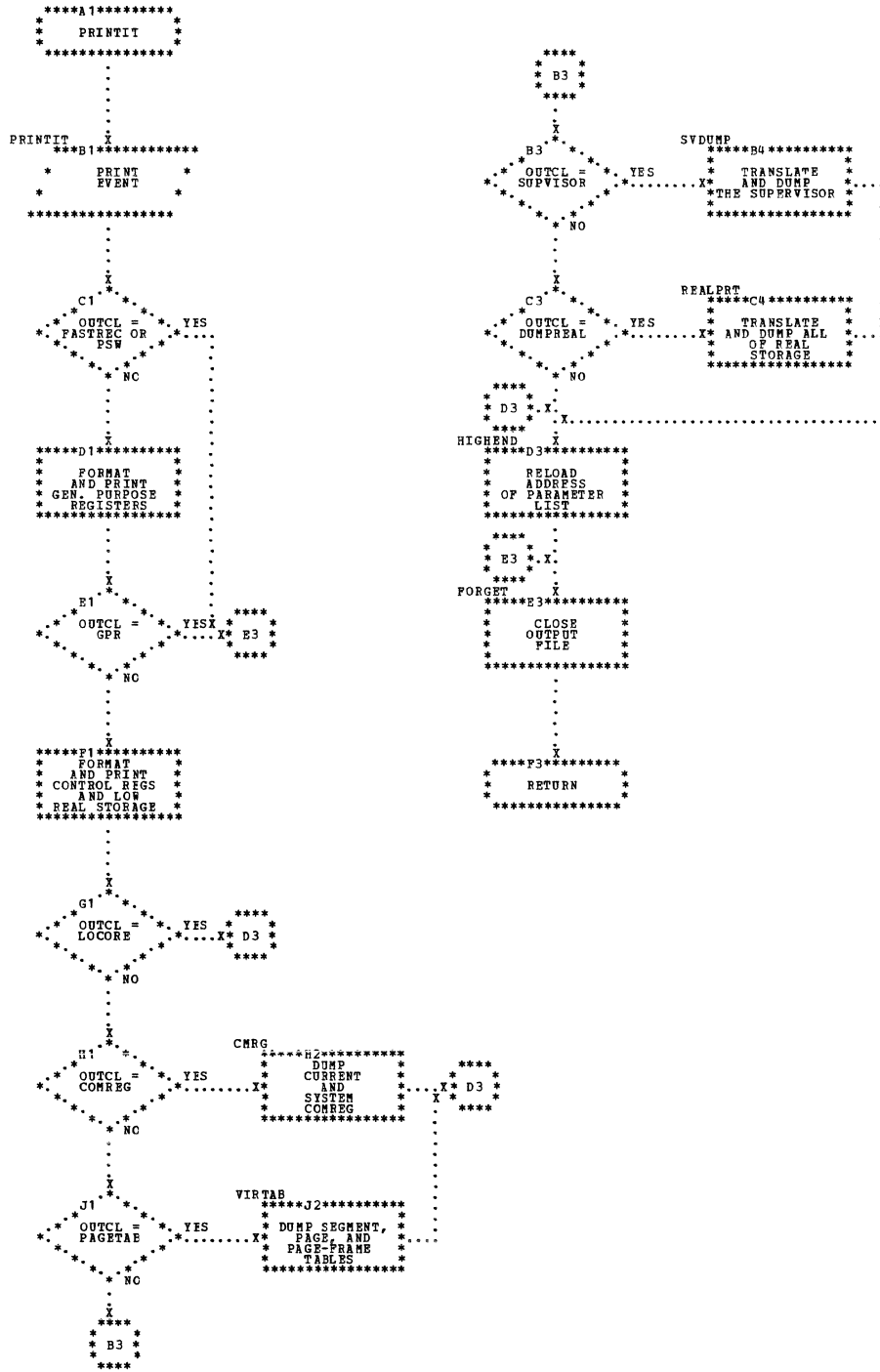
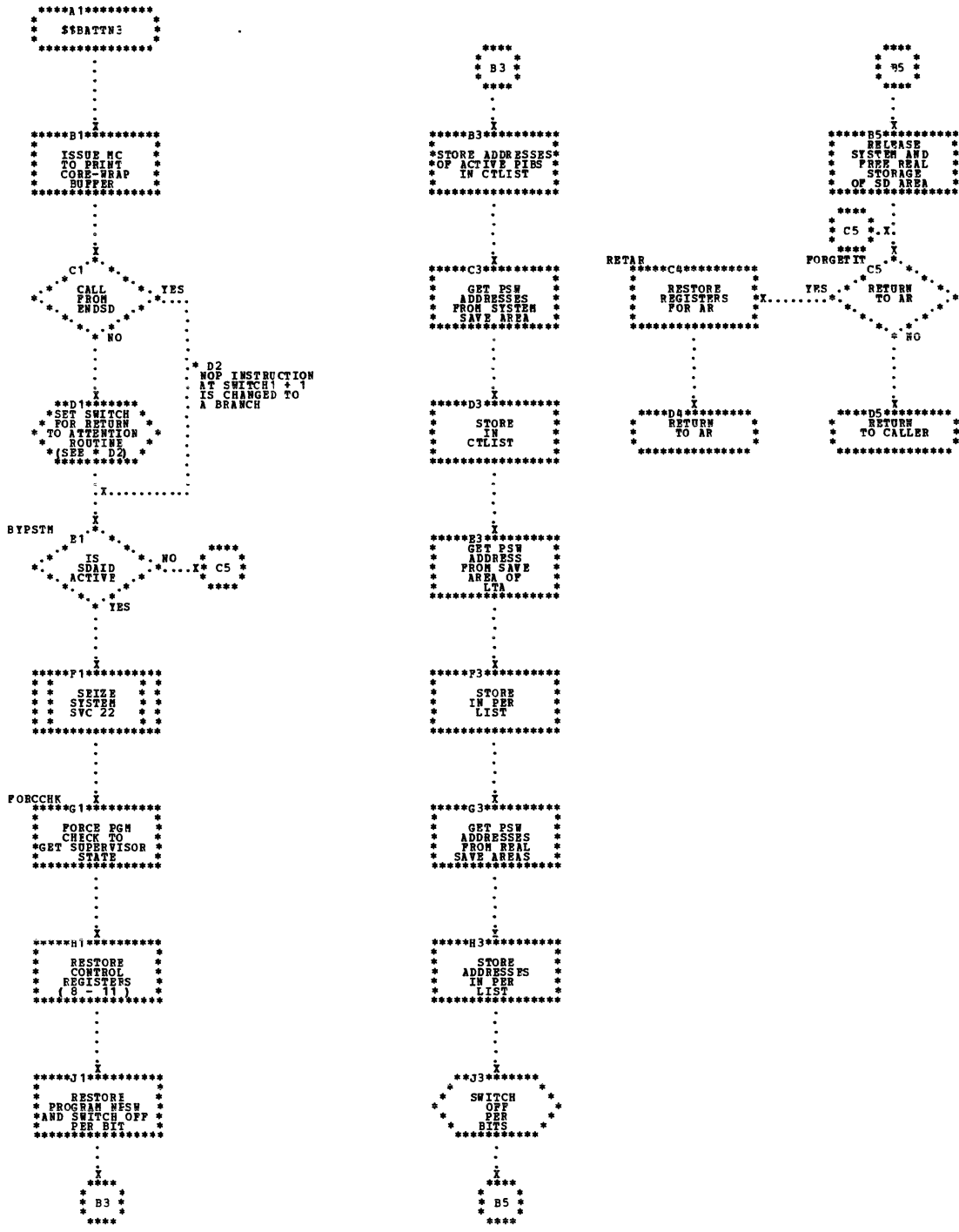






Chart GP. \$\$BATTN3



DUMPGEN generates a translating stand-alone dump program (called REALDUMP), which can be tailored to individual system requirements. Using the DUMPGEN CPTN control statements you can generate a stand-alone dump, which can dump the contents of real storage (unformatted or formatted with DOS-VS supervisor tables), as well as the formatted contents of the boundary box and the page pool.

**EXECUTING DUMPGEN**

A self-relocating copy of DUMPGEN is provided in the relocatable library of the IBM-supplied system, and must be cataloged to the core image library before execution. It can be executed in any partition by the command:

```
// EXEC DUMPGEN
```

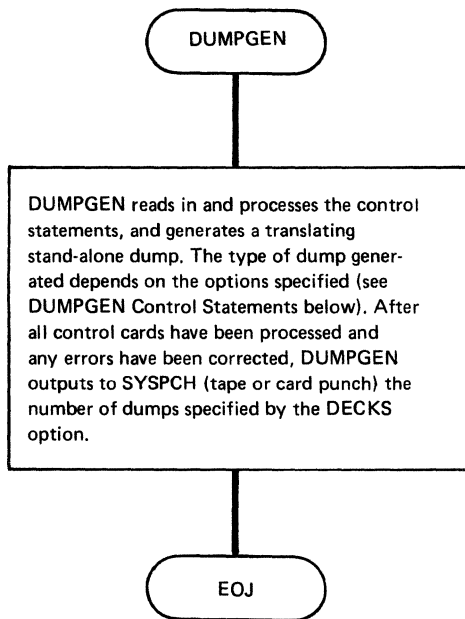


Chart 07. DUMPGEN (Dump Generator Program)

The entry point to DUMPGEN is BEGIN. The control statements (ASSGN and OPTN) are read from SYSIPT.

DUMPGEN Control Statements

**ASSGN:** The ASSGN control statement defines the output device for the stand-alone dump records. The format of the ASSGN statement is:

```
ASSGN SYSLST,X'cuu'
```

SYSLST is the only possible logical unit assignment.

X'cuu' must define the address of the SYSLST printer. If the ASSGN statement is omitted, X'00E' is assumed.

**OPTN:** The OPTN control statement is used to specify

- the type of printer-start control (manual or automatic),
- the number of card decks to be punched,
- the format of the dump,
- the type of tape output,
- a printout of the boundary box and page pool.

The format of the OPTN statement is:

CPTN operand

Figure 48 lists the operands for the OPTN statement. If any or all of the OPTN statements are omitted, the default operands, which are underlined, apply.

<u>INTR=YES</u>	Produces a dump program that, when loaded, enters the WAIT state. Either press the interrupt button to print the output on X'00E' or first press the STCP button and then the START button of the printer to be used as output.
<u>INTR=NO</u>	Produces a dump program that prints the output on the SYSLST printer defined with the ASSGN statement cr, if no ASSGN statement is specified, on X'00E'.
<u>DECKS=nnnnnnnn</u>	Specifies the number of REALDUMP card decks to be punched on SYSPCH. 1 to 99999999 may be specified. A blank card separates each deck produced.
<u>DECKS=1</u>	
<u>FORMAT=YES</u>	Produces a formatting dump that (if the EG communications region can be found) formats and displays the DOS/VIS supervisor tables.
<u>FORMAT=NO</u>	A non-formatting dump is generated.
<u>TAPEIPL=YES</u>	If SYSPCH is assigned to a tape unit, the records of the stand-alone dump program are written on tape; the dump program can be IPLed directly from the tape.
<u>TAPEIPL=NO</u>	If SYSPCH is assigned to a tape, the stand-alone dump program is written on tape preceded by an ASA character.
<u>PPOOL=YES</u>	The formatted contents of the boundary box and the page pool are printed (in sequence of ascending virtual addresses).
<u>PPOOL=NO</u>	No further information is printed.

Figure 48. OPTN Statement Operands

The control statements may be specified in any order and number; however, the following rules apply:

1. The last statement processed overrides any previous statements with the same operation and operand. For example, if DECKS=2 is followed by DECKS=5, five card decks are punched.
2. Decimal operands may contain leading zeros.
3. The operation code must be preceded by one or more blanks.
4. Only one operation and only one operand per control statement is allowed.
5. One or more blanks must follow the operand, if comments are desired.
6. The parameters (YES, NO, and nnnnnnnn) must follow the equal sign without intervening blanks.
7. Column 71 must be left blank.

#### DUMPGEN MESSAGES

The DUMPGEN-to-operator error message routines make it possible to:

- Cancel the job if SYSLOG is not a console printer keyboard or a display operator console
- Reissue the message if operator response is ALTERNATE CANCEL
- Process an operator response of END as IGNORE
- Cancel the job if operator response is CANCEL
- Ignore the control card in question when operator response is IGNORE.

If none of the preceding operator responses is issued, DUMPGEN assumes a correction has been made and processes it. See Appendix A for a cross-reference list of message numbers and flowcharts.

## REALDUMP (STAND-ALONE DUMP)

REALDUMP is the translating stand-alone dump generated by DUMPGEN. REALDUMP may be either formatting or non-formatting, depending on the FORMAT operand specified in the OPTN statement.

If FORMAT=NO was specified, the output from REALDUMP is a printout of:

- The general registers
- Formatted low real storage
- Real storage
- Page status information
- The control registers.

If FORMAT=YES was specified (and the BG communications region can be located), the DOS/VS supervisor tables are formatted and printed, following the dump of real storage.

For an example of the formatted REALDUMP output, refer to DOS/VS Serviceability Aids and Debugging Procedures, GC33-5380, Appendix G.

### EXECUTING REALDUMP

The stand-alone dump is loaded directly into the system (using the normal IPL

procedure), destroying the contents of bytes 0-23 (the IPL card) and the 210 bytes occupied by the dump program. These bytes are located in the program check area of the supervisor. The address of this area is contained in the last four bytes of the Program New PSW.

If information from any of these areas is needed, it must be manually displayed on the console, using the DM (display memory) command, before loading the dump program.

### REALDUMP OUTPUT

Each line of the output listing contains a maximum of eight fullwords. If the remaining portion of any line or group of lines is identical to the first word to be printed, the first word and the word "SAME" are printed. Printing is then suspended until a word with different characters is encountered.

If a line is identical to the line previously printed, the word SAME is printed and printing is suspended until a line with different contents is found.

The first and last line of every 2K block are printed, regardless of their contents.

Chart 08. Logic Flow of the REALDUMP Program

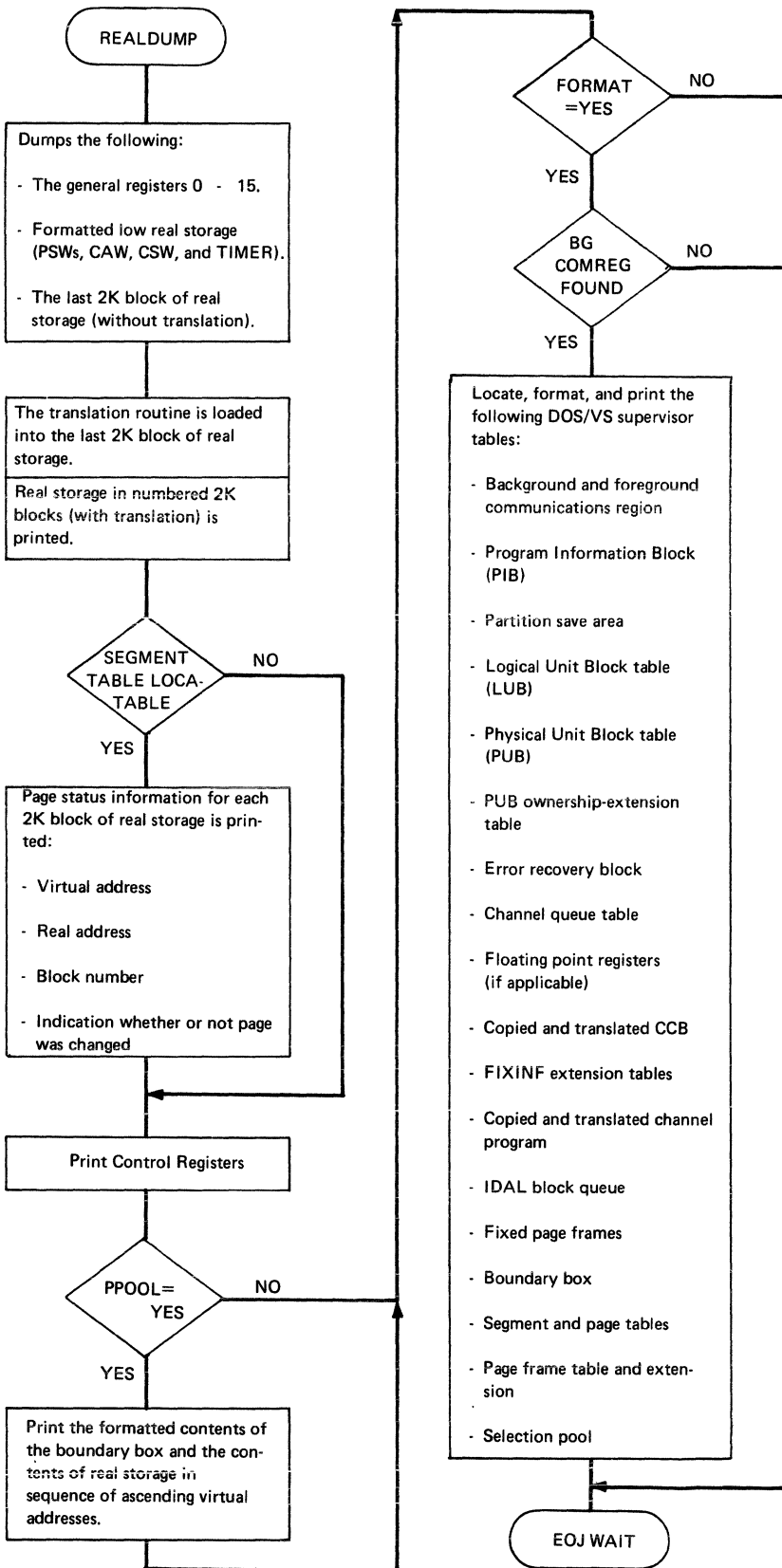
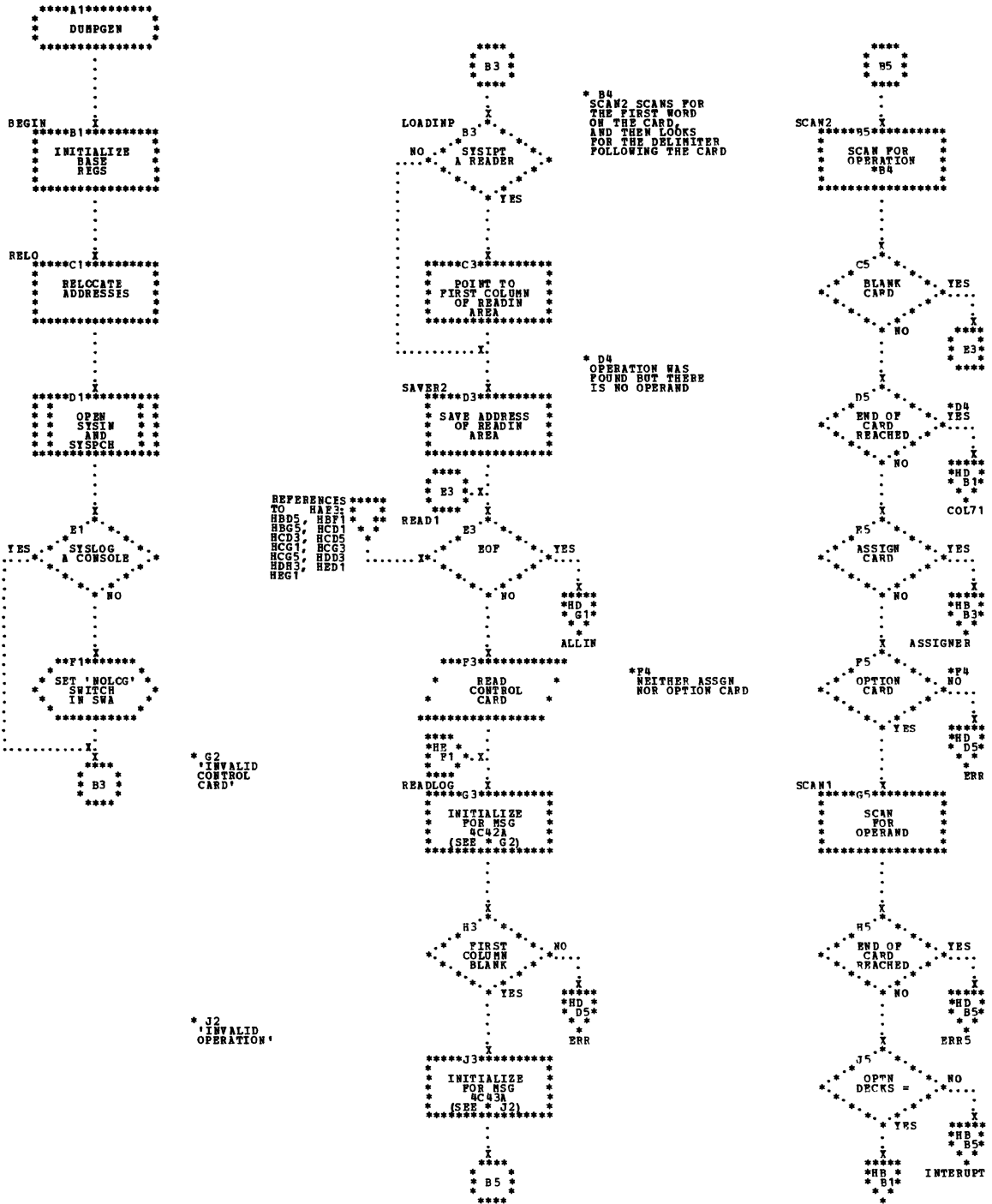


Chart HA. DUMPGEN (Part 1 of 5)



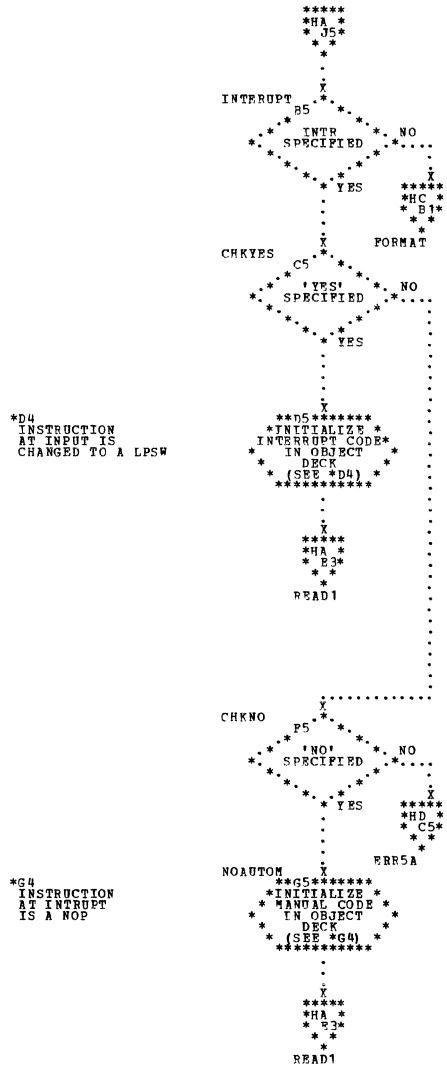
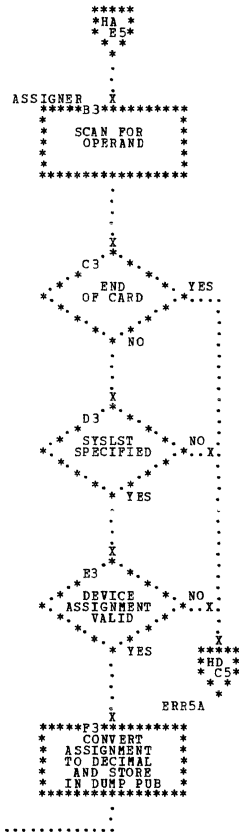
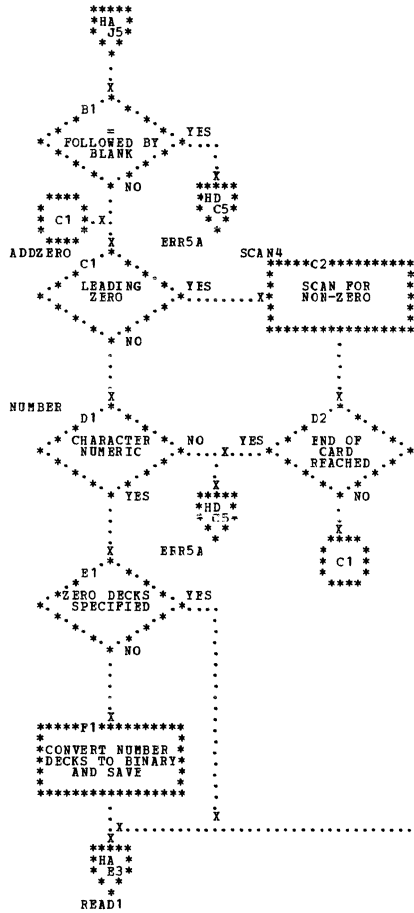










Chart HF. REALDUMP (Part 1 of 18)

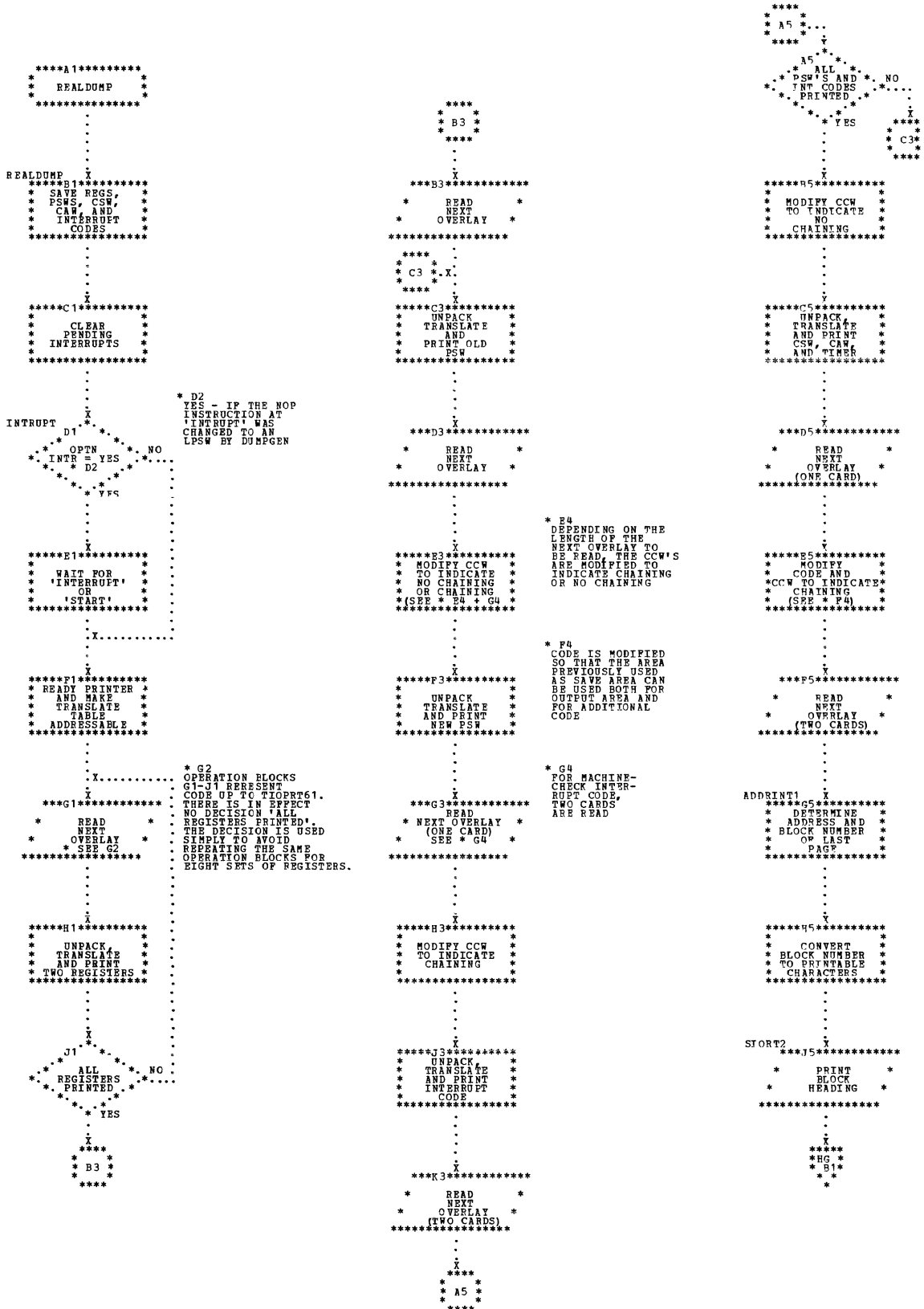
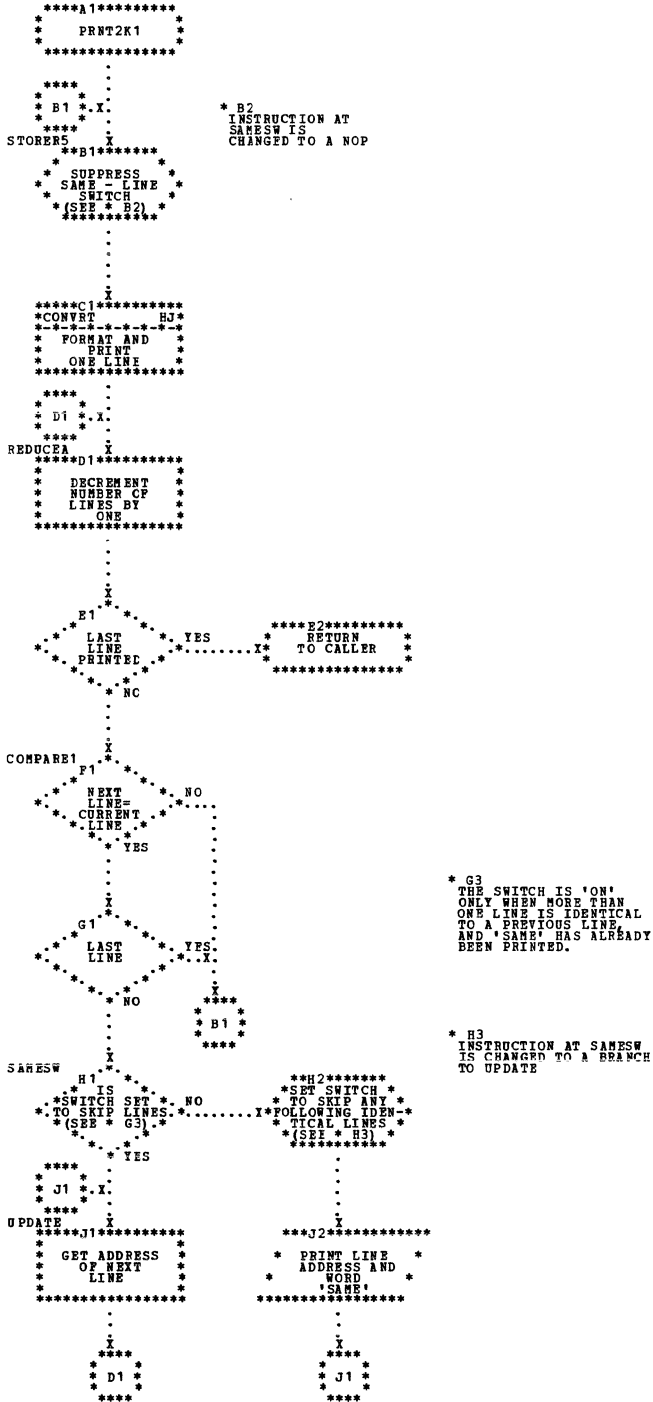




Chart HH. REALDUMP (Part 3 of 18)





```

*****
*HC *
* E3*
*
*
*
ENDUMP
*****B1*****
* PRINT *
* END OF *
* CORE DUMP' *
*****
*
*
*****C1*****
* READ IN NEXT *
* OVBHLY *
* (15 CARDS) *
*****
*
*
*****D1*****
* GET ADDRESS *
* OF SEGMENT *
* TABLE FROM *
* LOW REAL *
* STORAGE *
*****
*
*
E1
* ADDRESS *
* 0 *
*
* NO *
*
*
F1
* ADDRESS *
* IN SYSCOM= *
* ADDR IN *
* CR1 *
*
* YES *
*
*****F2*****
* PRINT ERROR *
* MESSAGE *
* (SEE *F3) *
*****
*
*
CHKCR1
G1
* ADDRESS *
* IN CR1 *
* VALID *
*
* YES *
*
*****G2*****
* PRINT *
* MESSAGE *
* (SEE *G3) *
*****
*
*
PGHINT2
H1
* ADDRESS *
* IN SYSCOM *
* VALID *
*
* YES *
*
*****H2*****
* PRINT *
* MESSAGE *
* (SEE *H3) *
*****
*
*
PGHINT3
*****J1*****
* PRINT *
* MESSAGE *
* (SEE *J2) *
*****
*
*****J2*****
* SEGMENT TABLE *
* CANNOT BE LOCATED' *
*****
*
*
*****K1*****
* SET SWITCH *
* TO SUPPRESS *
* EXECUTION *
* OF P POOL *
* CODE *
*****
*
*
PRNTR1
*****
*HL *
*D3*
*
*

```

```

*****
* B4 *
*
*
*
PRNTR1
*****B4*****
* PRINT *
* HEADING *
* FOR PAGESTATUS *
* INFORMATION *
*****
*
*
*HL *
* R1 *X. *
*
*
*****C4*****
* INITIALIZE NEW *
* ADDRESS OF *
* ERROR ROUTINE *
* (SEE *C5) *
*****
*
*
*
D4
* AREA *
* OUTSIDE REAL *
* STORAGE *
*
* NO *
*
*****
*HL *
*B5*
*
*
OUTSIDE1
E4
* SEGMENT *
* TABLE ENTRY *
* VALID *
*
* YES *
*
*****
*HL *
*D3*
*
*
PRNTR1
F4
* LENGTH *
* CODE = 15 *
*
* YES *
*
*****
*HL *
*G1 *X. *
*
*
*****
*HL *
*B3*
*
*
INVS1
PAGTAB1
G4
* INVALID *
* ADDRESS *
* SPACE *
*
* NO *
*
*****G3*****
* CR1 CONTENTS *
* IS TAKEN TO *
* LOCATE THE *
* SEGMENT TABLE' *
*****
*
*
H3
* IJBSGT *
* CONTENTS *
* IS TAKEN TO *
* LOCATE THE *
* SEGMENT *
* TABLE' *
*****
*
*
H4
* HAS *
* PAGE BEEN *
* USED *
*
* NO *
*
*****
*HL *
*B3*
*
*
J4
* PAGE *
* ON PDS *
*
* NO *
*
*****
*HL *
*B1*
*
*
*****K4*****
* MAKE VIRTUAL *
* AND REAL *
* ADDRESS *
* PRIVATE *
*****
*
*
*****
*HL *
*B1*
*
*

```



Chart HL. REALDUMP (Part 6 of 18)

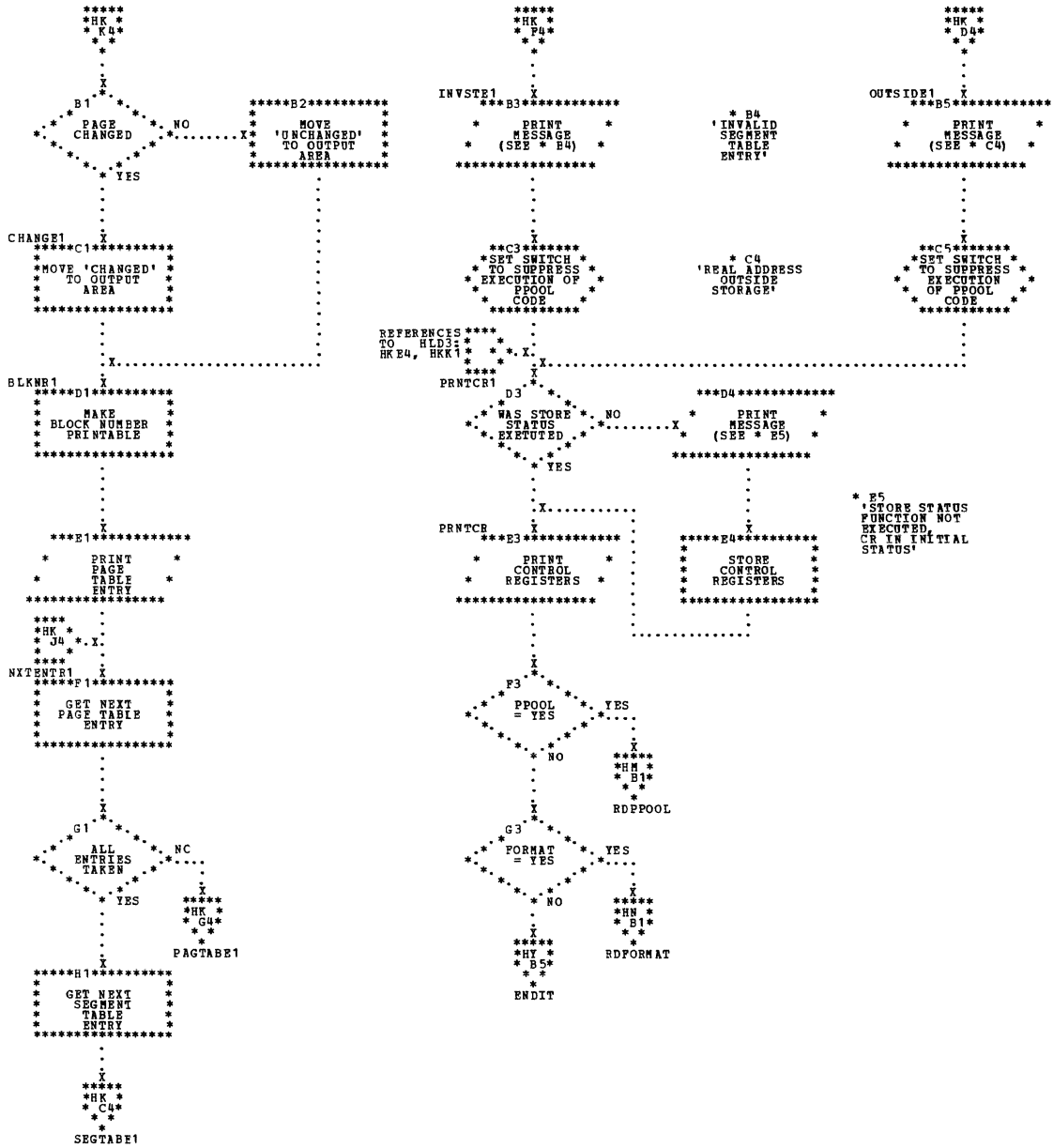


Chart HM. REALDUMP (Part 7 of 18)

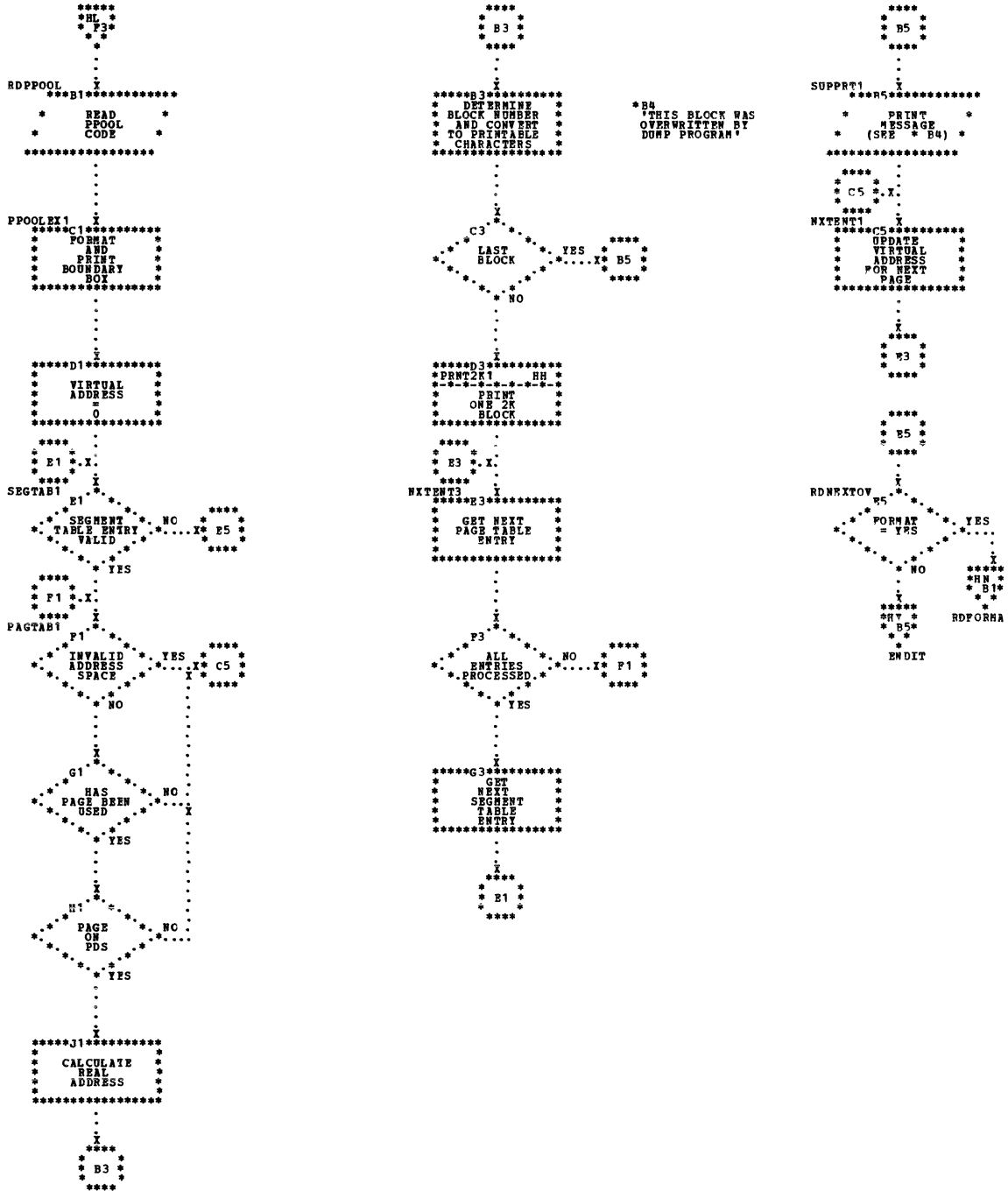
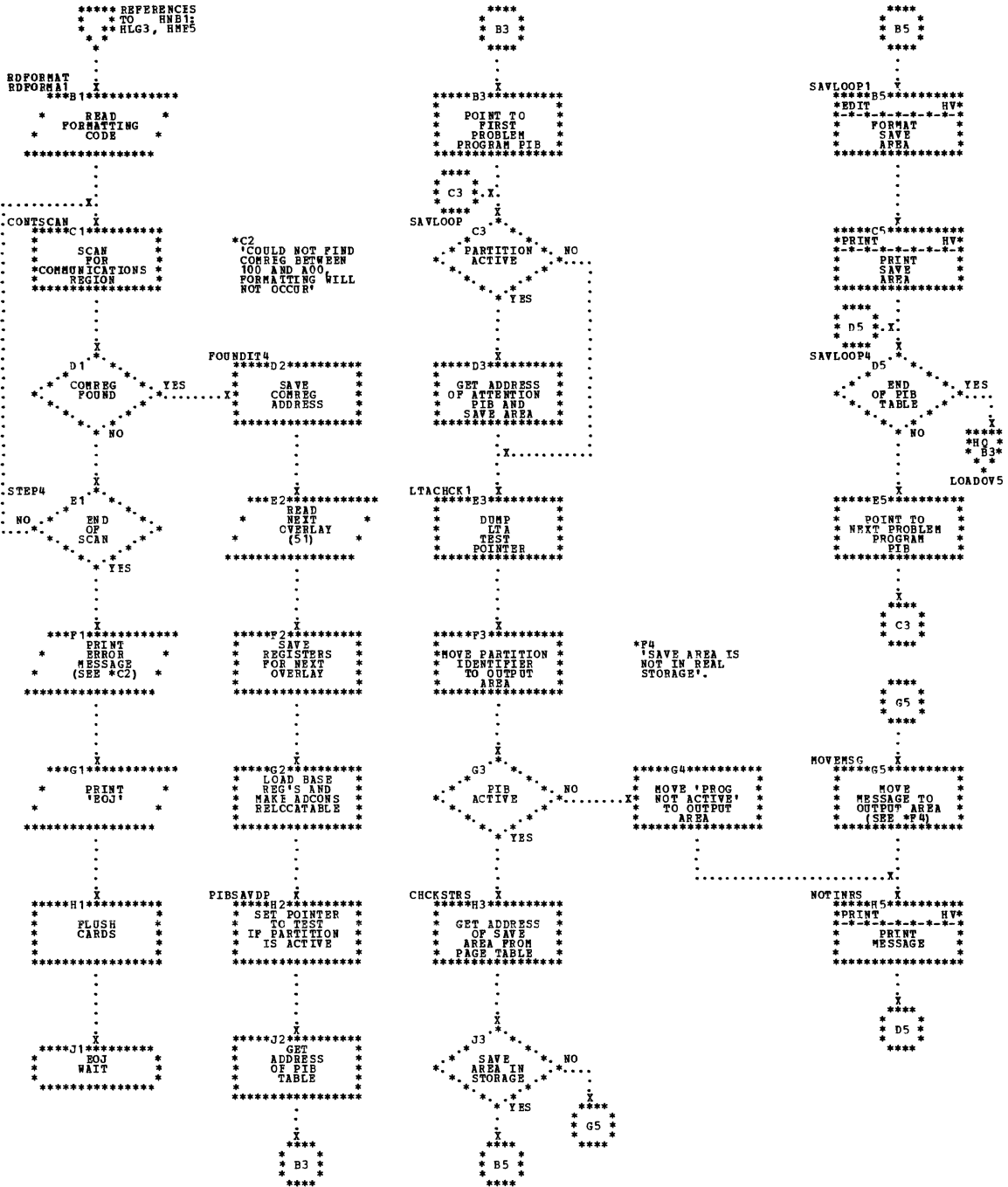


Chart HN. REALDUMP (part 8 of 18)







\* A2  
 NOTE: THE CODE FROM LUBLOOP TO LUBLOOP7  
 FORMATS AND PRINTS THE SYSTEM AND  
 PROGRAMMER LUBS FOR ALL PARTITIONS.  
 THE MESSAGES WITHIN THE CODE ARE  
 CHANGED (DEPENDING ON THE NUMBER OF  
 PARTITIONS) BY THE CODE LUBLOOP7  
 TO LU10

REFERENCES\*\*\*\*\*  
 TO HBBI: \* \*  
 HQB1, HQG1 \* \*

LUBCHCK X  
 \*\*\*\*\*B1\*\*\*\*\*  
 \* GET ADDRESS \*  
 \* OF LUB TABLE, \*  
 \* PUB TABLE, \*  
 \* PICL AND \*  
 \* \*\*\*\*\*

\* C2  
 FIRST TIME THROUGH:  
 'B3 SYSTEM LUBS'  
 LUBLOOP \*\*\*\*\*C1\*\*\*\*\*  
 \*PRINT HV \*  
 \*PRINT HEADING \*  
 \*FOR SYSTEM LUBS \*  
 \*(SEE \* C2) \*  
 \*\*\*\*\*

\*\*\*\*\*D1\*\*\*\*\*  
 \*EDIT HV \*  
 \*FORMAT \*  
 \*SYSTEM \*  
 \*LUB ENTRY \*  
 \*\*\*\*\*

\*\*\*\*\*E1\*\*\*\*\*  
 \* MOVE \*  
 \* ENTRY \*  
 \* TO OUTPUT \*  
 \* AREA \*  
 \*\*\*\*\*

F1 IS LUB UN-ASSIGNED \* YES \*  
 \* NO \*  
 \*\*\*\*\*

\*\*\*\*\*G1\*\*\*\*\*  
 \* GET DEVICE \*  
 \* ADDRESS \*  
 \* FROM \*  
 \* PUB TABLE \*  
 \*\*\*\*\*

\*\*\*\*\*H1\*\*\*\*\*  
 \*EDIT HV \*  
 \*FORMAT \*  
 \*DEVICE \*  
 \*ADDRESS \*  
 \*\*\*\*\*

\*\*\*\*\*J1\*\*\*\*\*  
 \* MOVE \*  
 \* ADDRESS \*  
 \* TO OUTPUT \*  
 \* AREA \*  
 \*\*\*\*\*

\*\*\*\*\*C3\*\*\*\*\*  
 \* \* \*  
 \*\*\*\*\*

\* C2  
 FIRST TIME THROUGH:  
 'B3 SYSTEM LUBS'  
 SECOND TIME THROUGH:  
 'F4 SYSTEM LUBS', ETC.

\*\*\*\*\*C3\*\*\*\*\*  
 \*PRINT HV \*  
 \*PRINT \*  
 \* SYSTEM LUB \*  
 \* ENTRY \*  
 \*\*\*\*\*

\*\*\*\*\*D3\*\*\*\*\*  
 \* POINT TO \*  
 \* NEXT \*  
 \* SYSTEM LUB \*  
 \* ENTRY \*  
 \*\*\*\*\*

E3 ALL SYSTEM LUBS PROCESSED \* NO \*  
 \* YES \*  
 \*\*\*\*\*

\*\*\*\*\*F3\*\*\*\*\*  
 \*PRINT HV \*  
 \*PRINT HEADING \*  
 \*FOR PROGRAMMER \*  
 \* LUBS \*  
 \*\*\*\*\*

\*\*\*\*\*G3\*\*\*\*\*  
 \*EDIT HV \*  
 \*FORMAT \*  
 \*PROGRAMMER \*  
 \* LUB ENTRY \*  
 \*\*\*\*\*

\*\*\*\*\*H3\*\*\*\*\*  
 \* MOVE \*  
 \* ENTRY TO \*  
 \* OUTPUT \*  
 \* AREA \*  
 \*\*\*\*\*

J3 IS LUB UN-ASSIGNED \* YES \*  
 \* NO \*  
 \*\*\*\*\*

\*\*\*\*\*B5\*\*\*\*\*  
 \* \* \*  
 \*\*\*\*\*

LUBLOOPS \*\*\*\*\*J4\*\*\*\*\*

\* MOVE 'UA' \*  
 \* TO OUTPUT \*  
 \* AREA \*  
 \*\*\*\*\*

\*\*\*\*\*P5\*\*\*\*\*  
 \* \* \*  
 \*\*\*\*\*

\*\*\*\*\*B5\*\*\*\*\*  
 \* \* \*  
 \*\*\*\*\*

\*\*\*\*\*B5\*\*\*\*\*  
 \* GET DEVICE \*  
 \* ADDRESS \*  
 \* FROM \*  
 \* PUB TABLE \*  
 \*\*\*\*\*

\*\*\*\*\*C5\*\*\*\*\*  
 \*EDIT HV \*  
 \*FORMAT \*  
 \*DEVICE \*  
 \*ADDRESS \*  
 \*\*\*\*\*

\*\*\*\*\*D5\*\*\*\*\*  
 \* MOVE DEVICE \*  
 \* ADDRESS TO \*  
 \* OUTPUT \*  
 \* AREA \*  
 \*\*\*\*\*

\*\*\*\*\*E5\*\*\*\*\*  
 \*PRINT HV \*  
 \*PRINT \*  
 \* PROGRAMMER \*  
 \* LUB ENTRY \*  
 \*\*\*\*\*

\*\*\*\*\*F5\*\*\*\*\*  
 \* POINT TO \*  
 \* NEXT \*  
 \* ENTRY \*  
 \*\*\*\*\*

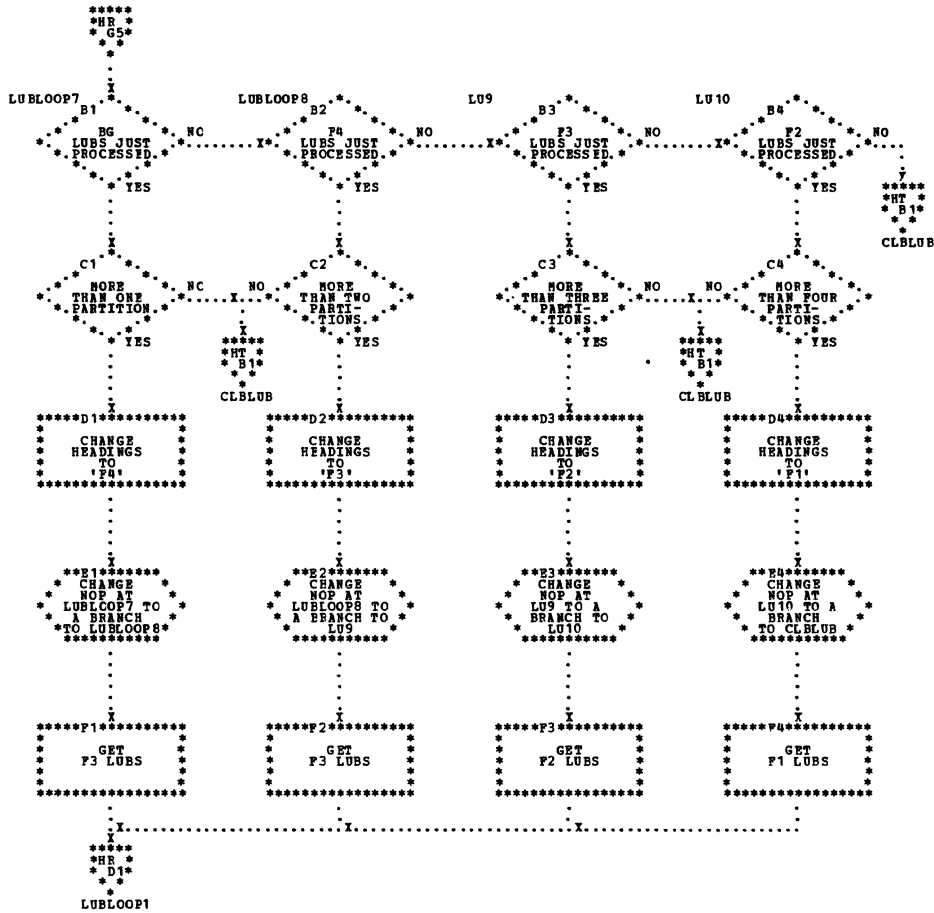
\*\*\*\*\*G5\*\*\*\*\*  
 \* ALL \*  
 \* PROG LUBS \*  
 \* PROCESSED \*  
 \*\*\*\*\*

\*\*\*\*\*H5\*\*\*\*\*  
 \* B1 \*  
 \* \*  
 \*\*\*\*\*

\*\*\*\*\*J4\*\*\*\*\*  
 \* MOVE 'UA' \*  
 \* TO OUTPUT \*  
 \* AREA \*  
 \*\*\*\*\*

\*\*\*\*\*P5\*\*\*\*\*  
 \* \* \*  
 \*\*\*\*\*

Chart HS. REALDUMP (Part 12 of 18)



\* NOTE  
 THE DECISIONS AT B1, B2, B3, AND B4  
 CONSIST OF A NOP AS THE FIRST INSTRUCTION  
 IN EACH BLOCK OF CODE. WHEN THE CODE IS  
 EXECUTED THE NOP IS CHANGED TO A BRANCH  
 TO THE NEXT SET OF CODE TO BE EXECUTED.  
 THAT IS, THE CODE FOR THE NEXT PARTITION.

REFERENCES\*\*\*\*\*  
TO HFB1: \* \* \*  
HSB4, HSC2 \* \* \*  
HSC4 \* \* \*

CLBLUB  
\*\*\*\*\*B1\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT HEADING \*  
\* 'PCIL LUBS' \*  
\*-----\*  
\*\*\*\*\*

\*\*\*\*\*C1\*\*\*\*\*  
\* GET ADDRESS \*  
\* OF BC PRIVATE \*  
\* CORP IMAGE \*  
\* LIBRARY \*  
\* LUB \*  
\*\*\*\*\*

\*\*\*\*\*D1\*\*\*\*\*  
\* SET \*  
\* PARTITION \*  
\* COUNTER \*  
\*\*\*\*\*

LISTCLB  
\*\*\*\*\*E1\*\*\*\*\*  
\*EDIT HV \*  
\*-----\*  
\* FORMAT \*  
\* LUB \*  
\* ENTRY \*  
\*\*\*\*\*

\*\*\*\*\*P1\*\*\*\*\*  
\* MOVE LUB \*  
\* ENTRY AND \*  
\* 'D1' TC \*  
\* OUTPUT AREA \*  
\*\*\*\*\*

\*\*\*\*\*G1\*\*\*\*\*  
\* IS UN- \*  
\* ASSIGNED \*  
\*-----\*  
\* YES \* \* \* \* \*  
\* \* \* \* \* X \* \* C3 \* \* \* \* \*  
\*-----\*  
\* NO \*  
\*\*\*\*\*

\*\*\*\*\*H1\*\*\*\*\*  
\* GET PHYSICAL \*  
\* UNIT ADDRESS \*  
\* (CUB) FROM \*  
\* PUB TABLE \*  
\*\*\*\*\*

\*\*\*\*\*J1\*\*\*\*\*  
\*EDIT HV \*  
\*-----\*  
\* FORMAT \*  
\* PHYSICAL UNIT \*  
\* ADDRESS \*  
\*\*\*\*\*

\*\*\*\*\*  
\* B3 \*  
\*\*\*\*\*

\*\*\*\*\*  
\* B3 \*  
\*\*\*\*\*

\*\*\*\*\*B3\*\*\*\*\*  
\* MOVE ADDRESS \*  
\* TO OUTPUT \*  
\* AREA \*  
\*\*\*\*\*

\*\*\*\*\*C3\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT \*  
\* LUB \*  
\*\*\*\*\*

\*\*\*\*\*D3\*\*\*\*\*  
\* POINT \*  
\* TO NEXT \*  
\* LUB \*  
\*\*\*\*\*

\*\*\*\*\*E3\*\*\*\*\*  
\* ALL \*  
\* LUBS \*  
\* PROCESSED \*  
\*-----\*  
\* YES \* \* \* \* \*  
\*-----\*  
\* NO \* \* \* \* \* X \* \* E1 \* \* \* \* \*

PUBCHCK  
\*\*\*\*\*P3\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT HEADINGS \*  
\* FOR \*  
\* PUB TABLE \*  
\*\*\*\*\*

\*\*\*\*\*G3\*\*\*\*\*  
\* GET ADDRESS \*  
\* OF SYSTEM \*  
\* COMEC AND \*  
\* PUB OWNERSHIP \*  
\* TABLE \*  
\*\*\*\*\*

PUBLOOP  
\*\*\*\*\*H3\*\*\*\*\*  
\*EDIT HV \*  
\*-----\*  
\* FORMAT \*  
\* PUB \*  
\* ENTRY \*  
\*\*\*\*\*

PUBLOOP2  
\*\*\*\*\*J3\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT PUB ENTRY \*  
\* AND PUB \*  
\* OWNERSHIP INFO \*  
\*\*\*\*\*

\*\*\*\*\*K3\*\*\*\*\*  
\* ALL \*  
\* PUBS \*  
\* PROCESSED \*  
\*-----\*  
\* YES \* \* \* \* \* X \* \* B5 \* \* \* \* \*

\*\*\*\*\*  
\* B5 \*  
\*\*\*\*\*

\*\*\*\*\*B5\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT HEADING \*  
\* 'ERROR RECOVERY \*  
\* BLOCK' \*  
\*\*\*\*\*

\*\*\*\*\*C5\*\*\*\*\*  
\* GET ADDRESS \*  
\* OF ERROR \*  
\* RECOVERY BLOCK \*  
\* AND MESSAGE \*  
\* TABLE \*  
\*\*\*\*\*

\*\*\*\*\*D5\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT PHASE \*  
\* NAME AND MESSA \*  
\* GE: 'FETCH NAME' \*  
\*\*\*\*\*

\*\*\*\*\*E5\*\*\*\*\*  
\* GET NEXT \*  
\* ENTRY AND \*  
\* NEXT MESSAGE \*  
\*\*\*\*\*

\*\*\*\*\*P5\*\*\*\*\*  
\*EDIT HV \*  
\*-----\*  
\* FORMAT \*  
\* ENTRY \*  
\*\*\*\*\*

\*\*\*\*\*G5\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT ENTRY \*  
\* WITH \*  
\* MESSAGE \*  
\*\*\*\*\*

\*\*\*\*\*H5\*\*\*\*\*  
\* ALL \*  
\* ENTRIES \*  
\* PROCESSED \*  
\*-----\*  
\* YES \* \* \* \* \*

\*\*\*\*\*J5\*\*\*\*\*  
\*PRINT HV \*  
\*-----\*  
\* PRINT HEADING \*  
\* FOR ERROR QUEUE \*  
\* ENTRIES \*  
\*\*\*\*\*

\*\*\*\*\*  
\* H1 \*  
\* B1 \*  
\*\*\*\*\*

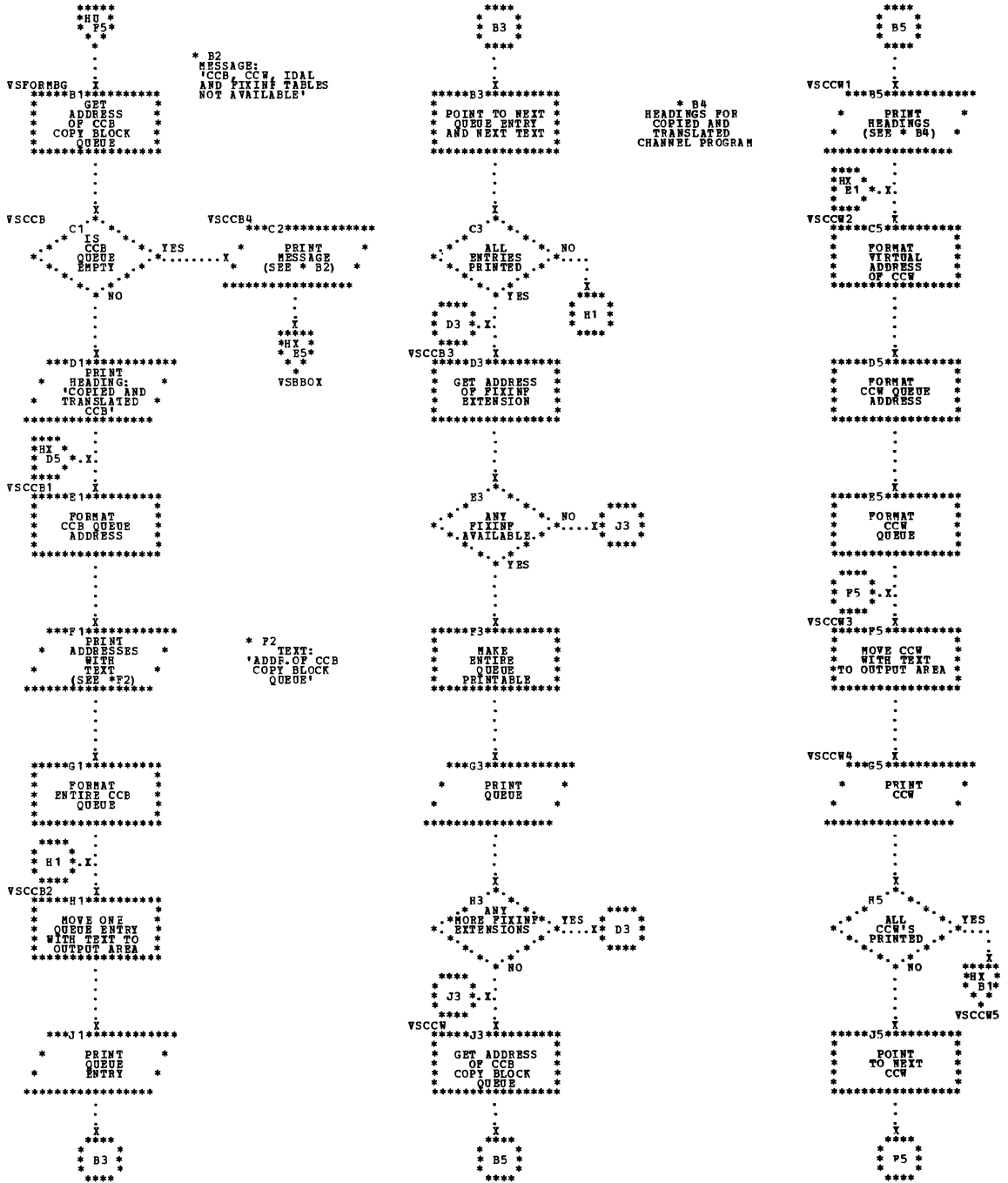


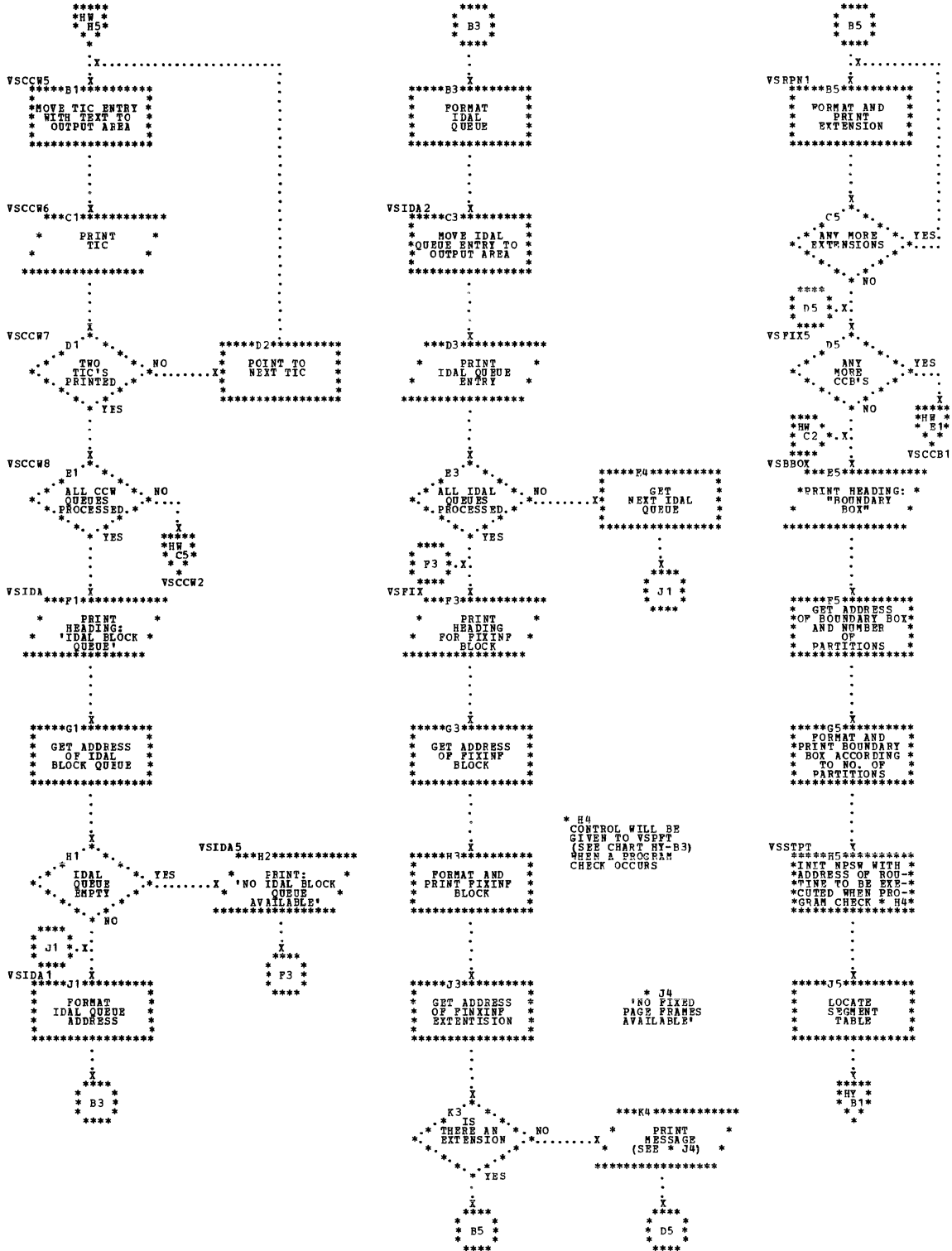


Chart HV. REALDUMP (Part 15 of 18)

```
*****A1*****  
* PRINT *  
*****  
.  
.  
.  
I  
PRINT *****B1*****  
* GET ADDRESS *  
* OF CCH *  
* AND STORE *  
* IT IN CSW *  
*****  
.  
.  
.  
I  
*****C1*****  
* PRINT *  
* ONE *  
* LINE *  
*****  
.  
.  
.  
I  
*****D1*****  
* CLEAR *  
* OUTPUT *  
* AREA *  
*****  
.  
.  
.  
I  
*****E1*****  
* RETURN *  
* TO CALLER *  
*****  
.  
.  
.  
I  
*****F1*****  
* EDIT *  
*****  
.  
.  
.  
I  
EDIT *****G1*****  
* UNPACK *  
* AND *  
* TRANSLATE *  
* ENTRY *  
*****  
.  
.  
.  
I  
*****H1*****  
* RETURN *  
* TO CALLER *  
*****
```

Chart HW. REALDUMP (Part 16 of 18)









FUNCTION

PDSDM is a program that dumps the page data set (SYSVIS). Depending on the parameters submitted on SYSLOG or SYSIPT, PDSDM dumps the contents of

- The entire page data set
- A specified virtual partition
- One or more pages of the page data set.

The dump is from

- SYSVIS to SYSLST or
- SYSVIS to SYS001 or
- SYS001 to SYSLST.

The page data set (SYSVIS) is always on disk, and is accessed by assigning SYS000 to the disk drive containing SYSVIS.

SYS001 is used to temporarily store the page data set dump. It must be assigned to disk or tape.

SYSLST may be assigned to a printer, a tape, or a disk drive.

The first parameter given to PDSDM specifies the logical unit (SYSLOG or SYSIPT) from which further parameters will be read. These parameters may be:

- Parameters that specify the logical units from and to which the selected part(s) are to be dumped.
- Parameters that specify the part(s) of the page data set to be dumped.

The output format is described under the heading 'Dump Page Data Set'.

This program is initiated by the job control statement // EXEC PDSDM. Refer to Section 2-C-6 of Serviceability Aids and Debugging Procedures, GC33-5380 for the proper label information.

FLOW OF CONTROL

The flow of control in PDSDM is as follows:

1. Initialize PDSDM
2. Accept parameters (Part 1), specifying dump input and output devices

3. Open files
4. Accept parameters (Part 2), specifying the part(s) of SYSVIS to be dumped
5. Dump page data set
6. Close files.

INITIALIZE PDSDM (CHART JA)

This part

- Loads the base registers
- Saves the contents of the boundary box, the number of partition supported, and the end address of real storage in the field F1STREC
- Opens SYSLOG file.

ACCEPT PARAMETERS - PART 1 (CHARTS JA AND JB), SPECIFYING DUMP INPUT AND OUTPUT DEVICES

This routine checks the parameters that specify the logical units from and to which the selected parts are to be dumped.

<u>Input</u>	<u>Action Taken</u>
IPT	Determines SYSIPT as input device for further parameters.
IOG	Determines SYSLOG as input device for further parameters.
Press END or ENTER on SYSLOG after LOG, or enter /* or /& on SYSIPT after // EXEC PDSDM	Dumps entire contents of page data set on SYS001 and terminates job.

TO=SYSLST                      Selects SYSVIS as input and SYSLST as output device for dump.

TO=SYS001                      Selects SYSVIS as input and SYS001 as output device for dump.

TO=SYSLST,T      Selects SYS001 as input and SYSLST as output device for dump.

The logical units selected for input and output are indicated in the field SWITCH.

In case of error, the incorrect parameter is marked with \* and a corresponding message is issued on SYSLOG. The invalid parameter may be corrected via SYSLOG.

OPEN FILES (CHARTS JB AND JJ)

The first part checks which input and output devices are assigned for the dump, tests for correct device assignments, and opens the corresponding files if the assignments are correct.

<u>Selected Output Device</u>	<u>Action Taken</u>
SYSLST	Opens SYSLST file.
SYS001	Tests if SYS001 is assigned to disk or tape. If yes, opens SYS001 file.

<u>Selected Input Device</u>	<u>Action Taken</u>
SYSVIS	Tests if SYS000 is assigned to disk. If yes, opens SYS000.
SYS001	Tests if SYS001 is assigned to disk or tape. If yes, opens SYS001.

In case of error, a corresponding message is issued on SYSLOG.

The second part tests if the dump is from

- SYSVIS to SYS001 or
- SYS001 to SYSLST or
- SYSVIS to SYSLST

and takes the following action:

<u>Dump From</u>	<u>Action Taken</u>
SYSVIS to SYS001	Writes the following information as first record on SYS001: internal file ID, number of partitions supported, end address of real storage, and contents of the boundary box.

SYS001 to SYSLST      Reads first record from SYS001 with above described contents and saves it in the field F1STREC.

SYSVIS to SYSLST      None.

ACCEPT PARAMETERS - PART 2 (CHARTS JC, JD, AND JG) SPECIFYING THE PART(S) OF SYSVIS TO BE DUMPED

This routine tests the parameters that specify the part(s) of the page data set to be dumped. The parameters tested and the corresponding actions taken are:

<u>Input</u>	<u>Action Taken</u>
(address,address)	Checks whether start and end addresses of selected pages are valid, translates them into record numbers and saves them in the table PARAM.
partition ID (F1,F2,F3,F4, or BG)	Checks for partition(s) specified, gets corresponding start and end addresses, translates them into record numbers, and saves them in the table PARAM.
address	Checks if address is valid, translates it into two equal record numbers and saves them in the table PARAM.
EOJ on SYSLOG	Terminates job.
IPT on SYSLOG	Switches from SYSLOG TO SYSIPT to read further parameters.
Press END or ENTER on SYSLOG	If this key is pressed after the message GIVE PARAMETERS has been issued, the contents of the entire page data set are dumped to the output device selected previously.

/\* or /% indicates the end of input to FDSM, if read from SYSIPT. If this card is encountered immediately after the card containing TO=, the entire page data set is dumped to the output device specified, and the job is terminated.



When all parameters have been checked and processed, control is passed to the dump page data set routine PRINT. After completion of the dump, control is returned for further parameter processing.

In case of error, the invalid parameter is marked with \*, and a corresponding message is issued on SYSLOG. Reading of input is switched to SYSLOG, and new parameters may be entered, or the correct parameters up to the invalid one may be processed.

#### DUMP PAGE DATA SET (CHARTS JE, JF, AND JH)

This routine dumps the part(s) selected from the page data set to the output device specified.

The format of the dump depends on the output device specified:

<u>Output Device</u>	<u>Dump Format</u>
SYS001	Blocks of 2K.
SYSLST	Each block has a block number, starting with 0 from the first page of the page data set. Printed on SYSLST, each line contains 32 bytes of the page data set in hexadecimal format, followed by the translation

in printable characters. Identical words and lines are printed only once followed by the word '--SAME--', and blocks containing only X'00' are not printed. Instead, the message 'BLOCK x (begin address - end address) CONTAINS ZEROS' is issued, or, if more than one block contains zeros, the message 'BLOCKS x-y (begin address - end address) CONTAIN ZEROS' is issued.

When all records indicated in the table PARAM have been processed, control is returned to the parameter acceptance routine.

#### CLOSE FILES (CHART JJ)

This part closes

- The input and output files used for the page data set dump
- The SYSLOG file
- The SYSIPT file, if it was opened.

In addition, any cards remaining in the reader are flushed. Finally, the program PDSDM is terminated.

Chart JA. PDSM - Parameter Acceptance (Part 1 of 4)

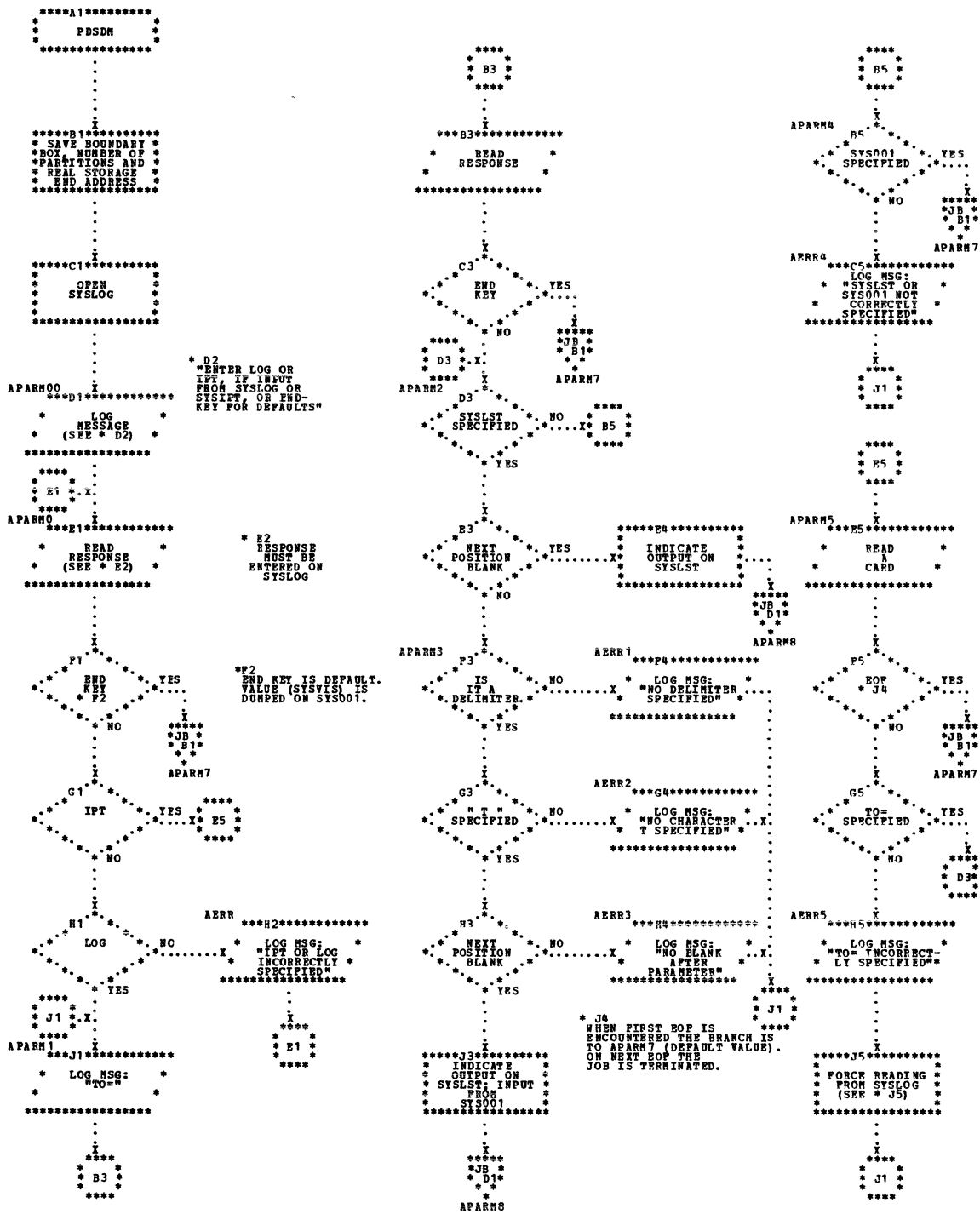








Chart JE. PSDSDM - PRINT

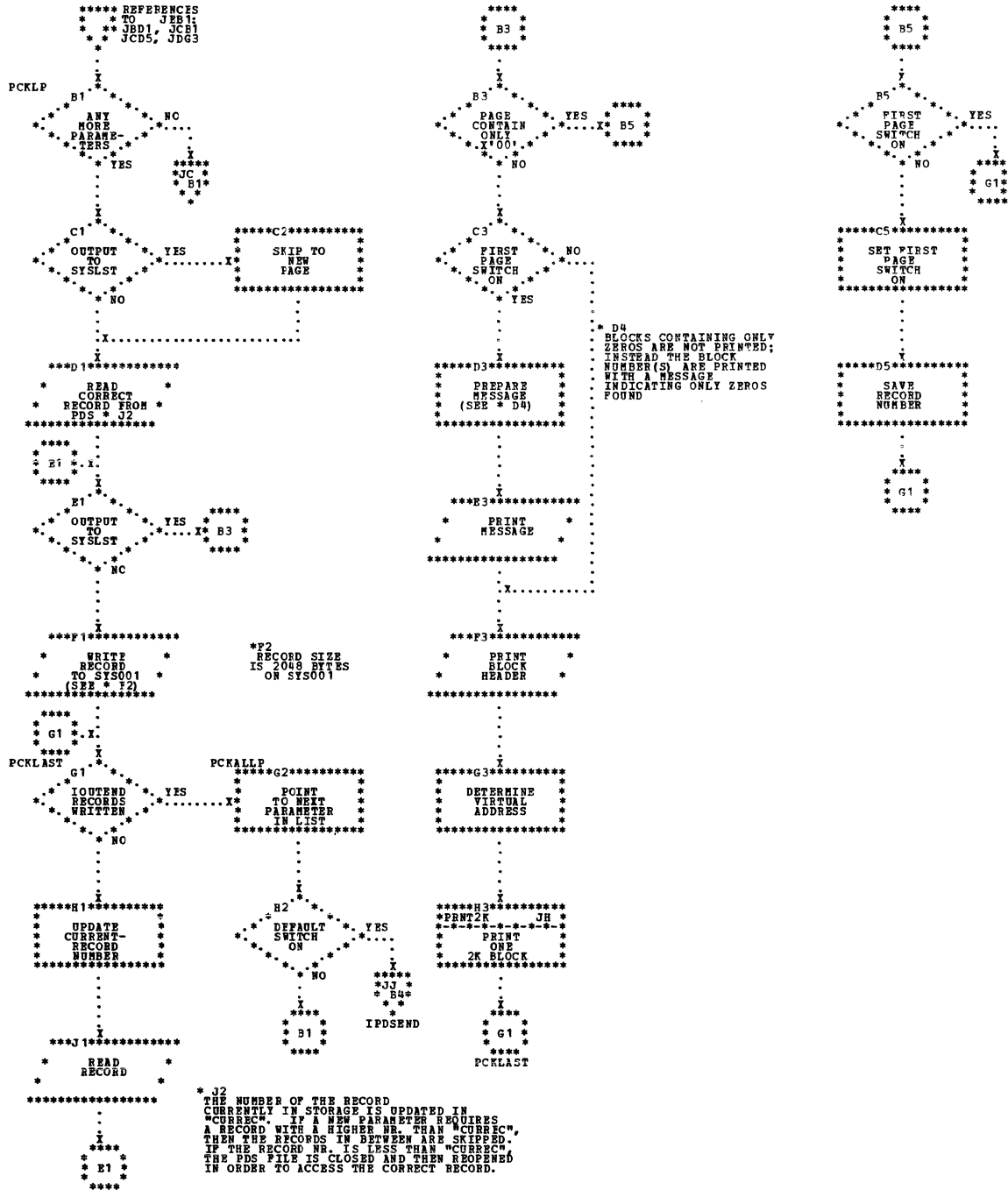




Chart JG. PDSDM - ADDRCHK

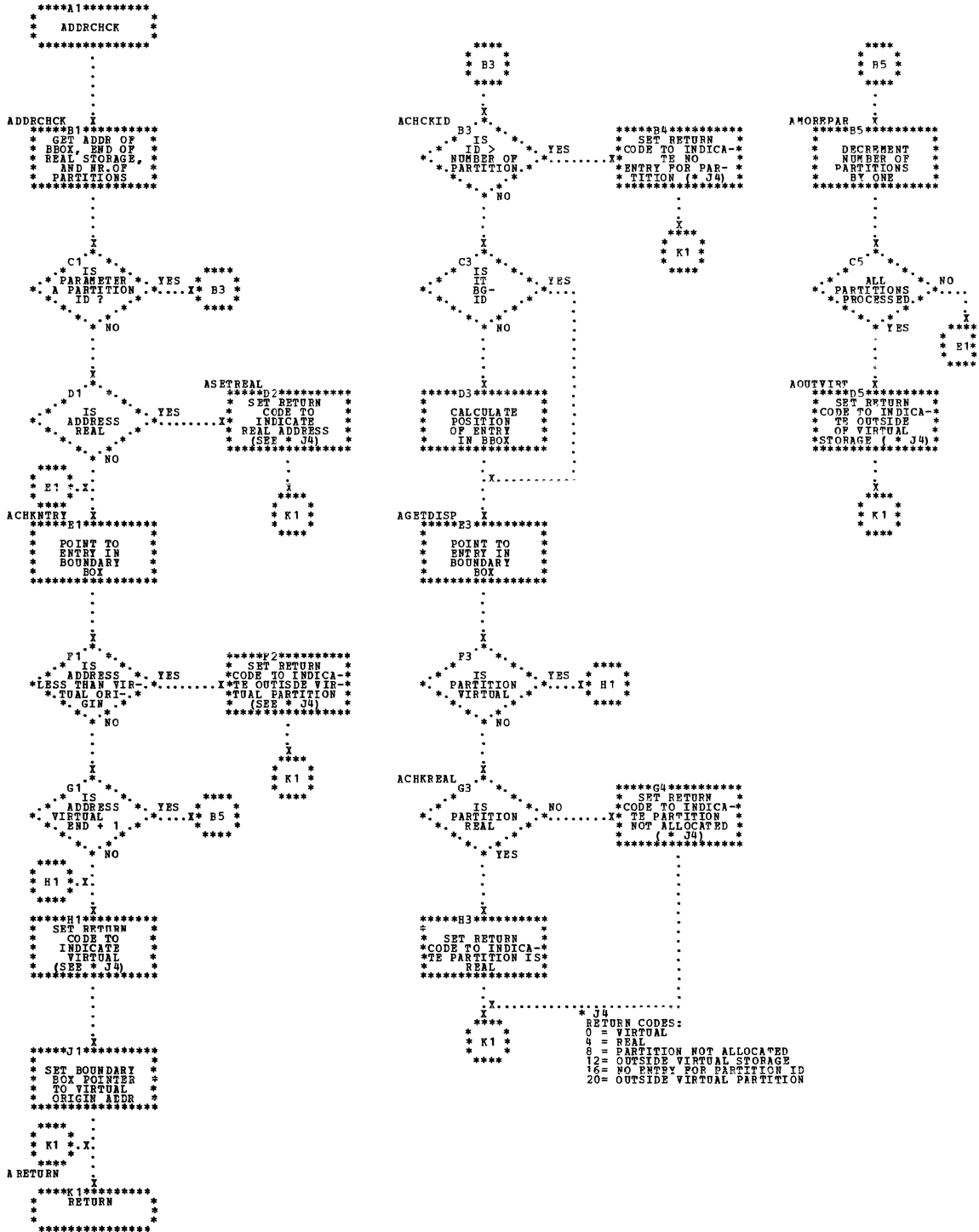




Chart JH. PDSDM - PRNT2K

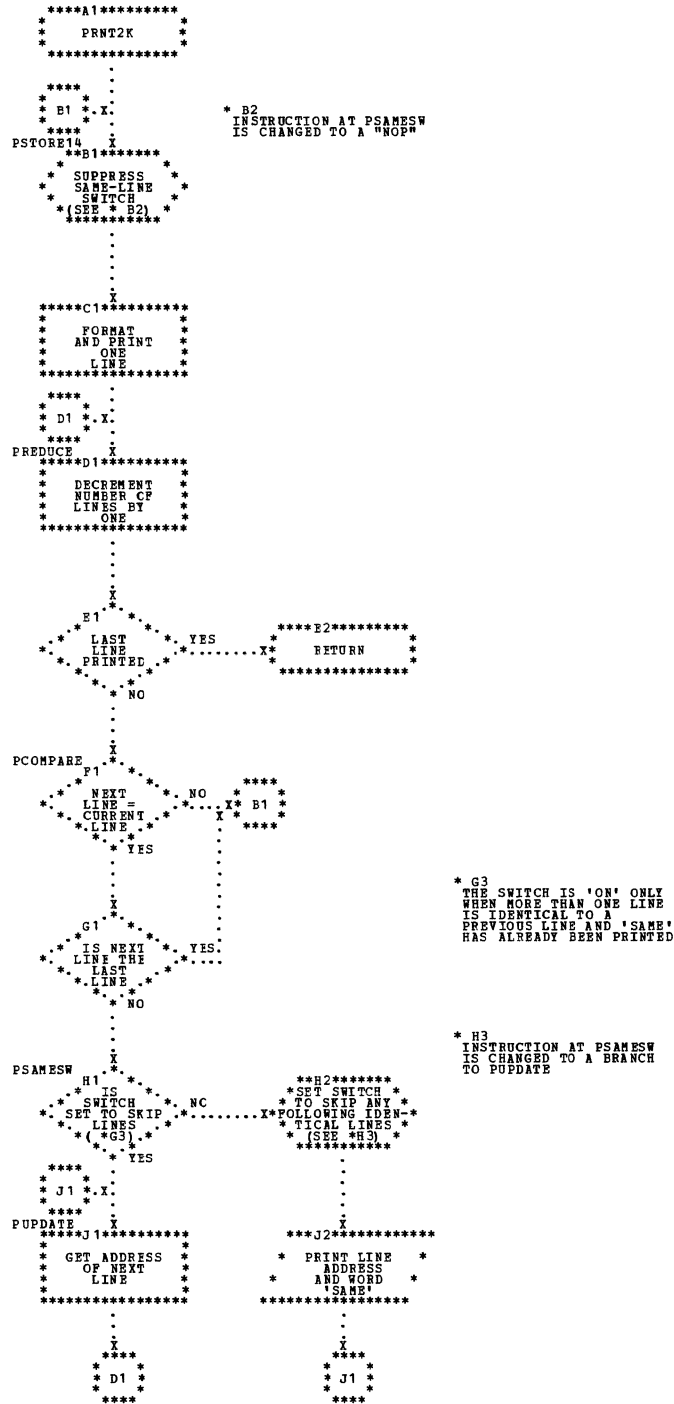
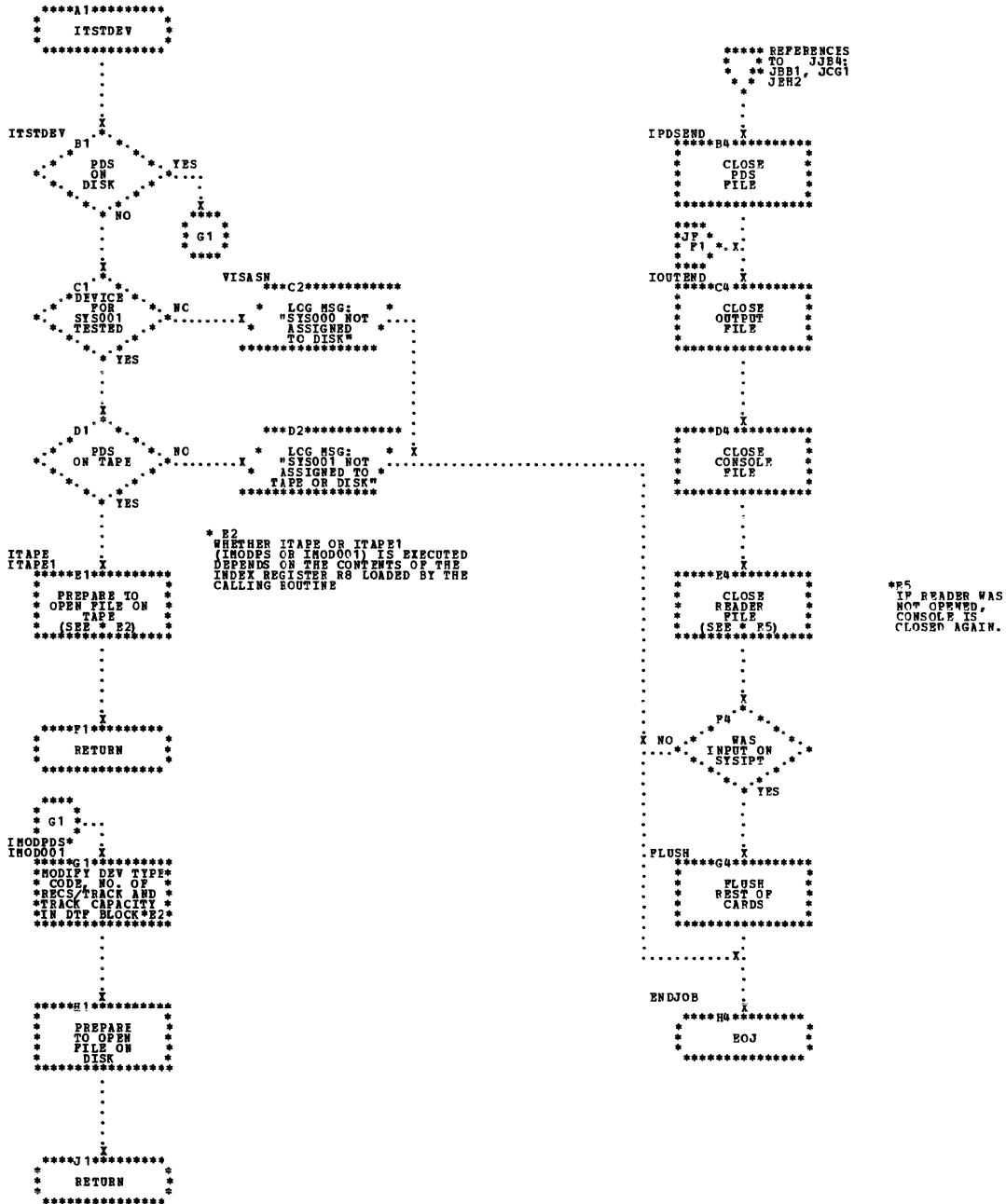


Chart JJ. PDSM - ITSTDEV and IPDSEND



This program displays on SYSLST the contents of the label information cylinder of SYSRES.

The label information contained in the different label information cylinder areas are selected from the following job control file definition statement cards:

The layout of the label information cylinder depends on:

- For disk label . DLBL, EXTENT . VOL, DLAP, XTENT

- SYSRES device type
- The number of partitions defined at system generation time.

- For tape label . TLBL . VOL, TPLAB

The different possible combinations are described in Figure 49.

These cards are read from SYSRDR and the information is formatted and written on SYSRES by job control.

Device Type		Track	Track Allocation	Number of Partitions						
				5	4	3	2	1		
2314/2319	3330/3333	3340	00	USRLABEL						
			01	BG PARSTD						
			02	F4 USRLABEL						
			03	F4 PARSTD						
			04	F3 USRLABEL						
			05	F3 PARSTD						
			06	F2 USRLABEL						
			07	F2 PARSTD						
			08	F1 USRLABEL						
			09	F1 PARSTD						
			10	Standard Labels for all partitions						
			11							
			12							
			13							
			14							
			15							
			16							
			17							
			18							
19										

Figure 49. Label Information Cylinder Layout

The format of the label information records is described in Figure 50.

The area in which the records are written depends on the user-specified job control OPTION statement.

LSERV is executed by the use of the following job control statement from either SYSLOG or SYSRDR:

```
// EXEC LSERV
```

If LSERV runs real, the partition must have at least 8K bytes of real storage allocated.

LSERV assumes that the label information cylinder on SYSRES is formatted as described in Figure 49 and that the formats of the label information records correspond to those described in Figure 50.

If a defined area contains no label information records or only data-secured file label information records, a message is printed indicating that no records are present in this area.

Otherwise the label information records (except for data-secured files) are formatted and printed on SYSIST. Figure 51 shows a sample output of LSERV.

TAPE			DISK				
	Field	Length		Field	Length		
Key	1	File Name	7	Key	1	File Name	7
	2	Not Used	1		2	Not Used	1
Data	1	Same as Key Field	8	Data	1	Same as Key Field	8
	2	Not Used	1		2	DLBL-Extent Indicator	1
	3	File Name	7		3	File Name	7
	4	Not Used	1		4	DA-IS Switch	1
	5	File Identifier	17		5	File Identifier	44
	6	File Serial Number	6		6	File Serial Number	6
	7	Volume Sequence Number	4		7	Volume Sequence Number	2
	8	File Sequence Number	4		8	Creation Date	3
	9	Generation Number	4		9	Expiration Date	3
	10	Creation Date	2		10	Retention Period	2
	11	Expiration Date	6		11	Open Code	1
	12	File Security Number	1		12	System Code	13
	13	Block Count	6		13	Volume Serial Number	6
	14	System Code	13		14	Extent Type	1
	15	Flag Bytes	2		15	Extent Sequence Number	1
			16		Extent Lower Limit	4	
			17		Extent Upper Limit	4	
			18		Logical Unit Address	2	
			19		2321 Lower Cell	1	
			20		2321 Upper Cell	1	
				Another extent if present for DA or IS files			

Note: For sequential disk files, a complete block is repeated for each additional extent. For direct-access or ISAM files, only fields 14 through 20 are repeated for each extent.

Figure 50. Format of the Label Information Records

DOS/VS LABEL CYLINDER DISPLAY

SYSRES VOLUME SERIAL NUMBER - DRV3.0

BG	USER LABELS (TEMPORARY PER PARTITION) TRACK 0 NONE
BG	PARTITION STANDARD LABELS (PERMANENT) TRACK 1 NONE
F4	USER LABELS (TEMPORARY PER PARTITION) TRACK 2 NONE
F4	PARTITION STANDARD LABELS (PERMANENT) TRACK 3 NONE
F3	USER LABELS (TEMPORARY PER PARTITION) TRACK 4 NONE
F3	PARTITION STANDARD LABELS (PERMANENT) TRACK 5 NONE
F2	USER LABELS (TEMPORARY PER PARTITION) TRACK 6 NONE
F2	PARTITION STANDARD LABELS (PERMANENT) TRACK 7 NONE
F1	USER LABELS (TEMPORARY PER PARTITION) TRACK 8 NONE
F1	PARTITION STANDARD LABELS (PERMANENT) TRACK 9 NONE

Figure 51. Sample LSERV Output (Part 1 of 2)

DOS/VS LABEL CYLINDER DISPLAY

STANDARD LABELS (ALL PARTITIONS-PERMANENT)

IJSYSRS

FILE IDENTIFIER	DOS.SYSRES.FILE
FILE SERIAL NUMBER	DRV3.0
VOLUME SEQUENCE NUMBER	01
CREATION DATE	OMITTED
EXPIRATION DATE	99/365
FILE TYPE	SEQUENTIAL

EXTENT INFORMATION

EXTENT SEQUENCE NUMBER	01
EXTENT TYPE	1 (PRIME DATA)
RELATIVE (TO ZERO) START ADDRESS IN TRACKS	0001
NUMBER OF TRACKS	0739
SYMBOLIC UNIT	SYSRES CCB FORMAT 0006
VOLUME SERIAL NUMBER	DRV3.0

IJSYSCL

FILE IDENTIFIER	DOS.SYSCLB.FILE
FILE SERIAL NUMBER	DRV3.0
VOLUME SEQUENCE NUMBER	01
CREATION DATE	OMITTED
EXPIRATION DATE	99/365
FILE TYPE	SEQUENTIAL

EXTENT INFORMATION

EXTENT SEQUENCE NUMBER	01	
EXTENT TYPE	1 (PRIME DATA)	
EXTENT LOWER LIMIT	CYLINDER	037
	HEAD	00
EXTENT UPPER LIMIT	CYLINDER	068
	HEAD	19
SYMBOLIC UNIT	SYSCLB CCB FORMAT 000B	
VOLUME SERIAL NUMBER	DRV3.0	

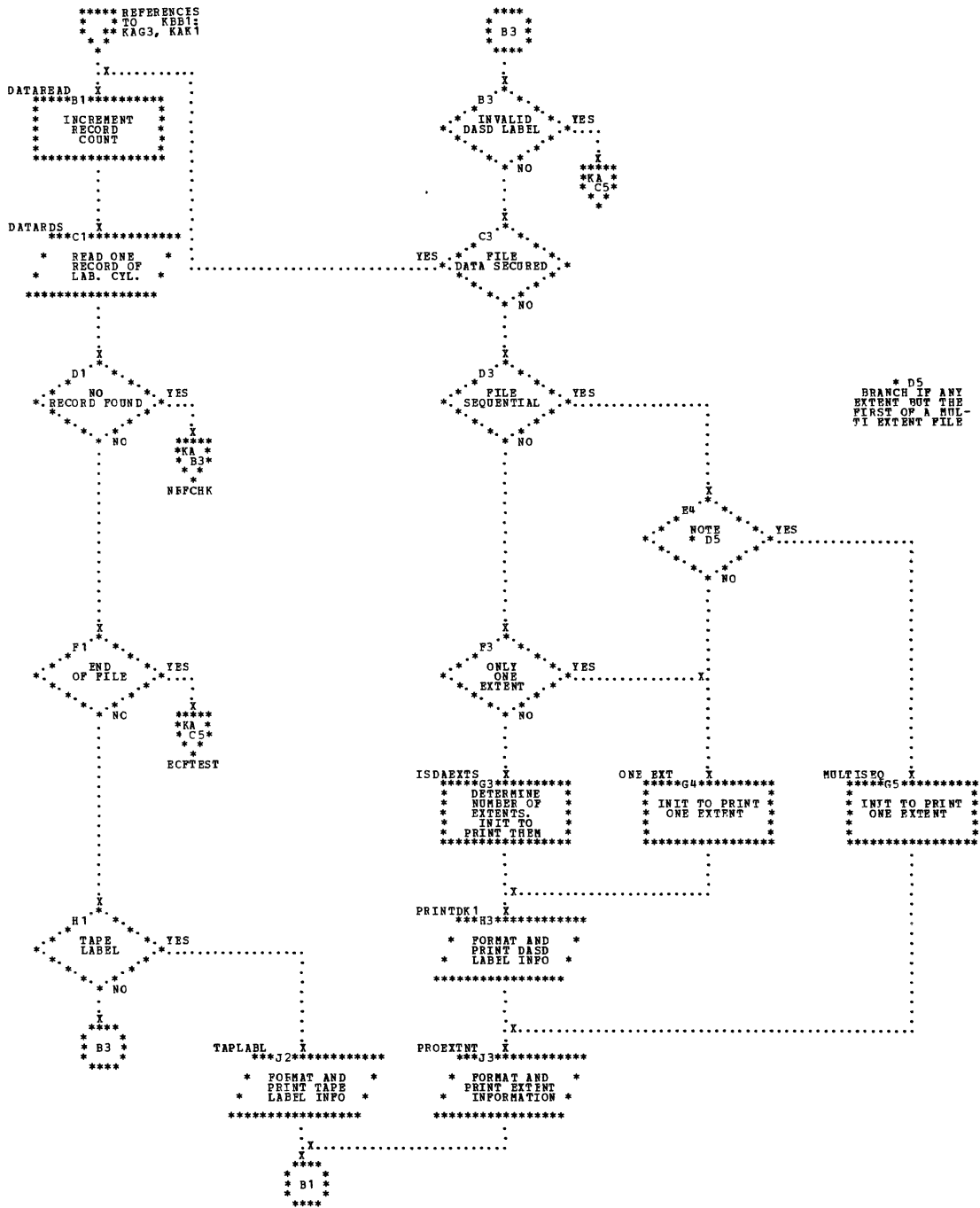
END OF LABEL CYLINDER DISPLAY

Figure 51. Sample LSERV Output (Part 2 of 2)





Chart KB. LSERV (IJBLSERV) (Part 2 of 2)



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ESTVUT (ERROR STATISTICS BY TAPE VOLUME UTILITY PROGRAM)

ESTVUT is a utility program used to list (on SYSLST) tape error statistics by volume from the TES history tape created by the EREP (Environmental Recording, Editing, and Printing) program. ESTVUT can be executed

from either a card reader or from SYSLOG. SYS005 must be assigned to the magnetic tape unit on which the TES history tape file is mounted. SYSLOG must be assigned to a 3210, 3215, or equivalent device.



EREP provides retrieval support for system environmental records created by RMSR. These records include error data for a CPU, channels, and attached I/O devices, as well as data about system events such as volume dismount or IPL. For the format of these records refer to the DCS/VS Error Recovery and Recording Transients PIM listed in the Preface.

EREP provides six types of error statistics retrieval. Error statistics may be edited and printed, selectively edited and printed by record type, summarized by record type, used to create a continuing history of system events on a History or RDE tape, used to create a history of magnetic tape volume reliability on a TES history tape, or used to create a system reliability summary.

Detailed operating information, including options available and execution procedures, can be found in Section 2-F-3 of DOS/VS Serviceability Aids and Debugging Procedures, GC33-5380.

#### EREP Requirements

EREP is executed as a problem program in either virtual or real mode, using standard job control language. It requires at least 10K bytes for execution. EREP also requires the following device assignments:

- SYSREC or SYS009 (depending on the option to be executed) -- Error statistics input to EREP
- SYSLOG -- Communications and/or control information input
- SYSLST -- As output
- SYS009 (depending on option to be executed) -- As output
- SYSIPT (optional) -- Control information input

EREP will use available partition space in excess of 10K bytes for all summary phases except 2715 and RDE. The extra address space, if executing in real mode, significantly improves performance during summarization.

#### EREP Processing

In order to perform all the functions of the current EREP program, many phases are required that execute in an overlay structure. Figure 52 lists all EREP phases and shows the relative position of each within the user partition.

As long as EREP is active within a partition, it will contain at least two of these phases: EREP, which is always resident, and one of the control phases.

In order to perform certain functions, the EREP phase, a control phase, a processing phase, and possibly a table phase may be required.

#### Processing by Option

Charts 9-18 show the structure and order of execution of EREP phases during the processing of an EREP option. They include, where helpful, a general explanation of the function of each phase, the type of phase, (control, table, etc.), and its relative position in the partition. Actual displacements into the partition may be obtained from Figure 53.

If you wish to examine the phases involved in the execution of an EREP option, start at:

Chart 9 -- EDIT or SELECT

Chart 14 -- SUM (except TES and RDE)

Chart 15 -- TES SUM

Chart 16 -- TES (except SUM)

Chart 17 -- HIST

Chart 18 -- RDESUM

Examination of these charts gives you a clear understanding of what EREP does before you refer to the functional charts for a particular phase.

## EREP Phase Functions

Charts MA-NL describe the function of EREP phases. Included are the input to the phase, the output of the phase, and the logical steps performed during execution of that phase. Associated with each step is the label, or labels, of the subroutine(s) required to perform that logical step. Most EREP table phases contain little or no executable code. The description of the operation of these phases has not been included in this manual. Some self-explanatory examples of the output of some of these phases have, however, been included for your convenience (see Appendix C).

## Phase Interaction

EREP control phases perform several general functions. They open required devices, communicate with the operator, examine the applicable options, call the required processing phases or other control phases, and provide common subroutines for the called processing phases.

EREP processing phases read SYSREC or History/RDE records (via subroutines in a control phase), examine the record type, call the applicable table phase if the record is of the correct type, and pass control to a list processor (usually in the control phase), to process the macros in the called table.

EREP table phases contain a series of macros, and some executable code, that controls the execution of subroutines to edit and write records in the applicable format. These macros may also point to DCs for headings and descriptors for the records.

If the description of an environmental event requires more than one SYSREC or History/RDE record, the EREP table phase may also cause these additional records to be read and may call other table phases to fully process the event description.

## Information Passing

There are two methods used to pass information between EREP phases.

The most widely used is a communication region. The EREP communication region is part of the EREP phase and is always resident in the EREP partition. This contains (after EREPMNTR has read and checked all available EREP control statements) all options, suboptions, and parameters to be processed, the EREPMNTR return address, a description of SYSREC, and areas to store information about individual error records.

The EREPEDIT common region is used as a communication link between EREPEDIT and the associated processing phases. It contains disk search information, a branch table used by the subroutines, and a table of pointers used by both EREPEDIT and the associated processing phases.

The second method to pass information (used during summarization, selective retrieval, and 2715 edit and print) is by loading an overlay phase around data gathered by the overlaid phase. Data area description is accomplished by DSECTS within the EREP phases.

## EREP Displacements

EREP is a self-relocating program that can execute in either a real or virtual partition. If EREP runs in a real partition, at least 10K bytes of real address area must be allocated to the partition. If additional 8K blocks of real address area are allocated when summarizing (except for 2715 and RDE records), the summarizing will be speeded up. This is true for real mode execution of EREP; for virtual mode it is not possible to give relative performance estimates.

All displacements in this chart are relative to the partition start address in an MPS system when tape labels are not being used. When tape labels are used, add 80 bytes (X'50') to all displacements.

<u>EREP Phase</u>	<u>Displacement (Dec)</u>	<u>Displacement (Hex)</u>
EREP	+0000	+0000
EREPMNTR	+0200	+00C8
EREPEDIT	+0200	+00C8
EREPHIST	+0200	+00C8
EREPSMCP	+0200	+00C8
EREPEDCP	+0200	+00C8
EREPSMES	+0200	+00C8
EREPEDES	+0200	+00C8
EREPTES	+0200	+00C8
EREPRDE	+0200	+00C8
EREPESTR	+4088	+0FF8
EREPESWK	+4088	+0FF8
EREFMCR	+4376	+1118
EREPCCCH	+4376	+1118
EREPUNIT	+4376	+1118
EREPPIPL	+4376	+1118
EREP2715	+4376	+1118
EREPRETV	+4376	+1118
EREPESTP	+4376	+1118
EREPSMTD*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMDA*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMUR*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMTP*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMCR*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMDK*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMCU*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMT1*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMD1*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMU1*	+5448+(n-1)8192	+1548+(n-1)2000
EREPSMTR	+5448	+1548
EREPEDTD*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDDA*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDUR*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDTP*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDCR*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDDK	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDCU*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDT1*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDX1*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDD1*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDU1*	+5448+(n-1)8192	+1548+(n-1)2000
EREPEDTR	+5448	+1548
EREPESHRT	+6424	+1918
EREPSHRU	+6424	+1918
EREP2400	+6424	+1918
EREP3420	+6424	+1918
EREP3410	+6424	+1918
EREPUCCM	+6424	+1918
EREPDOLD	+6424	+1918
EREP3330	+6424	+1918
EREP333X	+6424	+1918
EREP333Y	+6424	+1918
EREP333Z	+6424	+1918
EREP3340	+6424	+1918
EREP334X	+6424	+1918
EREP334Y	+6424	+1918
EREP334Z	+6424	+1918
EREPTPE1	+6424	+1918
EREPUOLD	+6424	+1918
EREP3211	+6424	+1918
EREP3500	+6424	+1918
EREP3540	+6424	+1918
EREPEOD	+6424	+1918
EREPADPT	+4482	+1182
EREPASTA	+4482	+1182
EREPSPCL	+4482	+1182

<u>EREPC Phase</u>	<u>Displacement (dec)</u>	<u>Displacement (Hex)</u>
EREPCCHC	+6424	+1918
EREPC145	+6424	+1918
EREPC145A	+6424	+1918
EREPC145B	+6424	+1918
EREPC145C	+6424	+1918
EREPC135	+6424	+1918
EREPC145	+6424	+1918
EREPC145D	+6424	+1918
EREPCMRC	+6424	+1918

\* If there is sufficient partition space, two or more of these phases may be loaded simultaneously. n indicates the load position relative to the beginning of the partition (first, second, etc.). The actual load address, relative to the partition start address, is obtained by substituting the load position for n and solving the expression.



EREP Edit or Selective Retrieval Phases

Processing Phase →		EREPMCAR			EREPCCH					EREPUNIT														EREP2715			EREPIPL														
		MCRC	M145	145D	C135	C125	CCHC	C145	145A	145B	145C	C135	C125	SHRT	SHRU	2400	3420	3410	UCCM	DOLD	UOLD	TPE1	3330	333X	333Y	333Z	3340	334X	334Y	334Z	3500	3211	3540	ADPT	SPCL	ASTA	EOD				
DEVICE TYPES	CPU	M/115	X			X																																			
		M/125	X				X																																	X	
		M/135	X			X																																		X	
		M/145	X	X	X																																			X	
		M/115-11, 158	X																																					X	
		MPX Channel						X	X		X		X	X																											
		SEL Channel						X	X	X			X																												
		BMPX Channel						X	X	X			X																												
		IFA						X	X			X	X	X																											
Printer	1403/1403U												X	X				X	X																						
	1443												X	X				X	X																						
	3211												X	X				X																							
Reader/Punch	1442N1/2596												X	X				X	X																						
	2501/2520/2540/3881												X	X				X	X																						
	3505/3525																	X																							
Tape	3410/3411												X				X	X																							
	3420												X			X	X	X																							
	2400												X		X			X																							
Disk	2314/2311/2321												X	X				X	X																						
	3330																	X				X	X	X	X																
	3340																	X							X	X	X	X													
Diskette	3540											X	X				X																X								
TP	2701/2702/2703												X					X			X																				
	2715																	X																	X	X	X				
MICR OCR	1255/1259/1419												X	X				X	X																						
	1287/1288												X	X				X	X																						
	3886												X	X				X	X																						
	Unsupported												X	X				X	X																						

Figure 52. EREP Table Phases Used for Device Processing (Part 1 of 2)

Figure 52. EREP Table Phases Used for Device Processing (Part 2 of 2)

FREP Summary Phases →

Processing Phase →		EREPSMCP										EREPEDCP												
		SMTR	SMDA	SMTD	SMUR	SMTU	SMCU	SMCR	SMDK	SMDI	SMTI	SMUI	EDTR	EDDA	EDTD	EDUR	EDTP	EDCU	EDCR	EDDK	EDDI	EDTI	EDXI	FDUI
Table Phase →																								
CPU	M/145					X											X							
	MPX Channel					X											X							
	SEL Channel					X											X							
	BMPX Channel					X											X							
	IFA					X											X							
Printer	1403/1403U				X										X									
	1443				X										X									
	3211											X												X
Reader/Punch	1442N1/2596				X										X									
	2501/2520/2540/3881				X										X									
	3505/3525												X											X
Tape	3410/3411										X												X	
	3420										X											X		
	2400			X											X									
Disk	2314		X											X										
	3330		X							X				X							X			
	3340		X							X				X							X			
Diskette	3540								X										X					
TP	2701/2702/2703					X									X									
	2715	X											X											
MICR OCR	1255/1259/1419							X										X						
	3886							X										X						

↓

DEVICE TYPES

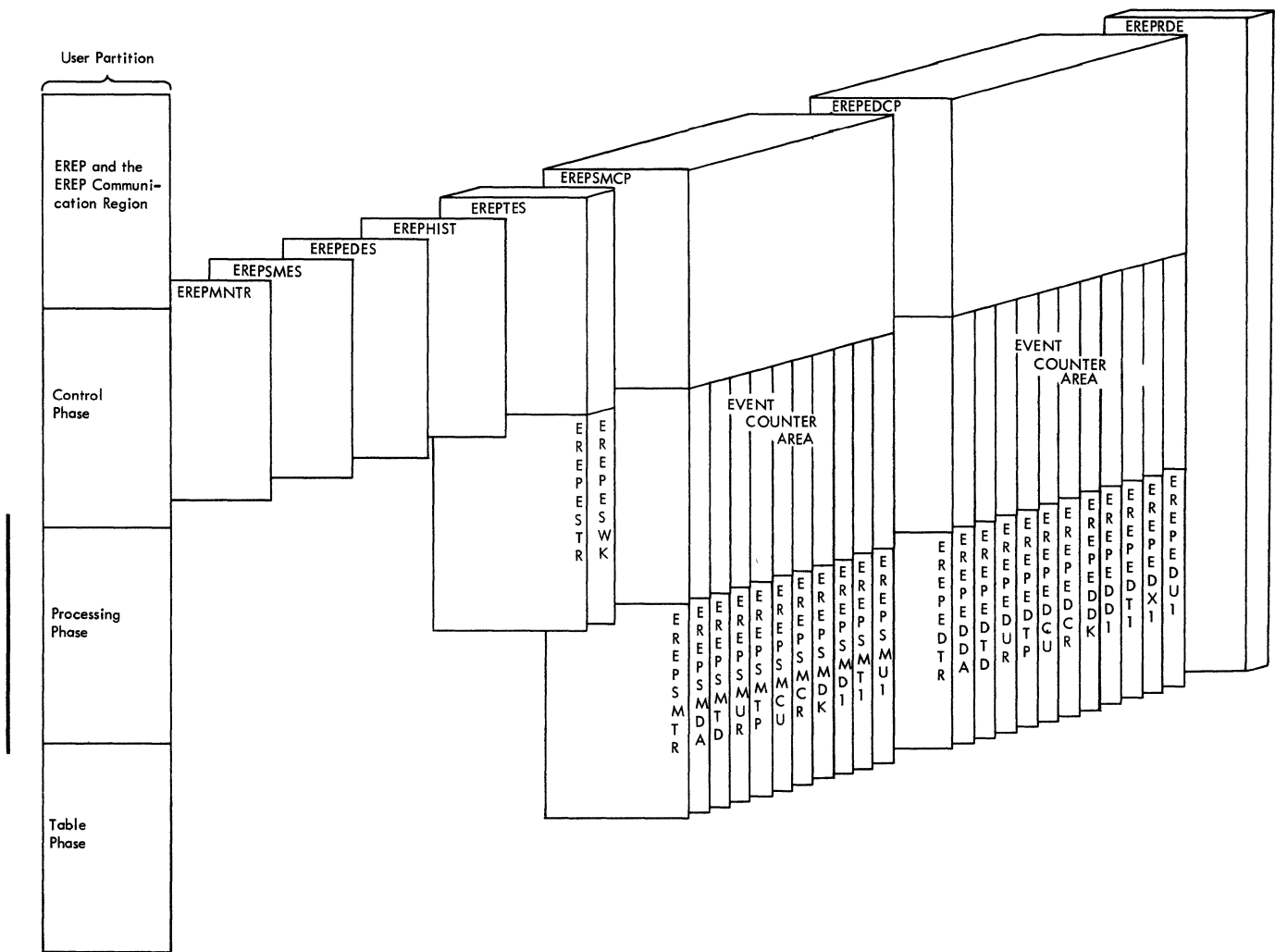


Figure 53. Relative Displacement of EREP Phases (Part 1 of 2)





Chart 10. Selective Retrieval and Edit Structure

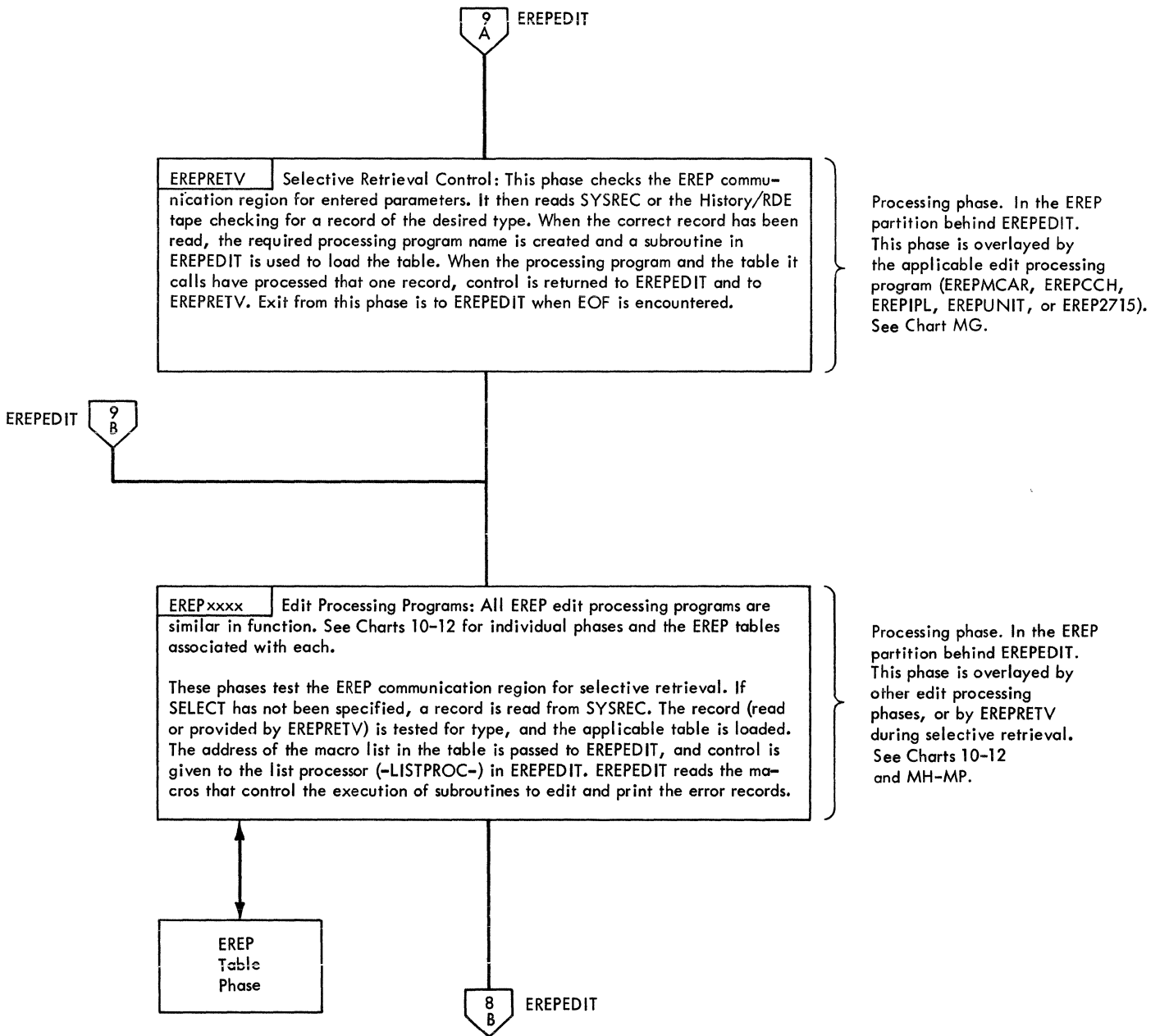


Chart 11. Edit Processor and Table Phase Structure (Part 1 of 3)  
See Charts HH and HJ

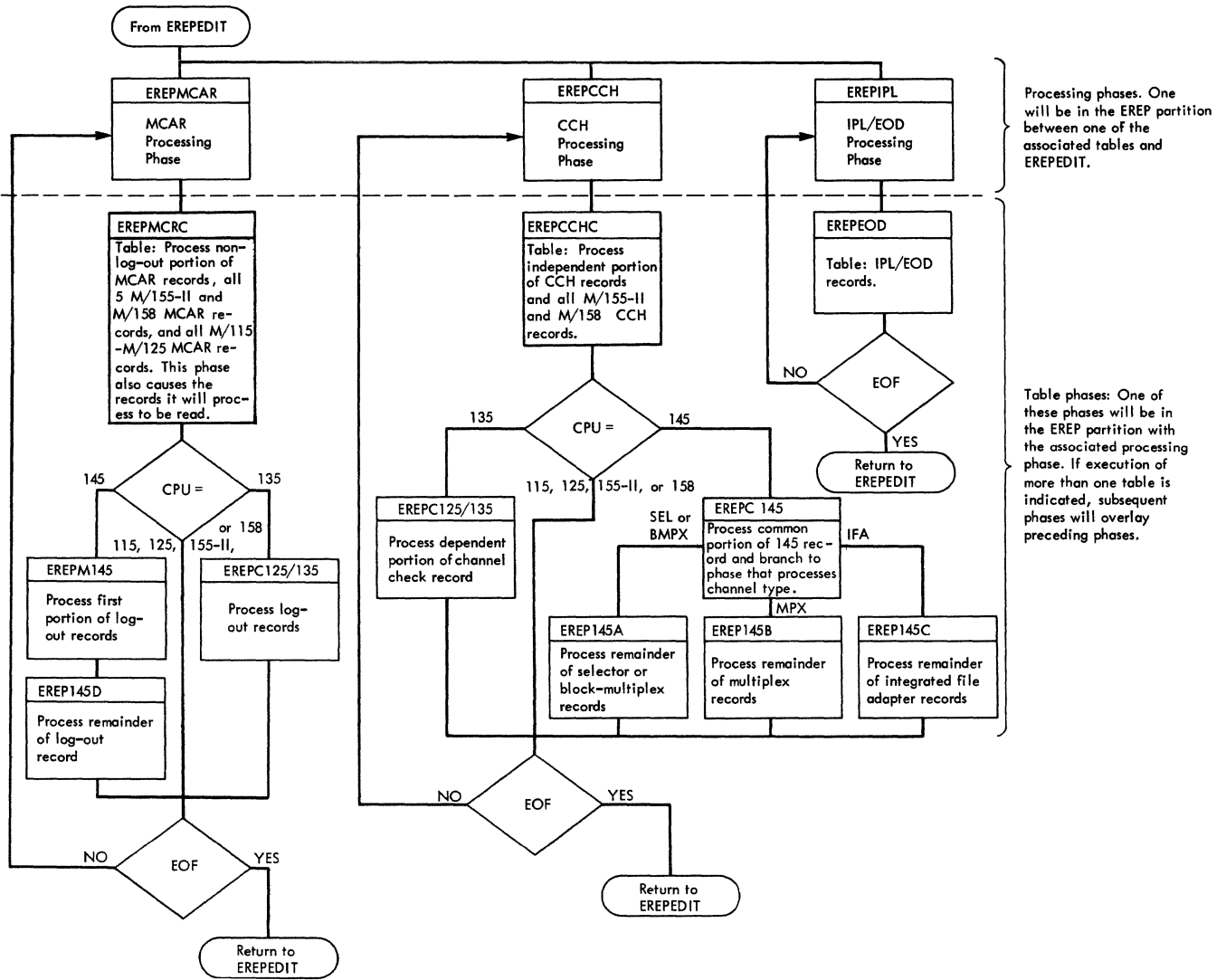


Chart 12. Edit Processor and Table Phase Structure (Part 2 of 3)  
See Chart HK

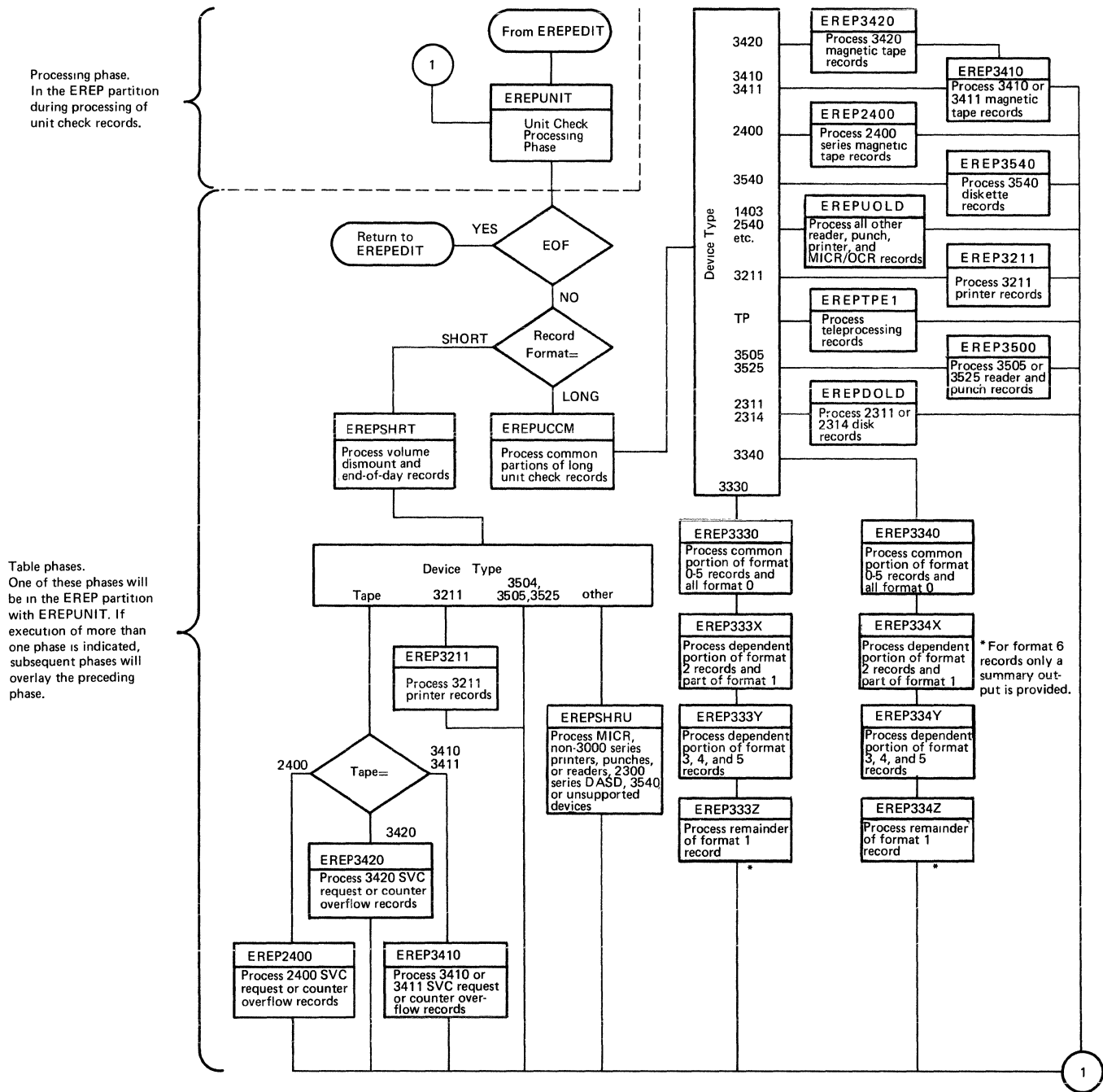




Chart 13. Edit Processor and Table Phase Structure (Part 3 of 3)  
 See Charts HL - HP

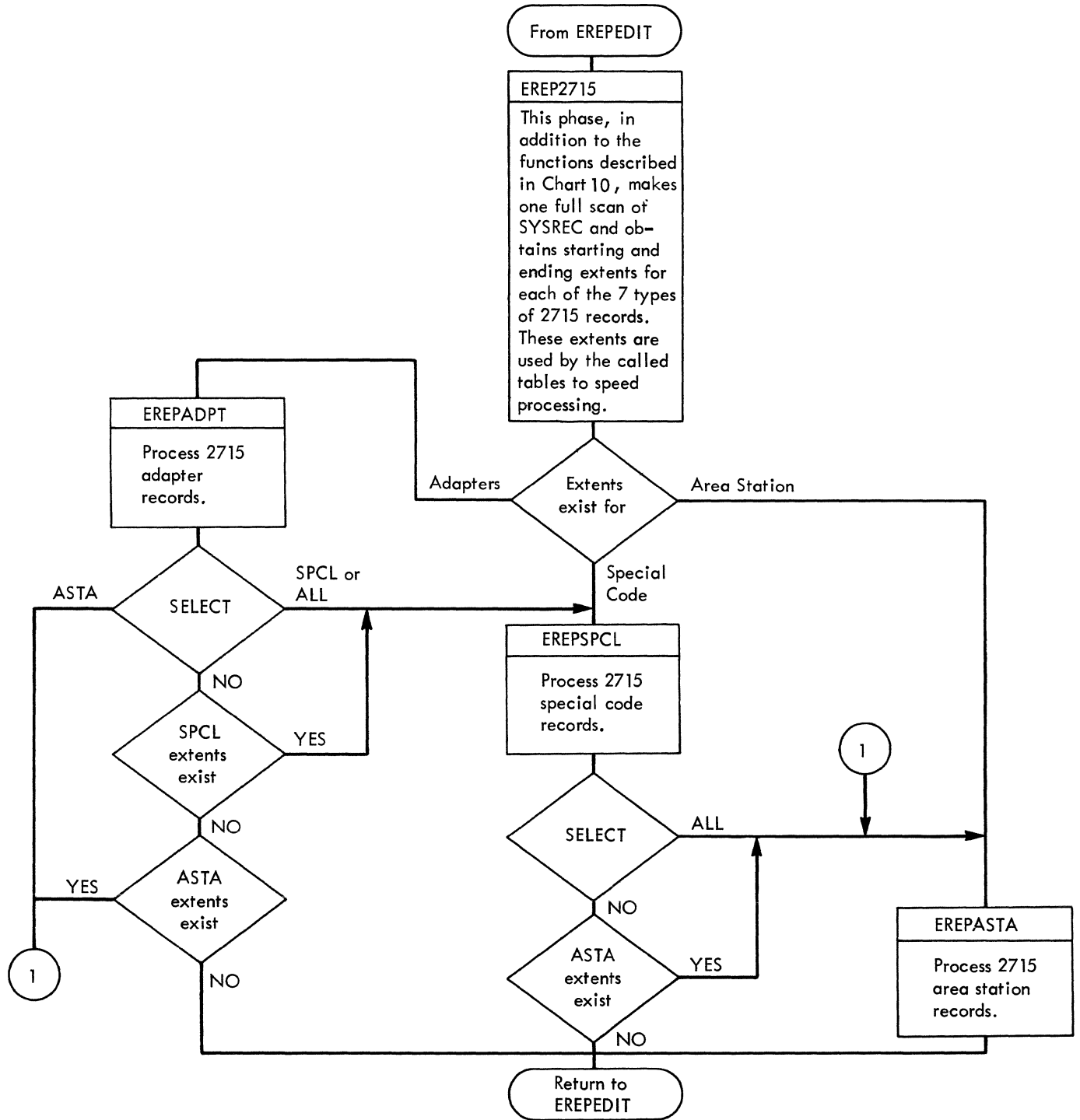


Chart 14. Summary Phases (Ncn-TES)

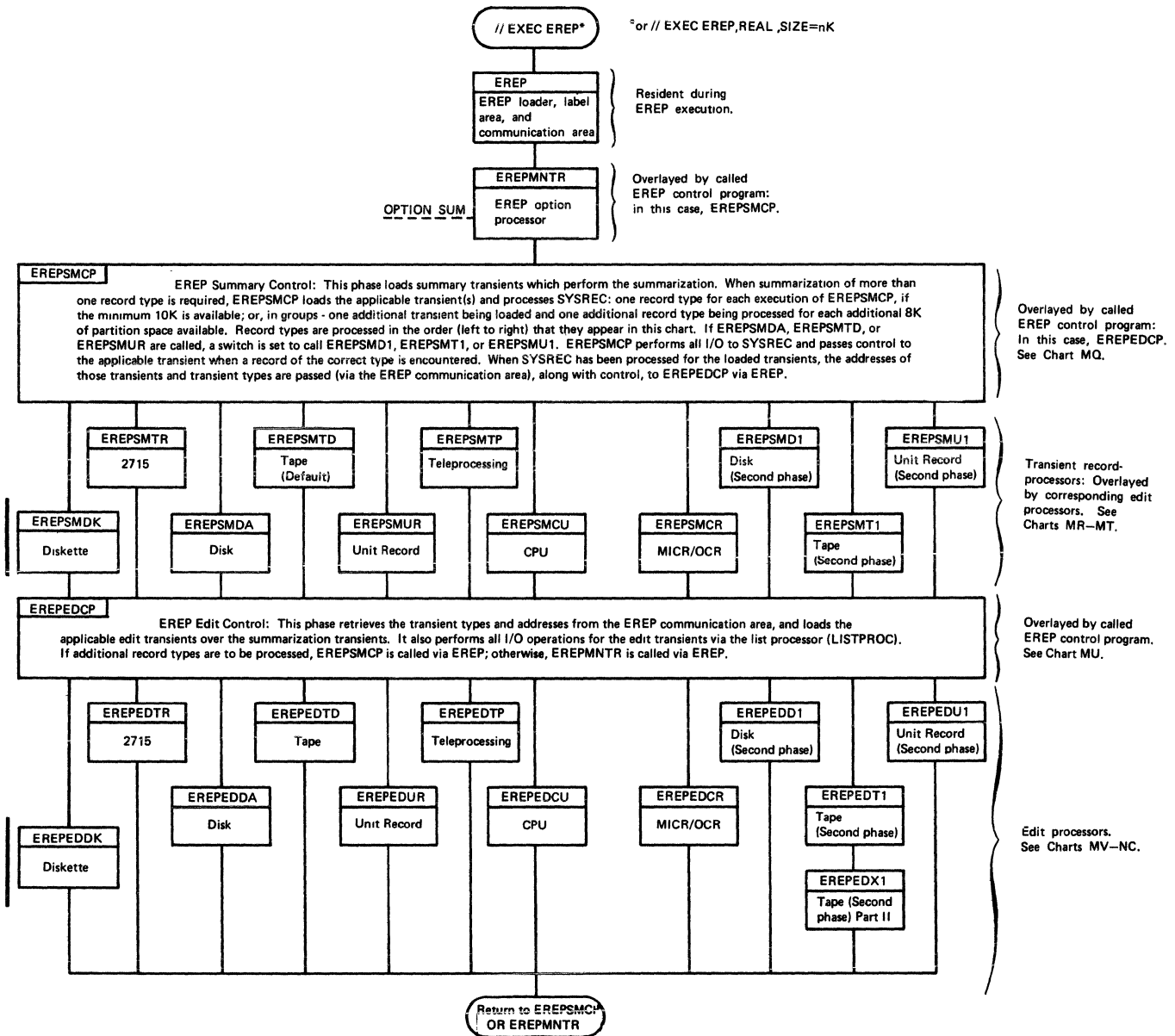


Chart 15. Option SUM: Structure for TES

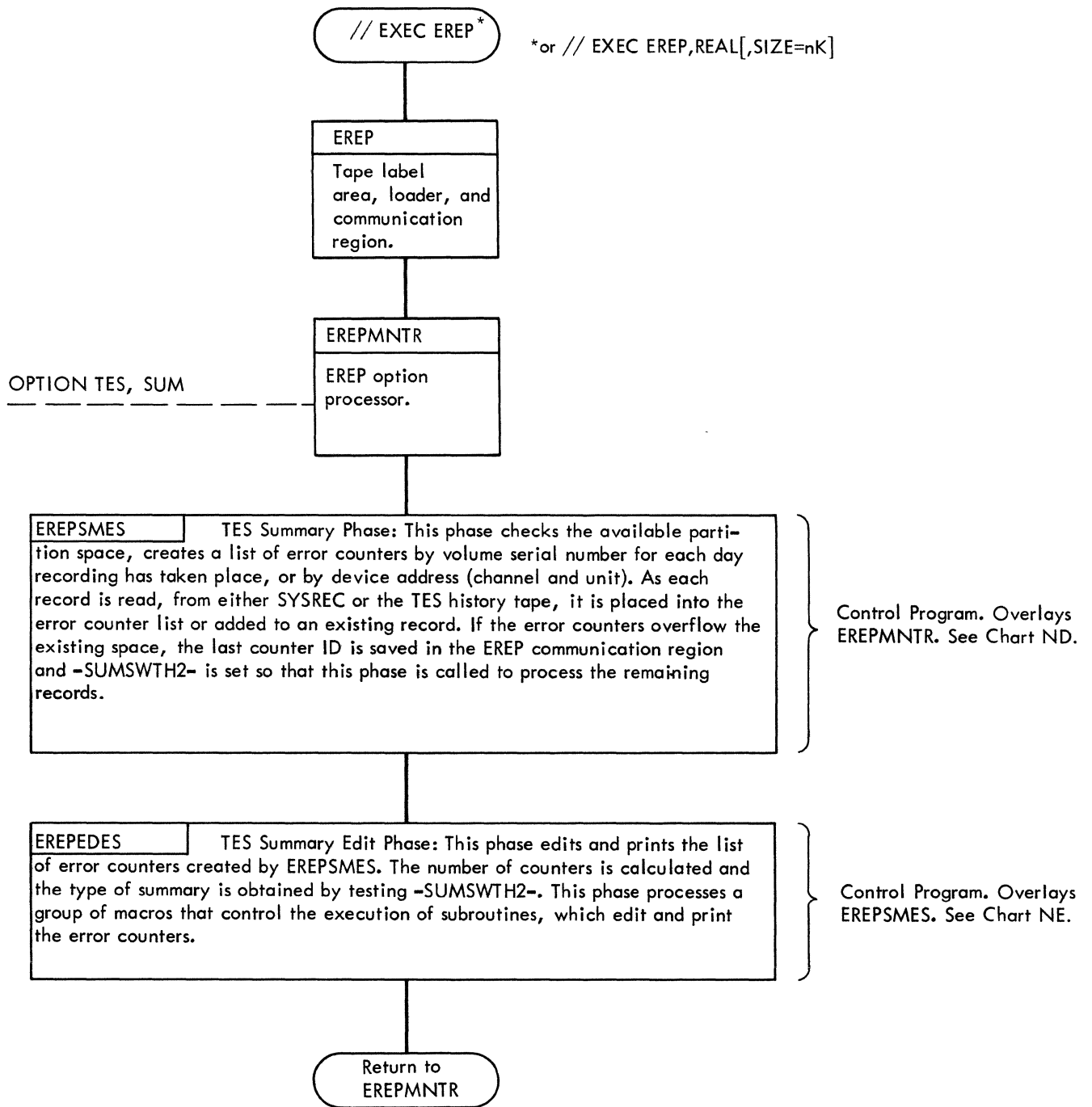


Chart 16. TES: Print and Tape Output

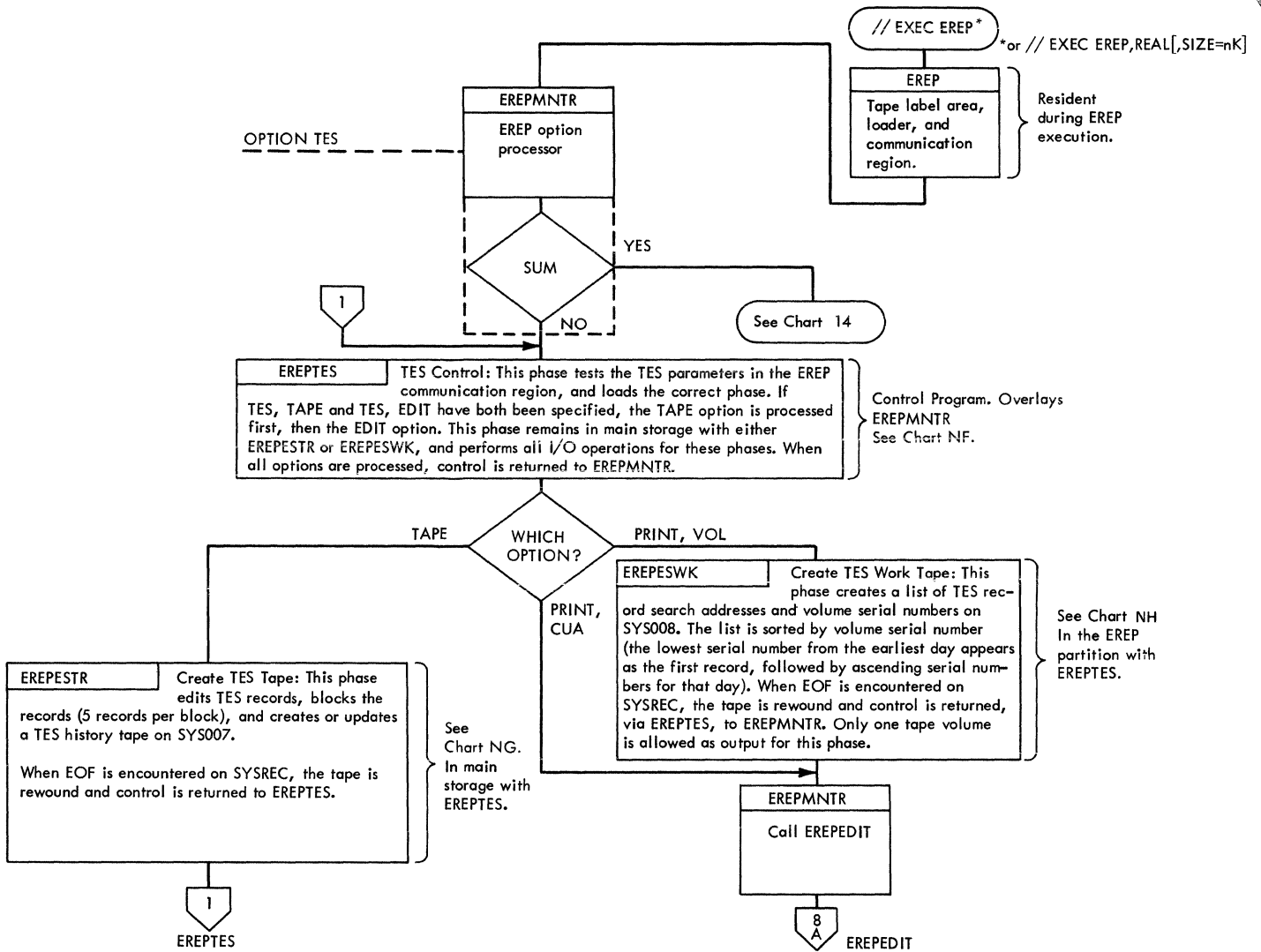


Chart 17. Option HIST: Structure

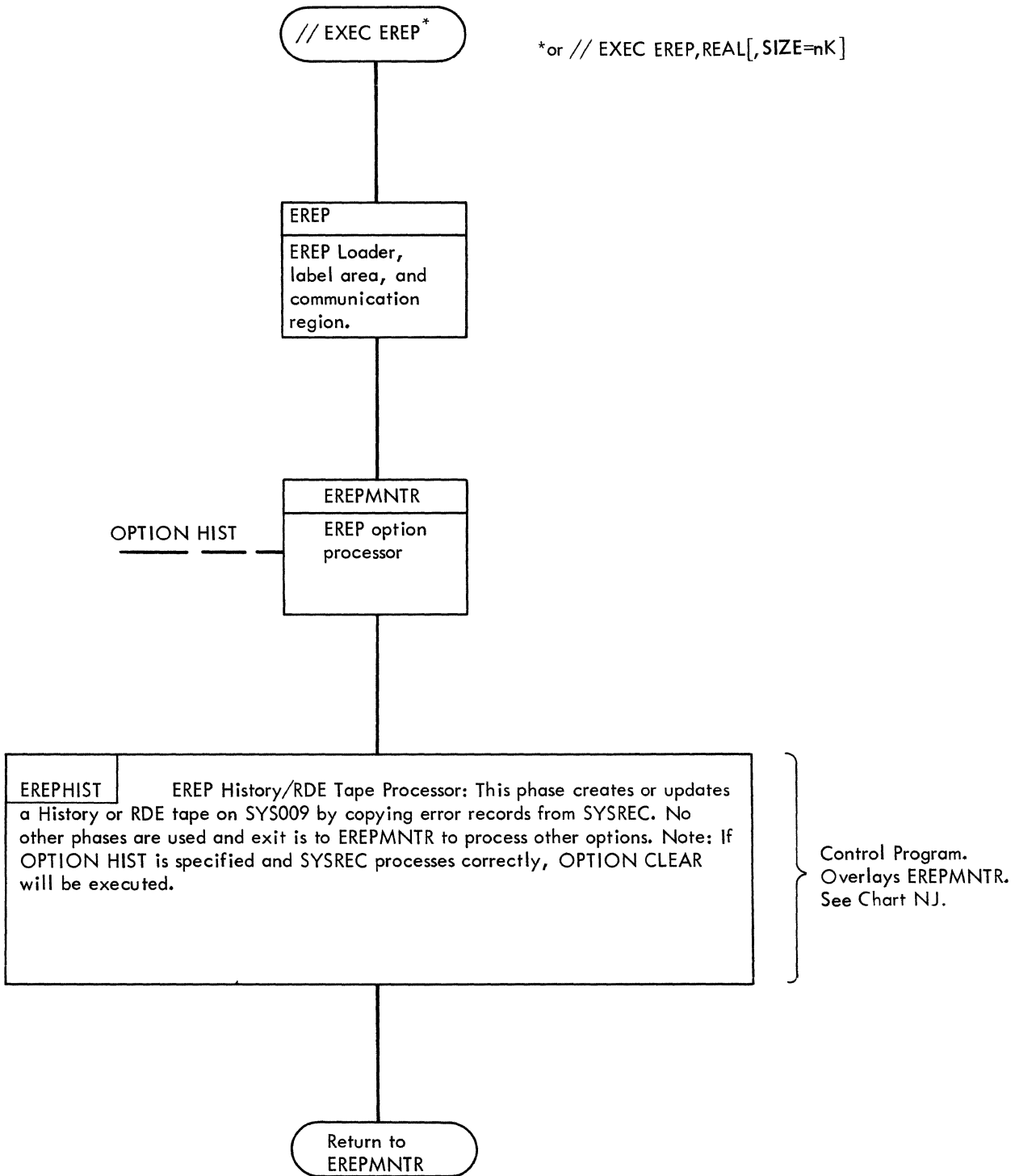


Chart 18. Option RDFSUM: Structure

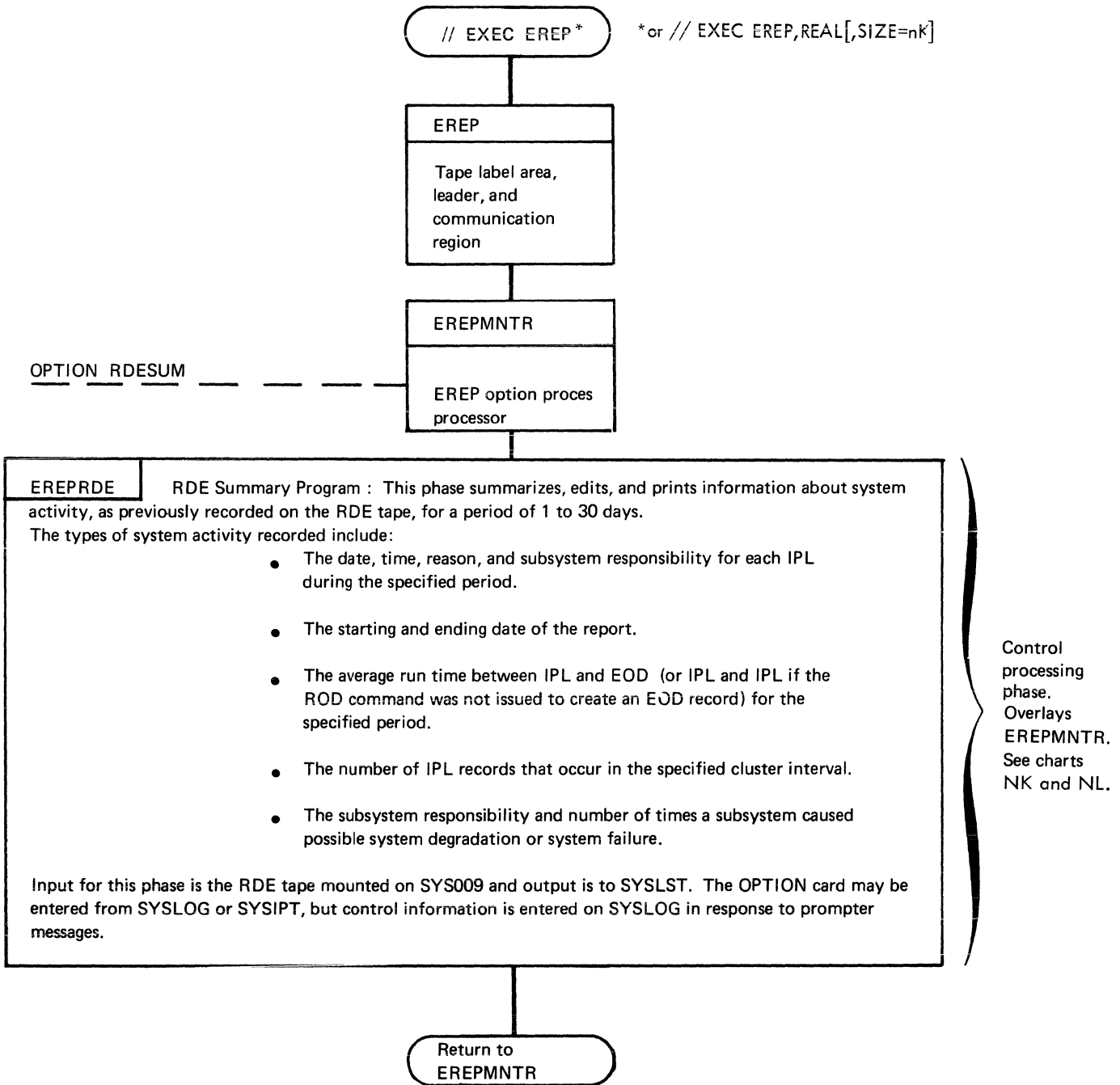


Chart MA. EREP: Function

\*or // EXEC EREP,REAL[,SIZE=nK]

Displacement from partition start addr.	Label List	
	EREPESTR	Label Area: 80 Bytes - Reserved for use by LIOCS during history or RDE tape processing
	LOADER LOADER2	EREPE Loader: 28 Bytes - 1. Set register 0 to -COMEND-. [Entry for loading EREP control program.] 2. Set the load name address in register 1. [Entry for loading any phase except control program.] 3. SVC 4, LOAD.
	LOADNAME EREPNAME	EREPE MNTR [This label is overlaid to call subsequent phases.]
		EREPE Communication Region: 168 Bytes
108	OPTNRETN	4 Bytes [Return address to EREP MNTR.]
112	TRSAVE	4 Bytes [Save area for 2715 address.]
116	TRACKS	2 Bytes [SYSREC cylinder/address.]
118	RFONE	5 Bytes [First SYSREC record address.]
123	RFCURR	5 Bytes [Current SYSREC record address.]
128	QUEUE	16 Bytes [EREPE option queue.]
		1 Byte [Reserved.]
145	SUBSWTH	4 Bytes [Option parameter switches.]
149	COMREGN	3 Bytes [General communications.]
152	SUMREGN	[Describes the 15-byte summary area.]
152	SUMPARM	2 Bytes [Summary parameter switches.]
154	SUMSWTH	3 Bytes [Summary options.]
157	SUMSWTH2	[Second byte of summary options.]
157	SUMCPUID	5 Bytes [Summary CPU ID.]
162	SUMSWBYT	1 Byte [Summary Switches.]
163	SUMTPCUA	2 Bytes [TP Line done.]
		2 Bytes [Reserved.]
167	SELREGN	[Describes the 52-byte selective retrieval area.]
167	SELPARM	3 Bytes [Select parameter switches.]
170	SELSWTH	2 Bytes [Record type switches.]
172	SELCMRG	[Describes the 47-byte selection area.]
172	SEL2715	1 Byte [2715 switches.]
173	COMDATE	6 Bytes [Date.]
179	COMTIME	4 Bytes [Time.]
183	COMJOB	8 Bytes [Job name.]
191	COMPROG	8 Bytes [Program name.]
199	COMCUA	2 Bytes [CUA.]
201	COMTERM	8 Bytes [Terminal name.]
209	COMCPU	3 Bytes [CPU serial number.]
212	COMDEV	1 Byte [Device type.]
213	COMVOL	6 Bytes [Volume serial number.]
		57 Bytes [Reserved.]
276	COMEND	[Control program load address.]

// EXEC EREP\*

SVC 4 Load Phase at -COMEND-

EREPE

Chart MB. EREPMNIR: Function (Part 1 of 2)

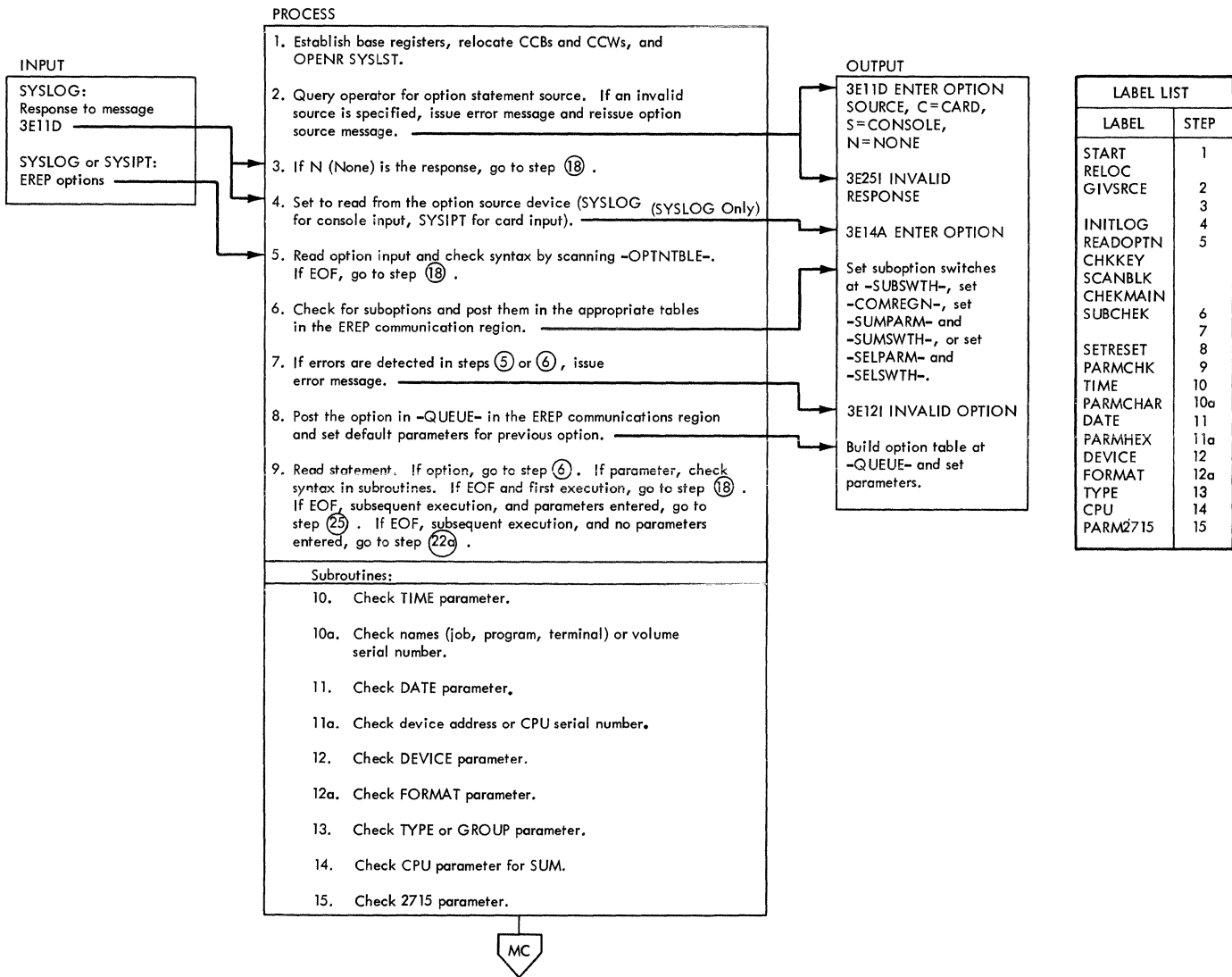




Chart MC. EREPMNTR: Function (Part 2 of 2)

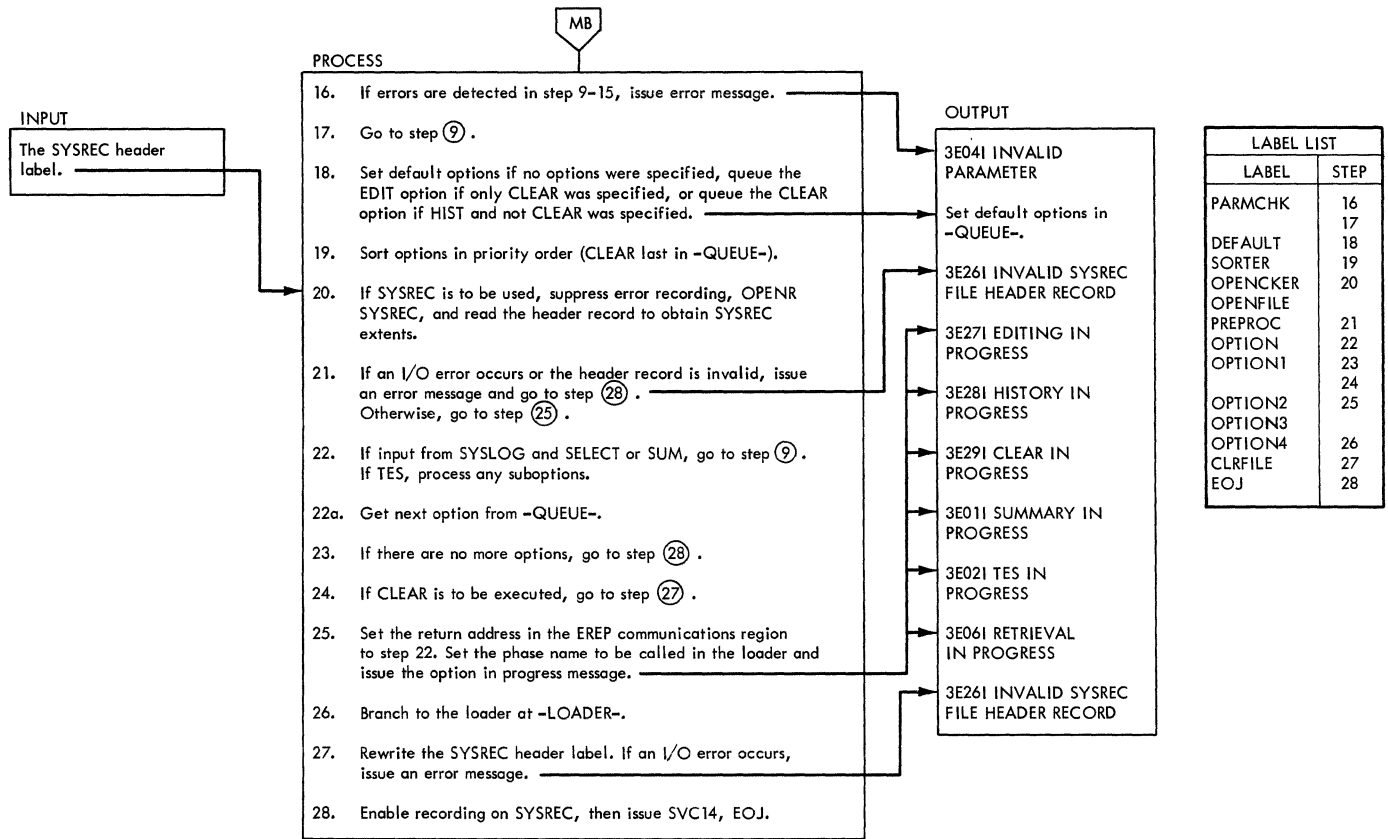


Chart MD. EREPEDIT: Function

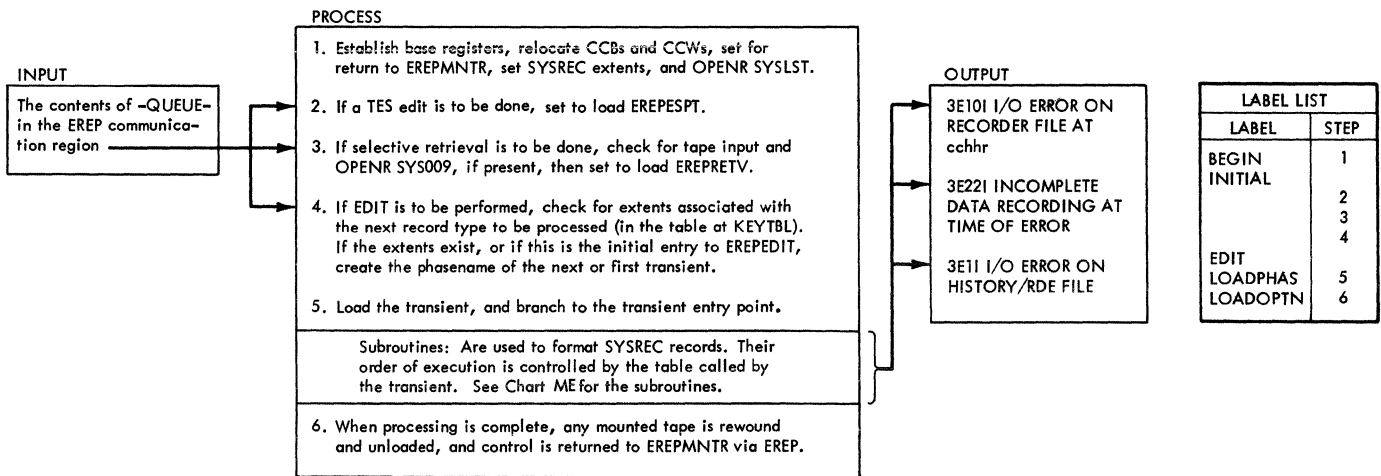


Chart ME. EREPEDIT: Subroutines

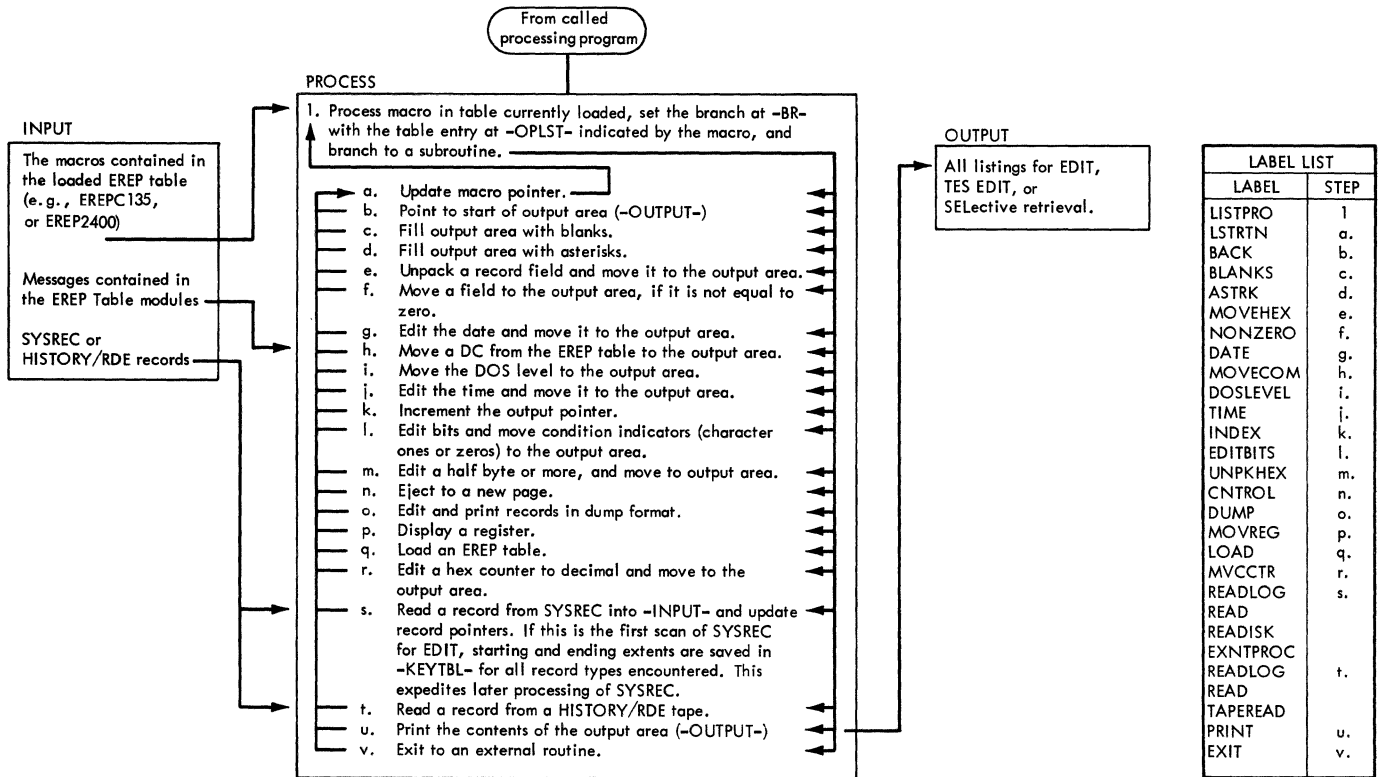


Chart MF. EREPESPT: Function

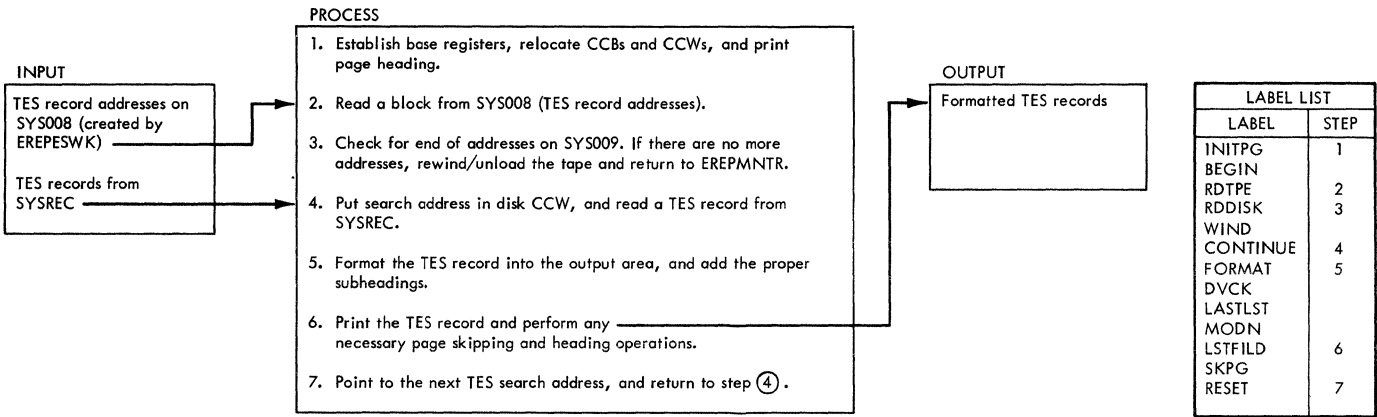


Chart MG. EREPRETV: Function

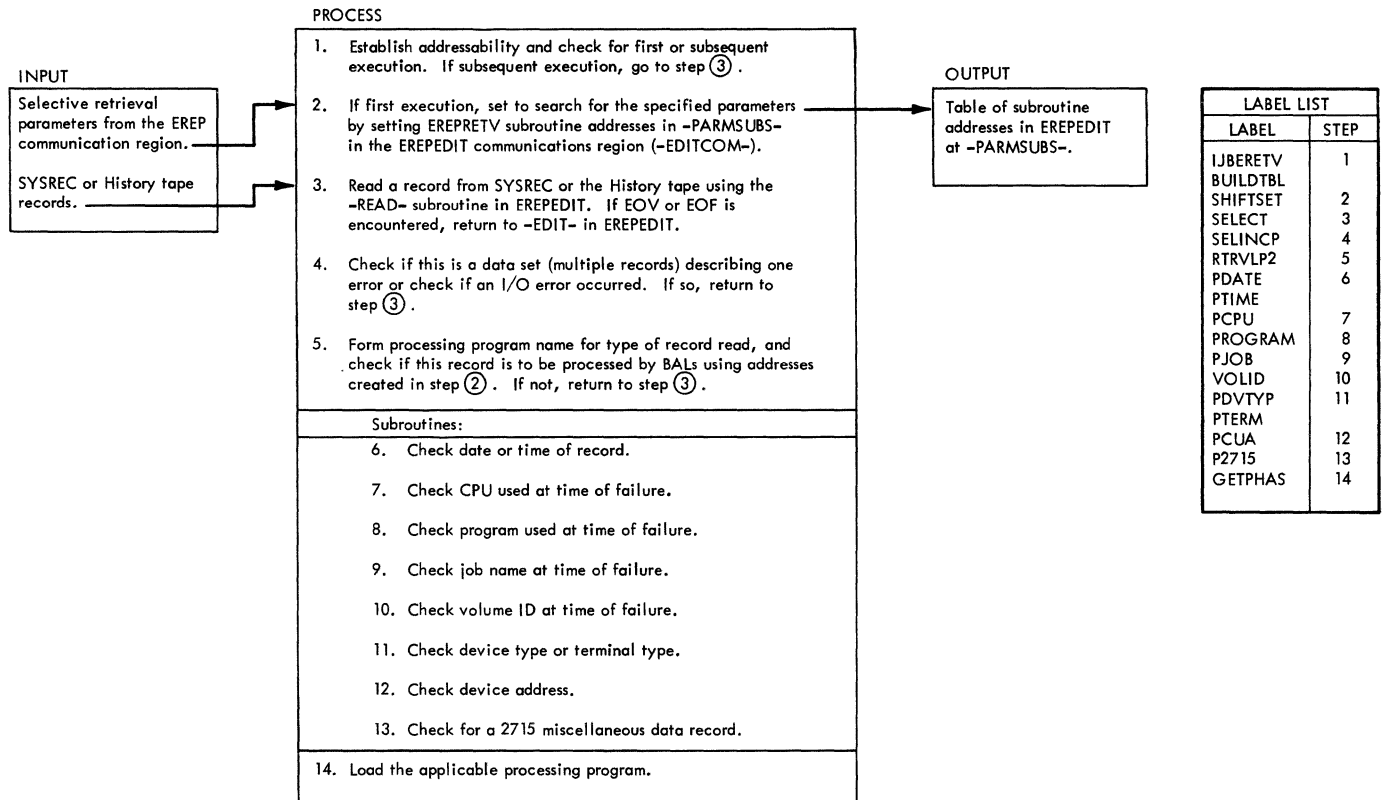


Chart MH. EREPMCAR and EREPCCH: Function

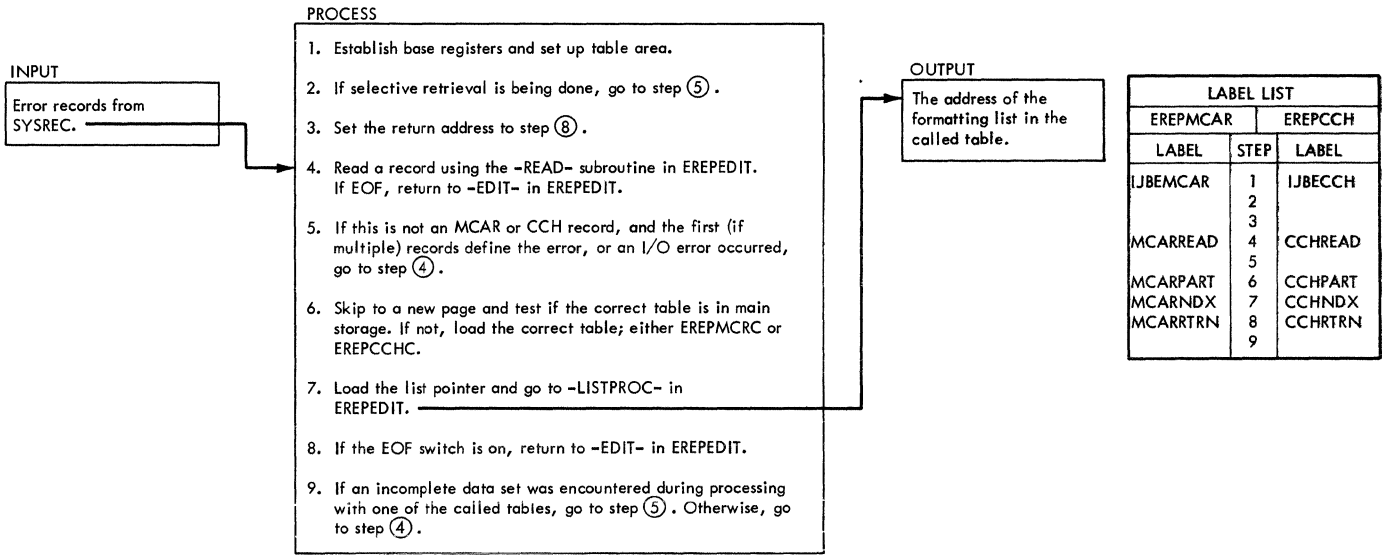


Chart MJ. EREPIPL: Function

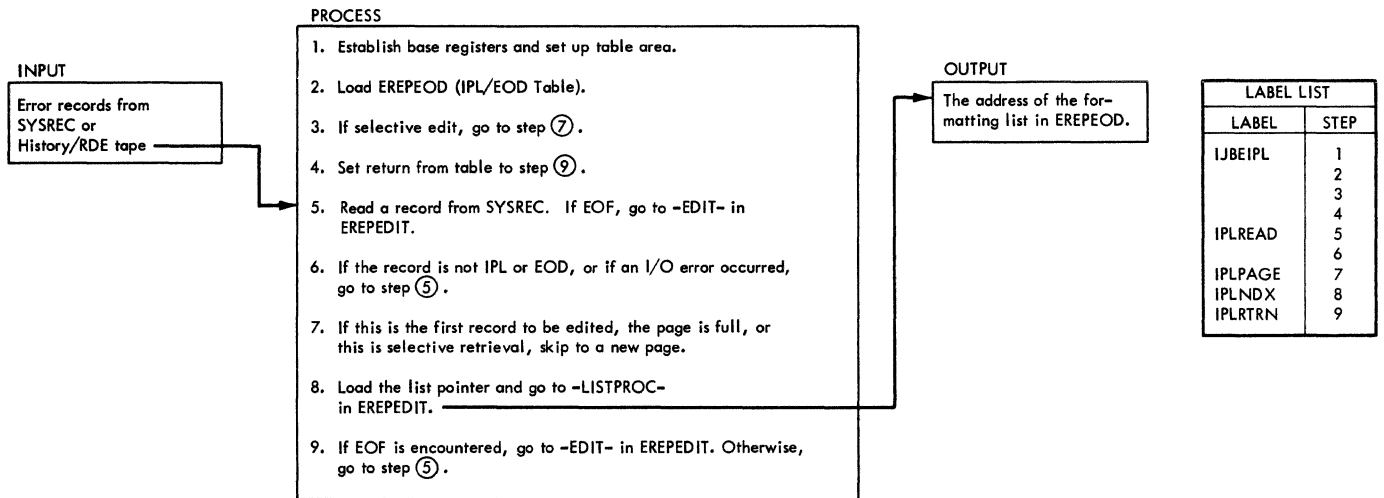


Chart MK. EREPUNIT: Function

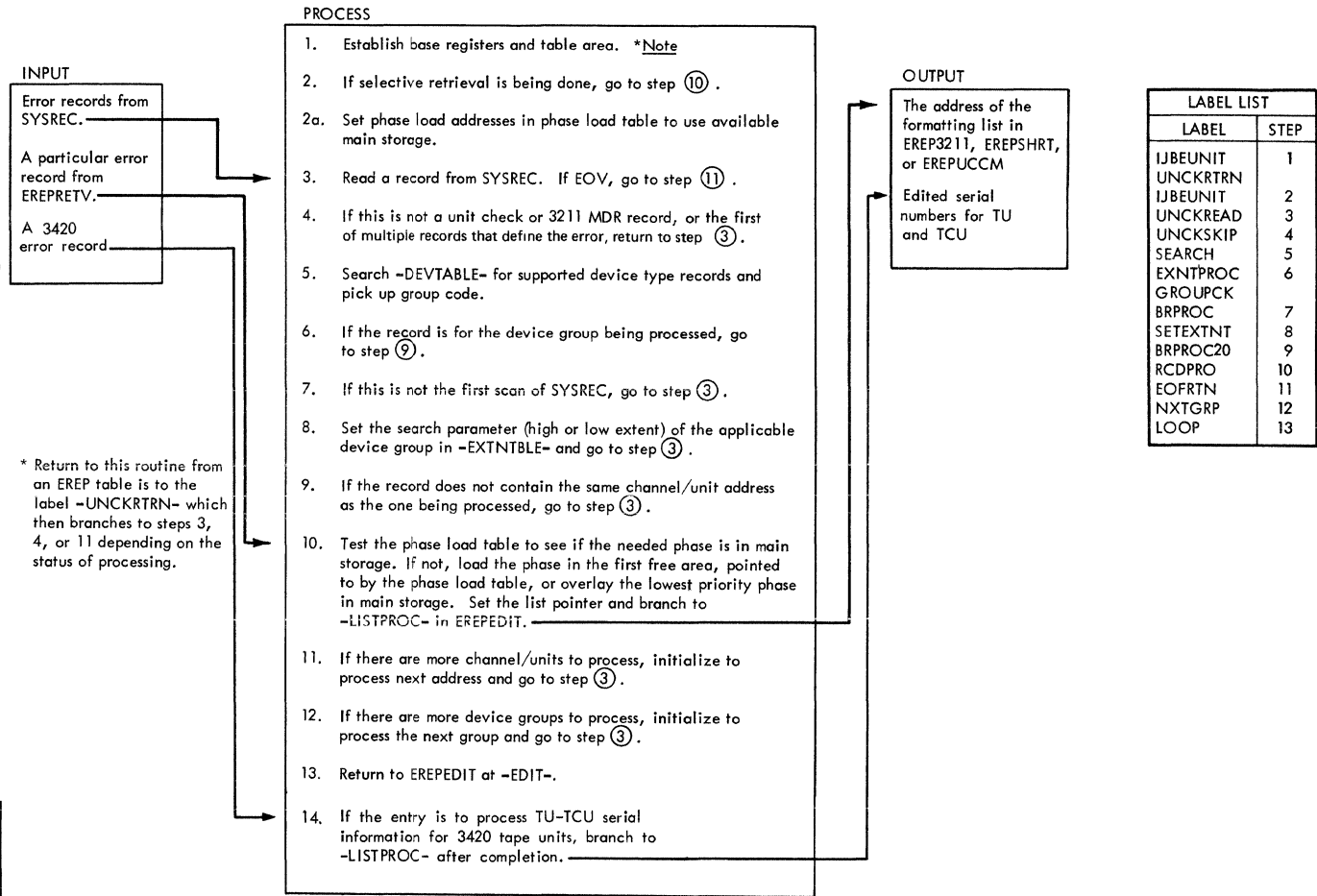




Chart ML. EREP2715: Function

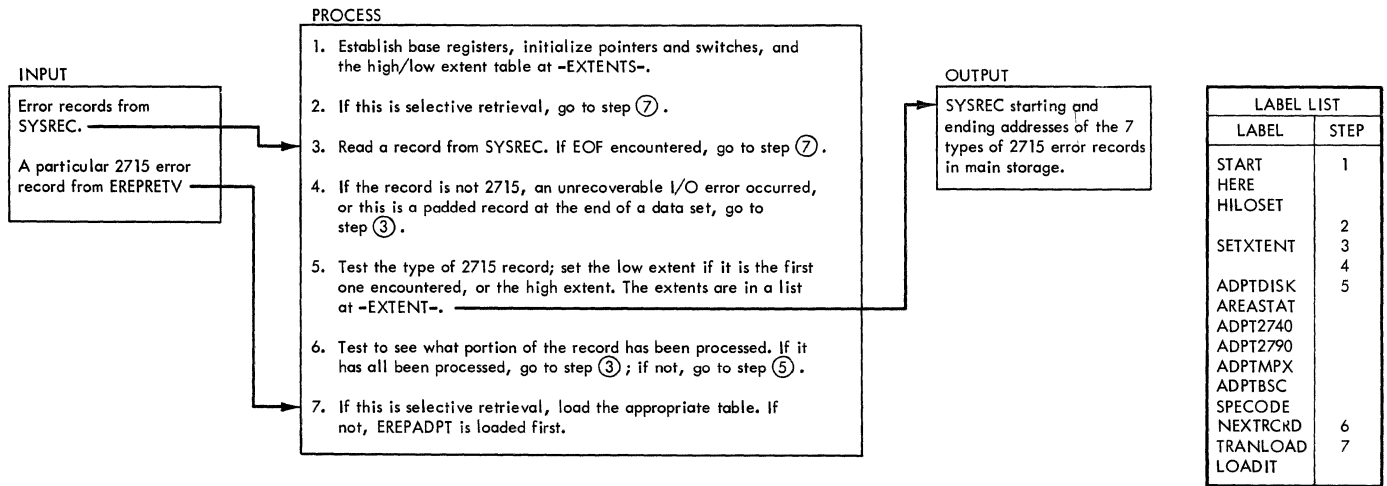


Chart MM. EREPADPT: Function

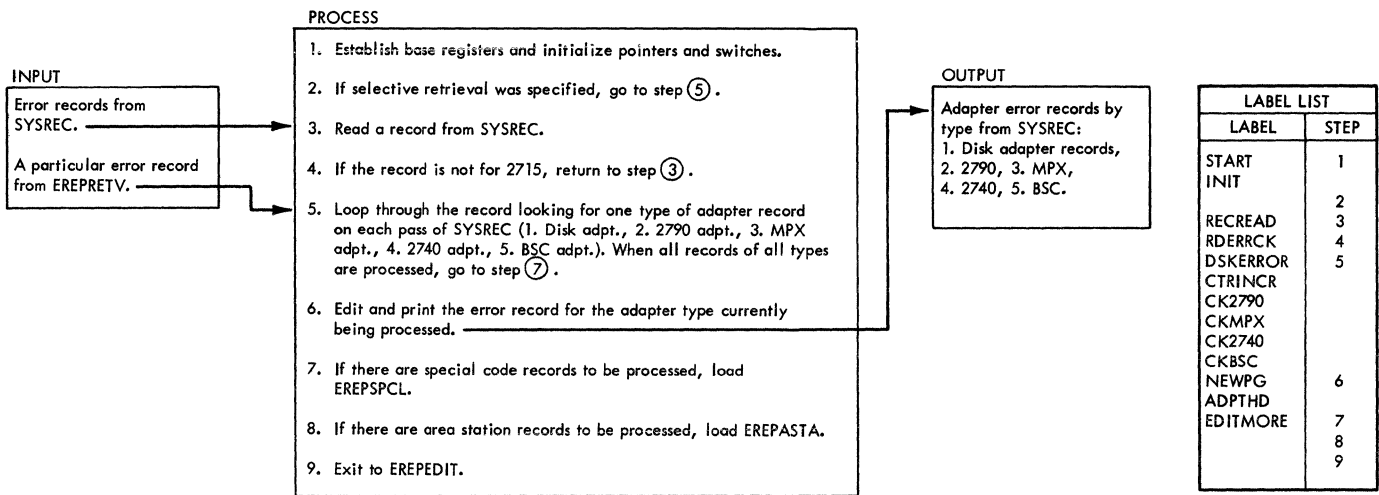


Chart MN. EREPSPCL: Function

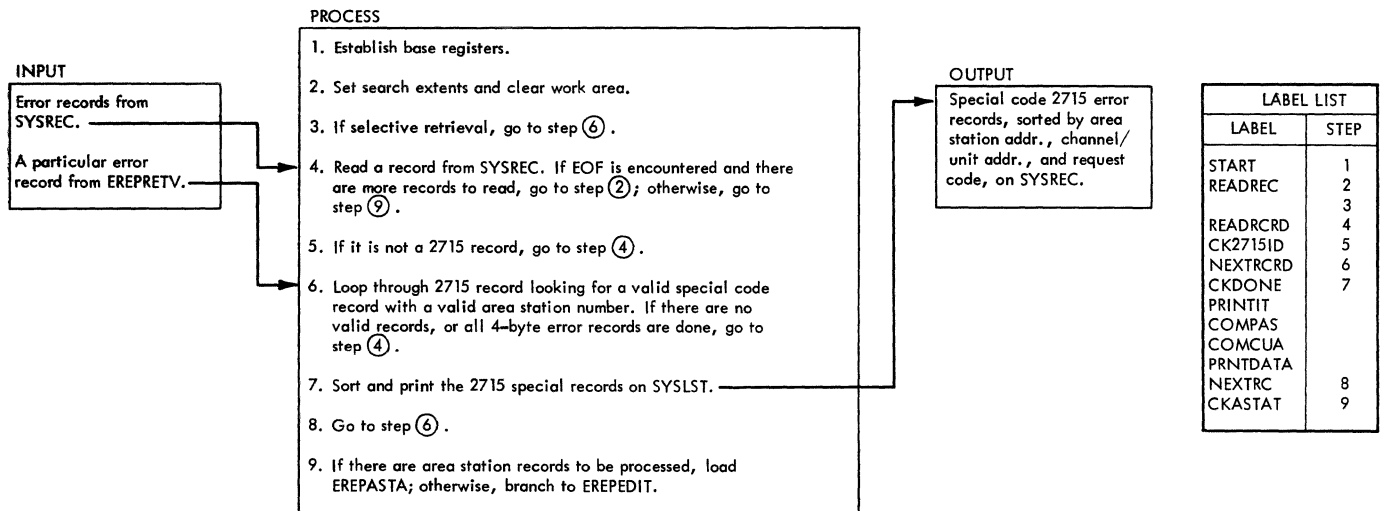
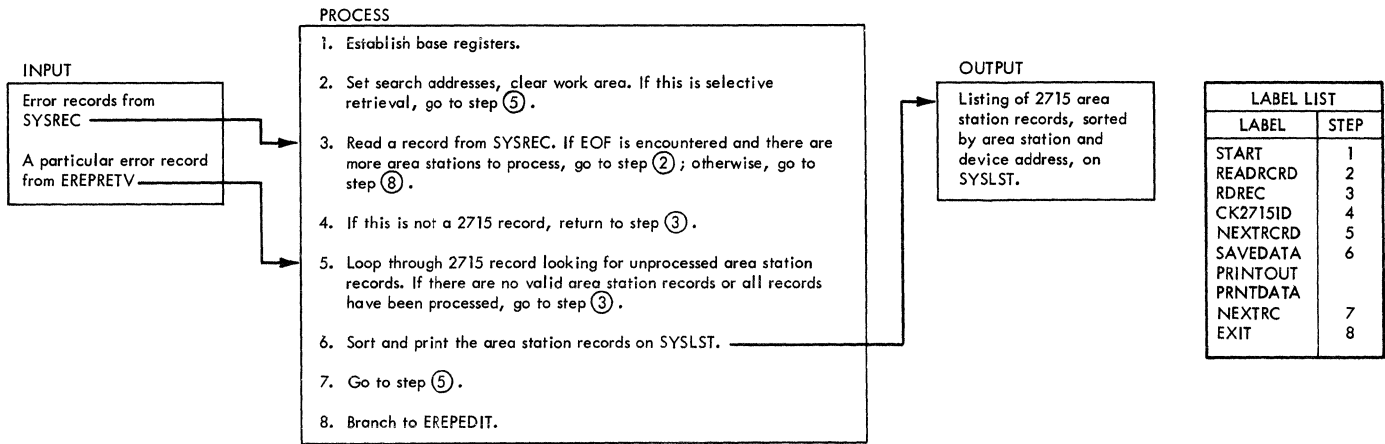
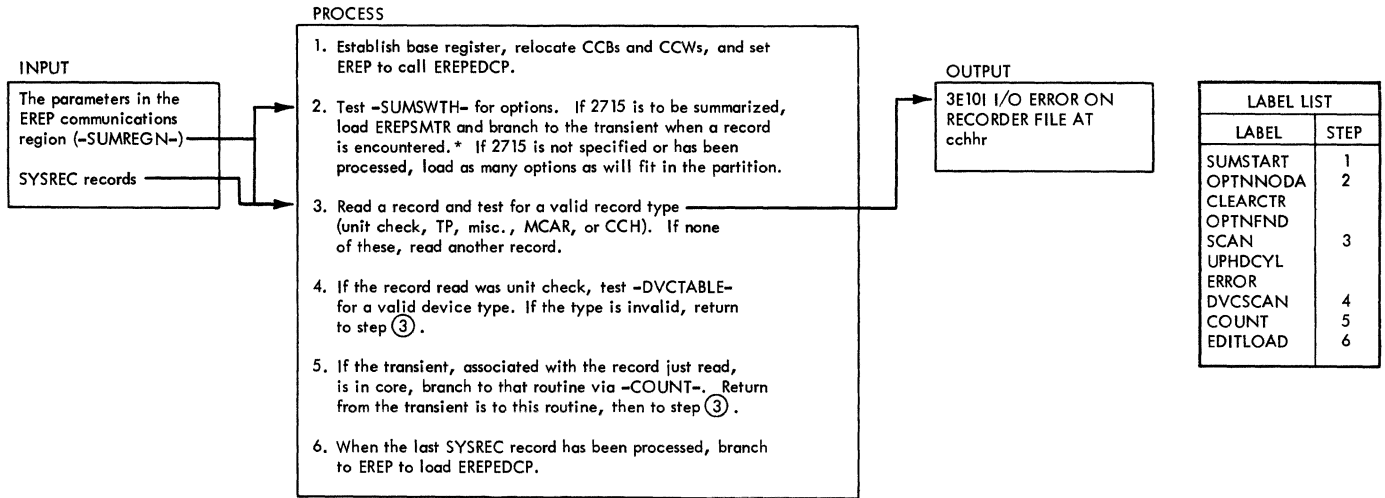


Chart MP. EREPASTA: Function



LABEL LIST	
LABEL	STEP
START	1
READRCRD	2
RDREC	3
CK2715ID	4
NEXTRCRD	5
SAVEDATA	6
PRINTOUT	
PRNTDATA	
NEXTRC	7
EXIT	8

Chart MQ. EREPSMCP: Function



\*Note: All 2715 addresses are processed, with multiple executions of EREPSMCP and EREPEDCP, if necessary, before other options are processed. 2715 summary will always use 10K, even if more room is available.

Chart MR. EREPSMTR: Function

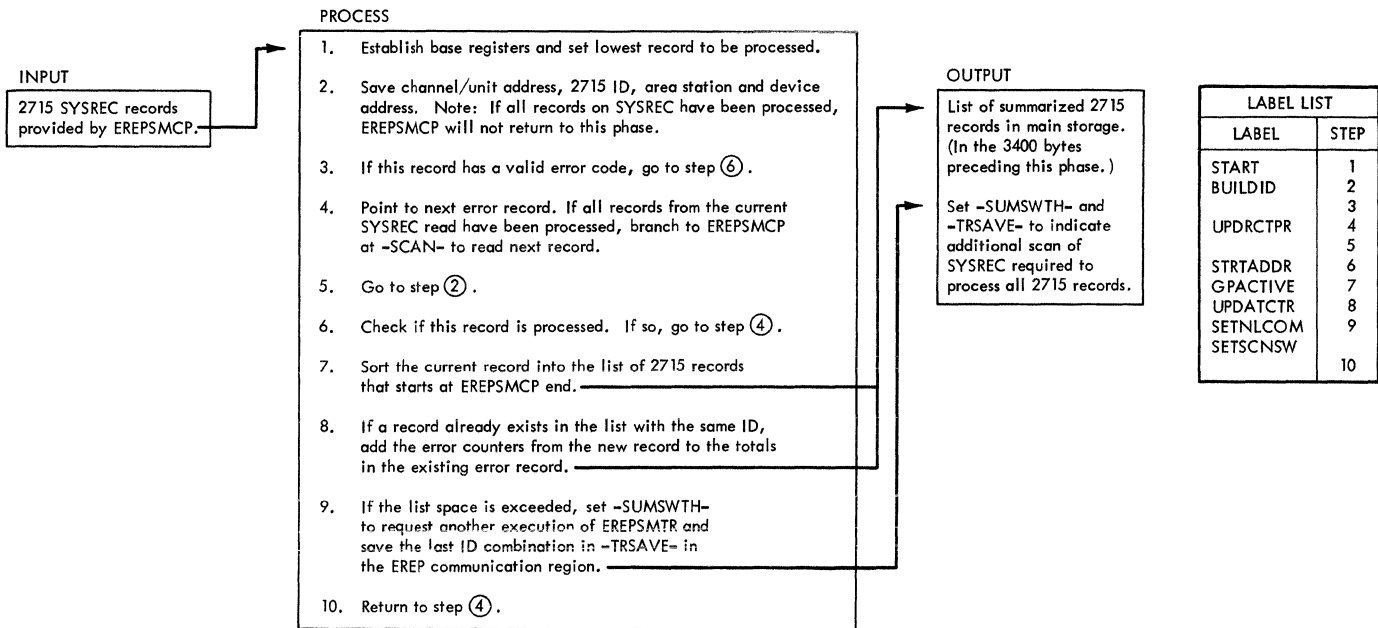


Chart MS. EREPSM - TD, DA, UR, CR, DK, CU, D1, T1, and U1: Function

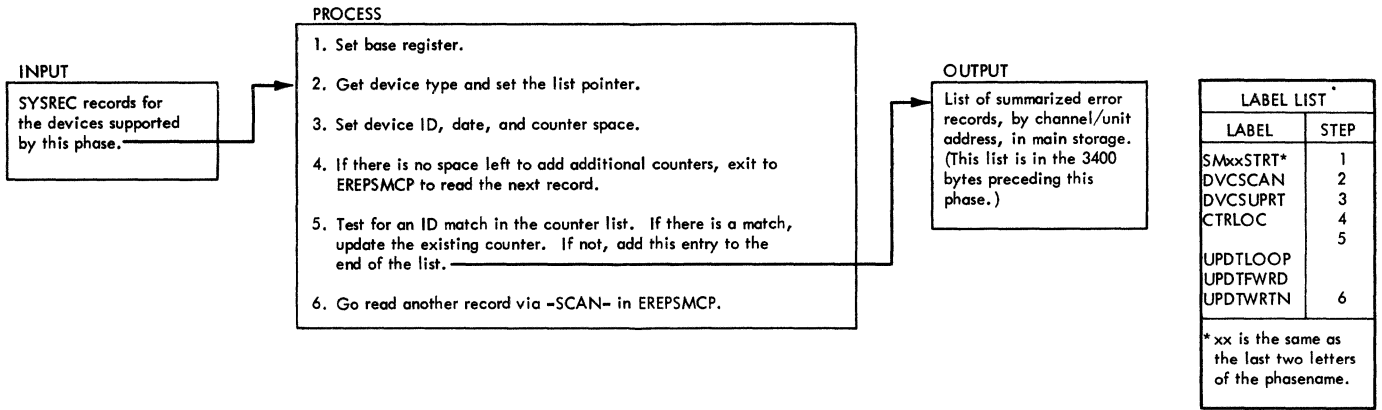


Chart MT. EREPSMTP: Function

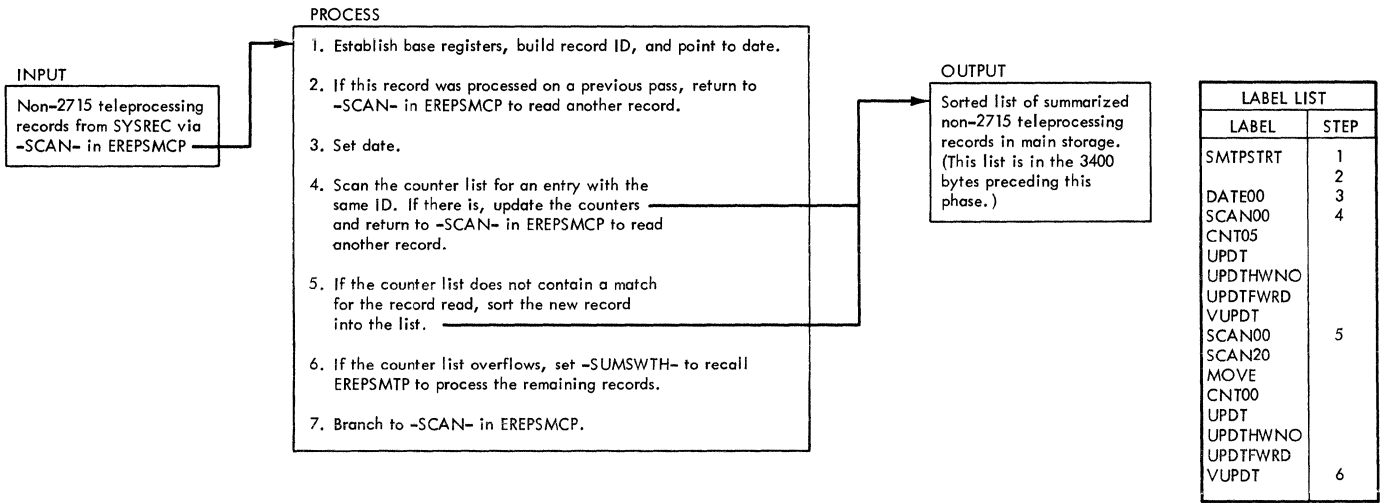
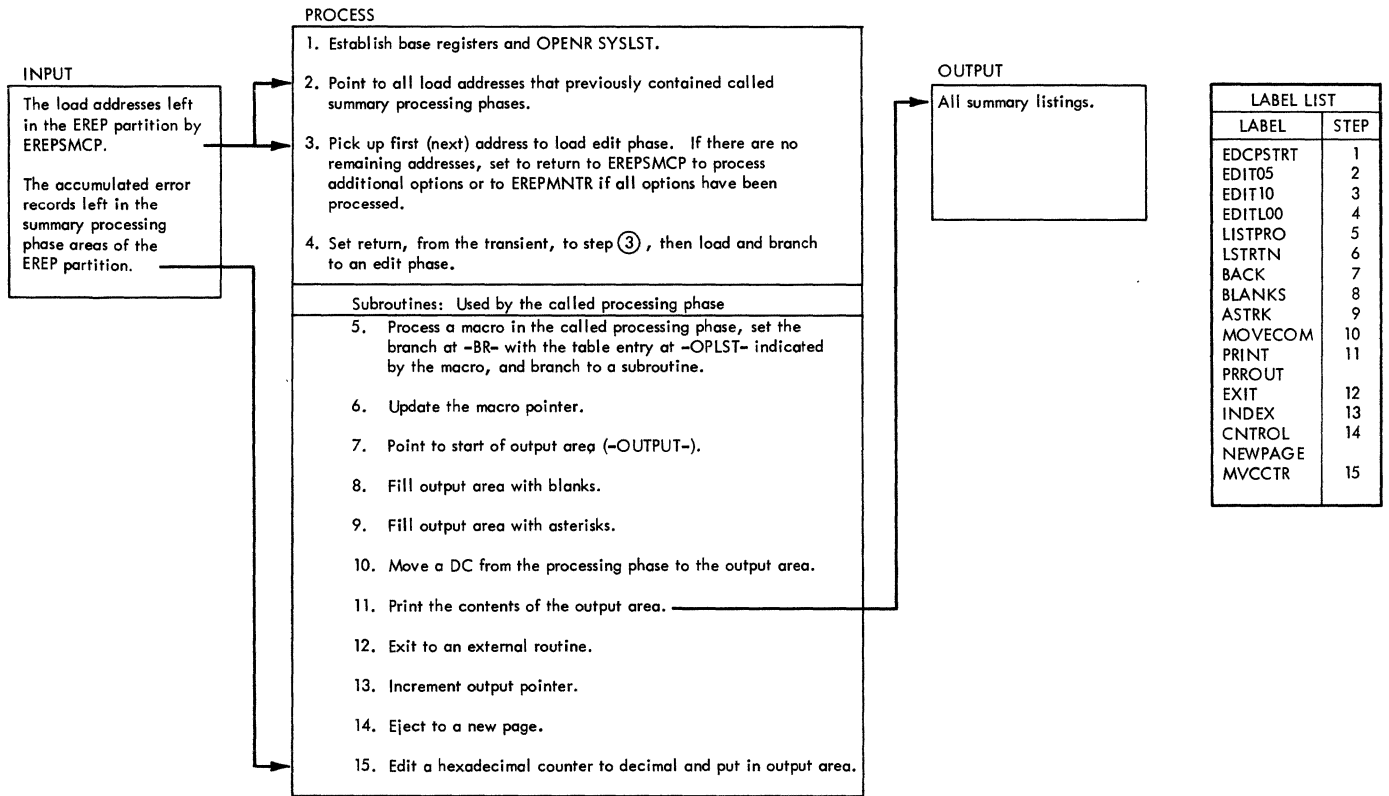


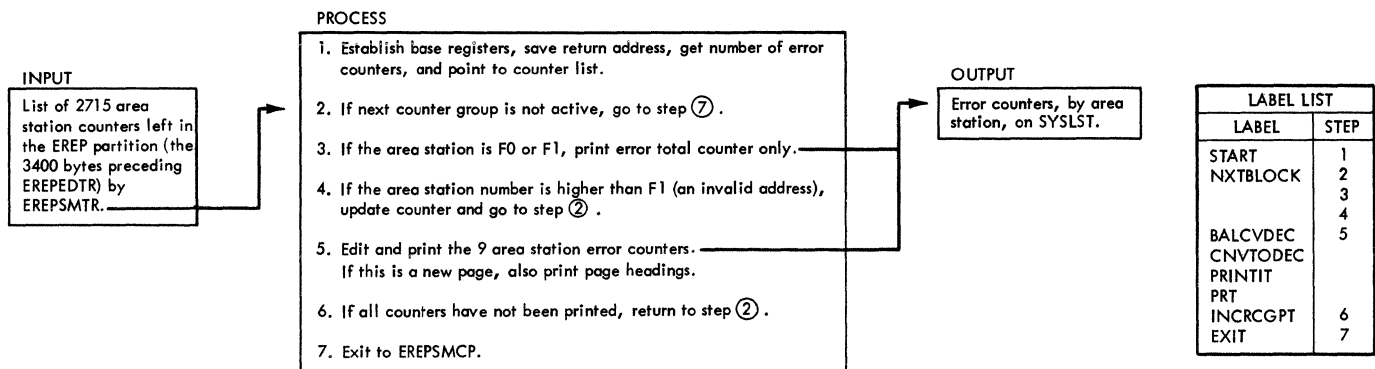


Chart MU. EREPEDCP: Function



LABEL LIST	
LABEL	STEP
EDCPSTRT	1
EDIT05	2
EDIT10	3
EDITL00	4
LISTPRO	5
LSTRTN	6
BACK	7
BLANKS	8
ASTRK	9
MOVECOM	10
PRINT	11
PRROUT	
EXIT	12
INDEX	13
CNTROL	14
NEWPAGE	
MVCCTR	15

Chart MV. EREPEDTR: Function



LABEL LIST	
LABEL	STEP
START	1
NXTBLOCK	2
	3
	4
BALCVDEC	5
CNVTODEC	
PRINTIT	
PRT	
INCRGPT	6
EXIT	7

Chart MW. EREPEDDA and EREPEDTD: Function

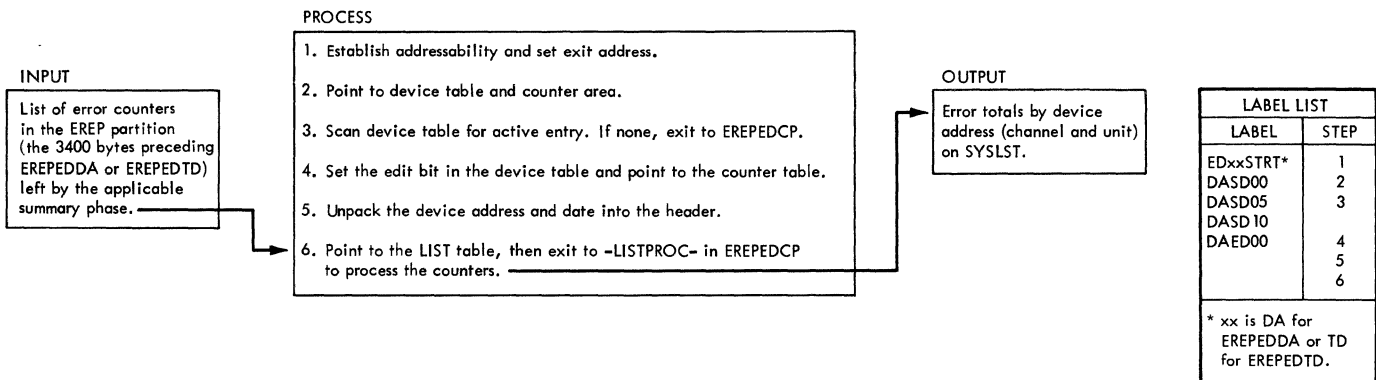


Chart MX. EREPEDCR, EREPEDDK, and EREPEDUR: Function

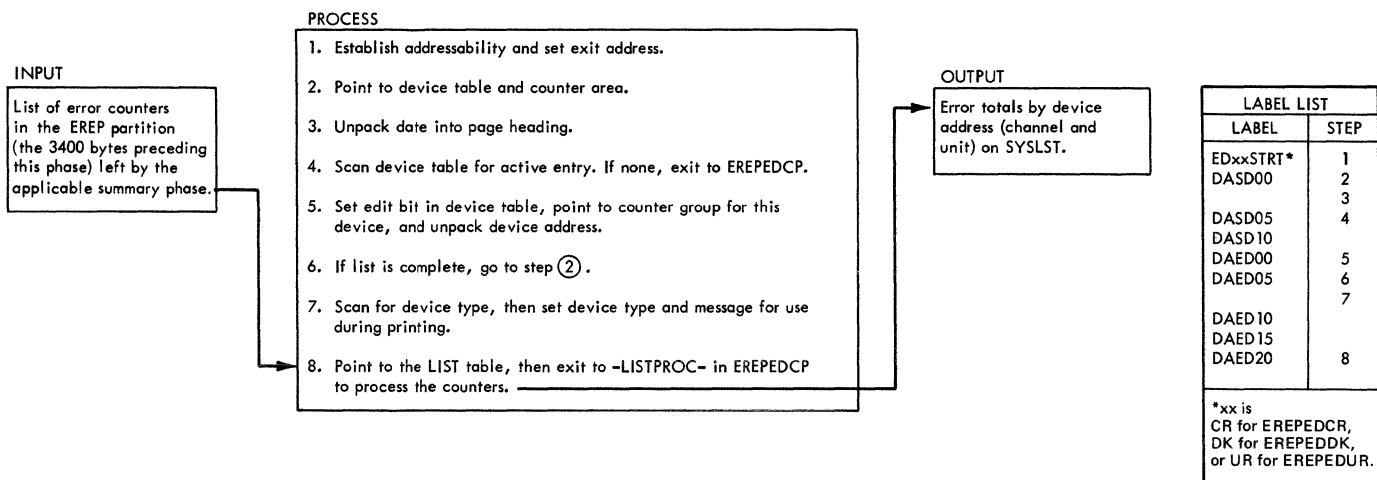


Chart MY. EREPEDTP: Function

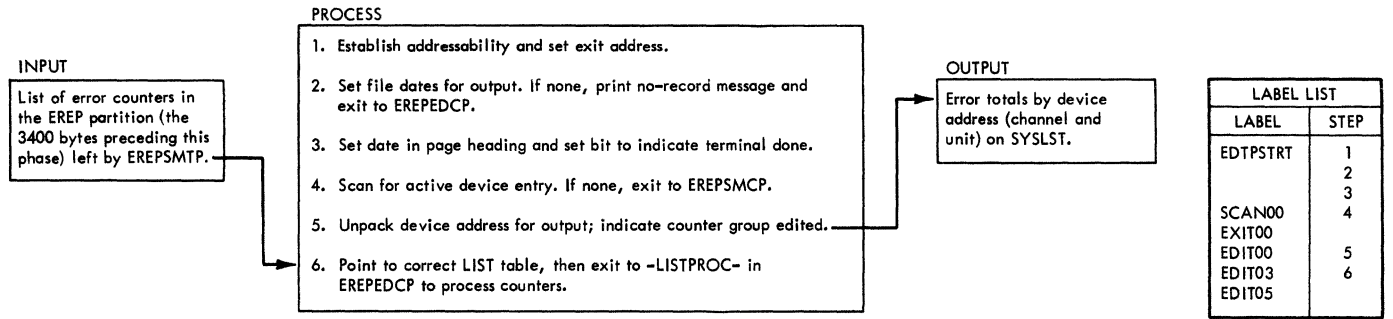


Chart MZ. EREPEDCU: Function

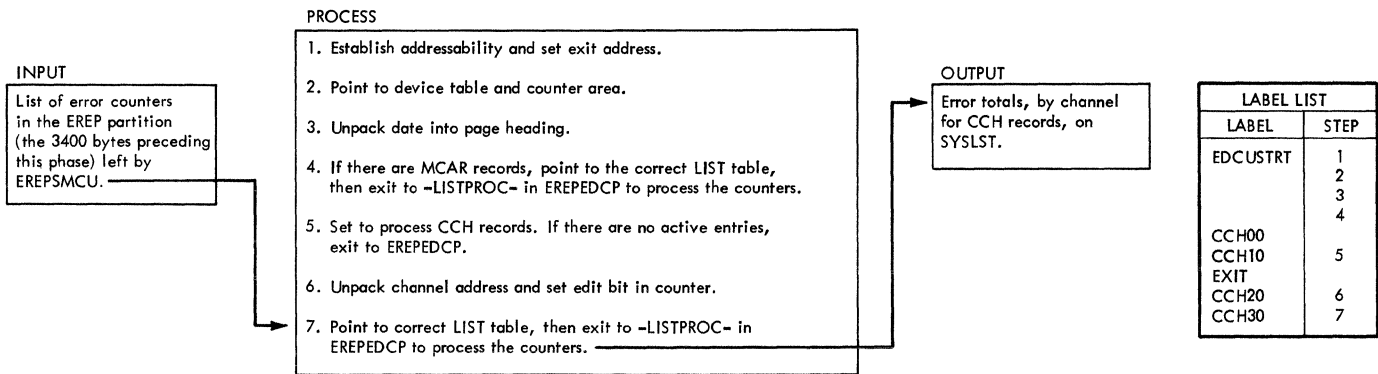


Chart NA. EREPEDD1: Function

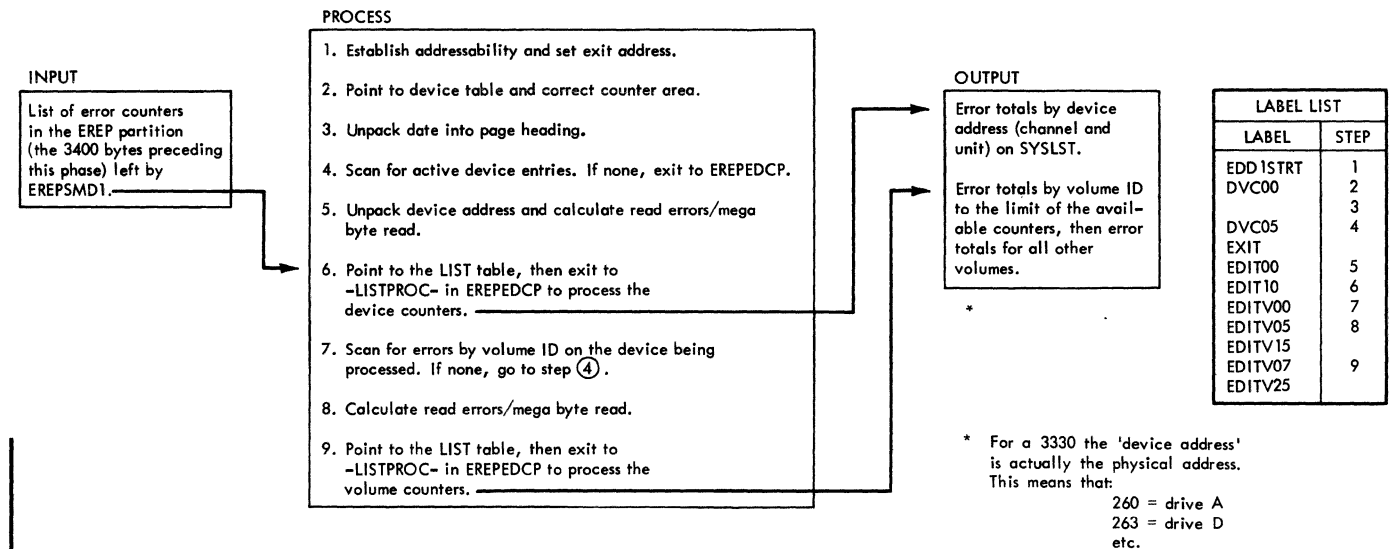


Chart NB. EREPEDT1 and EREPDX1: Function

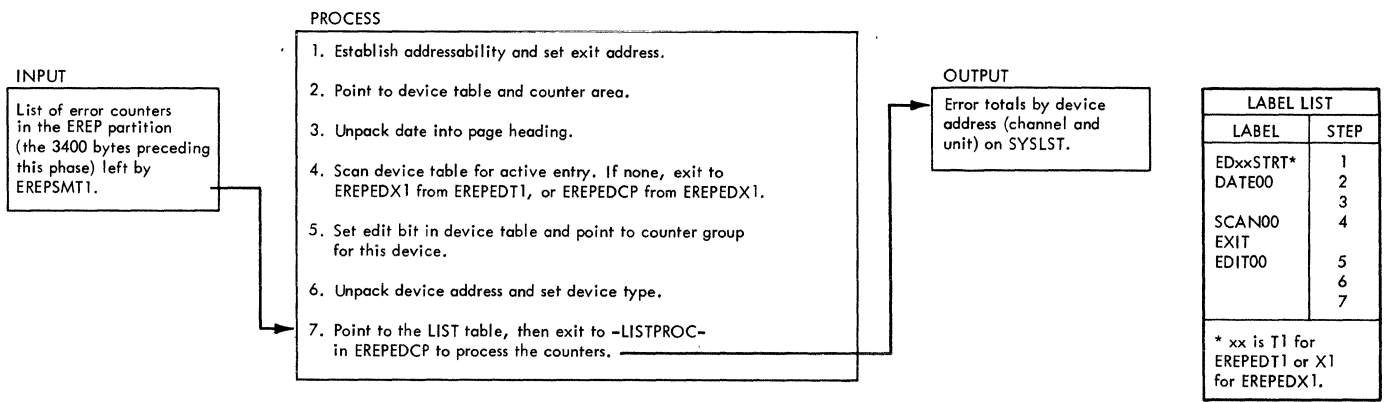




Chart NC. EREPEDU1: Function

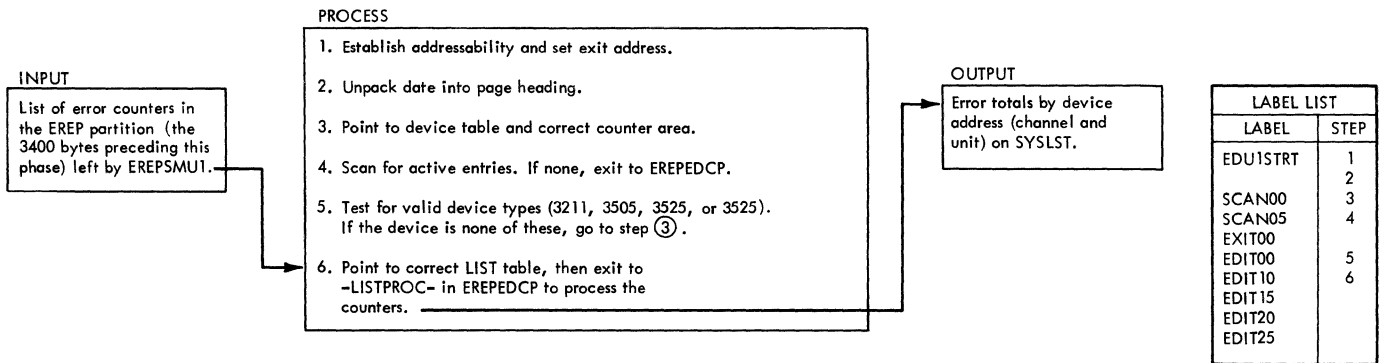


Chart ND. EREPSMES: Function

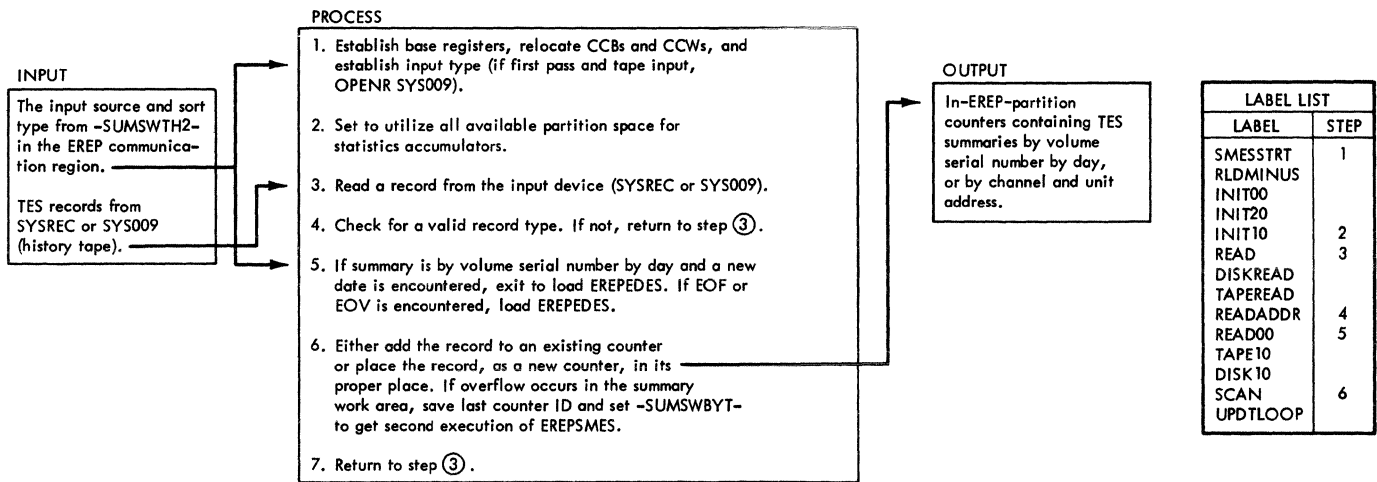


Chart NE. EREPEDES: Function

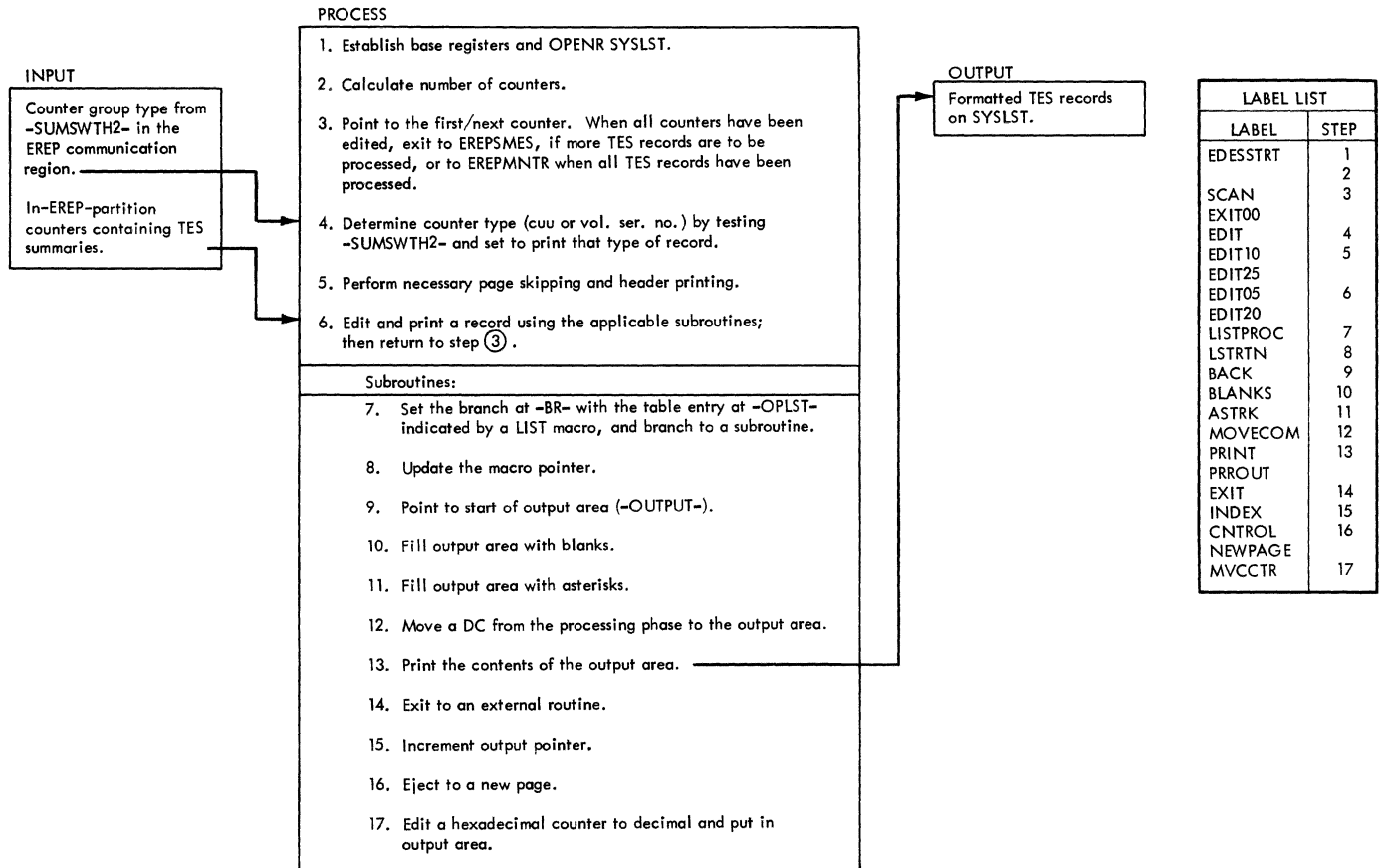


Chart NF. EREPTES: Function

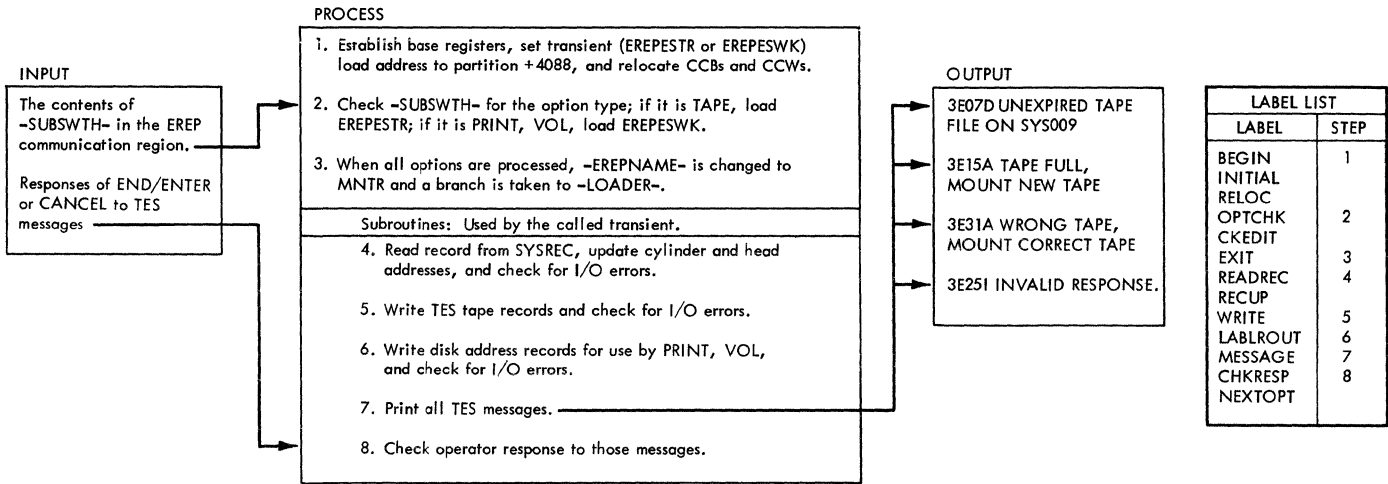
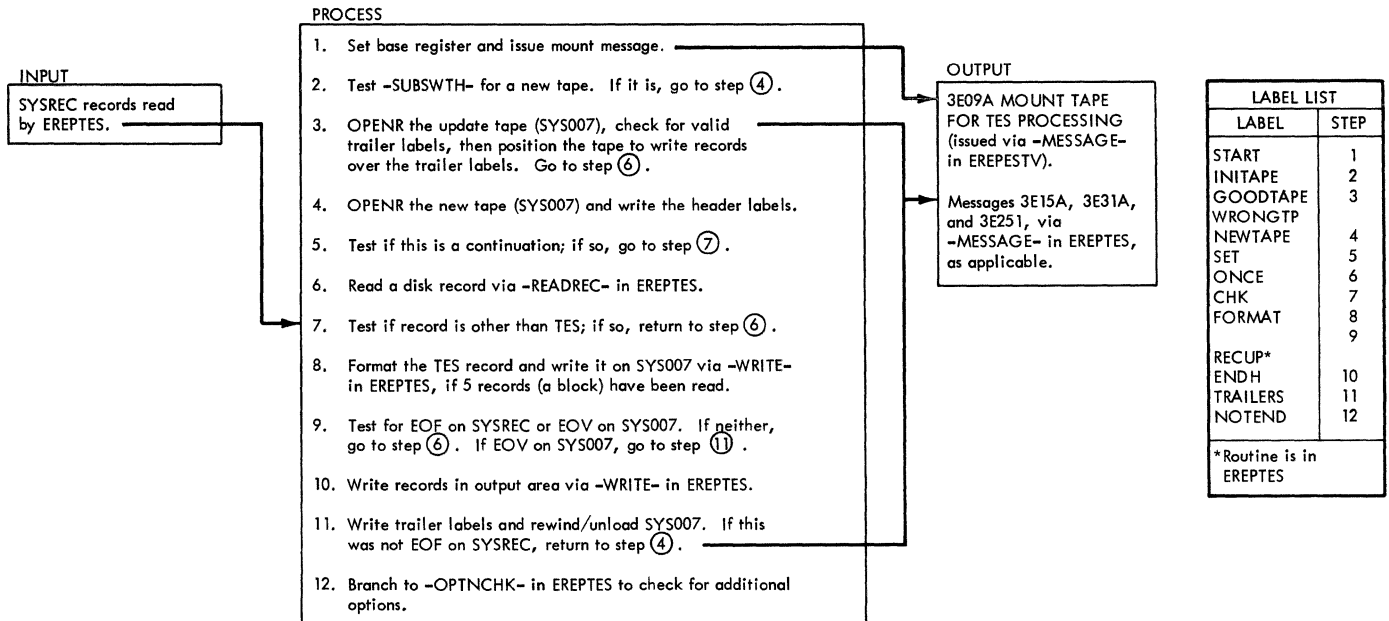


Chart NG. EREPESTR: Function



LABEL LIST	
LABEL	STEP
START	1
INITAPE	2
GOODTAPE	3
WRONGTP	
NEWTAPE	4
SET	5
ONCE	6
CHK	7
FORMAT	8
	9
RECU*	
ENDH	10
TRAILERS	11
NOTEND	12

\*Routine is in EREPTES

Chart NH. EREPESWK: Function

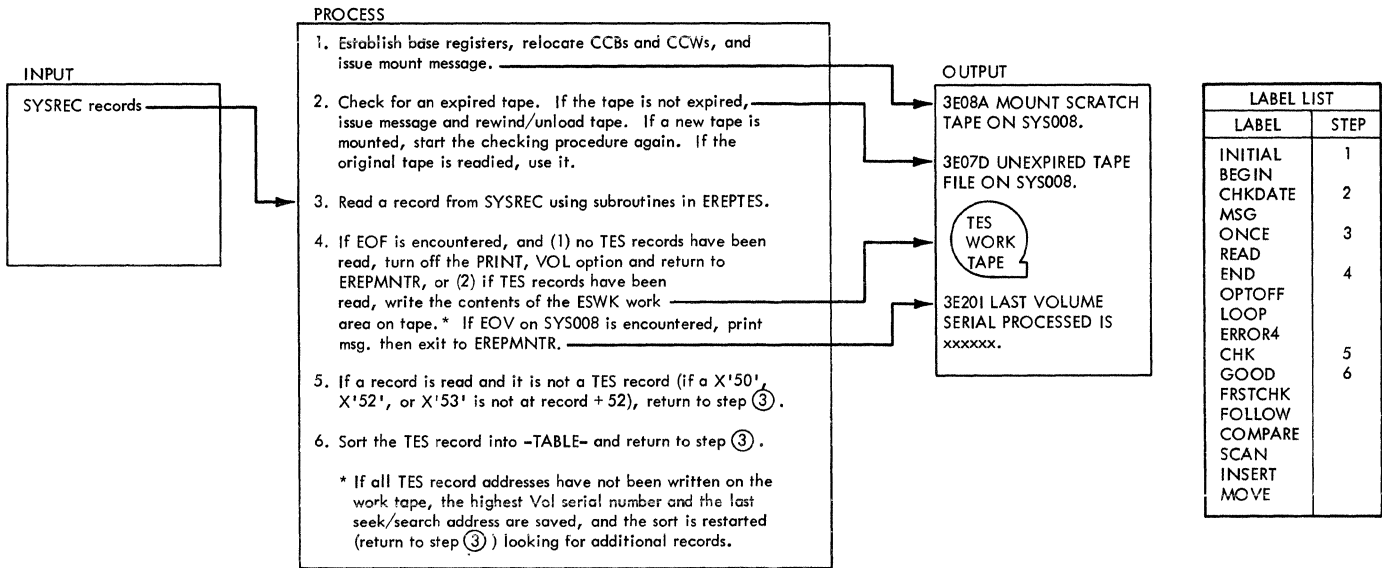


Chart NJ. EREPHIST: Function

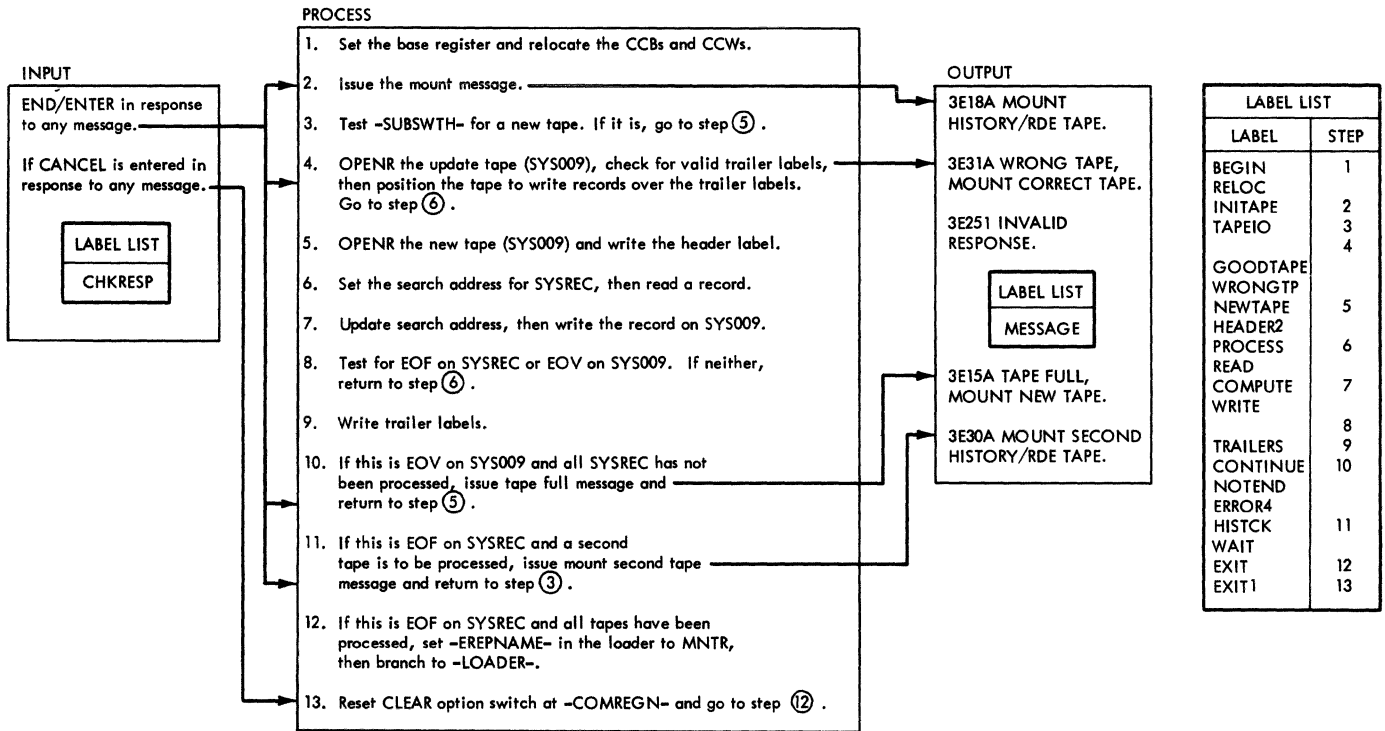


Chart NK. EREPRDE: Function (Part 1 of 2)

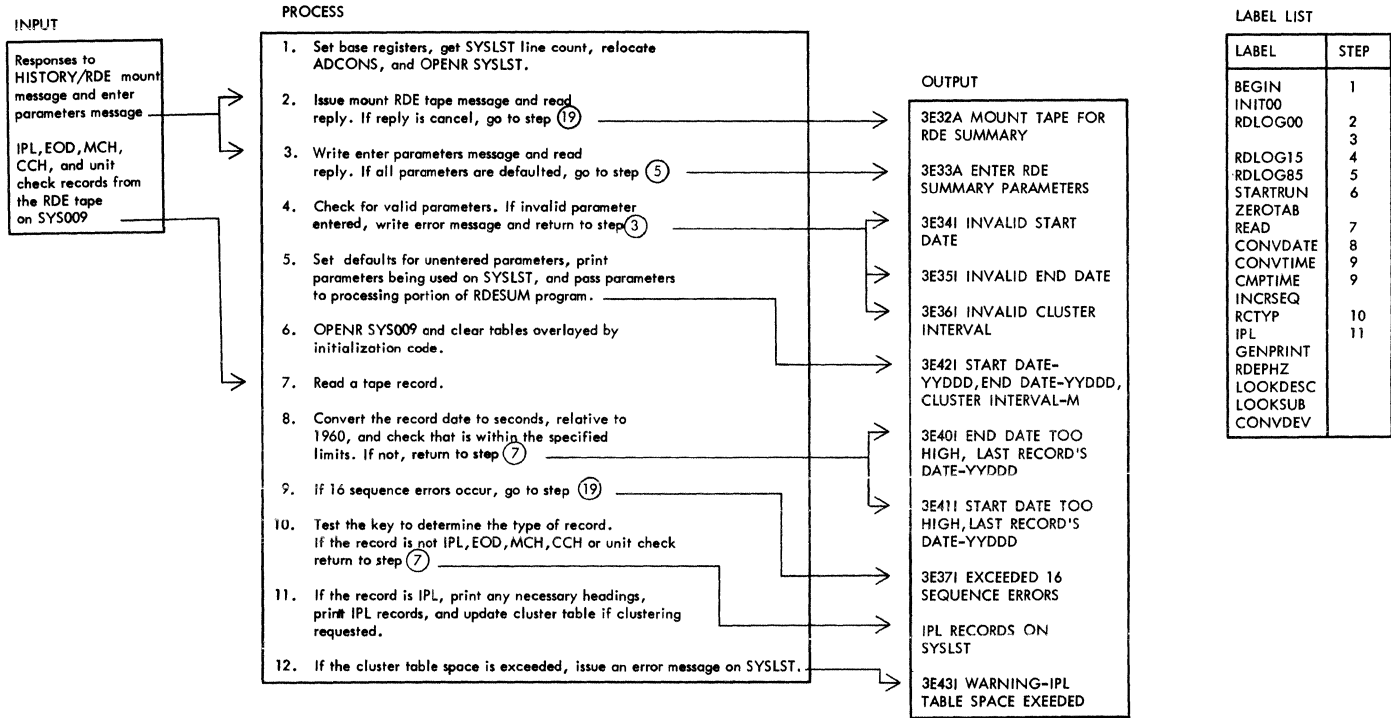




Chart NL. EREPRDE: Function (Part 2 of 2)

PROCESS

13. If the record is MCH, CCH, or OBR, determine if it is a System Incident or System Recovery Incident and update recording table.
14. If the record is EOD, calculate run time (IPL to EOD).
15. If end date is exceeded or EOF is encountered, print end date of report, calculate and print average run time (IPL to EOD), and print IPLs within clustering interval. If no IPLs processed, issue message.
16. If the number of IPL cluster intervals calculated exceeds the available table space, issue an error message.
17. Edit and sort SI/SRI table entries, then print number of SI/SRI for the CPU, real storage, channels, and subsystems.
18. If the number of subsystems encountered exceeds the available table space, issue an error message on SYSLST.
19. CLOSER SYS009 and rewind the tape, then CLOSER SYSLST and return to EREPMNTR via EREP.

OUTPUT

Average run time, report end date, and IPLs within specified cluster interval.

3E381 NO IPL RECORDS PROCESSED

3E431 WARNING-SEQUENCE TABLE SPACE EXCEEDED

System Incidents and System Recovery Incidents by Subsystem.

3E431 WARNING-SYSTEM/SUBSYSTEM TABLE SPACE EXCEEDED.

LABEL LIST

LABEL	STEP
MCH	13
CCH	
OBR	
EOD	14
WRAPUP	15
GRUPHD	
P2EJ1	17
PHASE2ND	19



3203, 3211, AND 5203 PRINTER SUPPORT PROGRAMS

Two programs are supplied to load UCB (Universal Character Set Buffer) and FCB (Forms Control Buffer) of the 3203, 3211, and 5203 printers. These programs are:

- \$\$BUFLDR (with subphases \$\$BUFID1 and \$\$BUFLD2), which loads the UCB and FCB with standard control information during IPL
- SYSBUFLD, which can be used to load the UCB and FCB with the required control information between jobs or job steps any time after IPL.

Note: For a 5203 printer without the universal character set feature, only the FCB is loaded.

\$\$BUFLDR is executed as part of the IPL procedure. If 3203, 3211, or 5203 printers are attached to the system, \$\$BUFLDR together with the corresponding standard buffer load phases must be available in the core image library. The standard buffer load phases for the individual printers are shown in the following table:

PRINTER	To load FCB	To load UCB
3203	\$\$BF3CB3	\$\$BUCB3
3211	\$\$BF3CB	\$\$BUCB
5203	\$\$BF5CB5	\$\$BUCB5*
*Not required for a 5203 without UCS feature.		

\$\$BUFLDR is loaded by phase \$IPLRT4. It scans the PUB table for 3203, 3211, and 5203 printer entries. When it finds an entry, it loads the UCB and calls phase \$\$BUFLD1. This phase loads the FCB of the printer and returns control to \$\$BUFLDR. \$\$BUFLDR then returns to scanning the PUB table for further printer entries. When all buffers have been loaded, \$\$BUFLDR returns to \$IPLRT4.

SYSBUFLD is invoked by a // EXEC SYSBUFLD statement and can be executed any time after IPL within the user job stream. With one invocation of SYSBUFLD you may load the FCB and UCB of a single printer, or any combination of buffers on several printers.

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One program, SYSBUFLD, is supplied to load the FCB (Forms Control Buffer) of the 2245 printer. SYSBUFLD is designed to execute as a job step within the user job stream. The EREP listing (printer output) for the 2245 printer is the same as that for unsupported devices.

Chart 19. Printer Buffer Load

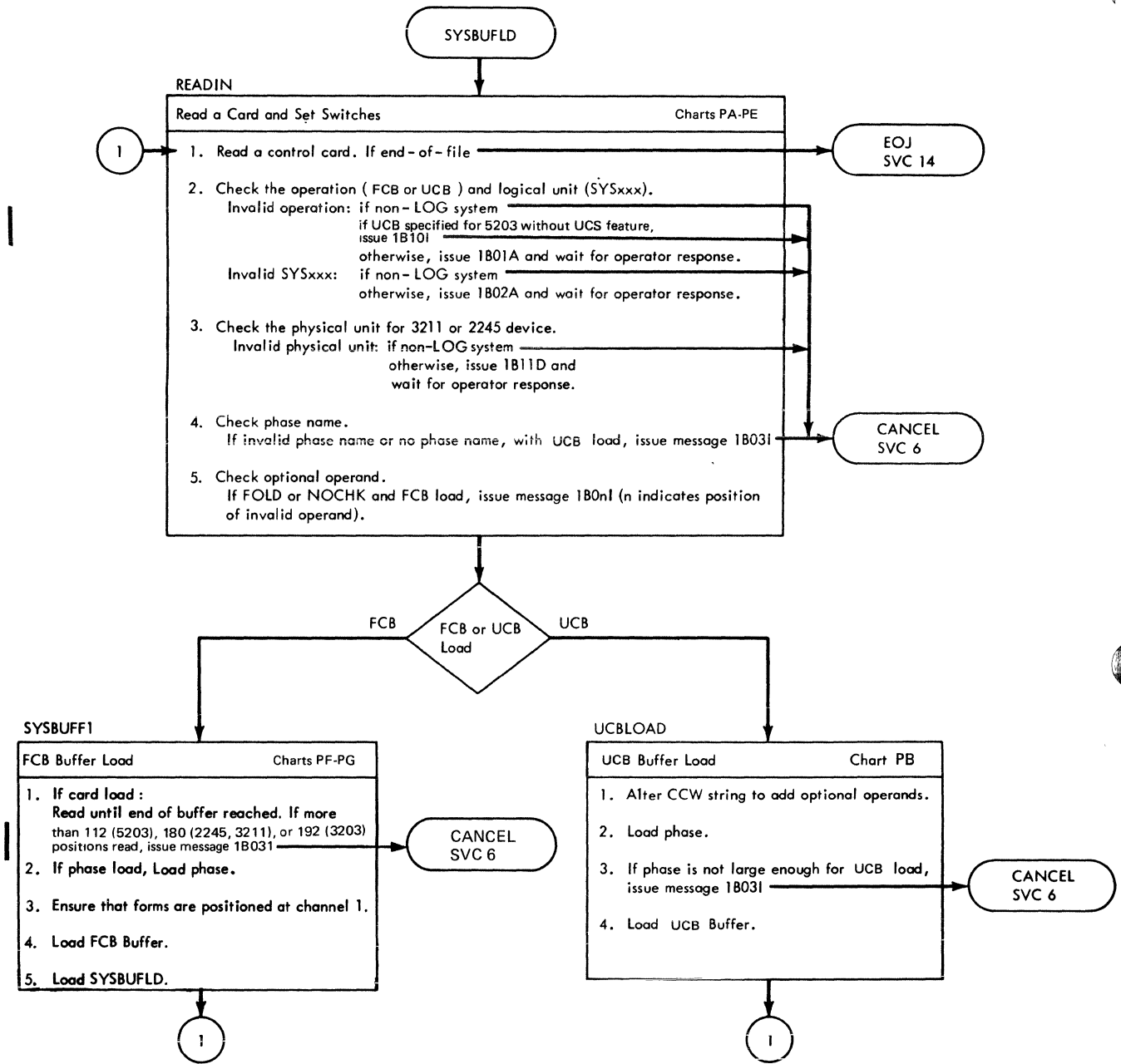




Chart PA. SYSBUFLD - Read Card and Print on SYSLOG and SYSLSL  
 Refer to Chart 19.

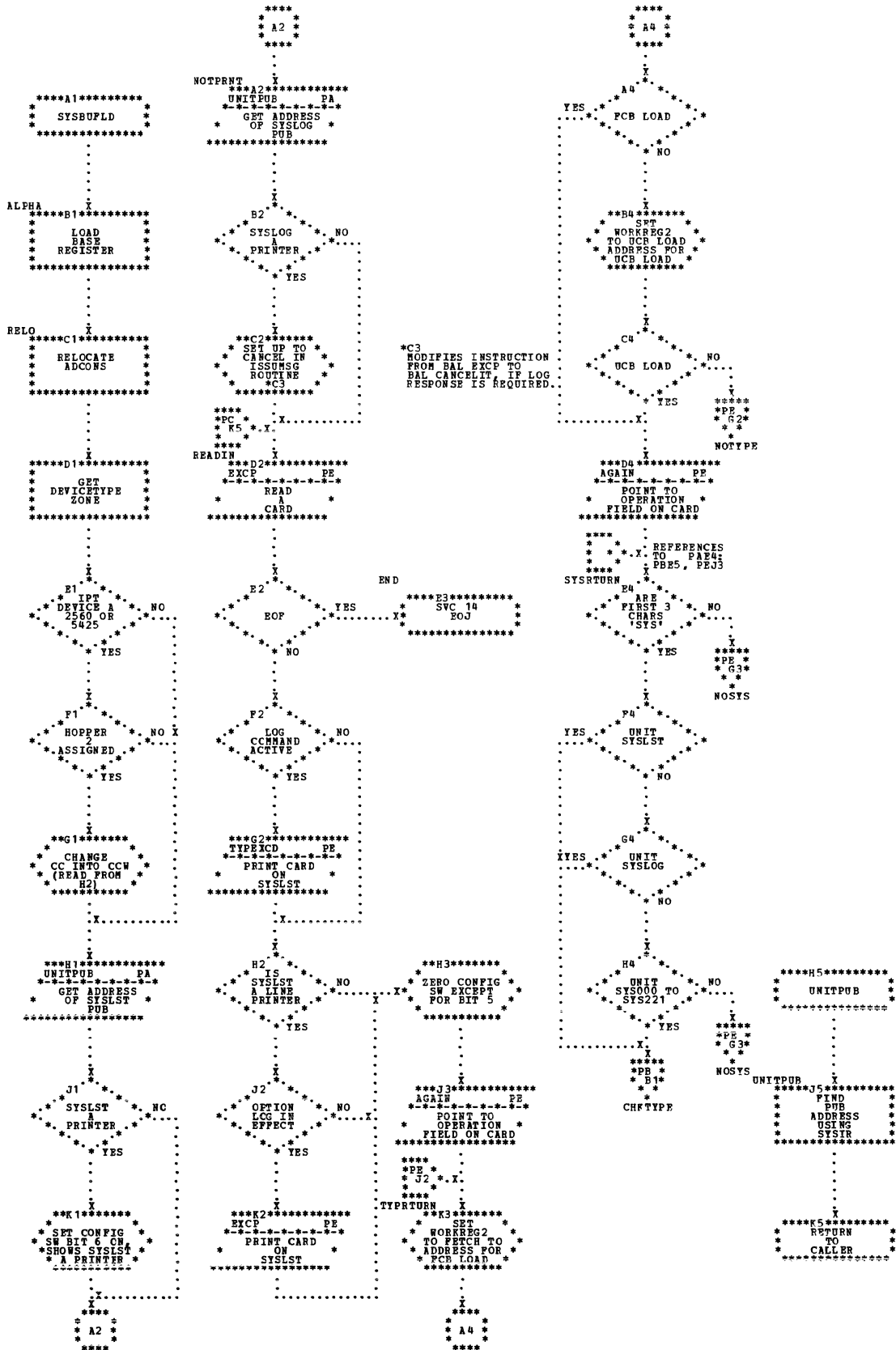




Chart PC. SYSEUFLD - Load UCB  
Refer to Chart 19.

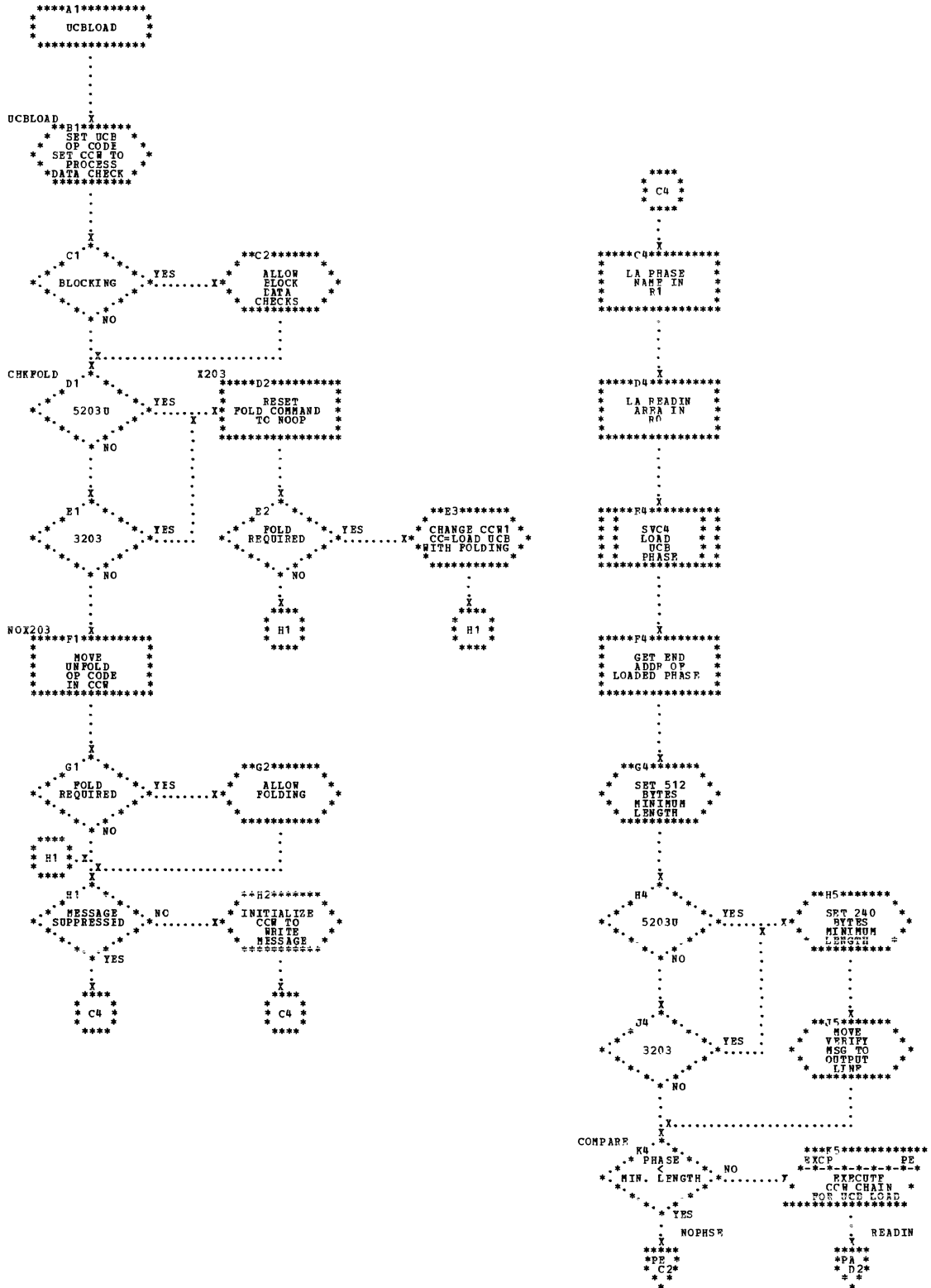




Chart PD. SYSBUFLD - Check Optional Operands  
Refer to Chart 19.

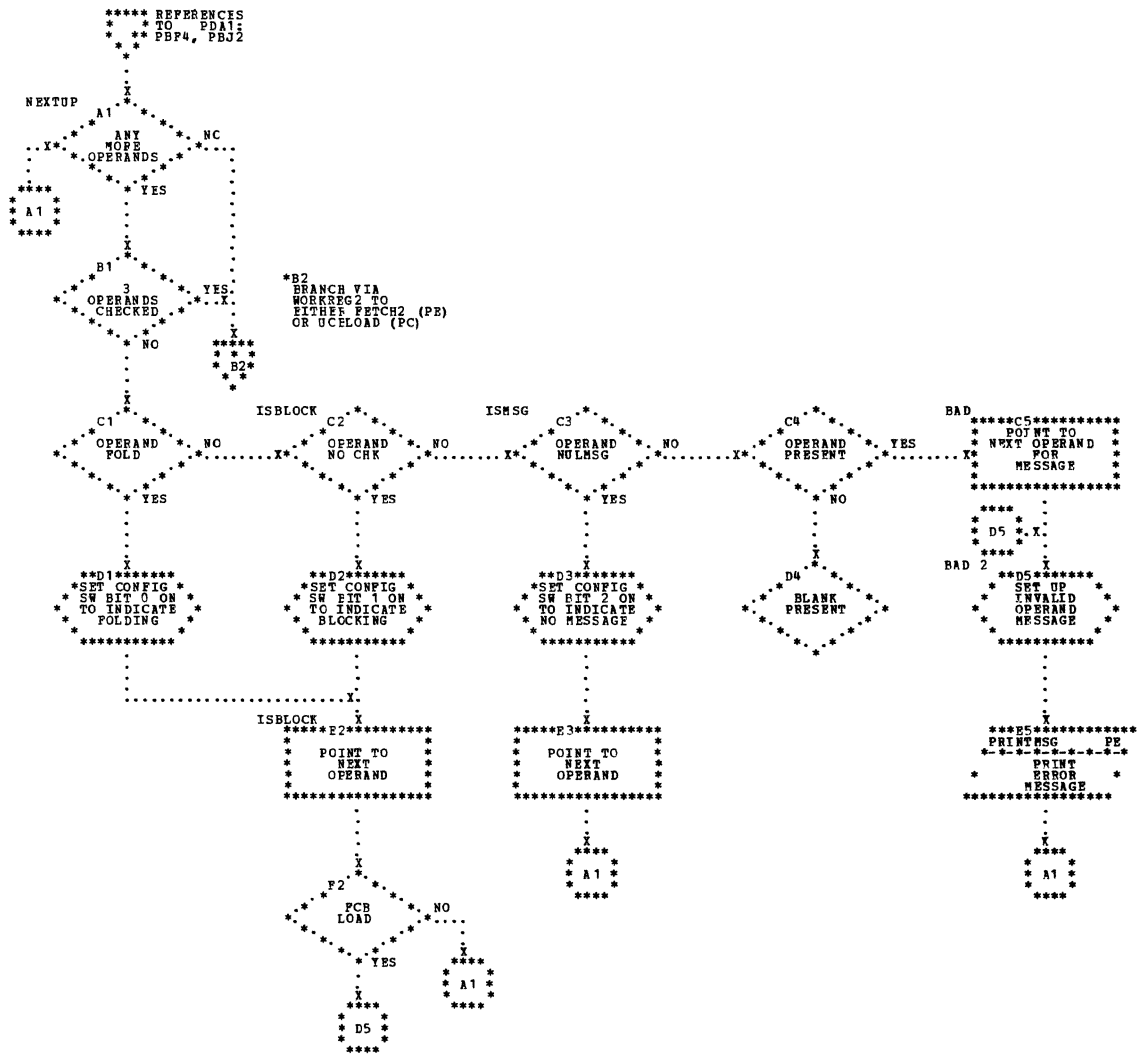


Chart PE. SYSEUFF1 - Miscellaneous Subroutines  
Refer to Chart 19.

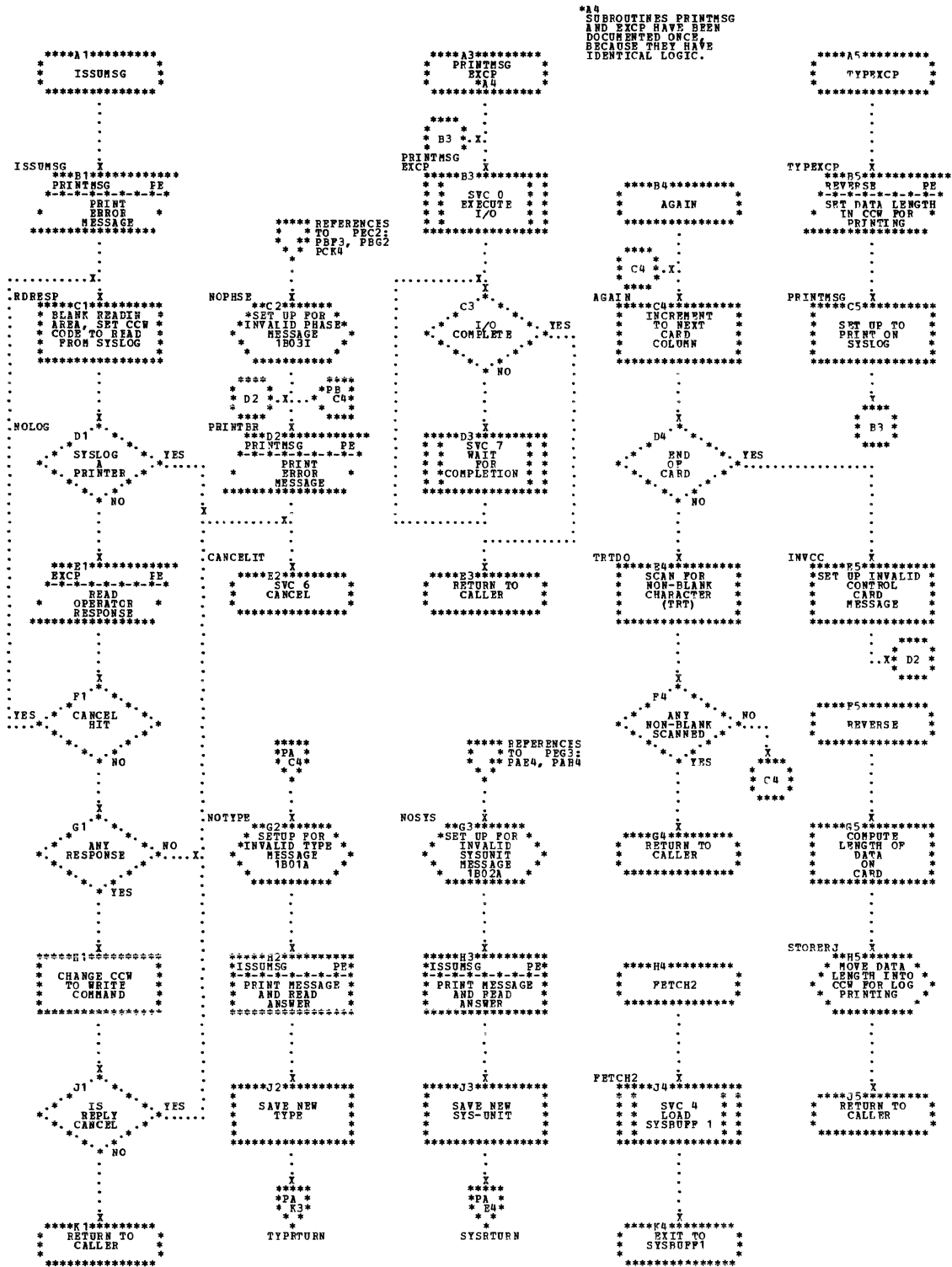


Chart PF. SYSBUFF1 - FCB Buffer Load (Part 1 of 2)  
Refer to Chart 19.

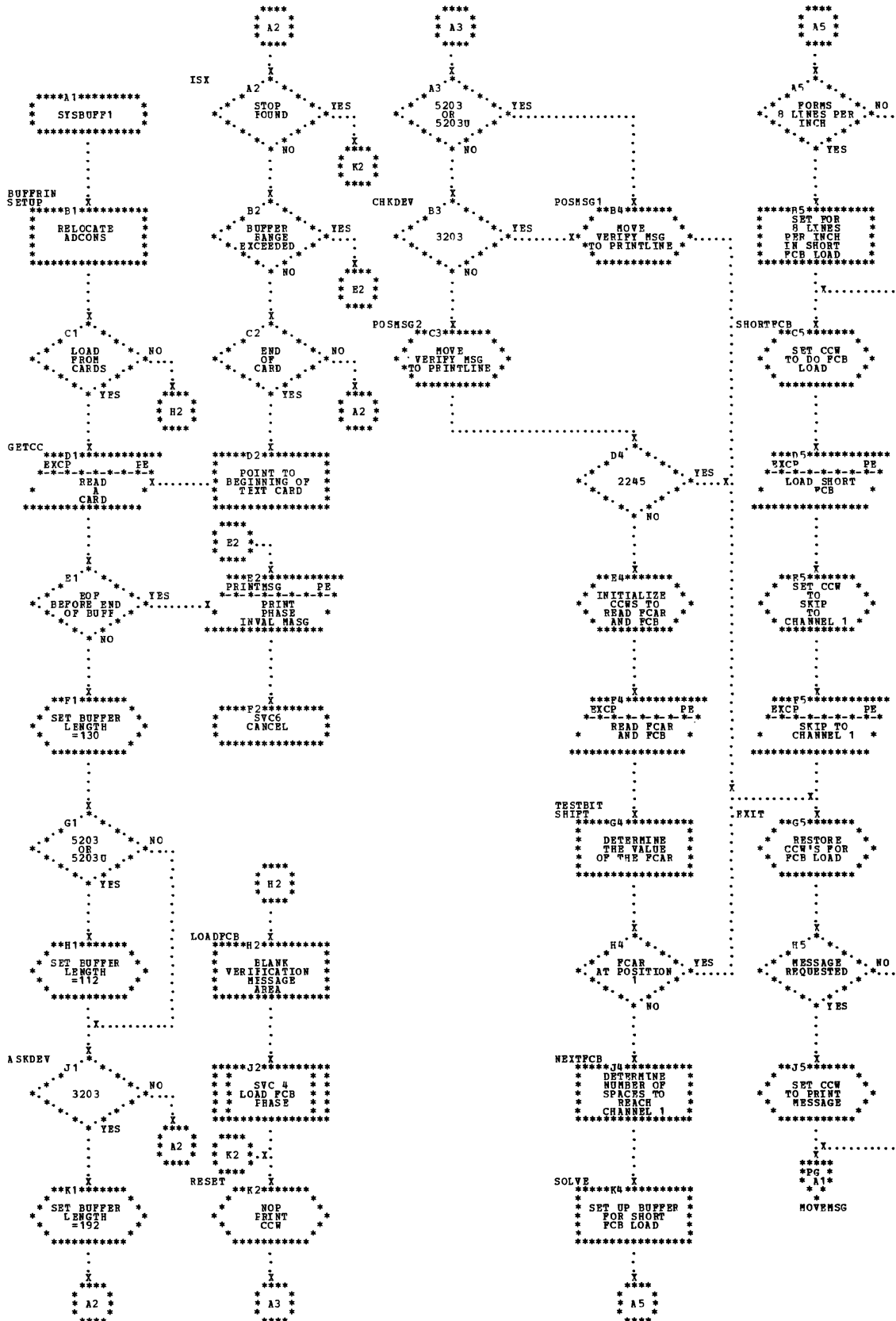
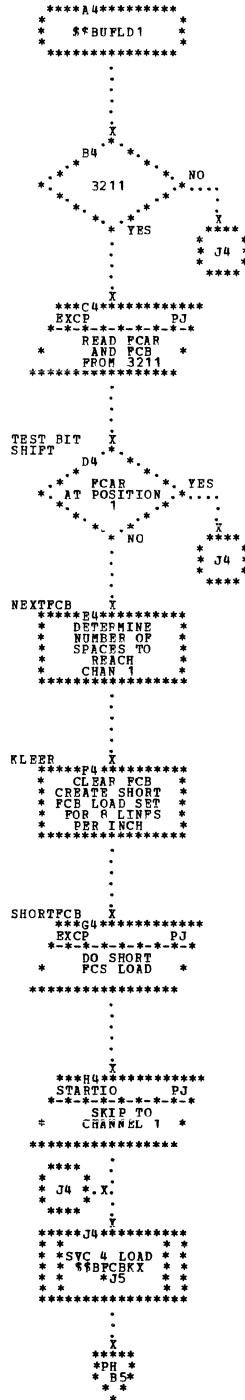
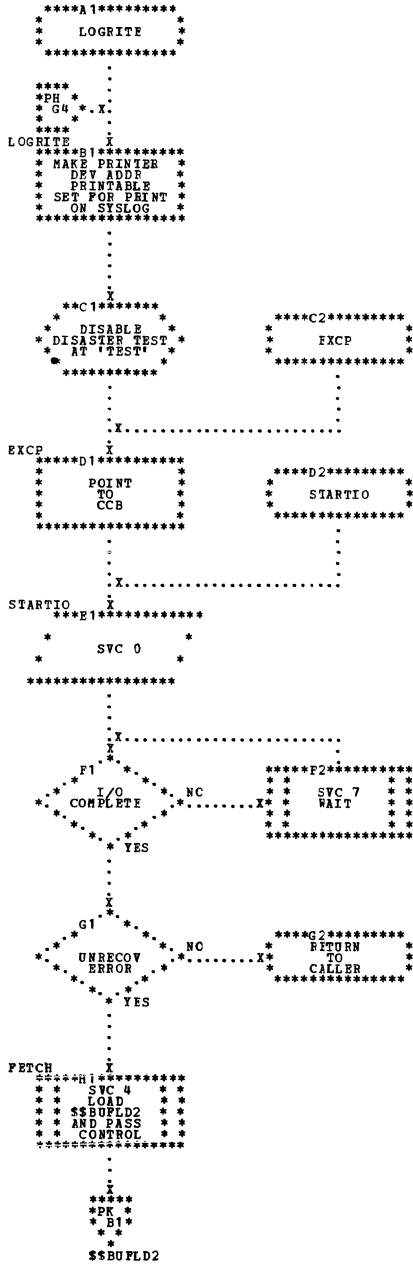






Chart PJ. Print Messages on SYSLOG and Load FCB  
Refer to Chart 20.



\*J5  
X= (BLANK) FOR 3211  
3 FOR 3203  
5 FOR 5203







APPENDIX A: ERROR MESSAGE CROSS REFERENCE LIST

PDAIDS ERROR MESSAGES

4C68 GB SDPAR  
4C69 GB SDPAR

<u>MESSAGE</u>	<u>CHART</u>	<u>PHASE</u>
4C10	AA	PDAID
4C11	AB,AK	PDAID
4C12	AK	PDAID
4C13	AH,AK	PDAID
4C14	AH	PDAID
4C15	AJ	PDAID
4C16	AJ	PDAID
4C17	AC	PDAID
4C17	AD	PDAID
4C17	AG	PDAID
4C17	AH	PDAID
4C17	AK	PDAID
4C20	AD,AH	PDAID
4C21	AE	PDAID
4C22	AF	PDAID
4C23	AE	PDAID
4C24	AM	\$\$BPDAID
4C24	CB	PDAIDITP
4C24	CD	PDAIDITT
4C24	DC	PDAIDFTT
4C24	DE	PDAIDFTT
4C24	EC	PDAIDGTP
4C24	EF	PDAIDGTT
4C24	FA	PDAIDTDT
4C24	FC	PDAIDTET
4C26	BC	PDLIST
4C27	AC	PDAID
4C28	AE	PDAID
4C50	AC	PDAID
4C51	AC	PDAID
4C52	AC	PDAID

DUMPGEN ERROR MESSAGES

<u>MESSAGE</u>	<u>CHART</u>	<u>PHASE</u>
4C42	HA	DUMPGEN
4C43	HA	DUMPGEN
4C44	HD	DUMPGEN
4C46	HE	DUMPGEN

PDSDM ERROR MESSAGES

<u>MESSAGE</u>	<u>CHART</u>	<u>PHASE</u>
0V20	JA	PDSDM
0V21	JC	PDSDM
0V22	JD	PDSDM
0V23	JA	PDSDM
0V24	JA	PDSDM
0V25	JA	PDSDM
0V26	JA	PDSDM
0V27	JA	PDSDM
0V28	JA	PDSDM
0V29	JA	PDSDM
0V30	JC,JD	PDSDM
0V31	JC	PDSDM
0V32	JC,JD	PDSDM
0V33	JC	PDSDM
0V34	JD	PDSDM
0V35	JC,JD	PDSDM
0V36	JC,JD	PDSDM
0V37	JD	PDSDM
0V39	JD	PDSDM
0V40	JC,JD	PDSDM
0V41	JF	PDSDM
0V42	JJ	PDSDM
0V43	JJ	PDSDM
0V44	JB	PDSDM

SDAID ERROR MESSAGES

<u>MESSAGE</u>	<u>CHART</u>	<u>PHASE</u>
04E6	GH	SDEHR
4C17	GA	SDAID1
4C17	GB	SDPAR
4C53	GA	SDAID1
4C54	GA	SDAID1
4C55	GA	SDAID1
4C56	GA	SDAID1
4C57	GE	SDAID2
4C58	GB	SDPAR
4C59	GB	SDPAR
4C60	GB	SDPAR
4C61	GB	SDPAR
4C62	GB	SDPAR
4C63	GB	SDPAR
4C64	GB	SDPAR
4C65	GB	SDPAR
4C66	GB	SDPAR
4C67	GB	SDPAR

EREP ERROR MESSAGES

<u>MESSAGE</u>	<u>CHART</u>	<u>PHASE</u>
3E01	MC	EREPMNTR
3E02	MC	EREPMNTR
3E04	MC	EREPMNTR
3E06	MC	EREPMNTR
3E07	NF	EREPIES
3E07	NH	EREPEWK
3E08	NH	EREPEWK
3E09	NG	EREPESTR

3E10	MD	EREPEDIT	3E34	NK	EREPRDE
3E10	MQ	EREPSMCP	3E35	NK	EREPRDE
3E11	MB	EREPMNTR	3E36	NK	EREPRDE
3E12	MB	EREPMNTR	3E37	NK	EREPRDE
3E14	MB	EREPMNTR	3E38	NL	EREPRDE
3E15	NF	EREPTES	3E40	NK	EREPRDE
3E15	NG	EREPESTR	3E41	NK	EREPRDE
3E15	NJ	EREPHIST	3E42	NK	EREPRDE
3E18	NJ	EREPHIST	3E43	NK	EREPRDE
3E20	NH	EREPESWK	3E43	NL	EREPRDE
3E21	MD	EREPEDIT			
3E22	MD	EREPEDIT			
3E25	MB	EREPMNTR			
3E25	NF	EREPTES			
3E25	NG	EREPESTR			
3E25	NJ	EREPHIST			
3E26	MC	EREPMNTR			
3E27	MC	EREPMNTR			
3E28	MC	EREPMNTR			
3E29	MC	EREPMNTR			
3E30	NJ	EREPHIST			
3E31	NF	EREPTES			
3E31	NG	EREPESTR			
3E31	NJ	EREPHIST			
3E32	NK	EREPRDE			
3E33	NK	EREPRDE			

PRINTER SUPPORT ERROR MESSAGES

<u>MESSAGE</u>	<u>CHART</u>	<u>PHASE</u>
0I26	PH	\$\$BFLDR
0I28	PK	\$\$BFLD2
1B01	PE	SYSBUFLD
1B01	PB	SYSBUFLD
1B02	PE	SYSBUFLD
1B03	PE	SYSBUFLD
1B10	PB	SYSBUFLD
1B11	PB	SYSBUFLD

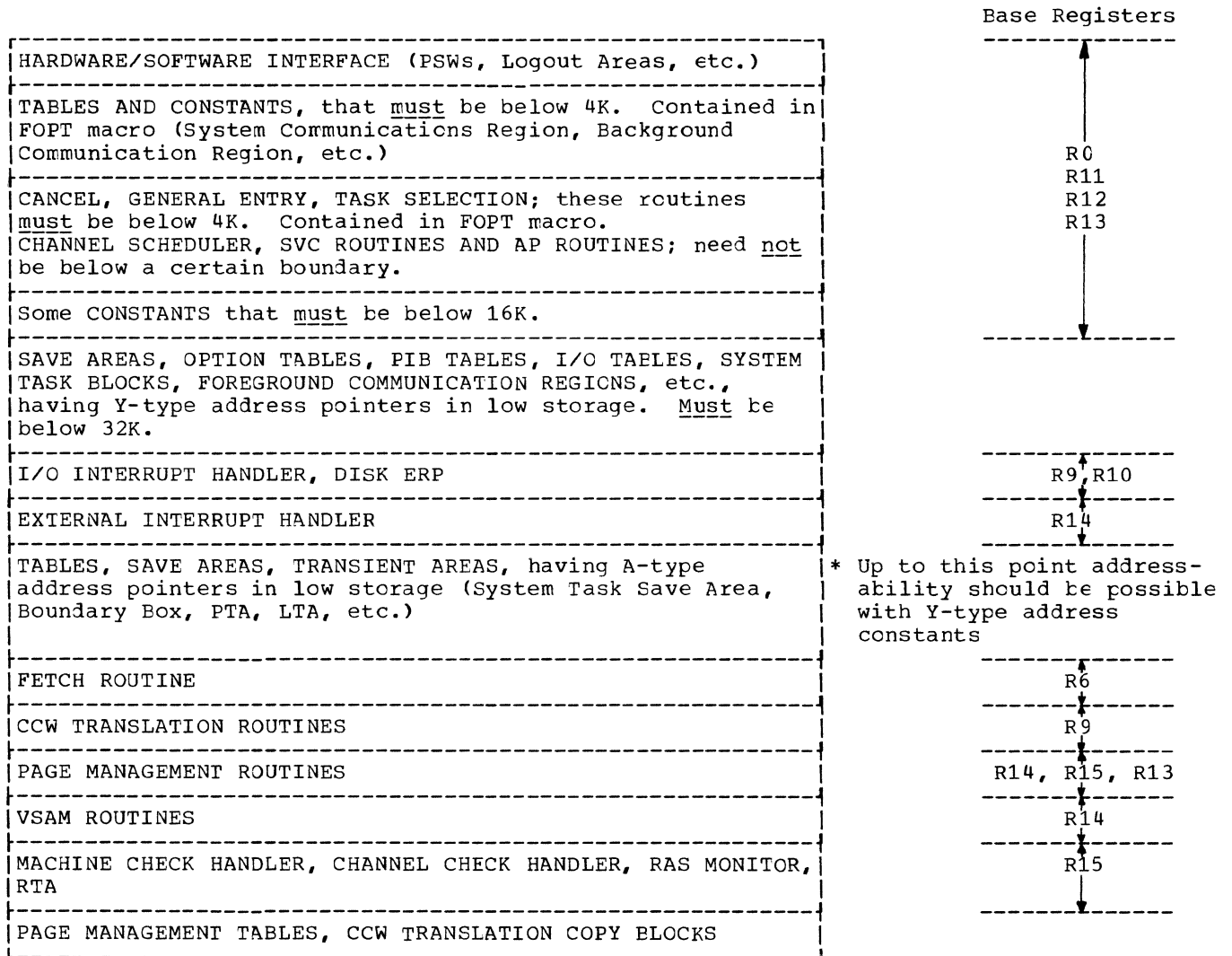
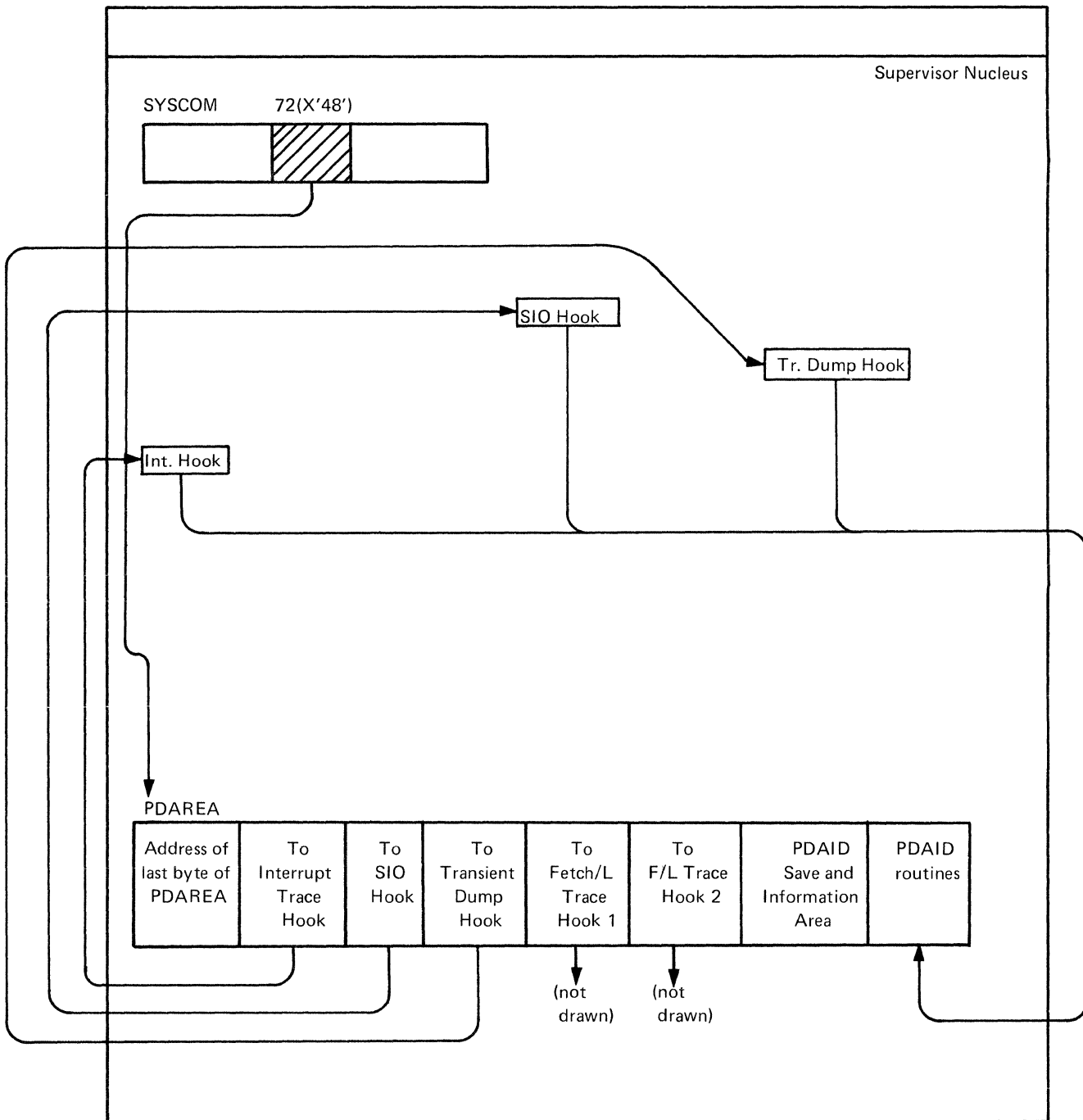


Figure 54. Supervisor Storage Allocation



Note: The PDAREA is present when PD=YES or PD=n was specified on the FOPT macro. The minimum size is 1400 bytes.

Figure 55. Accessing PDAID Routines

SVC		Macro Supported	Function
Dec	Hex		
*optional			
0	0	EXCP	Execute Channel Program
1	1	FETCH	Fetch any phase
2	2		Fetch a logical transient (B-transient)
3	3		Force dequeue
4	4	LCAD	Load any phase
5	5	MVCOM	Modify supervisor communication region (if issued by MVCOM macro) Fetch another physical transient (if issued by a physical transient)
6	6	CANCEL	Cancel a problem program or task
7	7	WAIT	Wait for a CCB or TECB
8	8		Transfer control to the problem program from a logical transient (B-transient)
9	9	LBRET	Return to a logical transient (B-transient) from the problem program after an SVC 8
10*	A	SETIME	Set timer interval
11	B		Return from a logical transient (B-transient)
12	C		Reset switches in partition communications region
13	D		Set switches in partition communications region
14	E	EOJ	Terminate job and go to job control for end of job step
15	F	SYSIO	Headqueue and execute channel program
16*	10	STXIT(PC)	Provide supervisor with linkage to user's PC routine for program check interrupts
17*	11	EXIT(PC)	Return from user's PC routine
18*	12	STXIT(IT)	Provide supervisor with linkage to user's IT routine for interval timer interrupt
19*	13	EXIT(IT)	Return from user's IT routine
20*	14	STXIT(OC)	Provide supervisor with linkage to user's OC routine for external or attention interrupts (operator command)
21*	15	EXIT(OC)	Return from user's OC routine
22	16	SEIZE	Seize/release system; enable/disable for external and I/O interrupts; set key in user's PSW
23*	17		Load phase header. Phase load address is stored at user's address

Figure 56. DOS/VS Supervisor Calls (Part 1 of 4)

SVC		Macro Supported	Function
Dec	Hex		
*optional			
24*	18	SETIME	Set timer interval and provide supervisor with linkage to user's TECB, if any
25*	19		Issue HALT I/O on a teleprocessing device, or HALT I/O on any device if issued by OLTEP. With multiprogramming, dequeue an unstarted OLTEP I/O request to a shared device
26*	1A		Validate address limits
27*	1B		Special HIO on teleprocessing devices
28*	1C	EXIT(MR)	Return from user's stacker select routine (MICR type devices only)
29*	1D	WAITM	Provide support for multiple wait macro WAITM
30*	1E	QWAIT	Wait for a QTAM element
31*	1F	QPOST	Post a QTAM element
32	20		Reserved
33	21		Reserved for internal macro COMRG
34	22	GETIME	Provides Time-of-Day and updates the DATE field
35*	23	HOLD	Hold a track for use by the requesting task only
36*	24	FREE	Free a track held by the task issuing the FREE
37*	25	STXII(AB)	Provide supervisor with linkage to user's AB routine for abnormal termination of a task
38*	26	ATTACH	Initialize a subtask and establish its priority
39*	27	DETACH	Perform normal termination of a subtask. It includes calling the FREE routine to free any tracks held by the subtask
40*	28	POST	Inform the system of the termination of an event and ready any waiting tasks
41*	29	DEQ	Inform the system that a previously enqueued resource is now available
42*	2A	FNQ	Prevent tasks from simultaneous manipulation of a shared data area (resource)
43	2B		Reserved
44*	2C		Provide supervisor support for external creation of unit check records by specific request
45*	2D		Provide emulator interface
46*	2E		Provide OLTEP with the facility to operate in supervisory state

Figure 56. DOS/VS Supervisor Calls (Part 2 of 4)

SVC		Macro Supported	Function
Dec	Hex		
*optional			
47*	2F	WAITF	Provide support for multiple wait macro WAITF for MICR type devices
48*	30		Fetch a CRT transient
49	31		Reserved
50	32		Reserved for LIOCS error recovery
51	33		Return phase header
52*	34	TTIMER	Return the remaining time interval, or cancel a time interval
53	35		Reserved
54	36	FREEREAL	Release page frames to selection pool
55	37	GETREAL	Provide interface between SDAID and PDAID initialization routine and page management routine, to create the PDAID alternate area or the SDAID buffer area
56*	38	GETPUB FREEPUB	Occupy or free PUB of the device used by POWER
57*	39		Make POWER-supported partition dispatchable
58	3A		Provide interface between job control and the supervisor. Get real storage for real jobs
59	3B		Provide interface between ECJ and the supervisor. Initialize specified page table entries
60	3C	GETADR	Provide virtual address of location within I/O areas for ERP and CRT routines
61*	3D	GETVIS	Get storage in virtual partition
62*	3E	FREEVIS	Free storage in virtual partition
63	3F	USE	Use a resource
64	40	RELEASE	Release a resource
65*	41	CDLOAD	Load VSAM or CI phase
66	42	RUNMODE	Return mode in which program is running
67*	43	PFIX	Fix page(s) in real storage
68*	44	PFREE	Free page(s) in real storage
69*	45	REALAD	Return real address corresponding to a given virtual address
70*	46	VIRTAD	Return virtual address corresponding to a given real address
71*	47	SETPFA	Establish or terminate the linkage between the supervisor and a user page-fault appendage routine

Figure 56. DOS/VS Supervisor Calls (Part 3 of 4)

SVC		Macro Supported	Function
Dec	Hex		
*optional			
72*	48	GETCBUF FREECBUF	Get or free copy buffer for IDAL or tape ERP
73*	49	SETAPP	Allow linkage to channel and appendage routines
74*	4A		Fix page(s) in real storage for restart
75	4B		Reserved
76	4C		Initiate recording of an RMSR I/O error
77	4D	TRANSCSW	Returns the virtual address of a copied CCW
78-84			Reserved
85	55	RELPAG	Release contents of one or more pages
86	56	FCEPGOUT	Force a page-out for one or more pages
87	57	PAGEIN	Page-in one or more pages

Figure 56. DOS/VS Supervisor Calls (Part 4 of 4)





KEY	EXPLANATION
NICL (Number in Class)	Byte 0 contains the number of system class LUBs. The remaining bytes contain the number of programmer class LUBs for each partition. The total number of bytes is one more than the number of partitions supported.
FICL (First in Class)	Byte 0 points to the first system class LUB table (LUBTAB). This is always the first entry in the LUB table. The remaining bytes point to the first programmer class LUBs in the LUB table partition areas. The total number of bytes is one more than the number of partitions supported.
LUBTAB (Logical Unit Block Table)	Byte 0 of each entry is an index pointer to an entry in the PUB table (PUBTAB) and to an entry in the PUB ownership table (PUBOWNER), or contains X'FF' if no logical unit is assigned. Byte 1 points to an entry in the JIB table (JIBTAB) or contains X'FF'.
PUBTAB (Physical Unit Block Table)	Bytes 0 and 1 of each entry contain the channel and unit address of the physical device. Byte 2 points to the entry in the channel queue (CHANQ) table or contains X'FF'. Byte 3 is a retry counter or, if the unit is a tape cartridge reader and the TEE=n parameter was included in the FOPT macro, contains a pointer to the Tape Error Block table (TEBTAB) entry for the device. Byte 4 contains the device type code. Byte 5 is an index pointer to the entry in the track-hold table, the seek-address block table, or the mode table (Model 125 only) when one of these options is active for the device. Otherwise this byte contains X'FF'. Byte 6 contains the channel scheduler flags and byte 7 the job control flags.
FOCL (First on Channel List)	Byte 0 points to the first PUBTAB entry for a device on channel 0. Byte 1 points to the first PUBTAB entry for a device on channel 1, and so on. X'FF' indicates that the associated channel is not supported.
PUBOWNER	Byte 0 of each entry is reserved. Byte 1 identifies the partition that owns the corresponding PUB.
FAVP (First Available Pointer)	This one-byte pointer to the next available entry in the JIB table (JIBTAB) is used by the ASSGN statement processor and by job control.
JIBTAB (Job Information Block Table)	Bytes 0 and 1 of each entry contain extent or LUB information used by job control and the ASSGN statement processor. Byte 2 is a flag byte. Byte 3 is a chain byte.

Figure 57. I/O Table Interrelationship (Part 2 of 3)

KEY	EXPLANATION
CHANQ (Channel Queue table)	Byte 0 in each entry points to the next entry in the queue for the same device (or the next free entry if in the free list), or it contains X'FF' if the entry is the last in a chain. Bytes 1, 2, and 3 contain the CCB address. Byte 4, if I/O was requested by a user program, contains the Partition Identification Key (PIK). If a system task requested I/O, then the zone field is all zeros and the numeric field contains the ID of the specific system task. Byte 5 contains a displacement index pointing to the LUBTAB entry related to the I/O request (absolute LUPTAB index). Byte 6 contains the relative LUBTAB index for system LUBs, or X'FF' for programmer LUBs. Byte 7 contains the displacement index of the PIBTAB entry for the task requesting I/O (TIK), or X'FF' if the channel queue entry is free.
FLPTR (Free List Pointer)	This one-byte pointer contains the displacement index of the next free entry in the channel queue table (CHANQ).
TEBTAB (Tape Error Block Table)	One entry is built for each tape cartridge reader at supervisor generation time if the FOPT macro parameter TEB=n is included.
THTAB (Track Hold Table)	This table is built at supervisor generation time if the TRKHLD=n parameter is included in the FOPT macro. Byte 0 in each entry points to the next entry in the chain of requests for a track to be held on a specific DASD (or the next free entry if in the free list), or it contains X'FF' if the entry is the last in a chain. Bytes 1, 2, and 3 contain the CCB address. Bytes 4 through 9 contain the disk address (BBCCHH) of the held track. Byte 10 contains the key of the owning task, or all zeros when the entry is free. Byte 11 is a flag and counter byte: bit 0 is turned on when a task requests a track already held by another task, and the value in the low-order half-byte is incremented by one each time a task requests a hold on a track that it already holds itself. <u>Note</u> : when multiple holds by one task are effective, the value in the lower-order half-byte is one less than the actual number of holds.
SAB (Seek Address Block)	This table is built at supervisor generation time if the SKSEP=n or YES parameter is included in the FCPT macro. Bytes 0 through 3 of each entry contain the current disk address (BCCH) for the device. Byte 4 contains X'FF' or points to the THTAB entry.
LMT (Line Mode Table)	This table is built at supervisor generation time when the TP=BTAM, or QTAM parameter is included in the SUPVR macro, and MCDL=115 or 125. An entry is built for each device for which the DVGEN macro includes the MODE=X'ssss' or X'ssssss' parameter. Each entry contains the actual mode setting for the device.

Figure 57. I/O Table Interrelationship (Part 3 of 3)

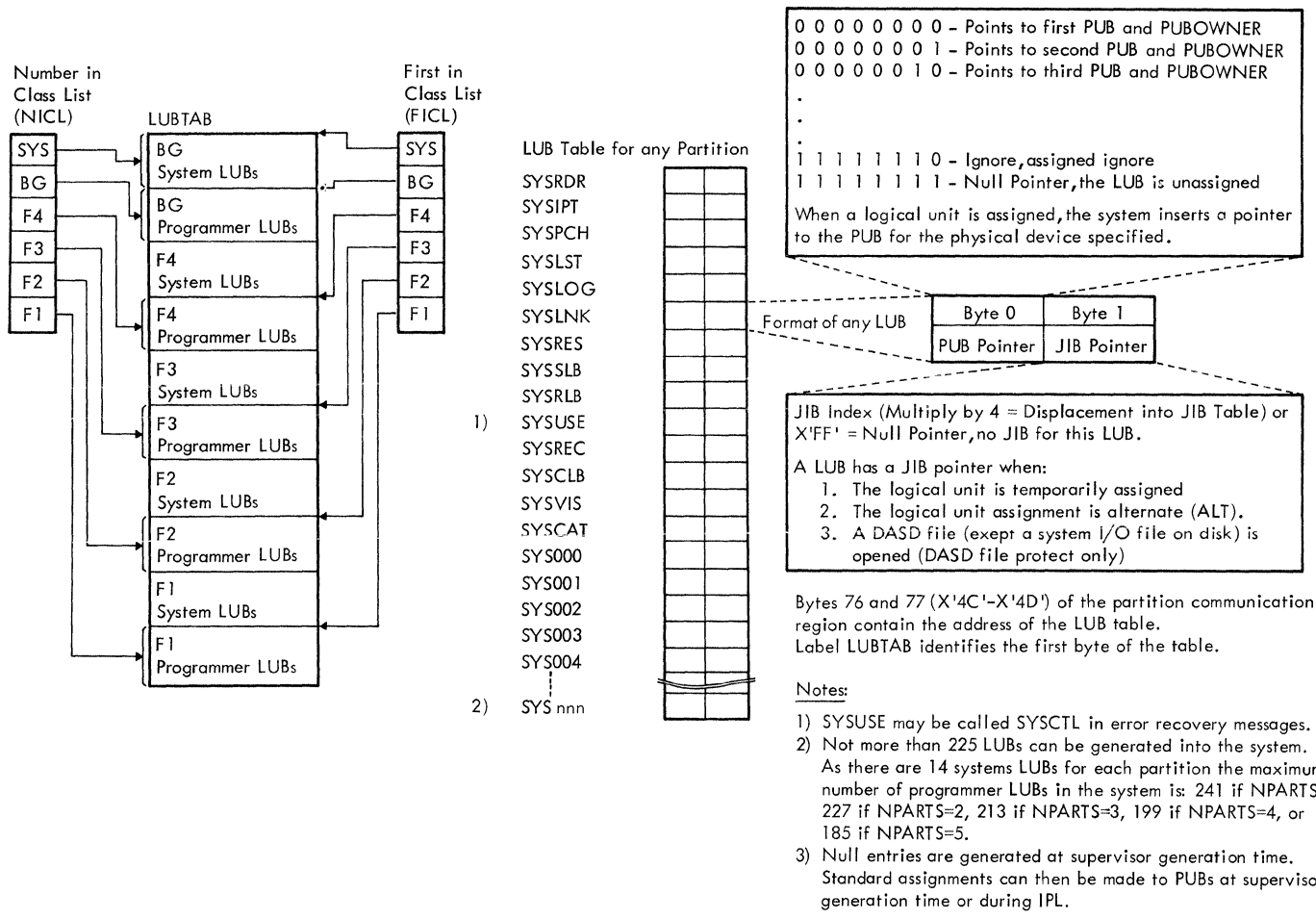
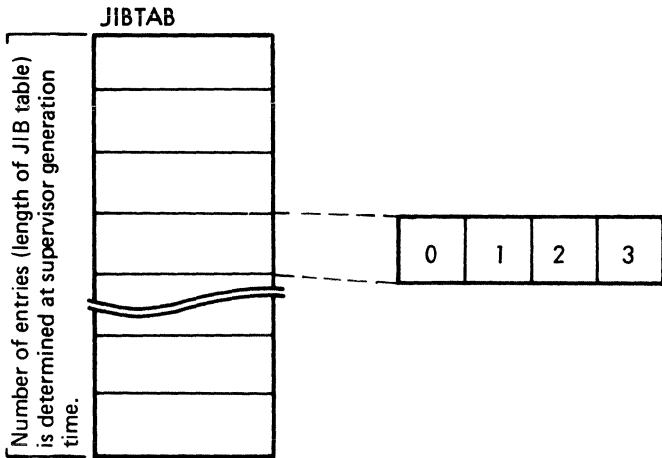


Figure 58. Logical Unit Block Table (LUBTAB)





Byte(s)	Description												
0-1 (Contents depends on the bit setting in byte 2)	<table border="1"> <tr> <td>Bit Setting</td> <td>Contents</td> </tr> <tr> <td>Byte 2</td> <td></td> </tr> <tr> <td>Bit 0=1 Stored standard assignment</td> <td>LUB entry of stored standard assignment. (PUB and JIB pointer)</td> </tr> <tr> <td>Bit 1=1 Alternate assignment</td> <td>Byte 0: PUB pointer Byte 1: X'00'</td> </tr> <tr> <td>Bit 2=1 2311/2314/2319 Extent</td> <td>Byte 0: Cylinder lower limit Byte 1: Cylinder upper limit (Note 1)</td> </tr> <tr> <td>Bit 3=1 2321/3330/3340 Extent</td> <td>For 2321: Lower limit (Cell or combined subcell and strip), or Upper limit (Cell or combined subcell and strip) For 3330 or 3340: Cylinder lower limit, or Cylinder upper limit (One cylinder number uses two bytes) (Note 2) (Note 3)</td> </tr> </table>	Bit Setting	Contents	Byte 2		Bit 0=1 Stored standard assignment	LUB entry of stored standard assignment. (PUB and JIB pointer)	Bit 1=1 Alternate assignment	Byte 0: PUB pointer Byte 1: X'00'	Bit 2=1 2311/2314/2319 Extent	Byte 0: Cylinder lower limit Byte 1: Cylinder upper limit (Note 1)	Bit 3=1 2321/3330/3340 Extent	For 2321: Lower limit (Cell or combined subcell and strip), or Upper limit (Cell or combined subcell and strip) For 3330 or 3340: Cylinder lower limit, or Cylinder upper limit (One cylinder number uses two bytes) (Note 2) (Note 3)
Bit Setting	Contents												
Byte 2													
Bit 0=1 Stored standard assignment	LUB entry of stored standard assignment. (PUB and JIB pointer)												
Bit 1=1 Alternate assignment	Byte 0: PUB pointer Byte 1: X'00'												
Bit 2=1 2311/2314/2319 Extent	Byte 0: Cylinder lower limit Byte 1: Cylinder upper limit (Note 1)												
Bit 3=1 2321/3330/3340 Extent	For 2321: Lower limit (Cell or combined subcell and strip), or Upper limit (Cell or combined subcell and strip) For 3330 or 3340: Cylinder lower limit, or Cylinder upper limit (One cylinder number uses two bytes) (Note 2) (Note 3)												
2	<p>Meaning if bit = 1:</p> <ul style="list-style-type: none"> <li>Bit 0: Stored standard assignment</li> <li>1: Alternate assignment</li> <li>2: 2311/2314/2319 extent</li> <li>3: 2321/3330/3340 extent</li> <li>4: The alternate assignment indicated in bit 1 is permanent. This bit is also on when one of the extent indicators (bit 2 and bit 3) is on.</li> <li>5: Cataloged procedures processing</li> <li>6/7: Reserved</li> </ul>												
3	Chain byte Contains the displacement index of the next JIB. X'FF' defines the end of the chain.												

Figure 59. Job Information Block (JIB) Table

Note 1: Only when file-protect on DASD.

Note 2: Two JIBs are required for a 2321/3330/3340 extent; one for lower limit and one for upper limit.

The lower limit defining JIB must be chained to the upper limit defining JIB.

For 2321, byte 1 of this type JIB contains the subcell number times 10 plus the strip number in binary.

Note 3: Extent information is supplied by the program initiator and logical IOCS open transient routines. The supervisor can then perform the file protect function for the specified file limits. File protection does not include supervisor and transient originated I/O.

Bytes 68-69 (X'44' - X'45') of the partition communication region contain the address of the JIB table entry. Label JIBTAB identifies the first byte of the table.

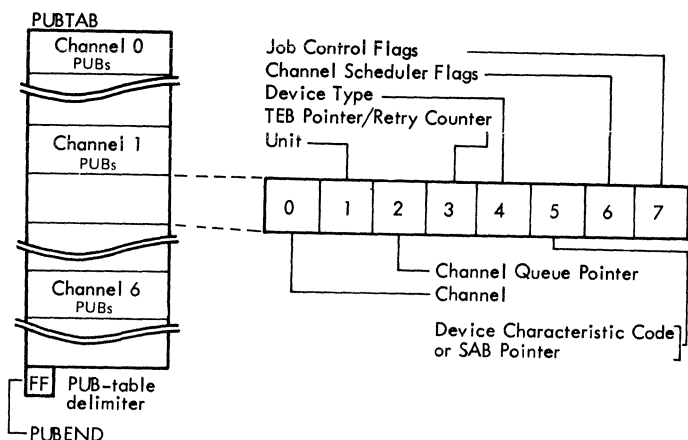


Figure 60. Physical Unit Block (PUB) Table

Bytes:

- 0: Channel number. (Hex 0-6, FF=NULL)
- 1: I/O device unit number
- 2: Hex 0, 1, 2, ... points to the first channel queue entry for this device
- 3: If device is a 2495 Tape Cartridge Reader and TEBs are specified, this byte is a TEB pointer. (Hex 1, 2, 3, ...). Otherwise, this byte is an ERP retry counter.
- 4: Device type code
- 5: SS of the MODE=parameter in the DVCGEN macro for tape unit.

For ICA line (Model 115, or 125), this byte contains the displacement index of the entry in the Line Mode Table (LMT). The address of the LMT is in SYSCOM.

For DASD with seek separation, this byte is used as the SAB Pointer. With Track Hold but not seek separation supported, this byte contains a pointer to the Track-Hold Table entry or X'FF' (with both SKSEP and TRKHLD specified, the Track-Hold pointer is found in the SAB entry).

For MICR type devices, this byte indicates which external interrupt line is in use. For a 3704/3705 Communications Controller, this byte contains the type number of the Channel Adapter.

For MFCM or MFCU:

- Bit 0: 1 = Repositioning required
- 1: 0 = SYSPCH temporarily assigned to hopper 1  
1 = SYSPCH temporarily assigned to hopper 2
- 2: 0 = SYSIPT temporarily assigned to hopper 1  
1 = SYSIPT temporarily assigned to hopper 2
- 3: 0 = SYSRDR temporarily assigned to hopper 1  
1 = SYSRDR temporarily assigned to hopper 2
- 5: 0 = SYSPCH permanently assigned to hopper 1  
1 = SYSPCH permanently assigned to hopper 2
- 6: 0 = SYSIPT permanently assigned to hopper 1  
1 = SYSIPT permanently assigned to hopper 2
- 7: 0 = SYSRDR permanently assigned to hopper 1  
1 = SYSRDR permanently assigned to hopper 2

6: Channel Scheduler Flags

- Bit 0: 1 = Device busy
- 1: 1 = Switchable device
- 2: 1 = EOJ for SYSRDR or SYSIPT
- 3: 1 = I/O error queued for recovery
- 4: 1 = Operator intervention required
- 5: 1 = Device End posting required
- 6: 1 = Burst or overrunable device on Byte MPX channel
- 7: 1 = 7-track tape unit

7: Job Control Flags

- Bit 0-4: Standard MODE assignment for 7-track tape (all ones if not tape, all zeros if device is down)
- 5: 1 = DASD device with disconnect command chaining feature
- 6-7: B'11' (both on) = Headqueue in progress\*  
B'01' = Headqueue requested\*

\*No I/O is started on a PUB or copied PUB that has both these switches on. If only bit 7 is on I/O can be started after seek separation.



Notes: A null entry is generated at supervisor generation time for each device to be supported by the supervisor. Then standard physical unit assignments are made to the PUB table. Physical unit assignments can also be made during IPL. PUBs are ordered by channel and priority within a channel.

An entry in the PUB Ownership Table is associated with each entry in the PUB Table, if the supervisor has been generated to support multiprogramming.

Bytes 64-65 (X'40' - X'41') of the partition communication region contain the address of the PUB Table entry. Label PUBTAB identifies the first byte of the table.

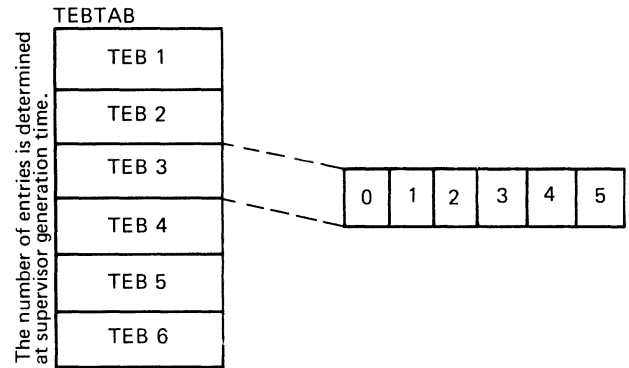


Figure 61. Tape Error Block (TEB) Table

Bytes:

- 0: Error recovery retry count.
- 1: Permanent read data check error count.
- 2: Number of times the read data check error routine is entered.
- 3: Number of times the write data check error routine is entered.
- 4: Write skip (erase gap) count.
- 5: Noise record count.

One TEB is generated for each 2495 Tape Cartridge Reader unit if the FOPT macro contains the TEB=n parameter. Job Control resets each TEB at normal or abnormal End-of-Job. An unused TEB contains HEX 'FFC000000000'. A TEB is referenced from byte 3 of a Tape Cartridge unit PUB.

Bytes 70-71 (X'46' - X'47') of the partition communications region(s) contain the address of the TEB Table entry. Label TEBTAB identifies the first byte of the table.

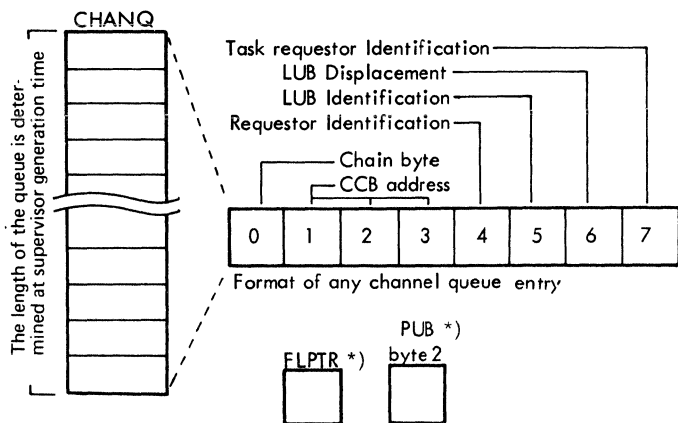


Figure 62. Channel Queue Table (CHANQ) and Entry

Bytes:

0: Contains the displacement within the channel queue of the next entry in the free list or in the list for a specific device, or X'FF' when it is the last entry in the free list or a device list.

1-3: Contains the CCB address for the specified device.

4: Contains a code identifying the task making the I/O request. This one byte entry indicates to which task the CCB belongs, and is in the form X'nk' where:

n = user storage protection key. (Attention or system task = 0, BG task = 1, FG tasks = 2-5 depending on the number of partitions).

k = 0 for Attention and all user tasks.

- 1 for RAS
- 2 for PMGR
- 3 for SUPVR
- 4 for CRT
- 5 for ERP
- 6 for PAGEIN

nk=FF for unused channel queue entries.

If the I/O request is from a subtask, this byte does not identify this subtask, but the partition.

5: Contains pointer (displacement index) to the entire LUB table identifying the logical unit making the I/O request. This is doubled to get the actual displacement into the full LUB table.

6: Contains X'FF' if the LUB is a programmer class, or the displacement within the partition LUB if it is a system class. (Not used by a non MPS Supervisor.)

7: Contains the displacement within the Pib table for the task requesting I/O, or X'FF' when the channel queue entry is in the free list. (Not used by a non MPS Supervisor.)

\*)Notes: FLPTR: The free list pointer contains the displacement within the channel queue of the first entry in the free list or X'FF' when the channel queue is full.

Byte 36 (X'24') of the System Communication Region (SYSCOM) contains the address of the Free List Pointer. Label FLPTR identifies the location of the pointer (1 byte).

PUB byte 2: The PUB channel queue pointer contains the displacement within the channel queue of the first entry for a specific device.

Bytes 37-39 (X'25' - X'27') of the System Communication Region (SYSCOM) contain the address of the Channel Queue table entry. Label CHANQ identifies the first byte of the table.

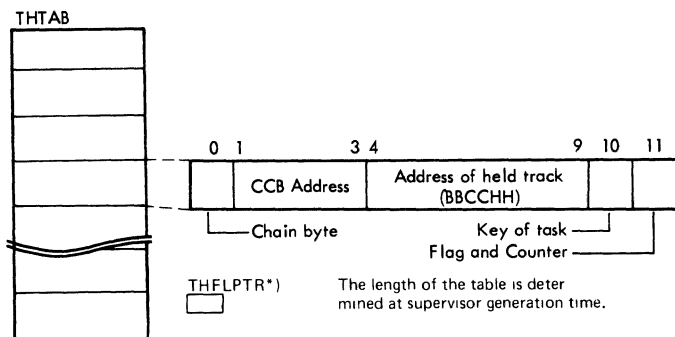


Figure 63. Track-Hold Table (THTAB)

Bytes:

- 0: Initially, pointer to next sequential entry or X'FF' (table delimiter) in last entry. After requests have been issued, this byte points to the entry for the next request for a track on the same device, or contains X'FF' (in entry for the last request), or it is a pointer in the free list chain.
- 1-3: Address of CCB associated with the task requesting the hold.
- 4-9: Disk address of the track being held (in the form BBCCHH).
- 10: Key of the task owning the track.

11: Bit 0=1: Indicates a task is waiting for this track

1-3: Unused

4-7: Counter of number of holds on the track. (The number of holds is one more than this value.)

\*)Note:

THFLPTR: The Track-Hold Free List Pointer (1 byte) contains the displacement within the Track-Hold Table of the first entry in the free list or X'FF' when the Track-Hold Table is full.

Bytes 77-79 (X'4D' - X'4F') of the System Communication Region (SYSCOM) contain the address of the Track-Hold Table. Label THTAB identifies the first byte of the table.

Byte 76 (X'4C') of the System Communication Region (SYSCOM) contains the address of the Track-Hold Free List Pointer. Label THFLPTR identifies the location of the pointer.

The half-word at THTAB-2 contains the total number of 12-byte entries comprising the Track-Hold Table.

SYSCOM

Hex Dec	0	4	8	0A	0C	10	14	18			
	0	4	8	10	12	16	20	24			
	Address of Error Block	Address of Attention Exit	Address of Operator Option Cancel Exit	Address of Operator Request Cancel Exit	Address of SYSRES PUB	Address of Fetch Routine	Address of I/O Interr. Routine	Address of Ext. Interr. Routine			
	xxxx	xxxx	xx	xx	xxxx	xxxx	xxxx	xxxx			
Displacement	1C	20	24	25	28	2A	2C	2E	30	34	
	28	32	36	37	40	42	44	46	48	52	
	Address of Logical Transient Area	Address of 1st byte of Problem Program Area	Free List Pointer	Address of Channel Queue	Number of Channel Queue Entries	Length of One Error Queue Entry	Number of Partitions	Not used	Address of Channel Buckets	Address of CRT Table	
	xxxx	xxxx	x	xxx	xx	xx	xx	xx	xxxx	xxxx	
	38	3C	40	44	46	48	4C	4D	50		
	56	60	64	68	70	72	76	77	80		
	Address of SAB Table	Address of Channel Control Table	Flags and Switches (See expansion) *	System Task Selection Control Field *	Address of Task Selection	Address of PD Area	TH Free List Pointer	Address of TH Table	Address of Timer Request Table		
	xxxx	xxxx	xxxx	xx	xx	xxxx	x	xxx	xxxx		
	54	58	5A	5C	60	64	68	6C	70		
	84	88	90	92	96	100	104	108	112		
	Address of AB Table	Key of Task owning LTA (LIK)	Key of Task running (TIK)	Address of POWER Table	Reserved	Address of RF Table	Address of EU ECB Table	Address of OLTEP bucket	Address of RAS Linkage Area		
	xxxx	xx	xx	xxxx	xxxx	xxxx	xxxx	xxxx	xxxx		
	74	78	7C	80	84	88	8C	90			
	116	120	124	128	132	136	140	144			
	Address of ASCII Translate Table	Address of PUB Ownership Table	Address of Job Accounting Common Table	Base Address of Page Management Routine	Base Address of Channel Program Translation Routine	Address of SDAID Comm. Area	Address of Line Mode Table	Address of VSAM Communication Area			
	xxxx	xxxx	xxxx	xxxx	xxxx	xxxx	xxxx	xxxx			
	94	98	9C	A0	A1	A2	A3	A4	A5	A6	A7
	148	152	156	160	161	162	163	164	165	166	167
	Address of PTA	Address of first System Task Block	Address of Task Block of Active System Task	1 byte for Alignment	Pointer to RAS Task Block	Pointer to PMGR Task Block	Pointer to SUPVR Task Block	Pointer to CRT Task Block	Pointer to ERP Task Block	Pointer to PAGEIN Task Block	Reserved (9 X'00')
	xxxx	xxxx	xxxx	x	x	x	x	x	x	xxxxxxxxxx	xxxxxxxxxx
	B0	B4	B8	BC	BE	C0	CB	CC	CE		
	176	180	184	188	190	192	203	204	206		
	Not used	Address of MVCFLD	Not used	Not used	Not used	Repositioning Information for MFCM ERP	Number of Error Queue Entries	Length of PUB Table in bytes	Number of Active Partitions		
	xxxx	xxxx	xxxx	xx	xx	xxxxxxxxxxxx	x	xx	xx		
	D0	D4	DR	DC	E0	E4	E8	EC			
	208	212	216	220	224	228	232	236			
	Address of Segment Table	Address of Page Frame Table	Address of Page Frame Table Extension	Address of Boundary Box	Address of DPD Table	Reserved	Address of VIRTAD Routine	Address of End of Real Storage			
	xxxx	xxxx	xxxx	xxxx	xxxx	xxxx	xxxx	xxxx			
	F0	F4	F5	F8							
	240	244	245	248							
	Address of Fetch Table	SVA Flag (see expansion)	Address of SVA	Address of System GETVIS AREA							
	xxxx	x	xxx	xxxx							

\* See next page for further explanation.

Figure 64. System Communications Region (SYSCOM) (Part 1 of 2)

Expansion of SYSCOM Flag Bytes

Byte		Description
Dec	Hex	
64	40	Reserved for RMS in Model 115 and 125  X'80' RMSR for channel attached devices, tapes and TP devices X'40' Full RMS support (MCAR/CCH and RMSR) X'20' MCAR/CCH support
65	41	X'80' Initial selection of ERP X'40' Reserved X'20' Timer interrupt pending X'10' MICR Stacker-select active X'08' Invalid address during fetch X'04' SIO routine entered after interrupt X'06' Reserved X'01' IPL in progress
66	42	X'80' Initial RAS request X'40' RAS Wait request outstanding X'20' RAS IPL in progress X'10' Reserved X'08' POWER supported X'06' POWER initialized X'02' GETREAL for SDAID or PDAID in progress X'01' Fetch for system task in progress (used by PDAID)
67	43	Reserved
244	F4	SVA Flag X'00' Do not test for warm start copy of SVA X'40' SDL active X'20' No 'Set SVA' or 'Set SDL' allowed X'10' Build of SDL in progress X'08' SDL overflow X'04' Reserved X'02' Reserved X'01' Reserved

Layout of System Task Selection Control Field

Byte		Description
Dec	Hex	
68	44	Always zero
69	45	SELECT byte:  X'00' No system task active X'01' RAS active X'02' PMGR active X'03' SUPVR active X'04' CRT active X'05' ERP active X'06' PAGEIN active

Note: The address of SYSCOM can be found at fixed location X'80' - X'83'.

Figure 64. System Communications Region (SYSCOM) (Part 2 of 2)

nnCOMREG

0	8	0A	0C	17	18	20	24	28	2C		
0	8	10	12	23	24	32	36	40	44		
Date	Address of PPBEG	Address of EOSSP	Problem Program Use	UPSI Byte	Job Name	Highest Storage Address of the Partition	End Address of Last Phase Fetched or loaded	Address of upper-most Byte of Phase with highest Ending Address	Label Area Length		
xxxxxxx	xx	xx	xxxxxxxxxxx	x	xxxxxxx	xxxx	xxxx	xxxx	xx		
2E	30	34	35	36	37	38	39	3A	3B	3C	3E
46	48	52	53	54	55	56	57	58	59	60	62
PIK	End of Virtual Storage Address	Machine Configur. Byte	System Configur. Byte 1	Standard Language Translator I/O Options	Dump, Log, RELLDR and ASCII Options	Job Control Byte	Linkage Control Byte	Language Translator Control Byte	Job Duration Indicator Byte	Disk Address of Label Cylinder	Address of FOCL
xx	xxxx	x	x	x	x	x	x	x	x	xx	xx
Job Control Switches											
40	42	44	46	48	4A	4C	4E	4F	58	5A	5C
64	66	68	70	72	74	76	78	79	88	90	92
Address of PUBTAB	Address of FAVP	Address of JIBTAB	Address of TEBTAB	Address of FICL	Address of NICL	Address of LUBTAB	Line Count for SYSLST	System Date	LIOCS Comm. Bytes	Address of 1st Part of PIB Table	ID Number of last Checkpoint or DASDFP Indicator
xx	xx	xx	xx	xx	xx	xx	x	xxxxxxxxx	xx	xx	xx
5E	60	62	64	66	68	6A	6C	6E			
94	96	98	100	102	104	106	108	110			
Job Zone in Minutes	Address of Disk Information Block (DIB)	Reserved	Address of PC Option Table less 8 bytes	Address of IT Option Table less 8 bytes	Address of OC Option Table less 8 bytes	Key of Program with Timer Support	Reserved	Logical Transient Key (LTK)			
xx	xx	xx	xx	xx	xx	xx	xx	xx			
70	74	78	7C	7E	80	84	86	87			
112	116	120	124	126	128	132	134	135			
Address of SYSPARM	Address of J.A. Partition Table	Address of TOD clock Common Area	Address of 2d part of PIB Table	Address of MICRDTF Table (PDTABB)	Address of QTAM Vector Table	Address of BG Comm. Region	Option Indicator	System Configuration Byte 2 and RMSR Open Flag Byte			
xxxx	xxxx	xxxx	xx	xx	xxxx	xx	x	x			
88	8C	8D	8E	8F	97	98	9F				
136	140	141	142	143	151	152	159				
Pointer to Option Table in SYSCOM	Standard Options	Temporary Options	Disk Configuration	Catalog Procedure Name	Switch for Catalog Procedure	JCL Statement Name	81 bytes SYSIN Indicator				
xxxx	x	x	x	xxxxxxxx	x	xxxxxxx	x				

Note: A communication region exists from each partition supported by the system.  
 The address of the communication region of the active partition is in fixed loc. X'14'-X'17'.

Figure 65. Partition Communications Region (Part 1 of 6)

Key to Communication Region Displacement

Key	Description of Use
0	MM/DD/YY or DD/MM/YY either set permanently by the job control date statement, or updated every time a GETIME macro is issued when Time-of-Day support is provided. Format controlled by BGCOREG+53. (System Configuration Byte, date convention bit 0.)
8	Address of the problem program area.
10	Address of the beginning of the problem program area (see above). Y(EOSSP) equals Y(PPBEG)
12	User area. If seek separation option is specified, bytes 12 and 13 are used at IPL time for the address of the seek address block.
23	User program switch indicator.
24	Job name set by the job control program from information found in the job statement
32	Address of the uppermost byte available to the problem program, that is, either the address of the uppermost byte of the partition as determined during processing of the ALLOC or ALLOCR macro or statement, or the end address of the area specified by the SIZE parameter in the EXEC statement.
36	Address of the uppermost byte of the last phase of the problem program fetched or loaded. Not filled in when phase is in SVA.
40	Highest ending real storage address of all the phases having the same first four characters as operand on the EXEC statement. For the phase \$LNKEDT, this field is not filled in. The address value may be incorrect if the program loads any of this phase above or below its link-edited origin address. If the EXEC statement has no operand, job control places in this location the highest ending address of all programs just link-edited.
44	Length of the problem program label area.
46	Partition Identification Key (PIK). The low-order byte identifies the active partition. Only significant for BG communication region.
48	End address of virtual storage.
52	Machine Configuration Byte (values set at supervisor generation time). Bit 0: Always set to indicate standard storage protect 1: 1 = Decimal feature (always set) 2: 1 = Floating-point feature 0 = No floating-point feature 3: 1 = Physical transient overlap option 0 = No physical transient overlap option 4: Always set to indicate standard timer feature 5: 1 = Channel switching device 0 = No channel switching device 6: 1 = Burst mode on multiplex channel support 0 = No burst mode on multiplex channel support 7: Indicates MCH/CCH in system
53	System Configuration byte. Bit 0: 1 = DDMMYY (Date convention bit set at generation time by STDJC) 0 = MMDDYY (Date convention bit set at generation time by STDJC) 1: 1 = Two or more partitions 0 = One partition only

Figure 65. Partition Communications Region (Part 2 of 6)

Key	Description of Use
53	System Configuration byte (Cont'd.) 2: 1 = DASD file-protect supported 0 = No file-protect support for DASD 3: 1 = DASD SYSIN - SYSOUT 0 = No DASD SYSIN - SYSOUT 4: 1 = Teleprocessing 0 = No teleprocessing 5: 1 = Two or more partitions 0 = One partition only 6: 1 = Asynchronous processing 0 = No asynchronous processing 7: 1 = Track Hold 0 = No Track Hold
54	This byte contains the standard language translator I/O options (set by STDJC macro).  Bit 0: DECK option 1 = yes, output object modules on SYSPCH 1: LIST option 1 = yes, output source module listings and diagnostics on SYSLST 2: LISTX option 1 = yes, output hexadecimal object module listings on SYSLST (compilers only) 3: SYM option 1 = yes, output symbol tables on SYSLST/SYSPCH 4: XREF option 1 = yes, output symbolic cross-reference list on SYSLST 5: ERRS option 1 = yes, output diagnostics on SYSLST (compilers only) 6: CHARSET option 1 = 48, input on SYSIPT is 48 or 60 character set 7: Reserved
55	This byte contains the standard supervisor options for abnormal ECJ, Relocating Loader and Control statement display and the indicator for the presence of the ASCII-EBCDIC and EBCDIC-ASCII translation tables.  Bit 0: Always set 1: DUMP option 1 = yes, dump registers and storage on SYSIST 2: 1 = partition in wait state, because a volume is to be recounted 3: LOG option 1 = yes, list all control statements on SYSIST 4: 1 = dummy device search in progress; do not enter ERP 5: Not used 6: Relocating Load option 1 = yes, Relocating Loader supported 7: ASCII option 1 = yes, ASCII supported
56	Job Control byte.  Bit 0: 1 = Job Accounting Interface (JA) not supported 0 = Job Accounting Interface (JA) is supported 1: 1 = Return to caller on LIOCS disk open failure 0 = Do not return to caller on LIOCS disk open failure 2: 1 = Job Control input from SYSRDR 0 = Job Control input from SYSLOG 3: 1 = Job Control output on SYSLOG 0 = Job Control output not on SYSLOG 4: 1 = Cancel job 0 = Do not cancel job 5: 1 = Pause at End-of-Job step 0 = No pause at End-of-Job step 6: 1 = SYSLOG is not a console printer-keyboard or DOC 0 = SYSLOG is a console printer-keyboard or DOC 7: 1 = SYSLOG is assigned to the same device as SYSLST 0 = SYSLOG is not assigned to the same device as SYSLST

Figure 65. Partition Communications Region (Part 3 of 6)



Key	Description of Use
57	Linkage control byte. <ul style="list-style-type: none"> <li>Bit 0: 1 = SYSLNK open for output 0 = SYSLNK not open for output</li> <li>1: 1 = Update of Second Level Directory and RAS Load list in progress (interface between \$MAINDIR and supervisor)</li> <li>2: 1 = Allow EXEC 0 = Suppress EXEC</li> <li>3: 1 = Catalog Linkage Editor output 0 = Do not catalog Linkage Editor output</li> <li>4: 1 = Supervisor has been updated 0 = Supervisor has not been updated</li> <li>5: Reserved</li> <li>6: 1 = Update of System Core Image Library in program (interface between \$MAINDIR and Supervisor)</li> <li>7: 1 = Check automatic condense limits and End-of-Job (interface between Librarian and Job Control)</li> </ul>
58	Language processor control byte. This is a set of switches used to specify nonstandard language translator options. The switches within the byte are controlled by job control OPTION statements and when set to 1, override standard options. The format of this byte is identical to the standard option byte (displacement 54) with one exception: Bit 7 in this byte is used to indicate to LIOCS that the rewind and unload option has been specified.
59	Job duration indicator byte. <ul style="list-style-type: none"> <li>Bit 0: 1 = Job in progress 0 = No job in progress</li> <li>1: 1 = Dump on an abnormal End-of-Job condition 0 = No dump on abnormal ECJ</li> <li>2: 1 = Pause at EOJ step (Set by attention routine for Job Control) 0 = No pause at EOJ (Set by attention routine for Job Control)</li> <li>3: 1 = Job control output on SYSLSST 0 = Output not on SYSLSST</li> <li>4: 1 = Job is being run out of sequence with a temporary assignment for SYSRDR 0 = Conditions for 1-setting not met</li> <li>5: 1 = PCIL is being condensed 0 = PCIL is not being condensed</li> <li>6: 1 = //DATE statement processed for current job 0 = No //DATE statement processed for current job</li> <li>7: 1 = Batch command just issued 0 = Condition for 1-setting did not occur</li> </ul>
60	Binary disk address of the volume label area (label cylinder).
62	As illustrated (for detailed figures see <u>DOS/VS Supervisor Logic</u> , SY33-8551).
76	As illustrated (for detailed figures see <u>DOS/VS Supervisor Logic</u> , SY33-8551).
78	Set to the value nn specified in the LINES=nn parameter of the STDJC macro.
79	The format of the system date contained within this field is determined by the IPL program from information supplied in the date convention bit (displacement 53). Bytes 85-87 contain the day count.
88	Bytes reserved for use by LIOCS. Transient dump programs insert a key to indicate to the LIOCS End-of-Volume routine, \$BECMT07, that it was called by a B-transient.
90	Address of the first part of the Program Information Block (PIB) table.

Figure 65. Partition Communications Region (Part 4 of 6)

Key	Description of Use
92	ID number of the last checkpoint. Byte 92 is also the temporary indicator of file protected DASD.  Bits 0-6 correspond to channels 0-6. A bit ON means DASDFP for that channel. Bit 7 indicates 2321 DASDFP support. Byte 93 is used at IPL time by PIOCS.  Bit 0: 1 = 3330 file protection 1: 1 = 3340 file protection
94	Job zone for Time-of-Day. If ZONE=EAST, value is positive; if ZONE=WEST, value is negative.
96	Address of disk I/O position data. This is the starting address of the Disk Information Block (DIB) table for the partition.
98	Reserved.
100	PC option table (zero if not specified).
102	IT option table (zero if not specified).
104	OC option table (zero if not specified).
106	X'0010' if interval timer support. X'0000' if no interval timer support.
108	Reserved.
110	Logical Transient Key (LTK) contains the same value as the PIK (PID) (Displacement 46) when the logical transient is requested. When the transient area is not in use, LTK is equal to zero. The SVC 2 routine sets the LTK. The SVC 11 routine resets the LTK. (Only significant in BG communication regions.)
112	Address of SYSPARM field.
116	Address of Job Accounting partition table.
120	Address of the Time-of-Day Clock common area.
124	Address of second part of Program Information Block (PIB) table.
126	Address of PDTABB, table of DTF addresses for MICR support.
128	Address of QTAM vector table (IJLQTAD).
132	Address of background communications region.
134	Option Indicator byte.  Bit 0: Reserved 1: 1 = EU interface active 0 = EU interface inactive 2: 1 = Teleprocessing request 0 = No teleprocessing request 3: 1 = Supervisor support for tape 0 = Supervisor does not support tape 4: Reserved 5: 1 = RETAIN support generated 0 = RETAIN support not generated 6: 1 = Linkage to Channel End Appendage Routine allowed 0 = Linkage to Channel End Appendage Routine not allowed 7: 1 = GETVIS function has been initiated 0 = GETVIS function has not been initiated

Figure 65. Partition Communications Region (Part 5 of 6)

Key	Description of Use
135	System Configuration byte 2 and RMSR Open Flag byte. Bit 0: 1 = PCIL supported 1: 1 = TOD-clock supported 2: 1 = PFI macro supported 3: 1 = Fetch \$\$BOPEN by \$JOBCTLJ 4: 1 = Fetch \$\$BOPEN by \$JOBCTLD 5: 1 = Fetch \$\$BCPEN by \$JOBCTLJ for WTM 6: 1 = QTAM supported 7: Reserved
136	Pointer to Option table in SYSCOM Reserved for compatibility reasons.
140	Standard Job Control Option byte. Bit 0: 1 = EDECK Standard Option 1: 1 = ALIGN Standard Option 2: ACANCEL Standard 3-7: Not used
141	Temporary Job Control Option byte. Bit 0: 1 = EDECK Temporary Option 1: 1 = ALIGN Temporary Option 2: 1 = ACANCEL temporary 3-7: Not used
142	Disk Configuration byte. Bit 0-4: Not used 5: 1 = 3340 supported 6: 1 = 3330 supported 7: Always 1; indicates 2311 and 2314/2319 supported
143	Cataloged Procedure Name.
151	Interface byte for Cataloged Procedures. Bit 0: 1 = Procedure being executed 1: 1 = Overwrite processing 2: 1 = Procedure with data 3: 1 = Overwrite request for Job Control 4: 1 = Insert request for Job Control 5: 1 = Procedure end 6: 1 = SYSLOG procedure 7: 1 = Overwrite request for Supervisor
152	JCL statement name for Cataloged Procedure.
159	SYSIN 81 bytes indicator. Bit 0: 1 = Permanent 81 bytes on SYSRDR 1: 1 = Permanent 81 bytes on SYSIPT 2: 1 = Temporary 81 bytes on SYSRDR 3: 1 = Temporary 81 bytes on SYSIPT 4-6: Not used 7: 1 = Allow /& for MAINT CATALS

Figure 65. Partition Communications Region (Part 6 of 6)

PIBTAB		0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Attention	PIB	Format of Attention PIB	Flag Byte	Cancel Code	SYSLOG ID (AR)	Always Zero	Inactive= Zero Active= Address of LTA Save Area (Note 2)			Switch Byte (See F)	Address of Save Area or Zero (Note 1) (Note 2)		X'07' PIB Assign Flag (See D)	BG User LUB Index	Number of BG Program LUBs	Not used	
Background	PIB		(See A)														
FG 4	PIB																
FG 3	PIB																
FG 2	PIB																
FG 1	PIB																
Subtask	PIB	Format of any Probl. Program or Subtask PIB	Flag Byte	Cancel Code	SYSLOG ID	DAT Flag (See B)	Address of Problem Program Save Area or LTA Save Area (Note 3)			Gate ID (See C)	Address of System Save Area		PIB Assign Flag (See D)	User LUB Index	Number of Program LUBs	Flag Byte (See E)	
Subtask	PIB		(See A)														

Notes:

1. a. When LTA is inactive = LTA save area address.
- b. When LTA is active for Problem Programs, this address is exchanged with that in the Problem Program PIB.
2. When LTA is active for Logical Attention, bytes 9-11 are zero and bytes 5-7 contain the LTA save area address.
3. When the Logical Transient Area is active the save area address in the Problem Program PIB is exchanged with that in the Attention PIB.

The number of Problem Program PIBs generated depends on the number of partitions specified during system generation. Subtask PIBs are generated only if AP=YES has been specified during system generation. The number of subtask PIBs generated depends on the number of partitions, that is:

Number of Partitions	Number of Subtasks
2	13
3	12
4	11
5	10

Bytes 90-91 (X'5A' - X'5E') of the partition communication region(s) contain the address of the PIB table. Label PIBTAB identifies the first byte of the table.

A Flag Byte (First byte in PIB)

The following flags are always used:

- X'71' = Program is waiting for SVC 58
- X'73' = Program is waiting because system is seized
- X'75' = Program is waiting for copy block
- X'77' = Program is waiting for TFREE
- X'79' = Program is waiting for channel queue entry
- X'7B' = Program is waiting for CCW translation
- X'7D' = Program is waiting for free console buffer table entry (used only when CBF=n)
- X'80' = Program is not active
- X'81' = Program is SVC 2-bound (waiting for the LTA to be released)
- X'82' = Program is SVC 7-bound (waiting for an I/O interruption)
- X'83' = Program is ready to run
- X'85' = Program is SVC 5-bound (waiting for the PTA to be released)
- X'86' = Initial selection of RAS (used only for RAS PIB Flag)
- X'87' = Program is set to common bound condition

Figure 66. Program Information Block (PIB) Table (Part 1 of 2)

The following flags are used only if NPARTS>1. X'61' through X'69' are used by the load leveller to deactivate a partition. The partition to which a flag refers depends on NPARTS as follows:

	NPARTS=			
	2	3	4	5
X'61' refers to	BG	BG	BG	BG
X'63' refers to	F1	F2	F3	F4
X'65' refers to	-	F1	F2	F3
X'67' refers to	-	-	F1	F2
X'69' refers to	-	-	-	F1

X'6B' = Program is SVC 35-bound  
 X'6D' = Program is waiting for next freed page frame  
 X'6F' = Program is IDRA-bound

The following flags are only used if AP=YES:

X'51' = Program is SVC 38-bound  
 X'53' = Program is SVC 41/42-bound

The following codes are only used if AP=YES and PFIY=YES. The codes are used by the PFIY routines to set a partition PFIY-bound. The partition to which a flag refers depends on NPARTS as follows:

	NPARTS=			
	2	3	4	5
X'47' refers to	BG	BG	BG	BG
X'49' refers to	F1	F2	F3	F4
X'4B' refers to	-	F1	F2	F3
X'4D' refers to	-	-	F1	F2
X'4F' refers to	-	-	-	F1

The following codes are used only if AP=YES and VSAM=YES. The codes are used by the VSAM routines to set a partition PFIY-bound. The partition to which a flag refers depends on NPARTS as follows:

	NPARTS=			
	2	3	4	5
X'3D' refers to	BG	BG	BG	BG
X'3F' refers to	F1	F2	F3	F4
X'41' refers to	-	F1	F2	F3
X'43' refers to	-	-	F1	F2
X'45' refers to	-	-	-	F1

The following flag is only used when CBF=n:

X'7D' = Program is waiting for free console buffer table entry.

The following flag is only used when TP=QTAM:

X'8B' = Task in QTAM wait.

## B PIB DAT Flag

X'01' = Return to reentrant supervisor routine  
 X'02' = Return to gated supervisor routine  
 X'04' = Move CCB at dispatching time  
 X'08' = Service delayed external interrupt  
 X'10' = Deactivation of this task is being delayed  
 X'20' = Reserved  
 X'40' = Task has seized the system  
 X'80' = Program is running in virtual mode

## C Gate Identifier

X'71' = Gating of SVC 58 required  
 X'53' = Gating of SVC 41/42 required

The flags are only used if the PIB DAT Flag is X'03', that is, the first two flags are on. (See B.)

## D PIB Assign Flag

X'80' = SYSRES DASD file protect inhibited (allow write operation on SYSRES)  
 X'40' = Channel appendage exit allowed (BTAM)  
 X'20' = Cancel in progress (used in terminator function)  
 X'10' = Cancel control (set on a foreground cancel)  
 X'08' = Hold foreground assignments  
 X'07' = Attention PIB

## E Program Program PIB Flag (Last byte in PIB)

Bit 0: 1 = Batched job in foreground (always on when tested)  
 1: 1 = Cancel in LTA and device not assigned  
 2: 1 = /& on SYSIN if DASD  
 3: 1 = Partition in stopped state  
 4: 1 = Fetch EOJ monitor  
 5: 1 = Task is canceled  
 6: 1 = Subtask(s) attached  
 7: 1 = in AE routine

## F Attention PIB Switch Byte

Bit 0: Reserved  
 1: 1 = Fetch Physical Attention Transient \$\$ABERRZ  
 2: 1 = Delay cancellation (fetch \$\$ABERZ1)  
 3: 1 = Emergency cancel request  
 4: Reserved  
 5: 1 = Command available (DOC)  
 6: 1 = Fetch Logical Attention Routine (\$\$BATINA)  
 7: 1 = External Interrupt request

Figure 66. Program Information Block (PIB) Table (Part 2 of 2)

Card Code	Actual IBM Device	Device- Type X'nn'	Device Type
2400T9	9-track Magnetic Tape units	50	Magnetic Tape
2400T7	7-track Magnetic Tape units	50	
3410T9	9-track 3410 Magnetic Tape units	53	
3410T7	7-track 3410 Magnetic Tape units	53	
3420T9	9-track 3420 Magnetic Tape units	52	
3420T7	7-track 3420 Magnetic Tape units	52	
2495TC	2495 Tape Cartridge Reader	51	Tape Cartridge Reader
1442N1	1442N1 Card Read Punch	30	Card Read Punches
2520B1	2520B1 Card Read Punch	31	
2560	2560 Multifunction Card Machine		
2596	2596 Card Read Punch	30	
3525RP	3525 Card Punch (with optional read feature)	32	
5425	Multifunction Card Unit	34	
2501	2501 Card Reader	10	Card Readers
2540R	2540 Card Reader	11	
3504	3504 Card Reader	12	
3505	3505 Card Reader	12	
2540P	2540 Card Punch	21	Card Punches
2520B2	2520B2 Card Punch	20	
1442N2	1442N2 Card Punch	22	
2520B3	2520B3 Card Punch	20	
3525P	3525 Card Punch	23	
1403	1403 Printer	40	Printers
1403U	1403 Printer with UCS feature	42	
1443	1443 Printer	41	
2260 (local)	1053 Printer with 2548 Control Unit. MODE operand must be entered as X'01'	C0	
3203	3203 Printer	4A	
3211	3211 Printer	43	
3277	3284 or 3286 Printer with 3272 Control Unit. (local 3270) MODE operand must be entered as X'01'	B0	
3277B	3284 or 3286 Printer with 3272 Control Unit, (local 3270) attached in burst mode to a multiplexer channel. MODE operand must be entered as X'01'	B0	
5203	5203 Printer	4C	
5203U	5203 Printer with UCS feature	4D	
1050A	3210, 3215 Console Printer Keyboards	00	Printer Keyboards
125D	Model 115/125 Integrated Display Operator Console	B2	Display Operator Consoles
125DP	Model 115/125 Integrated Display Operator Console with 5213 Console Printer attached	B2	

Figure 67. Device Type Codes (Part 1 of 2)

Card Code	Actual IBM Device	Device- Type X'mn'	Device Type
UNSP	Unsupported device	FF	Unsupported no burst mode on multiplexer channel
UNSPB	Unsupported device	FF	Unsupported with burst mode on multiplexer channel
2311	2311 Disk storage device	60	DASD
2314	2314 Direct-access storage facility	62	
2314	2319 Disk storage facility	62	
2321	2321 Data cell drive	61	
3330	3330-1, 3330-2, or 3333-1 Disk storage	63	
3340	3340 Disk Storage (General)	68	
3340	3340 Disk Storage with 3348 Model 35	69	
3340	3340 Disk Storage with 3348 Model 70	6A	
1419	1255 Magnetic Character Reader	72	MICR-Magnetic Ink Character Recognition devices
1419	1259 Magnetic Character Reader	72	
1419	1419 Magnetic Character Reader	72	
1419P	1419 Dual Address Adapter Primary Control Unit	73	
1419S	1419 Dual Address Adapter Secondary Control Unit	74	
2701	2701/2715 Data Adapter Unit	D0	Teleprocessing lines A=SAD0 command when B=SAD1 command enabling C=SAD2 command the line D=SAD3 command
A			
2702	2702 Transmission Control Unit	D1	
B			
2702			
C			
2703	2703 Transmission Control Unit	D2	
2703	Integrated Communication Adapter (Models 125/135)	D2	
2703	3704/3705 Communication Controller in Emulation Mode	D2	
2955	2955 Data Adapter Unit	D7	Data Link for RETAIN
1017	1017 Paper Tape Reader with 2826 Control Unit	78	Paper Tape Readers
2671	2671 Paper Tape Reader	70	
1018	1018 Paper Tape Punch with 2826 Control Unit	79	Paper Tape Punch
1419	1270 Optical Reader/Sorter	79	Optical Readers
1419P	1275 Optical Reader/Sorter	73	
1287	1287 Optical Reader	77	
1288	1288 Optical Page Reader	77	
3881	3881 Optical Mark Reader	11	
3886	3886 Optical Character Reader	7C	
3540	3540 Diskette Input/Output Unit	80	Diskette
2260	2260 Display Station	C0	Display Station
3277	3277 Display Station;	B0	
(local 3270)	MODE operand need not be entered		
3277B	3277 Display Station; attached in burst mode to a multiplexer channel. MODE operand need not be entered	B0	
(local 3270)			
7770	7770 Audio Response Unit	D3	Audio Response Unit

Figure 67. Device Type Codes (Part 2 of 2)

Density (Bytes per inch)	Parity	Convert Feature	Translate	SS Code *
200	odd	on	off	10
200	odd	off	off	30
200	odd	off	on	38
200	even	off	off	20
200	even	off	on	28
556	odd	on	off	50
556	odd	off	off	70
556	odd	off	on	78
556	even	off	off	60
556	even	off	on	68
800	odd	on	off	90
800	odd	off	off	B0
800	odd	off	on	B8
800	even	off	off	A0
800	even	off	on	A8
800	dual density nine-track			C8
1600	dual density nine-track			C0
6250	nine-track 3420			D0
* Refer to PUB Table (Figure 61), byte 5.				

Figure 68. Density Data



Cancel Code (hex)	Message Code	Descriptive Part of Message (or Condition)
10	----	Normal EOJ
11	0V07I	No channel program translation unsupported device
12	0V06I	Insufficient buffer space for channel program translation
13	0V05I	CCW with ccunt greater than 32K
14	0V04I	Page pool too small
15	0V02I	Page fault in disabled program
16	0V01I	Page fault in MICR stacker select or PHO routine
17	0S02I	(Same as 23 but causes dump because subtasks were attached when maintask issued CANCEL macro)
18	----	(Eliminates cancel message when maintask issues DUMP macro with subtasks attached)
19	0P74I	I/O Operator Option
1A	0P73I	I/O Error
1B	0P82I	Channel Failure
1C	0S14I	CANCEL ALL Macro
1D	0S12I	Maintask Termination
1E	0S13I	Unknown ENQ Requestor
1F	0P81I	CPU Failure
20	0S03I or 0S11I	Program Check
21	0S04I or 0S09I	Illegal SVC
22	0S05I or 0S06I	Phase Not Found
23	0S02I	Program Request
24	0S01I	Operator Intervention
25	0P77I	Invalid address
26*	0P71I	SYSxxx Not Assigned (unassigned LUB code)
27	0P70I	Undefined Logical Unit (invalid LUB code in CCE)
28	----	QTAM cancel in progress
29	0S15I	No relocating loader support (Fetch or load request for relocatable phase while supervisor does not support relocating load).
2A	0V10I	I/O error on page data set
2B	0P84I	I/O error during fetch from private core image library
2C	0V09I	Illegal parameter passed by PHO routine
2D	0P88I	Program cannot be executed/ restarted due to failing storage block

Figure 69. Cancel Codes and Messages (Part 1 of 2)

Cancel Code (hex)	Message Code	Descriptive Part of Message (or condition)
2E	0S16I	Invalid resource request (possible deadlock)
2F	0V03I	More than 255 PFIx requests for 1 page
30	0P72I	Reading Past /& Statement (on SYSRDR or SYSIPT)
31	0P75I	Error Queue Overflow
32	0P76I	Invalid DASD Address
33	0P79I	No Long Seek (disk)
34		Reserved
35	0P85I	Job control open failure
36	0V08I	Page fault in I/O appendage routine
37		Reserved
38	0V11I	Wrong privately translated CCW
39		Reserved
FF	0P78I	Unrecognized Cancel Code
	0P83A**	Supervisor Catalog Failure
	0P87A**	IPL failure

In addition to recognizing the cancel-codes above, the Terminator also recognizes the same codes with the X'80' bit on (cancel occurred in LTA). The X'80' bit is set by \$\$ABERZ1 when no more I/C requests are pending and the cancel procedure therefore can be continued. Later it is tested for by \$\$BEOJ and subsequently reset.

\*If the CCB is unavailable, the logical unit is SYSxxx.

\*\*This cancel code is not significant in case of a supervisor catalog or IPL failure, because the system is placed in a wait state without any further processing by the Terminator.

Figure 69. Cancel Codes and Messages (Part 2 of 2)

Displacement	Label	Description
0-15	(ACCTCOMN) ACCTSVRG	Temporary register save area
16-17	ACCTSVRX	Save area for remainder of overhead counter times distributed by partition on exit
18-19	ACCTSVRE	Save area for remainder of all-bound counter times distributed by partitions on entry
20-23	ACCTPCNT	Count of partitions using the Job Accounting interface
24	ACCTSAID	Owner of physical transient area *)
25	ACCTFAID	Interrupted program *)
26	ACCTRAID	ACTIVE PROGRAM *)
27	ACCTSWCH	Accounting switches: if bit=1, true; if bit=0, not true bit 0: cancel accounting      bit 4: IPL indicator bit 1: no active partitions   bit 5: not used bit 2: catalog in process    bit 6: not used bit 3: alternate label area   bit 7: not used
28-31	ACCTIME	Start time of current accounting interval, in complement format
32-33	ACCTRESC	Reserved
34-35	ACCTUSEP	Address of user save area (ACCTUSER)
36-37	ACCTUSEL	Length of user save area (set with 1st operand of FOPT macro parameter JALIOCS)
38-39	ACCT\$JOB	Job accounting partition indication
40-43	ACCTBLES	Address of BG Job Accounting Table

If multiprogramming is supported, this table is to be extended with one of the following fields (depending on the number of supported partitions), otherwise the table ends here.

NPARTS=2

44-47		Address of F1 Job Accounting Table
48-51	ACCTSEAS	Control Field: prevents the accounting routine being loaded twice

NPARTS=3

44-47		Address of F2 Job Accounting Table
48-51		Address of F1 Job Accounting Table
52-57	ACCTSEAS	Control Field: prevents the accounting routine being loaded twice

\*) These values are the same as the PIK values for the relevant tasks.

Figure 70. Job Accounting Interface Common Table (ACCTCOMN) (Part 1 of 2)

NPARTS=4

Displacement	Label	Description
44-47		Address of F3 Job Accounting Table
48-51		Address of F2 Job Accounting Table
52-55		Address of F1 Job Accounting Table
56-63	ACCTSEAS	Control Field: prevents the accounting routine being loaded twice

NPARTS=5

44-47		Address of F4 Job Accounting Table
48-51		Address of F3 Job Accounting Table
52-55		Address of F2 Job Accounting Table
56-59		Address of F1 Job Accounting Table
60-69	ACCTSEAS	Control Field: prevents the accounting routine being loaded twice

Bytes 124-127 (X'7C' - X'7F') of the System Communication Region (SYSCOM) contain the address of the Job Accounting Interface Common Table. Label ACCTCOMN identifies the first byte of the table.

Figure 70. Job Accounting Interface Common Table (ACCTCOMN) (Part 2 of 2)

Displacement	Label	Description
0-3	(ACCTABLE) ACCTWK1	Work area used in SIO update
4-7	ACCTWK2	Work area used with ACCTWK1 in start/stop time routine
8-11	ACCTSVPT	Job card pointer; address of job card field following jobname
12-13	ACCTPART	ID of partition in charge (partition switch name)
14-15	ACCTLEN	Length of SIO area=6n+1, where n=number of devices for this partition in SYSGEN option JA=(n1,n2,n3,n4,n5)
16-21	ACCTLOAD	Label area instruction; moves JAI label area address to OPEN/CLOSE transients
22-23	ACCTRES3	Reserved
24-27	ACCTLADD	Address of alternate label area
28-31	ACCTCPU	Counter for CPU time elapsed in a jobstep, counted in 300th of a second
32-35	ACCTOVHT	Counter for overhead time; time not charged to any partition
36-39	ACCTBNDT	Counter for all-bound time; system wait state time divided between running partitions

Figure 71. Job Accounting Interface Partition Table (ACCTxx) (Part 1 of 2)

Displacement	Label	Description
40-47	ACCTSVJN	Save area for job name during simulated EOJ
48-55	ACCTJBNM	Job name; taken from job card
56-71	ACCTUSRS	User information; 16 bytes from job card
72-73	ACCTPTID	Partition ID: 'BG', 'F4', 'F3', 'F2', or 'F1' in EBCDIC format
74	ACCTCNCL	Cancel code; see Cancel Codes and Messages
75	ACCTYPER	Type of record: 'S' = job step, 'L' = last step of job
76-83	ACCTDATE	Date in format specified at SYSGEN (MM/DD/YY or DD/MM/YY)
84-87	ACCTSTRT	Start time of job, in packed decimal (DHHMMSSF; F = sign)
88-91	ACCTSTOP	Stop time of job, in same format as ACCTSTRT
92-95	ACCTRES	Reserved
96-103	ACCTEXEC	Phase name; taken from execute card
104-107	ACCTHICR	End address of active program phase, from COMREG
108-111	ACCTIMES	CPU time elapsed in a job step; counted in 300th of a second
112-115	-----	Overhead time; elapsed time not charged to any partition, in 300th of a second
116-119	-----	All-bound time; system wait state time divided between running partitions, in 300th of a second
120	ACCTSIO	SIO tables: 6 bytes for each device specified by SYSGEN options, as follows: 2 bytes for device address (Ocuu), 4 bytes for count of SIOs in current jobstep
-----	-----	Overflow byte: normally X'20', but is X'30' if more devices are used within a partition than specified by SYSGEN options

Notes:

1. DSECT ACCTABLE symbolically addresses the JAI Partition Tables with labels as shown. Each partition in which JAI is supported has its own JAI Partition Table, labeled ACCTBG, ACCTF4, ACCTF3, ACCTF2, ACCTF1, for active partitions BG, F4, F3, F2, and F1, respectively.
2. The address of this table is in the partition Communication Region at displacement 116 (X'74').

Figure 71. Job Accounting Interface Partition Table (ACCTxx) (Part 2 of 2)



APPENDIX C: SAMPLES OF EREP OUTPUT

--- I/O DEVICE EDITING ---

TASK IDENTITY - EXAMPLE  
RECORD TYPE - UNIT CHECK

DAY YEAR HH MM SS  
DATE - 135 73 TIME - 06 16 45

CPU MODEL 0155 SERIAL 510764  
DOS/V5 RELEASE LEVEL 29

FAILING CHANNEL/UNIT ADDRESS 0382 DEVICE TYPE 3420

FAILING CCW	CC	DA	FL	CT		K	CA	US	CS	CT
02	0038FE	CO	00	0050		CSW	00	003EF0	0E	08 0000

--- UNIT STATUS ---				--- CHANNEL STATUS ---			
ATTENTION	0	CHANNEL	1	PRGM-CTLD IRPT	0	CHAN DATA CHECK	1
STATUS MODIFIER	0	DEVICE END	1	INCORRECT LENGTH	0	CHAN CTL CHECK	0
CONTROL UNIT END	0	UNIT CHECK	1	PROGRAM CHECK	0	I/F CTL CHECK	0
BUSY	0	UNIT EXCEPTION	0	PROTECTION CHECK	0	CHAINING CHECK	0

NUMBER OF I/O RETRIES 00003  
TCU SERIAL 0000 TU SERIAL 0000 VOLUME SERIAL 123456 MODE SET C3

BYTE 0	BYTE 1	BYTE 2	BYTE 3	0	BYTE 4	BYTE 5	BYTE 6	BYTE 7
CMND REJ 0	NOISE 1	TRK ERR 0 0	R/W VRC 0	ALU CHECK 0	NEW SUBSY 0	7 TRACK 0	LAMP FAIL 0	
INTV REQ 0	TU STAT A 1	TRK ERR 1 0	MTE/LRCR 0	REJECT TU 0	NEW SUBSY 0	WRT HD CR 0	TP BOT LF 0	
BUS CHK 1	TU STAT B 0	TRK ERR 2 0	SKEW 0	TAPE IND 0	WRT TM CK 0	DUAL DEN 0	TP BOT RH 0	
EQUIP CK 0	7 TRACK 0	TRK ERR 3 0	EDC/CRC 0	WR TR VRC 0	PE ID BUR 0	NRZI 0	RESET KEY 0	
DATA CK 0	LOAD PT 0	TRK ERR 4 0	VRC/ENV 0	U-PGM DET 0	ST RD CK 0	MODEL 0	DSE 0	
OVERRUN 0	WRITE STA 0	TRK ERR 5 0	1600 BPI 0	LOOP W/R 0	PART RCD 0		ERASE HD 0	
WRD CNT 0	FL PROT 0	TRK ERR 6 0	BACKWARD 0	TU CK 0	EX POST A 0		AIR PRESS 0	
DT CNVT 0	NOT CAPAB 0	TRK ERR 7 0	C COMPARE 0	RPQ 0	RPQ 0		LOAD FAIL 0	

BYTE 8	BYTE 9	BYTE 10	BYTE 11	BYTE 12	BYTE 17	BYTE 18	BYTE 19
IBG DROP 0	BIT 0 0	CND S REJ 0	B/BUS/LSR 0	B/BUS/LSR 0	2 CHAN SW 0	PRW CK 0	DE DR 7 0
FD THR CK 0	VEL CHG 0	SPARE 0	SPARE 0	SPARE 0	FEATURE 0	SPARE 0	DE DR 6 0
BIT 2 0	RESERVED 0	CTL S REJ 0	XFEK/LOIC 0	XFER/LOIC 0	FEATURE 0	SPARE 0	DE DR 5 0
ERLY BOR 0	RESERVED 0	NO BOR 0	INST/HIIC 0	INST/HIIC 0	FEATURE 0	SPARE 0	DE DR 4 0
ERLY END 0	SPARE 0	WIM-EQ CK 0	U-PGM ERR 0	U-PGM ERR 0	EC OF CU 0	EC OF DRV 0	DR DE 3 0
SLOW BOR 0	SPARE 0	TACH FAIL 0	D BUS PTY 0	D BUS PTY 0	EC OF CU 0	EC OF DRV 0	DE DR 2 0
SLOW END 0	SPARE 0	SPARE 0	SPARE 0	SPARE 0	EC OF CU 0	EC OF DRV 0	DE DR 1 0
VEL TRY 0	CTL U RES 0	VEL CHK 0	AOC ALU1 0	BOC ALUZ 0	EC OF CU 0	EC OF DRV 0	DE DR 0 0

BYTE 20	BYTE 21	BYTE 22	BYTE 23	--- SDR AREA ---			
DE DR F 0	LD BUTTON 0	FRU ALU1 0	FRU ALUZ 0	NOISE 000	WR TR VRC 00	ERLY BOR 00	
DE DR E 0	LFT REEL 0			R/W VRC 000	WRT TM CK 00	ERLY END 00	
DE DR D 0	RMT REEL 0			MTE/LRCR 000	ST RD CK 00	SLOW BOR 00	
DE DR C 0	TAPE PRES 0			EDC/CRC 000	PART RCD 00	SLOW END 00	
DE DR B 0	REELS LOD 0			VRC/ENV 000	EX POST A 00	VEL RTY 00	
DE DR A 0	LD RWD 0			OVERRUN 00	IBG DROP 00	VEL CHG 00	
DE DR 9 0	LD COMPL 0			SKEW CK 00	FD THR CK 00	BACKWARD 0000	
DE DR 8 0	LD CK 0			C COMP 000	PE ID CK 0	BUS CHK 00	

----- DEVICE DEPENDENT INFORMATION -----			
BLOCK LENGTH	00000	PERM WRTS	003
TEMP READ	002	NOISE BLKS	100
TEMP WRT	005	ERASE GAPS	00000
SIO COUNT	00003	CLEAN ACT	00003
PERM RDS	001		
		TRK IN ERR	000000000

Example Unit check record for 3420

DETAIL -- MAGNETIC TAPE ERROR STATISTICS BY VOLUME      DATE 73/135

VOLUME SERIAL	DATE	TIME OF DAY	CUA	TU SERIAL	RD/ WRT	PERM READ	PERM WRT	TEMP READ	TEMP WRT	SIO COUNT	BLOCK LENGTH	PROGRAM ID	CPU ID	MOD NO	DENSITY
123456	73/135	06.16.45	0382	0000	R	001	003	002	005	00003	00000	EXAMPLE	0155		8/1600
123456	73/135	06.16.45	0383		R	006	002	001	004	00005	00000	TEST	0155	1	8/1600
983451	73/135	06.16.45	0382	0000	R	000	000	001	003	00007	00000		0155		8/1600
983451	73/135	06.16.45	0383		R	006	002	002	003	00005	00000	TEST2	0155	1	8/1600

Example of Error Statistics by Tape Volume Number.

DETAIL -- MAGNETIC TAPE ERROR STATISTICS BY TAPE UNIT      DATE 73/135

CUA	TU SERIAL	DATE	VOLUME SERIAL	TIME OF DAY	TEMP READ	TEMP WRT	SIO COUNT	DENSITY	NRZI NOISE	R/W VRC	WR VRC	TG MTE	LRC EDC	CRC ENV	ECC ERR	SKEW BOR	ERLY CHG	VEL TIE
0382	0000	73/135	123456	06.16.45	002	005	00003	8/1600	000	000	00	000	000	000	00	00	00	000
0382	0000	73/135	983451	06.16.45	001	003	00007	8/1600	214	064	05	213	193	212	12	15	01	003
0383		73/135	123456	06.16.45	001	004	00005	8/1600	000	000		000	000	000	00	00		000
0383		73/135	983451	06.16.45	002	003	00005	8/1600	000	000		000	000	000	00	00		000

Example of Error Statistics by Tape Unit.



--- I/O DEVICE EDITING ---

RECORD TYPE - VOLUME DISMOUNT

DAY YEAR HH MM SS  
DATE - 135 73 TIME - 06 16 45

CPU MODEL 0155 SERIAL 510764  
DOS/VS RELEASE LEVEL 29

FAILING CHANNEL/UNIT ADDRESS 0382 DEVICE TYPE 3420

NUMBER OF SIOS FOR THIS VOLUME 00000007  
VOLUME ID 983451  
TAPE MODE C3

SDR COUNTERS

--- DEVICE DEPENDENT DATA ---

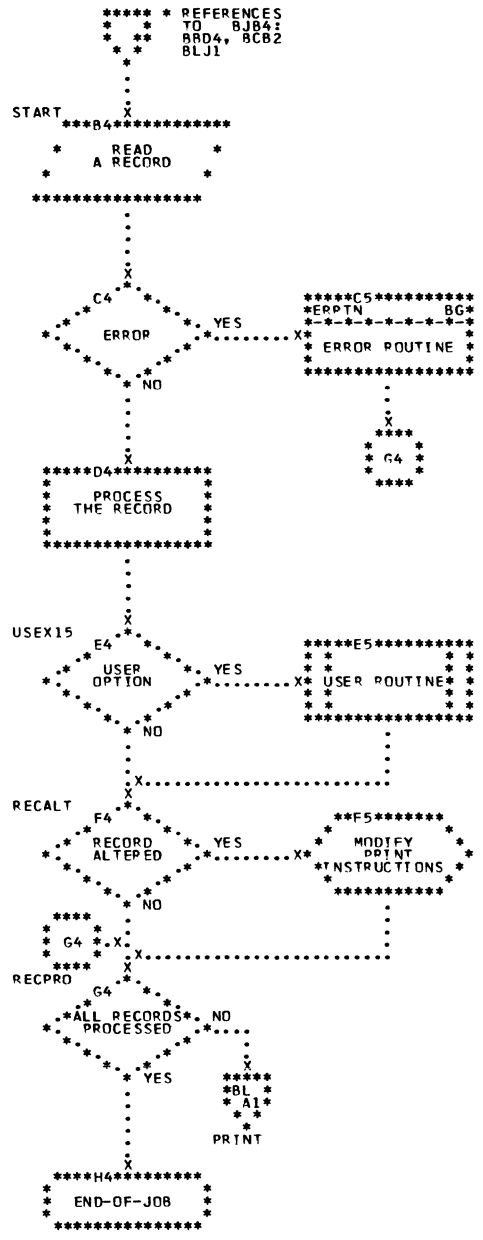
NOISE	214	R/W VRC	064	BLK LENGTH	00000	NOISE BLKS	080
MTE/LRCR	213	EDC/CRC	193	TEMPY READ	001	TEMPY WRT	003
VRC/ENV CK	212	OVERRUN	00	PERM READ	000	PERM WRT	000
SKEW	12	C COMPARE	00	ERASE GAPS	00000	CLEANER ACTNS	00003
WRITE TRIG VRC	05	PE ID BURST CHK	0	TRKS IN ERR	000000011		
WRITE TM CHK	00	START READ CHK	00				
PARTIAL RECORD	00	EXCESSIVE POSTAMBLE	15				
IBG DROP/WRITE	00	FEED THRU CHK	00				
END VEL CHK	15	EE READ BACK CHK	00				
SLOW BOR	00	SE READ BACK CHK	00				
VELOCITY RETR	03	VEL CHNG DURING WT	01				
BACKWARD	063	BUS OUT CHK	00				
ALU HARDWARE ER	00						

Example Volume Dismount for 3420.



APPENDIX D: EXPLANATION OF FLWCCHART SYMBOLS

DESCRIPTION	EXAMPLE
<pre> *****A1***** * *   PROCESS   * *   *B2      * * *****           </pre>	<p>A group of program instructions that perform a processing function of the program. The label, if any, is shown above the block.</p>
<pre> *****B1***** *LABEL1      *BW* *---*---*---* * *   SUBROUTINE * *****           </pre>	<p>*B2 IF ANY ADDITIONAL EXPLANATION IS REQUIRED, ITS LOCATION ON THE CHART IS IDENTIFIED BY AN ASTERISK AND THE BLOCK ID.</p>
<pre> **C1***** * * PREPARATION * *****           </pre>	<p>Description or title of a routine that is detailed on another flowchart. The starting label of the routine and the flowchart ID appear above the stripe.</p>
<pre> *****D1***** * * PREDEFINED *   PROCESS * *****           </pre>	<p>An instruction, or group of instructions, that changes portion of a routine or initializes a routine for a given condition.</p>
<pre> ***E1***** * * INPUT/OUTPUT * *****           </pre>	<p>A group of operations not detailed in the flowcharts in this manual, such as user routines.</p>
<pre> F1 * * DECISION *           </pre>	<p>Any function of an input/output device or program, usually branching to an I/O routine to perform the function stated in the block.</p>
<pre> ***G1***** * *   TERMINAL * *****           </pre>	<p>Points where the program branches to alternate processing, based upon variable conditions such as program switch settings and test results.</p>
<pre> **** * C2 * ****           </pre>	<p>The beginning or end of a program or routine.</p>
<pre> ***** *BD * *D4* * * FIL INPT           </pre>	<p>On-page connector. An entry from or an exit to another function on the same flowchart. The location in the connector identifies the block to which entry on a chart is made.</p>
<pre> ***** *BD * *D4* * * FIL INPT           </pre>	<p>Off-page connector. An entry from, or exit to, a given point on another flowchart. The characters in the connector identify the chart and block to which or from which control is passed. The corresponding label, if any, is placed outside the outgoing connector. For multiple entries, an asterisk is placed in the connector and the locations from which control is passed are listed nearby.</p>



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APPENDIX E: LABEL LIST

Label	Phase	Location	Label	Phase	Location
AAAGET	PDAID	ACA 1	AREASTAT	EREP2715	MLB1
AAAGET1	PDAID	ACB1	ARETURN	PDS DM	JGK1
AEORT	DUMPGEN	HDH5	AROUND	LSERV	KAF3
ACCEPT	PDAIDQTW	EJF4	AROUND13	PDAID	ADG3
ACCEPT	PDAIDQTT	EMD5	AROUND14	PDAID	AGF2
ACCPARM	PDS DM	JCB 1	ASETREAL	PDS DM	JGD2
ACCPAR1	PDS DM	JCG 1	ASKAAA	PDAID	ACC1
ACCPAR2	PDS DM	JCJ1	ASKAID	PDAID	AAJ2
ACCPAR3	PDS DM	JCC5	ASKDEV	SYSBUFF1	PFJ1
ACCPAR4	PDS DM	JDB1	ASKODQST	PDAID	ABB2
ACCPAR5	PDS DM	JDG1	ASKSVCQS	PDAID	ABG3
ACCPAR6	PDS DM	JDB3	ASK4AID	PDAID	AAD1
ACCPAR7	PDS DM	JDG3	ASSIGNER	DUMPGEN	HBB3
ACCPAR8	PDS DM	JDB5	ASTRK	EREPEDIT	MEA5
ACCPAR81	PDS DM	JDE5	ASTRK	EREPEDCP	MUB4
ACHCKID	PDS DM	JGB3	ASTRK	EREPEDES	NEC3
ACHKNTRY	PDS DM	JGE 1			
ACHKREAL	PDS DM	JGG3	BACK	EREPEDIT	MEA3
ADDRCHCK	PDS DM	JGB 1	BACK	EREPEDCP	MUB2
ADDR EDIT	SDEHR	GLJ1	BACK	EREPEDES	NEC1
ADDRINT1	REALDUMP	HFG5	BAD	SYSBUFLD	PDC5
ADDZERO	DUMPGEN	HBC1	BAD 2	SYSBUFLD	PDD5
ADPTESC	EREP2715	MLB4	BADCARD	PDAID	AFB5
ADPTDISK	EREP2715	MLA5	BALCVDEC	EREPEDTR	MVA3
ADPTHD	EREPADPT	MMC2	BCTLOOP	PDAID	ALJ5
ADPT2740	EREP2715	MLB2	BEGIN	PDAIDITP	CBH4
ADPT2790	EREP2715	MLB3	BEGIN	PDAIDITT	CDH4
ADVANCE	LSERV	KAD3	BEGIN	DUMPGEN	HAB1
AERR	PDS DM	JAH2	BEGIN	ESTVUT	LAB1
AERR 1	PDS DM	JAF4	BEGIN	EREPEDIT	MDA1
AERR 10	PDS DM	JCH4	BEGIN	EREPESPT	MFA2
AERR 2	PDS DM	JAG4	BEGIN	EREPES	NFA1
AERR 3	PDS DM	JAH4	BEGIN	EREPESWK	NHA2
AERR 4	PDS DM	JAC5	BEGIN	EREPHIST	NJA1
AERR 5	PDS DM	JAH5	BEGIN	EREPRDE	NKA1
AERR 6	PDS DM	JCB4	BEGIN	\$\$BUFLD2	PKB1
AERR 6	PDS DM	JDG2	BLANKS	EREPEDIT	MEA4
AERR 8	PDS DM	JCE4	BLANKS	EREPEDCP	MUB3
AFTDUB	\$\$BSDAID	GFD 1	BLANKS	EREPEDES	NEC2
AFTFIX	\$\$BSDAID	GFJ1	BLK	ESTVUT	LAA4
AGAIN	SYSBUFLD	PEC4	BLKNR1	REALDUMP	HLD1
AGETDISP	PDS DM	JGE3	BLNKCHK	PDAID	AFE1
ALLIN	DUMPGEN	HDG1	BLNKCHKC	PDAID	AFC5
ALLTRACE	PDAIDGTW	EAJ1	BOPLY	PDAIDQTW	ELE4
ALLTRACE	PDAIDGTP	EDF3	BOPLY	PDAIDQTT	EMK3
ALLTRACE	PDAIDGTT	EGF3	BPUT	PDLIST	BCH4
ALPHA	SYSBUFLD	PAB1	BRNCH1	PDAIDGTW	EAB3
AMOREPAR	PDS DM	JGB5	BRNCH1	PDAIDGTW	EAB5
AOUTVIRT	PDS DM	JGD5	BRNCH1	PDAIDGTP	EDG1
APARM0	PDS DM	JAE1	BRNCH1	PDAIDGTP	EDG4
APARM00	PDS DM	JAD1	BRNCH1	PDAIDGTT	EGG1
APARM1	PDS DM	JAJ1	BRNCH1	PDAIDGTT	EGG4
APARM2	PDS DM	JAD3	BRNCH2	PDAIDGTW	EAC3
APARM3	PDS DM	JAF3	BRNCH2	PDAIDGTW	EAC5
APARM4	PDS DM	JAB5	BRNCH2	PDAIDGTP	EDH1
APARM5	PDS DM	JAE5	BRNCH2	PDAIDGTP	EDH4
APARM7	PDS DM	JBB1	BRNCH2	PDAIDGTT	EGH1
APARM8	PDS DM	JBD1	BRNCH2	PDAIDGTT	EGH4
APLOOP	REALDUMP	HQD 1	BRPROC	EREPUNIT	MKB3
AFFAEND	PDAIDTDT	FAG4	BRPROC20	EREPUNIT	MKB5
AFFAEND	PDAIDTDP	FCG4	BUILDID	EREPSMTR	MRA2

Label	Phase	Location	Label	Phase	Location
BUILITBL	EREPRETV	MGA2	CKDEVCE2	PDAIDITW	CAB3
BYPSTM	\$\$BATTN3	GPE1	CKDEVCE2	PDAIDITP	CBB4
			CKDEVCE2	PDAIDITT	CDB5
CANCELIT	SYSBUFLD	PEE2	CKDEVCE3	PDAIDIW	CAC3
CANCRTN	PDAID	AGF5	CKDEVCE3	PDAIDITT	CDC5
CCH	EREPFRDE	NLA2	CKDEVST	PDAIDFTT	DBE3
CCHNDX	EREPCCCH	MHB3	CKDEVST	PDAIDFTT	DDG3
CCHPART	EREPCCCH	MHB1	CKDEVST	PDAIDGTP	ECB3
CCHR FAD	EREPCCCH	MHA4	CKDEVST	PDAIDGTT	EFB3
CCHR TRN	EREPCCCH	MHB5	CKDONE	EREPS PCL	MNB1
CCH00	EREPEDCU	MZA2	CKEDIT	EREPTES	NFA5
CCH10	EREPEDCU	MZA3	CKENTRY	PDAIDGTP	ECF2
CCH20	EREPEDCU	MZA5	CKENTRY	PDAIDGTT	EFF2
CCH30	EREPEDCU	MZB1	CKMPX	EREPADPT	MMB3
CDERRLOG	PDAID	AFK3	CKOUTOD	PDAID	ADB1
CGBJC2	REALDUMP	HQE5	CKREC	PDLIST	BCC4
CHANGE1	REALDUMP	HLC1	CKTR	PDLIST	BCE4
CHKIST1	REALDUMP	HKG1	CK2715ID	EREPS PCL	MNA2
CHKSTRS	REALDUMP	HNH3	CK2715ID	EREPA STA	MPA4
CHEKER	PDAIDITP	CBH5	CK2740	EREPADPT	MMB4
CHEKMAIN	EREPMNTR	MBB3	CK2790	EREPADPT	MMB2
CHK	EREPSTR	NGB3	CLBLUB	REALDUMP	HTB1
CHK	EREPESWK	NHC1	CLEAR	PDAIDITP	CBJ5
CHKAA	PDAID	ACF1	CLEAR	PDAIDITT	CDH5
CHKAA1	PDAID	ACG1	CLEAR	PDAIDFTP	DCJ2
CHKAA3	PDAID	ACG3	CLEAR	PDAIDFTT	DEJ2
CHKAA4	PDAID	ACE4	CLEAR	PDAIDGTP	ECJ5
CHKAA5	PDAID	ACF4	CLEAR	PDAIDGTT	EFJ5
CHKAA6	PDAID	ACB4	CLEAR	PDAIDQTT	ENG3
CHKAA8	PDAID	ACC4	CLEAR	PDAIDTDT	FBG2
CHKAA2	PDAID	ACC2	CLEAR	PDAIDTDP	FDF2
CHKATOP	PDAID	ALG5	CLEARCTR	EREPS MCP	MQA3
CHKDATE	EREPESWK	NHA3	CLEARTRW	SDAID1	GAJ1
CHKDFV	SYSBUFF1	PFB3	CLKRUNS	SDAID1	GAH1
CHKFLP	SDPAR	GDB2	CLMERK	SDEHR	GLF3
CHKFOLD	UCBLOAD	PCD1	CLOSPRT	SDEHR	GLG3
CHKIPL	DUMPGEN	HCB3	CLRFILE	EREPMNTR	MCC2
CHKKEY	EREPMNTR	MBB1	CMPTIME	EREPRDE	NKC1
CHKMCRE	PDAID	AFH1	CMRG	\$\$BSDAID	GMH2
CHKNO	DUMPGEN	HBF5	CNTROL	EREPE DIT	MCC5
CHKNO	DUMPGEN	HCF1	CNTROL	EREPE DCP	MUC5
CHKNC	DUMPGEN	HCF3	CNTROL	EREPEDES	NED4
CHKNO	DUMPGEN	HCF5	CNT00	EREPSMTP	MTC1
CHKNC	DUMPGEN	HDG3	CNT05	EREPSMTP	MTA4
CHKNSWRT	SDPAR	GDB1	CNVTCUU	PDAID	ALF1
CHKRESP	EREPTES	NFC2	CNVTODEC	EREPE DTR	MVA4
CHKSYVS	DUMPGEN	HDB3	CNVT2BIN	PDAID	ALD5
CHKTYPE	SYSBUFLD	PBB1	COL71	DUMPGEN	HDB1
CHKYES	DUMPGEN	HBC5	COMCPU	EREP	MAG2
CHKYES	DUMPGEN	HCC1	COMCUA	EREP	MAF5
CHKYES	DUMPGEN	HCC3	COMCUA	EREPS PCL	MNR4
CHKYES	DUMPGEN	HCC5	COMDATE	EREP	MAF1
CHOTC9	PDAID	ALE4	COMDEV	EREP	MAG3
CKASAT	EREPS PCL	MNC2	COMEND	EREP	MAG5
CKBSC	EREPADPT	MMB5	COMJOB	EREP	MAF3
CKCHARIN	PDAID	ALF5	COMPARE	EREPESWK	NHC5
CKCQP	PDAIDFTP	DBF2	COMPARE	UCBLOAD	PCK4
CKCQP	PDAIDFTT	DDH2	COMPARE1	REALDUMP	HHF1
CKCQP	PDAIDGTP	ECC1	COMPARM	SDPAR	GDF1
CKCQP	PDAIDGTT	EFC1	COMPAS	EREPS PCL	MNB3
CKDEVCE1	PDAIDITW	CAA3	COMP CD	PDLIST	BCC2
CKDEVCE1	PDAIDITP	CBA4	COMPLOW	REALDUMP	HJC1
CKDEVCE1	PDAIDITT	CDA5	COMP NEX	SDAID1	GAA3
CKDEVCE1	PDAIDQTW	EKC1	COMPO	REALDUMP	HJF1
CKDEVCE1	PDAIDQTW	EKC3	COMPROG	EREP	MAF4
CKDEVCE1	PDAIDQTT	ENB1	COMPUTE	EREPHIST	NJC1

Label	Phase	Location	Label	Phase	Location
COMPY	SDAID1	GAK1	DEVTST	PDAIDITP	CBC1
CCMRIGN	EREP	MAC3	DEVTST	PDAIDITT	CDC1
CCMTERM	EREP	MAG1	DEVTST	PDAIDQTT	EMC1
COMTIME	EREP	MAF2	DISKREAD	EREPSMES	NDB2
CGMVCL	EREP	MAG4	DISK10	EREPSMES	NDC2
CONFLCT	SYSBUFLD	PBC4	DMPST	PDAIDQTW	EKJ3
CCNT	PDAIDFTP	DBD1	DOIO	SYSBUFF1	PGH1
CONTINUE	PDAIDGTP	EEG2	DOIT	PDAIDITT	CEE2
CONTINUE	EREPSPT	MFB1	DOIT	PDAIDGTW	EBJ3
CONTINUE	EREPHIST	NJC4	DOIT	PDAIDGTT	EFG2
CONTINUE	\$\$BUFLDR	PHE1	DOIT	PDAIDQTW	ELB4
CCNTSCAN	SDPAR	GCD1	DOIT	PDAIDQTT	ENE4
CCNTSCAN	REALDUMP	HNC1	DONE	REALDUMP	HPE3
CONVDATE	EREPRDE	NKB4	DONSET	PDAIDITT	CDF3
CCNVDEV	EREPRDE	NKD4	DONTSET	PDAIDGTT	EHF4
CONVERT	PDAID	ALE5	DONTSET	PDAIDQTT	EMG3
CCNVERT	SDAID1	GAB3	DOPRINT	SDAID1	GAD5
CCNVERT	ESTVUT	LAA2	DOPT	PDAIDITP	CCB3
CONVRT	ESTVUT	LAF4	DOPT	PDAIDFTP	DBJ2
CCNVFT2	ESTVUT	LAF5	DOPT	PDAIDGTP	ECG2
CONVIIME	EREPRDE	NKB5	DOPTR	PDAIDFTT	DDC5
CORECMP1	PDAIDTDT	FAC3	DOSLEVEL	EREPEdit	MEB5
CCREICMP1	PDAIDTDP	FCC3	DSKERROR	EREPADEP	MMA5
COREDUMP	PDAIDTDT	FAB3	DUMP	REALDUMP	HGD3
CCREICMP	PDAIDTDP	FCB3	DUMP	EREPEdit	MED1
COUNT	EREPSMCP	MQB4	DUMPAREA	PDAIDTDT	FAB1
CPTNFETN	EREP	MAB1	DUMPAREA	PDAIDTDP	FCB1
CPU	EREFMNR	MBD4	DUMPTST	PDAIDITW	CAD3
CQCHCK	REALDUMP	HUB3	DUMPTST	PDAIDITP	CBB2
CQLOCP	REALDUMP	HUD3	DUMPTST	PDAIDITT	CDC2
CQLOOP1	REALDUMP	HUF3	DUMPTST	PDAIDQTT	EME1
CQLOCP2	REALDUMP	HUF4	DUPLREQ	SDAID1	GAH4
CQLOCP3	REALDUMP	HUJ3	DVCK	EREPSPT	MFB3
CRDINP	PDAID	AEB1	DVSCAN	EREPSMCP	MQB3
CTRINCR	EREPADEP	MMB1	DVSCAN	EREPSM**	MSA2
CTRLOC	EREPSM**	MSA4	DVCSUPRT	EREPSM**	MSA3
CUUCNVT	PDAID	ALE1	DVC00	EREPEDD1	NAA2
CUUOKEY	PDAID	ALK1	DVC05	EREPEDD1	NAA3
CUUPUBCK	PDAID	ALH1			
CUURES P	PDAID	ABE2	EALL	PDLIST	BAJ2
			ED**STR	EREPEDD1	MWA1
DAED00	EREPEDD1	MWA5	ED**STR	EREPEDD1	MXA1
DAED00	EREPEDD1	MXA5	ED**STR	EREPEDD1	NBA1
DAEDC5	EREPEDD1	MXB1	EDCPSSTR	EREPEDCP	MUA1
DAED10	EREPEDD1	MXB2	EDCUSTRT	EREPEDCU	MZA1
DAED15	EREPEDD1	MXB3	EDD1STR	EREPEDD1	NAA1
DAED20	EREPEDD1	MXB5	EDSSTR	EREPEDES	NEA1
DASD00	EREPEDD1	MWA2	EDIT	REALDUMP	HVG1
DASD00	EREPEDD1	MXA2	EDIT	EREPEdit	MDA3
DASD05	EREPEDD1	MWA3	EDIT	EREPEDES	NEA4
DASD05	EREPEDD1	MXA3	EDITBITS	EREPEdit	MEC3
DASD10	EREPEDD1	MWA4	EDITLOAD	EREPSMCP	MQB5
DASD10	EREPEDD1	MXA4	EDITL00	EREPEDCP	MUA4
DATARDS	LSE RV	KBC1	EDITMORE	EREPADEP	MMC3
DATAREAD	LSE RV	KBB1	EDITV00	EREPEDD1	NAB2
DATE	EREFMNR	MBC4	EDITV05	EREPEDD1	NAB3
DATE	EREPEdit	MEB3	EDITV07	EREPEDD1	NAB5
DATEVENT	SDEHR	GKB1	EDITV15	EREPEDD1	NAB4
DATEC0	EREFSMTP	MTA2	EDITV25	EREPEDD1	NAC1
DATE00	EREPEDD1	NBA2	EDIT00	EREPEDD1	MYA4
DBLSCHK	PDAID	AEG2	EDIT00	EREPEDD1	NAA5
DBLSYTP	PDAID	AED3	EDIT00	EREPEDD1	NBA5
DEFAULT	EREFMNR	MCA2	EDIT00	EREPEDD1	NCA5
DENS	ESTVUT	LAD3	EDIT03	EREPEDD1	MYA5
DEVASSGN	DUMPGEN	HEB3	EDIT05	EREPEDCP	MUA2
DEVICE	EREFMNR	MBD1	EDIT05	EREPEDD1	MYB1

Label	Phase	Location	Label	Phase	Location
EDIT05	EREPEDES	NEB2	ERRHR	PDAIDFTT	DDD5
EDIT10	EREFEDCP	MUA3	ERRHR	PDAIDGTP	ECH2
EDIT10	EREPEDD1	NAB1	ERRHR	PDAIDGTT	EFH2
EDIT10	EREFEDU1	NCB1	ERRHR	PDAIDQTT	ENF4
EDIT10	ERFPEDES	NEA5	ERRMSG	SDPAR	GBG2
EDIT15	EREFEDU1	NCB2	ERROR	PDAIDTDT	FAG5
EDIT20	EREFEDU1	NCB3	ERROR	EREPSMCP	MQB2
EDIT20	EREFEDU1	NEB3	ERROR	\$\$BUFLD2	PKC2
EDIT25	EREFEDU1	NCB4	ERROR4	EREPESWK	NHB5
EDIT25	ERFPEDES	NEB1	ERROR4	EREPHIST	NJC5
EDTPSTRT	ERFEDTP	MYA1	ERRSTAT	SDAID1	GAF1
EDU1STRT	EREFEDU1	NCA1	ERR0	PDAID	AJG2
ELEMEV	SDEHR	GJB3	ERR5	DUMPGEN	HDB5
ENABLE	PDAIDTDT	FAK1	ERR5A	DUMPGEN	HDC5
ENABLE	PDAIDTDP	FCK1	ESTVUT	ESTVUT	LAA1
END	EREFESWK	NHB2	EXCP	SY\$BUFLD	PEB3
END	SY\$BUFLD	PAE3	EXCP	\$\$BUFLD1	PJD1
ENDBR	PDLIST	BAF1	EXECUNPK	PDLIST	BCE5
ENDD	ESTVUT	LAA5	EXIT	EREPEDIT	MEF3
ENDH	ERFPESTR	NGC1	EXIT	ERFPASTA	MPB5
ENDIT	REALDUMP	HYB5	EXIT	EREFEDCP	MUC3
ENDJOB	PDSDM	JBE4	EXIT	EREPEDTR	MVB3
ENDJCP	PDSM	JJH4	EXIT	EREPEDCU	MZA4
ENDMSG	\$\$BUFLDR	PHC5	EXIT	EREPEDD1	NAA4
ENDPRT	LSERV	KAH5	EXIT	EREPED**	NBA4
ENDSI	\$\$B\$DAID	GNB5	EXIT	EREPEDES	NED2
ENDT	ESTVUT	LAC5	EXIT	EREPES	NFB1
ENDUMP	REALDUMP	HKB1	EXIT	EREPHIST	NJD4
ENTCHNG	PDAIDITW	CAD5	EXIT	SY\$BUFF1	PGF5
ENTCHNG	PDAIDFTW	DAC4	EXIT00	EREPEDTP	MYA3
ENTER5	REALDUMP	HQD3	EXIT00	EREFEDU1	NCA4
ENTR	PDAIDIT	CDJ3	EXIT00	EREPEDES	NEA3
ENTR	PDAIDQTT	EJE2	EXIT1	EREPHIST	NJD5
ENTR	PDAIDQTT	EMG1	EXNTPROC	EREPEDIT	MEE3
ENTRY	PDAIDITP	CBE4	EXNTPROC	EREPUNIT	MKB1
ENTRYB	PDAIDTDT	FAC1	EXTINT	SDEHR	GGH5
ENTRYB	PDAIDTDP	FCC1			
ENTRYQ	PDLIST	BEF2	FASTCOM	\$\$B\$DAID	GNC1
ENTRYQ1	PDLIST	BFC3	FASTREC1	\$\$B\$DAID	GNE1
EOD	EREFERDE	NLA4	FASTREC2	\$\$B\$DAID	GNB2
ECFRUCUT	PDSM	JFB1	FASTREC3	\$\$B\$DAID	GNB3
EOFRIN	EREPUNIT	MKC2	FETCH	\$\$BUFLD1	PJH1
ECFTEST	LSFV	KAC5	FETCH 1	SY\$BUFF1	PGJ1
EOJ	PDAID	AHB4	FETCH1	\$\$BUFLDR	PHG4
EOJ	\$\$BPDAID	AME3	FETCH2	SY\$BUFLD	PEJ4
FCJ	PDLIST	BAF2	FILLUP	PDLIST	BAC1
EOJ	EREPNTR	MCC3	FINAL	PDAIDFTP	DBF1
ECJJT	LSFV	KAE5	FINAL	PDAIDFIT	DDF1
EOJRTN	PDAID	AAF5	FINAL	PDAIDGTP	EEG4
EOJRTNA	PDAID	AAJ4	FINAL	PDAIDGTT	EHG4
ERBLOOP	REALDUMP	HTE5	FINDFIR	PDLIST	BEE2
ERBLOOP1	REALDUMP	HUC1	FINDFIR1	PDLIST	BFG1
ERBLOOP3	REALDUMP	HUD1	FINDIGNS	PDAID	ACC5
ERBLOOP4	REALDUMP	HUE1	FINDIT	PDLIST	BDB4
EREPNAME	EREF	MAA5	FINDSVCS	PDAID	ABF3
EREPSTRT	EREP	MAA1	FIRST	REALDUMP	HQF5
ERR	DUMPGEN	HDD5	FIRSTIN	PDLIST	BCD2
ERRCCND	\$\$BPDAID	AMF5	FIXING	\$\$B\$DAID	GFA5
ERRCCND	PDAIDITP	CCB5	FLAGSET	PDLIST	BEC4
ERRCCND	PDAIDITP	CEB4	FLENTER	PDAIDFTW	DAB2
ERRCCND	PDAIDGTP	ECE4	FLTRC	PDLIST	BEB2
ERRCCND	PDAIDGTT	EFE4	FLTRSET	PDLIST	BAB4
ERRCCND	PDAIDQTT	ENB5	FLUSH	PDSDM	JJG4
ERRHR	PDAIDITP	CCC3	FOLLOFF	SDPAR	GCE2
ERRHR	PDAIDITP	CEF2	FOLLON	SDPAR	GCF2
ERRHR	PDAIDFTP	DBK2	FOLLOW	EREPESWK	NHC4



Label	Phase	Location	Label	Phase	Location
FORCCHK	\$\$BSDAID	GPF1	IJBEIPL	EREPIPL	MJA1
FORCCHK	\$\$BATN3	GPG1	IJBEMCAR	EREPCCH	MHA1
FORCDEF	SDAID1	GAF3	IJBEUNIT	EREUNIT	MKA1
FORGET	\$\$BSDAID	GME3	IJERETV	EREPRETV	MGA1
FORGETIT	\$\$BATN3	GPC5	IMODUNT	PDSDM	JBC3
FORMAT	PDAIDFTP	DBG5	IMOD001	PDSDM	JJG1
FORMAT	PDAIDGTP	EED1	INCRCGPT	EREPEDTR	MVB2
FORMAT	DUMPGEN	HCB1	INCREMENT	PDAIDFTW	DAG2
FORMAT	EREPMNTR	MBD2	INCRMNT	PDAIDITW	CAF3
FORMAT	EREPESTR	MFB2	INCRSEQ	EREPRDE	NKC2
FORMAT	EREPESTR	NGB4	INDEX	EREPEDIT	MEC2
FORMAT1	PDAIDGTP	EEE1	INDEX	EREPEDCP	MUC4
FOUND	\$\$BUFLDR	PHB3	INDEX	EREPEDES	NED3
FOUNDIT4	REALDUMP	HND2	INIT	EREPADET	MMA2
FPCHCK	REALDUMP	HUD5	INITAPE	EREPESTR	NGA2
FROMSIO	PDAIDITW	CAC1	INITAPE	EREPHIST	NJA3
FRSTCHK	EREPESTR	NHC3	INITIAL	EREPEDIT	MDA2
FSTIMSW	PDAID	AAF2	INITIAL	EREPTES	NFA2
FTRCHK	PDAID	AHE4	INITIAL	EREPESTR	NHA1
			INITLOG	EREPMNTR	MBA4
GENPRINT	EREPRDE	NKC5	INITPG	EREPESTR	MFA1
GET	PDLIST	BAH3	INITPIKS	PDAID	ABC5
GETADDRS	PDAIDQTW	ELG2	INITRTN	PDAID	ADB3
GETCC	SYSUFF1	PFD1	INITZ	PDAIDITW	CAB5
GETIGNS	PDAID	AHB1	INITZ	PDAIDITP	CBF3
GETPHAS	EREPRETV	MGD2	INITZ	PDAIDFTW	DAA4
GETSUPST	\$\$BDAID	AMG1	INITZ	PDAIDQTW	ELB2
GETSVCS	PDAID	AJB1	INITZ	PDAIDQTT	EMC3
GIVSRCE	EREPMNTR	MBA3	INIT00	EREPSMES	NDA3
GO	PDAIDTDT	FAB5	INIT00	EREPRDE	NKA2
GO	PDAIDTDP	FCB5	INIT10	EREPSMES	NDA5
GOBACK	PDAIDGTP	EAD4	INIT20	EREPSMES	NDA4
GOBACK	PDAIDGTP	EDJ2	INSERT	EREPESTR	NHD2
GOBACK	PDAIDGTT	EGJ2	INSRTEV	SDEHR	GLH1
GCOD	EREPESTR	NHC2	INSUFF	SDAID1	GAB5
GOODIAPE	EREPESTR	NGA3	INTCHK	SDEHR	GHB3
GCODIAPE	EREPHIST	NJA5	INTCHK1	SDEHR	GHC3
GORTN	PDAID	AGE2	INTENTER	PDAIDITW	CAH4
GOTHRU	PDLIST	BDC4	INTERUPT	DUMPGEN	HHB5
GFACTIVE	EREPMNTR	MRA5	INTEXT2	SDEHR	GHC4
GROUPCK	EREUNIT	MKB2	INTEXT3	SDEHR	GHD4
GRUPHD	EREPRDE	NLB1	INTEXT4	SDEHR	GHE4
GSVCERT	PDLIST	BBC2	INTRUPT	REALDUMP	HFD1
GSVCTYPE	PDLIST	BBG2	INVCC	SYSBUFLD	PEE5
GTST4FF	PDLIST	BBF2	INVSTE1	REALDUMP	HLB3
			IOBCKT	PDAIDITT	CDB3
HALF	PDLIST	BEH4	IOPNLST	PDSDM	JBE2
HALF1	PDLIST	BFB3	IOPNOUT	PDSDM	JB1
HAPDAT	SDEHR	GKE1	IOPNDS	PDSDM	JBE3
HDLEV	SDEHR	GKD3	IORTN	PDAIDQTW	EJB2
HEADER2	EREPHIST	NJB3	IOTREC	PDLIST	BDB2
HERE	EREPESTR	MLA2	IOTRFMT	PDLIST	BDE2
HIGHEND	\$\$BSDAID	GMD3	IOTRSET	PDLIST	BAD4
HILOSET	EREPESTR	MLA3	IOUTEND	PDSDM	JJC4
HISTCK	EREPHIST	NJD2	IPDSEND	PDSDM	JJB4
			IPL	EREPRDE	NKC4
IGET1ST	PDSDM	JBB5	IPLNDX	EREPIPL	MJA4
IGNDEV	PDAIDITP	CBC3	IPLPAGE	EREPIPL	MJA3
IGNDEV	PDAIDQTW	ELE2	IPLREAD	EREPIPL	MJA2
IGNDEV	PDAIDQTT	EMH3	IPLRTRN	EREPIPL	MJA5
IGNRESP	PDAID	AHD1	ISBLOCK	SYSBUFLD	PDC2
IGNRSPCD	PDAID	AHJ3	ISBLOCK	SYSBUFLD	PDE2
IGNRTN	PDAID	AGD4	ISDAEXTS	LSERV	KBG3
IGNTCER	PDAID	AHG3	ISIT4	SDEHR	KKG3
IGTRERCD	PDAID	AKB5	ISMSG	SYSBUFLD	PDC3
IJBECCH	EREPCCH	MHA2	ISPRNT	SYSBUFF1	PGG1

Label	Phase	Location	Label	Phase	Location
ISSUMSG	SYSEUFLD	PEB1	LUBCHCK	REALDUMP	HRB1
ISX	SYSEUFLD	PFA2	LUBLOOP	REALDUMP	HRC1
ITAPE1	PDSDM	JJE1	LUBLOOP1	REALDUMP	HRD1
ITSTIEV	PDSDM	JJB1	LUBLOOP2	REALDUMP	HRF2
ITSTPDS	PDSDM	JBD3	LUBLOOP3	REALDUMP	HRC3
			LUBLOOP4	REALDUMP	HRG3
KEEPIGN	PDAID	AEK4	LUBLOOP5	REALDUMP	HRJ4
KEEPTRC	PDAID	AEK3	LUBLOOP6	REALDUMP	HRF5
KEYCHK	PDAID	AFA1	LUBLOOP7	REALDUMP	HSB1
KEY14CHK	PDAID	AFF3	LUBLOOP8	REALDUMP	HSB2
KEY15CHK	PDAID	AFG4	LU10	REALDUMP	HSB4
KEY2CHK	PDAID	AFB1	LU9	REALDUMP	HSB3
KEY3CHK	PDAID	AFC1			
KEY4CHK	PDAID	AFG1	MCARNDX	EREPCCH	MHB2
KEY5CHK	PDAID	AFE1	MCARPART	EREPCCH	MHA5
KEY6CHK	PDAID	AFG2	MCARREAD	EREPCCH	MHA3
KEY7CHK	PDAID	AFF1	MCARTRN	EREPCCH	MHB4
KLEER	\$\$BUFLD1	PJF4	MCEVENT	SDEHR	GJB1
			MCH	EREPRDE	NLA1
LABLROUT	EREPTES	NFB5	MESSAGE	EREPTES	NFC1
LASTLST	EREPEPST	MFB4	MODN	EREPEPST	MFB5
LASTFEC	PDLIST	BAG2	MONENDB	SDEHR	GGD3
LEAVDISP	SDEHR	GHE1	MONENDX	SDEHR	GGE3
LISTCLB	REALDUMP	HTE1	MONEND1	SDEHR	GGC3
LISTPRO	EREPEPIT	MEA1	MONEND2X	SDEHR	GGG3
LISTPRO	EREPEDCP	MUA5	MONENTPG	SDEHR	GGB1
LISTEROC	EREPEDES	NEB4	MONRETPN	SDEHR	GGF3
LOAD	EREPEPIT	MED3	MONRETPO	SDEHR	GGF1
LCADER	EREPE	MAA2	MONTSTDIT	SDEHR	GGJ1
LOADER2	EREPE	MAA3	MONTSTM	SDEHR	GKH3
LOADFCB	SYSEUFLD	PFH2	MONTSTM C	SDEHR	GGH1
LCADINP	DUMPGEN	HAB3	MOVE	ESTVUT	LAB4
LOADIT	EREPE2715	MLC3	MOVE	EREPSMTP	MTB5
LOADNAME	EREPE	MAA4	MOVE	EREPEPST	NHD3
LOADOPTN	EREPEPIT	MDA5	MOVECOM	EREPEPIT	MEB4
LOADOV5	REALDUMP	HQB3	MOVECOM	EREPEDCP	MUB5
LOADPHAS	EREPEPIT	MDA4	MOVECOM	EREPEDES	NEC4
LOADREGS	PDLIST	BEE4	MOVEHEX	EREPEPIT	MEB1
LCADR11	PDAIDTDT	FAD3	MOVEMSG	REALDUMP	HNG5
LCADR11	PDAIDTDP	FCD3	MOVEMSG	SYSEUFLD	PGA1
LOAD1	\$\$BUFLDR	PHE3	MOVESAME	REALDUMP	HJH1
LOAD3	SYSEUFLD	PBF2	MOVEUP	PDAIDGTW	EAJ5
LOG	DUMPGEN	HDE5	MOVEUP	PDAIDGTP	EEC3
LOGDEL	PDAID	AED4	MOVEUP	PDAIDGTT	EHF1
LOGINV	PDAID	AGE3	MOVEUP	PDAIDQTW	EKE5
LOGRITE	\$\$BUFLD1	EJB1	MOVREG	EREPEPIT	MED2
LCOKLESC	EREPRDE	NKD2	MOVSTAT	PDAIDFTP	DBF3
LOOKSUB	EREPRDE	NKD3	MOVSTAT	PDAIDFTT	DDH3
LCOP	PDAIDGTW	EAE3	MOVSTAT	PDAIDGTP	ECC2
LOOP	PDAIDGTW	EAE5	MOVSTAT	PDAIDGTT	EFC2
LOOP	PDAIDGTP	EDK1	MOVUP	PDAIDFTP	DBG4
LOOP	PDAIDGTP	EDK4	MOVUP	PDAIDFTT	DDG4
LOOP	PDAIDGTT	EGK1	MPS	\$\$BPDAID	AMC3
LOOP	PDAIDGTT	EGK4	MSG	EREPEPST	NHA4
LOOP	REALDUMP	HPC1	MSGLNG	PDAID	AGD1
LOOP	EREPEPIT	MKC4	MULTISEQ	LSERV	KBG5
LOOP	EREPEPST	NHB4	MVCCTR	EREPEPIT	MED4
LOOP2	\$\$BUFLDR	PHD1	MVCCTR	EREPEDCP	MUD2
LOSTHEX	PDAID	AFH5	MVCCTR	EREPEDES	NEE1
LSTALNT	SDPAR	GDB5			
LSTCICHK	PDAID	AED1	NEWCHK	PDAIDITW	CAA2
LSTFIELD	EREPEPST	MFC1	NEWLINE	PDLIST	BEH5
LSTRTN	EREPEPIT	MEA2	NEWLINE1	PDLIST	BFC4
LSTRTN	EREPEDCP	MUB1	NEWLINE1	REALDUMP	HGG1
LSTRTN	EREPEDES	NEB5	NEWPAGE	EREPEDCP	MUD1
LTACHCK1	REALDUMP	HNE3	NEWPAGE	EREPEDES	NEE5

Label	Phase	Location	Label	Phase	Location
NEWPG	EREFADPT	MMC1	ONLIES	PDAID	ACE5
NEWTAPE	EREFESTR	NGA5	OPCDMV	LSERV	KAK1
NEWTAPE	EREPHIST	NJB2	OPENCKER	EREPMNTR	MCA4
NEXT	PDAIDFTP	DBE1	OPENFILE	EREPMNTR	MCA5
NEXT	PDAIDFTT	DDE1	OPTCHK	EREPTES	NFA4
NEXT	PDAIDGTP	EEF4	OPTION	EREPMNTR	MCB2
NEXT	\$\$BUFLDR	PHB2	OPTIONS	PDAID	ACB5
NEXTCLB	REALDUMP	HTC3	OPTION1	EREPMNTR	MCB3
NEXTFCB	SYSBUFF1	PFJ4	OPTION2	EREPMNTR	MCB4
NEXTFCB	\$\$BUFLD1	PJE4	OPTION3	EREPMNTR	MCB5
NEXTIN	PDLIST	BCE2	OPTION4	EREPMNTR	MCC1
NEXTONE	PDAIDITP	CCD1	OPTNFND	EREPSMCP	MQA4
NEXTOPT	EREPTES	NFC3	OPTNNODA	EREPSMCP	MQA2
NEXTPAG1	REALDUMP	HGE3	OPTOFF	EREPSWK	NHB3
NEXTRC	EREPSCL	MNC1	OR 14	PDSDM	JDD2
NEXTRC	EREFASTA	MPB4	OR 17	PDSDM	JCD4
NEXTRCRD	EREF2715	MLC1	OR 17	PDSDM	JDJ2
NEXTRCRD	EREPSCL	MNA5	ORRCHAR	DUMPGEN	HEE1
NEXTRCRD	EREPASTA	MPA5	OUT	PDAIDITT	CDJ5
NEXTUP	SYSBUFLD	PDA1	OUT	PDAIDGTT	EHJ1
NOAID	PDAID	AAC4	OUT	PDAIDQTT	ENJ3
NCALT	PDAIDITW	CAC4	OUTAREA	PDLIST	BAB1
NCALT	PDAIDGTW	EBH2	OUTPUT	PDAIDITT	CEA2
NOALT	PDAIDQTW	ELJ1	OUTSIDE1	REALDUMP	HLB5
NCAUTOM	DUMPGEN	HBG5	OVERFLOW	PDAIDQTW	EK B5
NOD	PDAID	AGF4	OVFLIND	PDAIDITP	CB D4
NCDBL1	PDAID	AEB4	OVFLIND	PDAICITT	CDD5
NODBL2	PDAID	AEA5	OVFLIND	PDAIDQTW	EKF3
NCEDIT	REALDUMP	HPF3	OVFLIND	PDAIDQTT	ENE1
NOGOT	PDAID	ALD4			
NOLOG	SYSBUFLD	PED1	PAGE	ESTVUT	LAE1
NCMORE	PDAID	AGG2	PAGTABE1	REALDUMP	HKG4
NONEED	PDAID	AAB4	PAGTAB1	REALDUMP	HMF1
NCNMES	PDAID	AKJ1	PARAMCNT	PDAID	ALB1
NONZERO	EREPEDIT	MEB2	PARMCHAR	EREPMNTR	MBC3
NOPHS	SYSBUFLD	PEC2	PARMCHK	EREPMNTR	MBC1
NOPHS	SYSBUFLD	PBB5	PARMCHK	EREPMNTR	MCA1
NORMAL	\$\$BPDAID	AMD2	PARMHEX	EREPMNTR	MBC5
NCSYS	SYSBUFLD	PEG3	PARM2715	EREPMNTR	MBD5
NOTEND	EREFESTR	NGC3	PCKALLP	PDSDM	JEG2
NOTEND	EREPHIST	NJD1	PCKLAST	PDSDM	JEG1
NCTINRS	REALDUMP	HNH5	PCKLP	PDSDM	JEB1
NOTONCUU	PDAID	ALC2	PCOMPARE	PDSDM	JHF1
NCTOF	SDAID1	GAG1	PCPU	EREPRETV	MGB4
NOTPRNT	SYSBUFLD	PAA2	PCUA	EREPRETV	MGC5
NOTYPE	SYSBUFLD	PEG2	PDATE	EREPRETV	MGB2
NCTYF2	SYSBUFLD	PBF1	PDVTP	EREPRETV	MGC3
NOX203	UCBLOAD	PCF1	PEREVENT	SDEHR	GKB3
NRFCHK	LSERV	KAB3	PGMEVENT	SDEHR	GKB5
NUMBER	DUMPGEN	HBD1	PGMINT2	REALDUMP	HKH1
NXTBLOCK	EREPEDTR	MVA2	PGMINT3	REALDUMP	HKJ1
NXTELEM	SDEHR	GLE1	PHASE2ND	EREPRDE	NLB3
NXTENTR1	REALDUMP	HLF1	PIBCHCK	REALDUMP	HPE5
NXTENT1	REALDUMP	HMC5	PIBLOOP	REALDUMP	HPD5
NXTENT3	REALDUMP	HME3	PIBLOOP1	REALDUMP	HPF5
NXTGRP	EREFONIT	MKC3	PIBSAVDP	REALDUMP	HNH2
			PJOB	EREPRETV	MGC1
OBR	EREPRDE	NLA3	POSMSG1	SYSBUFF1	PFB4
ODCARD	PDAID	ABD1	POSMSG2	SYSBUFF1	PFC3
OLERFCD	PDAID	AKB4	PPOOLEX1	REALDUMP	HMC1
OKIGN	PDAIDGTW	EBF3	PREDUCE	PDSDM	JHD1
OKIGN	PDAIDGTP	EED4	PREOPEN	PDSDM	JFB3
OKIGN	PDAIDGTT	EHD4	PREPROC	EREPMNTR	MCB1
ONCE	EREPESTR	NGB2	PRINT	PDAIDFTP	DBH2
ONCE	EREPESWK	NHA5	PRINT	PDAIDFTT	DD B5
ONE EXT	LSERV	KBG4	PRINT	REALDUMP	HVB1

Label	Phase	Location	Label	Phase	Location
PRINT	EREPEDIT	MEF2	RDREC	EREPASTA	MPA3
PRINT	EREFEDCP	MUC1	RDRESP	SYSBUFLD	PEC1
PFINT	EREFEDES	NEC5	RDTPE	EREPESPT	MFA3
PRINTBR	SYSBUFLD	FED2	READ	ESTVUT	LAG1
PFINTEUF	SDEHR	GLB1	READ	EREPEDIT	MEE1
PRINTDK1	LSEFV	KBH3	READ	EREPEDIT	MEE5
PRINTIT	\$\$BSDAID	GMB1	READ	EREPSMES	NDB1
PFINTIT	EREFSPCL	MNB2	READ	EREPESWK	NHB1
PRINTIT	EREPEDTR	MVA5	READ	EREPHIST	NJB5
PRINTMSG	SYSBUFLD	PEC5	READ	EREPRDE	NKB3
PRINTOUT	EREPASTA	MPB2	READAAA	PDAID	ACD1
PRNTEBK1	REALDUMP	HKB4	READADDR	EREPSMES	NDB4
PFNTCR	REALDUMP	HLE3	READIN	SYSBUFLD	PAL2
PRNTCR1	REALDUMP	HLD3	READISK	EREPEDIT	MEE2
PFNTLATA	EREFSPCL	MNB5	READLOG	DUMPGEN	HAG3
PRNTDATA	EREPASTA	MPB3	READLOG	EREPEDIT	MED5
PROCESS	EREPHIST	NJB4	READLOG	EREPEDIT	MEE4
PFOEXTNT	LSEFV	KBJ3	READOPTN	EREPMNTR	MBA5
PROGRAM	EREPRETV	MGB5	READRCRD	EREFSPCL	MNA4
PRROUT	EREFEDCP	MUC2	READRCRD	EREPASTA	MPA2
PRROUT	EREFEDES	NED1	READREC	EREFSPCL	MNA3
PRT	EREPEDTR	MVB1	READREC	EREPTES	NFB2
PFTCL	PDAID	AKC2	READ00	EREPSMFS	NDE5
PRTDATEV	SDEHR	GKG1	READ1	DUMPGEN	HAE3
PFTFRCD	PDAID	AKF3	REALDUMP	REALDUMP	HFE1
PRTFV	SDEHR	GKF3	REALPRT	\$\$BSDAID	GMC4
PRTGET	PDAID	ABB5	RECREAD	EREPADPT	MMA3
PFTMC	SDEHR	GJD3	RECUP	EREPTES	NFE3
PRTMCL	SDEHR	GLE5	RECUP	EREPESTR	NGB5
PFTQUES	PDAID	AKB1	REDUCEA	REALDUMP	HHD1
PRTTE	SDEHR	GLG2	RELCCW1	REALDUMP	HGB3
PFTVAL	PDAID	AKF1	RELD	PDLIST	BDJ3
PRTZERO	SDEHR	GLB5	RELEASE	SDEHR	GGB3
PSAMESW	PDSDM	JHH1	RELO	DUMPGEN	HAC1
ESTOFE14	PDSDM	JHB1	RELO	SYSBUFLD	PAC1
PTERM	EREPRETV	MGC4	RELO	\$\$BUFLDR	PHB1
PTIME	EREPRETV	MGB3	RELOAD	PDLIST	BAK4
PUBCHK	REALDUMP	HTF3	RELOC	PDAIDGTP	EDE5
PUBCHK	PDAID	ALB3	RELOC	PDAIDGTT	EGD5
PUBCCMP	PDAID	ALC3	RELOC	EREPMNTR	MBA2
PUBLICOP	REALDUMP	HTH3	RELOC	EREPTES	NFA3
PUBLICLOOP2	REALDUMP	HTJ3	RELOC	EREPHIST	NJA2
FUNCHRT	DUMPGEN	HEE4	REPUNCH	DUMPGEN	HEH4
PUPDATE	PDSDM	JHJ1	RESET	EREPESPT	MFC3
PUT	PDAIDTDT	FAJ1	RESET	SYSBUFF1	PFK2
PUT	PDAIDTDP	FCJ1	RESPOF2	PDAID	AJE3
PUTIN	PDAIDITP	CCB1	RESTSET	PDAIDFTP	DBC1
P2EJ1	EREPRDE	NLB2	RESTSET	PDAIDFTT	DDC1
P2715	EREPRETV	MGD1	RESTSET	PDAIDGTW	EBC3
			RESTSET	PDAIDGTP	EDD5
			RESTSET	PDAIDGTT	EGC5
QTAMFRMT	PDLIST	BFE1	RETAR	\$\$BATN3	GPC4
QTAMREC	PDLIST	BFB1	RETMCNOS	SDEHR	GJF3
QUEUE	EREP	MAC1	RETOUR	\$\$BSDAID	GFG3
			RETURN	PDLIST	BBB2
FCDPRO	EREPUNIT	MKC1	RETURN	PDAIDTDT	FAH2
RCTYP	EREPRDE	NKC3	RETURN	PDAIDTDP	FCH2
RDDISK	EREPESPT	MFA4	RFCURR	EREP	MAB5
RDEPHZ	EREPRDE	NKD1	RFONE	EREP	MAB4
RDERRCK	EREPADPT	MMA4	RLDMINUS	EREPSMES	NDA2
RLFORMA1	REALDUMP	HNB1	RTRVLP2	EREPRETV	MGB1
RDLOG00	EREPRDE	NKA3	R7PLUS	SDEHR	GLC3
RDLOG15	EREPRDE	NKA4			
RDLOG85	EREPRDE	NKA5			
RDNEXTOV	REALDUMP	HME5	SAMESW	REALDUMP	HHH1
RDPPCOL	REALDUMP	HMB1	SAVEDATA	EREPASTA	MPE1
RDPRT	PDAID	AKD1	SAVEIT	PDAIDITP	CBF5

Label	Phase	Location	Label	Phase	Location
SAVEIT	PDAIDITT	CDF5	SHORTFCB	SYSBUFF1	PF55
SAVENUM	PDAID	ALH5	SHORTFCB	\$\$BUFLD1	PJG4
SAVEPSW	PDAIDFTW	DAC1	SHORTOUT	PDLIST	BCG5
SAVEF2	DUMPGEN	HAD3	SHRTMSG	PDAID	AGC1
SAVESTUF	PDAIDGTP	EEG3	SIO	\$\$BPDAID	AMC4
SAVESTUF	PDAIDGTT	EHG1	SIOCHK	PDAIDQIW	EKG3
SAVEWORD	REALDUMP	HJC3	SIOENT	PDAIDGTT	EFA1
SAVIT	PDAIDQTT	ENG1	SIOENT	PDAIDQTT	EMB2
SAVLOOP	REALDUMP	HNC3	SIOENTER	PDAIDITW	CAB1
SAVLOOP1	REALDUMP	HNB5	SIOENTSV	PDAIDFTP	DBE2
SAVLOOP4	REALDUMP	HND5	SIOENTSV	PDAIDFTT	DDG2
SAVPSW	PDAIDFTP	DBB3	SIOENTSV	PDAIDGTP	ECB1
SAVPSW	PDAIDFTT	DEB3	SIOENTSV	PDAIDGTT	EFB1
SAVUM	PDAIDITP	CBA5	SIORD4	REALDUMP	HGC3
SAVUM	PDAIDITT	CDA4	SIORTN	\$\$BPDAID	AMB4
SAVUM	PDAIDGTP	EEB2	SIORTN	PDAIDITP	CBB1
SAVUM	PDAIDGTT	EHC1	SIORTN	PDAIDITT	CDB1
SAVUM	PDAIDQTT	ENB2	SIORTN	PDAIDQTW	EJB1
SCAN	EREFMCP	MQA5	SIORTN	PDAIDQTT	EMB1
SCAN	EREFMCP	NDC3	SIORT2	REALDUMP	HFJ5
SCAN	EREFPEDES	NEA2	SKPG	EREFESPT	MFC2
SCAN	EREFESWK	NHD1	SM**STRT	EREFM**	MSA1
SCANBLK	EREFMNT	MBB2	SMSSTRT	EREFMCP	NDA1
SCAN00	EREFMTP	MTA3	SMTSTRT	EREFMTP	MTA1
SCAN00	EREFEDTP	MYA2	SOLVE	SYSBUFF1	PFK4
SCAN00	EREFED**	NBA3	SORESCAN	PDAIDTDT	FAC5
SCAN00	EREFEDU1	NCA2	SORTER	EREFMNT	MCA3
SCAN05	EREFEDU1	NCA3	SPCLNGRD	PDAID	AGC4
SCAN1	DUMPGEN	HAG5	SPTST1	PDLIST	BAJ1
SCAN2	DUMPGEN	HAB5	SPECODE	EREF2715	MLB5
SCAN20	EREFMTP	MTB4	SPLIT	PDAIDTDT	FAH4
SCAN4	DUMPGEN	HBC2	SPLIT	PDAIDTDP	FCH4
SDMACHEK	SDEHR	GHH4	STAGN	PDLIST	BDJ5
SEARCH	EREFUNIT	MKA5	START	PDAID	AAB1
SEGTABE1	REALDUMP	HKC4	START	\$\$BPDAID	AMB1
SEGTAB1	REALDUMP	HME1	START	SDPAR	GBB1
SELCMRG	EREF	MAE4	START	EREFMNT	MBA1
SELECT	EREFRET	MGA4	START	EREF2715	MLA1
SELINCP	EREFRET	MGA5	START	EREPADPT	MMA1
SELPARM	EREF	MAE2	START	EREFSPCL	MNA1
SELREGN	EREF	MAE1	START	EREPASTA	MPA1
SELSWTH	EREF	MAE3	START	EREFMTR	MRA1
SEL2715	EREF	MAE5	START	EREFEDTR	MVA1
SERVENQ	SDEHR	GJG5	START	EREFSTR	NGA1
SERVHDL	SDEHR	GJB5	STARTIO	\$\$BUFLD1	PJE1
SET	EREFESTR	NGB1	STARTRUN	EREFPRDE	NKB1
SETBR	PDAIDFTP	DBB1	STDLPRT	LSERV	KAJ4
SETBR	PDAIDFTT	DEB1	STEP4	REALDUMP	HNE1
SECKSW	SDEHR	GGD1	STINST	PDAIDFTP	DBF5
SETXTNT	EREFUNIT	MKB4	STMREGS	PDAID	AAD4
SFTLSTSW	PDAID	ABE2	STOPONEV	SDEHR	GGD5
SFTNLCOM	EREFMTR	MRB2	STOPTST	SDEHR	GGC5
SFTPAR	SDAID1	GAG3	STORECAW	PDAIDTDP	FCC5
SETRSET	EREFMNT	MBB5	STORERJ	SYSBUFLD	PEH5
SETRTERM	SDPAR	GDB4	STORER5	REALDUMP	HHB1
SETRGO	SDPAR	GDG2	STORE2ND	PDLIST	BBK1
SETRTKWD	SDPAR	GDJ2	STORLIN	PDLIST	BBJ2
SFTSCNSW	EREFMTR	MRB3	STRTADDR	EREFMTR	MRA4
SETTEST	PDAIDFTW	DAE4	SUBCHEK	EREFMNT	MBB4
SETUF	PDAIDGTW	EBH3	SUBSWTH	EREF	MAC2
SETUP	PDAIDQTW	ELJ3	SUMCPUID	EREF	MAD3
SETUF	SYSBUFF1	PFB1	SUMPARM	EREF	MAC5
SETXIENT	EREF2715	MLA4	SUMREGN	EREF	MAC4
SHIFT	SYSBUFF1	PFG4	SUMSTART	EREFMCP	MQA1
SHIFT	\$\$BUFLD1	PJD4	SUMSWBYT	EREF	MAD4
SHIFTSET	EREFRET	MGA3	SUMSWTH	EREF	MAD1

Label	Phase	Location	Label	Phase	Location
SUMSWTH2	EREP	MAD2	TRASDAID	\$\$BSDAID	GFB1
SUMTPCUA	EREP	MAD5	TRCIGNIC	PDAID	AHF1
SUPPFT1	REALDUMP	HMB5	TRMODCUR	\$\$BSDAID	GFE4
SUPRZ	ESI VUT	LAJ4	TRSAVE	RFEP	MAB2
SVCCNVT	PDAID	AJC3	TRTDO	SYSBUFLD	PEE4
SVCENT	PDAIDQTT	EMC5	TRY	PDAIDITP	CCD3
SVCENTER	PDAIDFTW	DAB1	TRY	PDAIDITT	CEG2
SVCENTRY	PDAIDGTW	EAD1	TRY	PDAIDFTP	DCB2
SVCENTRY	PDAIDGTP	EDB3	TRY	PDAIDFTT	DEB2
SVCENTRY	PDAIDGTT	EGB3	TRY	PDAIDGTP	ECJ2
SVCENTRY	PDAIDQTW	EJB4	TRY	PDAIDGTT	EFJ2
SVCENTRY	PDAIDQTT	EMB4	TRY	PDAIDQTT	ENG4
SVCERR	PDAID	AJF2	TRY	PDAIDTDT	FAD5
SVCFRCD	PDAID	AJB5	TRY	PDAIDTDP	FCD5
SVCGIT	PDAID	ABB4	TRYPT	PDAIDITP	CCF1
SVCIGN	PDAID	ABC3	TRYPT	PDAIDFTP	DBG3
SVCINIT	PDAIDGTW	EBB3	TRYPT	PDAIDFTT	DDJ3
SVCINIT	PDAIDGTP	EDC5	TRYPT	PDAIDGTP	FCD2
SVCINIT	PDAIDGTT	EGB5	TRYPT	PDAIDGTT	EFD2
SVCINIT	PDAIDQTW	ELB1	TSTBYT	SDPAR	GRF1
SVCINIT	PDAIDQTT	EMA3	TSTBYTRT	SDPAR	GCB1
SVCK	PDAIDGTP	EEE4	TSTFLLP	SDPAR	GDJ1
SVCP1	PDAIDGTW	EAE1	TSTTYP	SDPAR	GDD1
SVCP1	PDAIDGTP	EDB2	TST3203	\$\$BUFLDR	PHK1
SVCP1	PDAIDGTT	EGB2	TST3211	SYSBUFLD	PBB2
SVCRESP	PDAID	AJD1	TYPE	EREPMNTR	MBD3
SVCSCD	PDAID	ABB3	TYPEXCP	SYSBUFLD	PEB5
SVDUMP	\$\$BDAID	GMB4	TYPINRTN	PDAID	AGE4
SWOFF	REALDUMP	HJB3	TYPOUT	SDPAR	GBC1
SWTCHTST	PDAIDGTW	EBD4	TYPOUTRT	PDAID	AGB1
SWTCHTST	PDAIDGTP	EEC5	TYPRTURN	SYSBUFLD	PAK3
SWTCHTST	PDAIDGTT	EHC5	TYP2RTRN	SYSBUFLD	PBD1
SW1	PDAID	AKC4			
SW14	PDAID	AFK5	UCBLOAD	UCBLOAD	PCB1
SW15	PDAID	AJC5	UNCKREAD	EREPUNIT	MKA3
SW6	PDAID	AKC5	UNCKRTRN	EREPUNIT	MKA2
SW8	PDAID	AKG3	UNCKSKI P	EREPUNIT	MKA4
SYSRTURN	SYSBUFLD	PAE4	UNITPUB	SYSBUFLD	PAJ5
			UNPACK	PDAIDTDT	FAE3
TAPEIO	EREPHIST	NJA4	UNPACK	PDAIDTDP	FCE3
TAPEFEAD	EREPEDIT	MEF1	UNPACK1	REALDUMP	HJE1
TAPERREAD	EREP SMES	NDB3	UNPAKT	PDAIDITP	CCC1
TAPE10	EREP SMES	NDC1	UNPK	PDLIST	EDF4
TAPLABL	LSERV	KBJ2	UNPKHEX	EREPEDIT	MEC4
TDGO	PDAID	ADG5	UNPK1	PDLIST	BCF4
TDMPFEC	PDLIST	BCJ1	UNRECSET	PDLIST	BCB2
TESTIGN	\$\$BUFLD2	PKE1	UPDATCTR	EREP SMTR	MRB1
TEST1	PDAIDFTW	DAD2	UPDATE	REALDUMP	HHJ1
TEST1	PDAIDFTP	DBB4	UPDRCTPR	EREP SMTR	MRA3
TEST1	PDAIDFTT	DDB4	UPDT	EREP SMTP	MTA5
TEST1	PDAIDGTW	EAH1	UPDTFWRD	EREP SM**	MSB1
TEST1	PDAIDGTP	EDC2	UPDTFWRD	EREP SMTP	MTB2
TEST1	PDAIDGTT	EGC2	UPDTHWNO	EREP SMTP	MTB1
TEST1	PDAIDQTW	EJG4	UPDTLOOP	EREP SM**	MSA5
TEST1	PDAIDQTT	EME5	UPDTLOOP	EREP SMES	NDC4
TEST2	PDAIDFTT	DDC4	UPDTWRTN	EREP SM**	MSB2
THEPUT	PDLIST	BAG4	UPHDCYL	EREP SMCP	MQB1
TIME	EREP MNTR	MBC2	UPREG	PDLIST	BBG3
TIME	EREPEDIT	MEC1			
TIO1	REALDUMP	HGF3	VARRPL	SDPAR	GDH3
TIO1	REALDUMP	HJD3	VERMOD	DUMPGEN	HEB4
TRACKS	EREP	MAB3	VIRTAB	\$\$BDAID	GMJ2
TRAILERS	EREPESTR	NGC2	VISASN	PDSDM	JJC2
TRAILERS	EREPHIST	NJC3	VOLID	EREPRETV	MGC2
TRANLOAD	EREP2715	MLC2	VSBBOX	REALDUMP	HXE5
TRANSFCH	PDAID	AHF4	VSCCB	REALDUMP	HWC1

Label	Phase	Location	Label	Phase	Location
VSCCE1	REALDUMP	HWE1	ZERO1	DUMPGEN	HEE5
VSCCB2	REALDUMP	HWH1	ZERO2	DUMPGEN	HEF5
VSCCB3	REALDUMP	HWD3	ZERO3	DUMPGEN	HEG5
VSCCE4	REALDUMP	HWC2			
VSCCW	REALDUMP	HWJ3			
VSCCW1	REALDUMP	HWB5			
VSCCW2	REALDUMP	HWC5			
VSCCW3	REALDUMP	HWF5			
VSCCW4	REALDUMP	HWG5			
VSCCW5	REALDUMP	HXB1			
VSCCW6	REALDUMP	HXC1			
VSCCW7	REALDUMP	HXD1			
VSCCW8	REALDUMP	HXE1			
VSFIX	REALDUMP	HXF3			
VSFIX5	REALDUMP	HXD5			
VSFOEMBG	REALDUMP	HWB1			
VSIDA	REALDUMP	HXF1			
VSIDA1	REALDUMP	HXJ1			
VSIDA2	REALDUMP	HXC3			
VSIDA5	REALDUMP	HXH2			
VSPF1	REALDUMP	HYB3			
VSPFT1	REALDUMP	HYD3			
VSPFT19	REALDUMP	HYE3			
VSRPN1	REALDUMP	HXB5			
VSSELP	REALDUMP	HYG3			
VSSTET	REALDUMP	HXH5			
VSSTPT1	REALDUMP	HYC1			
VSSTPT10	REALDUMP	HYB2			
VSSTPT2	REALDUMP	HYE1			
VSSTPT4	REALDUMP	HYG1			
VUPD1V	EREFSMTP	MTB3			
WAIT	\$\$BPD AID	AMP4			
WAIT	PDAIDITP	CCG3			
WAIT	PDAIDITT	CEK2			
WAIT	PDAIDGTP	ECE5			
WAIT	PDAIDGTT	EFE5			
WAIT	PDAIDQTT	ENK4			
WAIT	PDAIDTDT	FBB1			
WAIT	PDAIDTDP	FDB1			
WAIT	EREPHIST	NJD3			
WAITEUT	PDLIST	BAJ4			
WANTPRNT	PDAIDGTT	EHH1			
WANTERT	PDAIDITP	CBG5			
WANTFRT	PDAIDITT	CDG5			
WANTERT	PDAIDQTT	ENH1			
WHICH	PDAIDFTP	DCF4			
WHICH	PDAIDFTT	DEF4			
WHICH	PDAIDGTP	ECF3			
WHICH	PDAIDGTT	EFF3			
WHICH	PDAIDQTT	ENC5			
WHICHAID	PDAID	AAB2			
WHICHONE	PDAID	AAH1			
WHICHONE	DUMFGEN	HEC4			
WIND	EREPESTP	MFA5			
WRAPUP	PDAIDGTW	EBJ5			
WRAPUP	EREPRDE	NLA5			
WRITE	ERFETES	NFB4			
WRITE	EREPHIST	NJC2			
WRITMSG	PDSDM	JFF1			
WRONGTAPE	EREPESTR	NGA4			
WRONGTP	EREPHIST	NJB1			
X203	UCBLOAD	PCD2			
ZERO TAB	EREPRDE	NKB2			





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