

October through December 1963

Recent IBM Patents

October 1963

Random Access Data Storage Access Mechanism	G. H. May	3,105,680
Two-Step Serial Adder	K. A. Bell and J. S. Conzola	3,105,898
Decision Element Circuits	A. Rose and J. E. Parton	3,105,923
Character Recognition System	E. C. Greanias and A. Hambrun	3,105,956
Memory Systems	E. J. Slobodzinski	3,105,958
Transducer Positioning System	L. D. Stevens and J. B. Clark	3,105,963
Magnetic Shift Register	M. K. Haynes and N. F. Lockhart	3,106,702
Recording and Printing Apparatus	K. N. Kapur	3,106,881
Document Preparation Device	G. F. Daly and F. Saltz	3,107,292
Pulse Responsive Photosensitive Electro-optical Circuit	D. D. Willard	3,107,301
Comparing Circuit	K. B. Day, Jr. and R. G. Rinaldi	3,107,339
Editing Machine	E. Estrems and M. Papo	3,107,342
Information Retrieval System	A. L. Poole	3,107,343
Esaki Diode Memory with Diode Coupled Readout	A. J. Gruodis	3,107,345
Data Storage Apparatus	D. P. Darwin, D. K. Rex and R. M. Kellogg	3,107,346
Method of Forming Circuit Cards	J. L. Sterling	3,107,414
Thermoscope	H. T. Johnston, Jr.	3,107,529
Stacking Device	M. Fiehl	3,107,912
Technique for Processing Ferrite Cores	J. M. Brownlow	3,108,074
Storage Register	K. E. Schreiner	3,108,193
Parametron System	P. F. Wu	3,108,195
Series Combination of Matched Tunnel Diode Pair, Pulse Delay Means and Push-Pull Power Supply	A. S. Farber	3,108,199
Tunnel Diode Controlled Transistor Regulator	S. A. Butler and H. S. Yourke	3,108,218
Delay Compensation by Distributed Synchronous Pulses	G. L. Clapper	3,108,228
Logical Clearing of Memory Devices	W. Buchholz and L. E. Kanter	3,108,256
Locking and Unlocking of Memory Devices	W. Buchholz	3,108,257
Communicator	W. R. Uttal	3,108,268
Closed Loop Control System	J. L. Sweeney	3,108,272
High Speed Printer	M. Preisinger	3,108,534
Frequency Responsive Printing System	H. P. Luhn	3,109,058
Electrostatic Writing and Printing Device	C. K. Clauer and J. D. Kuehler	3,109,062
Data Boundary Cross-Over and/or Advance Data Access System	W. Wolensky	3,109,162

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Wear Resistant Magnetic Recording Media	L. di Ricco	3,109,749
Resonant Logic Circuits Employing Tunnel Diodes	R. F. Penoyer	3,109,935
Method of Making Plural Layer Thin Film Devices	B. I. Bertelsen	3,110,620
Wire Printer	L. Scharff	3,110,884
Superconductive Solderless Connector	N. Theodoseau	3,111,352
Methods for Making Record Materials	R. M. Lindquist and Z. Reyes	3,111,407
Electrodeposition of Magnetic Cobalt-Nickel Alloys	I. Tsu and M. C. Fritsch	3,111,463
Accounting System	L. D. Lipschutz	3,111,576
Utilizing Predicted Parity	F. Gerrand and G. Thomas	3,111,578
Graded Energy Gap Semiconductor Devices	L. P. Hunter	3,111,611
Superconductive Parametric Amplifier	R. W. Landauer	3,111,628
Conversion Apparatus	E. R. Marsh and W. D. Minnich	3,111,648
Capacitor Digital Data Storage and Regeneration System	W. N. Carroll	3,111,649
Superconductive Ring Circuit	J. L. Anderson	3,111,650
High Speed Thin Magnetic Film Memory Array	N. C. Ford, Jr.	3,111,652
Method and Apparatus for Photographically Recording Numerical Values	A. H. Dickinson	3,112,152
Arithmetic Circuitry	J. E. Heywood	3,112,396
Tape Storage Apparatus for Tape Processing Units	H. P. Wicklund and H. A. Panissidi	3,112,473

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Metal Plating Procedure	H. E. MacCormack	3,113,035
Electronic Circuits	G. H. Perry and E. W. Shallow	3,113,296
Binary Information Transfer Device	W. Dietrich	3,113,297
Type Belt Device	V. R. Simpson	3,113,509
Printing-Roller Etching Machine of the Spray Type	J. Bock, E. V. Hartman and P. A. Tatusko	3,113,901
Form-Reading and Information-Converting Apparatus	R. W. Avery	3,114,035
Self-Clocking System for Binary Data Signal	C. M. Melas	3,114,109
Single Transistor Magnetic Multivibrator Amplifier	J. E. Taunt	3,114,115
Single Error Correcting System Utilizing Maximum Length Shift Register Sequences	N. M. Abramson	3,114,130
Single Track Character Sensing	R. J. Furr, J. E. Pierpont, Jr., G. L. Shelton, Jr. and J. A. Weidenhammer	3,114,131
Magnetic Core Matrix	G. N. West	3,114,133
Switching Circuit	L. D. Seader	3,114,134
High Speed Memory	W. D. Pricer	3,114,135
Analog to Digital Converter	J. J. Woo	3,114,144
Electrical Contact Formation	G. R. Gunther-Mohr and N. N. Winogradoff	3,114,195
Educational Device	W. R. Uttal	3,114,210
Fluid Devices for Computers	H. H. Glaettli	3,114,390
Fluid Apparatus	H. A. Panissidi	3,114,394
Pulse Generator	H. R. Foglia	3,114,843
Superconductor Commutator Circuits	N. H. Meyers	3,114,845
Reversible Electronic Counter	M. E. Arthur	3,114,833
Signaling System	A. P. Arneth, Jr.	3,114,894
Superconductive Transmission Line Memory Utilizing Reflectrons	M. J. Freiser and J. C. Swihart	3,114,895
Fluid Control Means	W. R. Stewart	3,115,012
Snap-In Housing	W. E. Stark	3,115,308
High-Speed Multiplier	G. T. Paul, R. J. Bahnsen and J. F. Dirac	3,115,574
Information-Gated Flip-Flop Adapted to Generate Output in Response to Millimicrosecond Sampling Pulse from Blocking Oscillator	D. J. Hinkein	3,115,583
Raster Scanning System	J. L. Sweeney	3,116,436
Peak Sensing System Employing Sampling and Logic Circuits Converting Analog Input to Polarity-Indicating Digital Output	W. P. Margopoulos	3,116,458
Photoconductive-Electroluminescent Information Storage Apparatus	D. H. Smith and G. R. Hoffmann	3,116,474