

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

France

387,462	P. Lemble	Electronic document approval system
394,596	C. Garcia, G. Carle, J. Calvignac, M. Badoui, and P. Vachee	Control interface mapping on minimum bandwidth for BMI
477,455	A. Blanc, G. Underboeck, and J. C. Abbate	Data circuit terminating equipment (DCE) including timing arrangements circuits controlled by processing means
562,207	C. Cederbaum, M. Combes, P. Mone, and R. Chancelou	Method of forming thin film pseudo-planar PFET devices and structures resulting therefrom

Germany

4,320,941.6	G. Schuettler and W. Christiansen	Facility and method for the handling of objects
4,445,386.8	W. Haenel	Winding-on device for light conductor
59,302,807.4	B. Swoboda, I. Wild, and J. Schwarze	Method for separation of a foreground information from a background information in a document
69,120,282.6	H. Horbach and P. Fink	Direct chip attach concept

Japan

2,051,692	M. A. Ono	A vertical menu system
2,051,693	D. Frank, D. C. Frank, H. Satoh, S. Matsubara, S. Thompson, and S. P. Thompson	Method for displaying a separator line
2,054,494	H. Ishikawa and K. Kawase	A compliance control method by using wrist and arm
2,054,514	K. Koyamada	New visualization primitive for 3D scalar field
2,054,526	A. Doi and A. Koide	A method for high speed generating images
2,054,537	L. D. Tipton and M. Takekoshi	Seek method for spiral media
2,054,552	K. Hokazono, T. Shimofusa, and Y. Amano	Driving method for laser
2,056,007	S. Kiyono	A method to produce insulation area on bus-bar leads for semiconductor packages
2,056,776	H. O. Jung-Kook, J. U. Takahashi, and M. Kusaba	Video scene information entry system
2,521,020	K. Inoue, K. Itoh, and T. Inui	Automatic initialization of operating system and application programs when the system configuration is changed
2,521,026	M. Kobayashi, M. Naitoh, and R. Kataoka	Monitor interface architecture for black level transfer
2,521,027	K. Kawase, M. A. Ono, and T. Moriyama	Effective task distribution and execution mechanism for PHIGS processing on multiprocessor
2,524,477	H. Kamiya, H. Ono, and S. Suzuki	Liquid crystal panel fabrication method (panel alignment)
2,526,012	H. Sugi	Dual keyboard operation

2,528,585	I. Yoneda, S. Yamamoto, and T. Ueda	Low power servo control algorithm in constant velocity seeking mode
2,530,113	H. Asano and M. Murakami	ATA DREQ timing
Switzerland		
553,593	E. Courtens and J. D. Mannhart	Pinning structures for superconducting films and method for making same
United Kingdom		
466,692	A. R. Hearn, D. J. Sebborn, G. N. Dew, and J. T. Young	Disk file including means for ensuring disk/hub concentricity
516,900	P. J. Lupton and S. M. Baker	Data backup and recovery in a data processing system
541,870	P. Warren	Image processing apparatus with improved data conversion testing
United States		
5,502,333	C. L. Bertin, E. L. Hedberg, and W. J. Howell	Semiconductor stack structures and fabrication/sparing methods utilizing programmable spare circuit
5,502,390	F. W. Olsen	Adiabatic conductor analyzer method and system
5,502,392	A. Deutsch, G. Arjavalasingam, G. V. Kopesay, and J. K. Tam	Methods for the measurement of the frequency dependent complex propagation matrix, impedance matrix and admittance matrix of coupled transmission lines
5,502,400	G. Williams II, R. Lynch, R. A. Wood, and R. R. Livolsi	Logically configurable impedance matching input terminators for VLSI
5,502,458	G. W. Braudaway and H. R. Delp	Method and apparatus for creating and displaying faithful color images on a computer display
5,502,667	C. L. Bertin, E. L. Hedberg, G. A. Kelley, H. L. Kalter, and W. J. Howell	Integrated multichip memory module structure
5,502,673	S. R. Riggio	Non-volatile register system utilizing thin-film floating-gate amorphous transistors
5,502,683	A. Marchioro	Dual ported memory with word line access control
5,502,711	A. R. Clark, A. S. Patapoutain, and R. A. Hutchins	Dual digital phase locked loop clock channel for optical recording
5,502,728	T. B. Smith	Large, fault-tolerant, non-volatile, multiported memory
5,502,731	D. Meltzer	Delay test coverage without additional dummy latches in a scan-based test design
5,502,732	J. Hinojosa, R. X. Arroyo, T. A. Gruwell, and W. E. Burky	Method for testing ECC logic
5,502,774	D. Kanevsky and J. Bellegarda	Automatic recognition of a consistent message using multiple complementary sources of information
5,502,811	R. A. Ripberger	System and method for striping data to magnetic tape units
5,502,826	B. Blaner and S. Vassiliadis	System and method for obtaining parallel existing instructions in a particular data processing configuration by compounding instructions
5,502,833	G. S. Delp and J. W. Byrn	System and method for management of a predictive split cache for supporting FIFO queues
5,503,286	H. A. Nye III, H. M. Tong, J. F. Roeder, and P. A. Totta	Electroplated solder terminal
5,503,698	C. A. Kovac, H. Ito, M. Goldberg, M. J. Palmer, P. A. Poore, and R. A. Pollak	Bonding method employing organometallic interconnectors
5,503,951	D. S. O'Grady and S. D. Flanders	An attenuating phase-shift mask structure and fabrication method
5,503,961	H. Linde, R. A. Previti-Kelly, and T. J. Reen	Process for forming lift-off structures in negative photosensitive polyimide films
5,504,362	G. Fiorenza, M. J. Saccamango, M. M. Pelella, and R. W. Young	Electrostatic discharge protection device
5,504,375	K. N. Tu, L. Shi, and W. H. Carlson	Asymmetric studs and connecting lines to minimize stress
5,504,434	D. J. Schepis and K. Seshan	Instrument for the measurement of electrical characteristics during manufacturing processes
5,504,441	L. J. Sigal	Two-phase overlapping clocking technique for digital dynamic circuits
5,504,459	J. E. Gersbach and T. Williams	Filter network for phase-locked loop circuit
5,504,611	G. Miracle, P. Potvin, and Q. J. Carbone	Apparatus for repowering and monitoring serial links
5,504,612	F. Topolovec, J. Covell, J. M. Cobb, and U. A. Ziemins	Synchronized XY laser scanner
5,504,675	B. J. Cragun, S. Lund, and T. M. Kelsey	Method and apparatus for automatic selection and presentation of sales promotional programs
5,504,757	A. B. McNeill and S. E. Cook	Method for selecting transmission speeds for transmitting data packets over a serial bus

5,504,828	D. L. Karst, J. Trehwella, M. Cina, M. Oprysko, and M. B. Ritter	Apparatus for extending bandwidth of large core fiber optic transmission links
5,504,846	J. O. Fisher	Method and apparatus for improved area demarcation in bit mapped image derived from multi-color bit mapped image
5,504,857	A. Kamerman, A. S. Lett, G. Bozman, G. Eisenberger, J. J. McAssey, J. J. Myers, P. S. Wang, R. R. Baird, and W. H. Tetzlaff	Highly available fault tolerant relocation of storage with atomicity
5,504,859	C. F. Webb, J. S. Liptay, and R. N. Gustafson	Data processor with enhanced error recovery
5,504,861	D. L. Jaworski, R. M. Kern, and R. N. Crockett	Remote data duplexing
5,504,878	A. S. Coscarella and R. J. Gallagher	Central time-of-day reference system with synchronism of local time-of-day clocks over non-dedicated serial data links
5,504,882	C. J. Xydes, C. Y. Ng, J. R. Paveza, L. R. Shipman, Jr., and P. K. Chai	Fault tolerant data storage subsystem employing hierarchically arranged controllers
5,504,886	D. Y. Chang and T. R. Malkemus	System and method for applying user supplied relation definitions to application files for a relational database
5,504,887	A. Malhotra and K. J. Perry	Storage clustering and packing of objects on the basis of query workload ranking
5,504,894	C. N. Nikolaou, D. Ferguson, and L. Georgiadis	Workload manager for achieving transaction class response time goals in a multiprocessing system
5,504,904	P. E. Newman and R. A. Dayan	Personal computer having operating system definition file for configuring computer system
5,504,905	C. Lay, F. C. Stanley, J. W. Cleary, M. E. Medford, and M. R. Primm	A method and apparatus for communicating system configuration information in an information handling network
5,504,928	J. A. Cook and L. Vepstas	Multiple command set support for rendering components
5,504,932	B. Blaner, S. Vassiliadis, and T. L. Jeremiah	System for executing scalar instructions in parallel based on control bits appended by compounding decoder
5,505,320	F. C. Burns, R. Sebesta, R. L. Lewis, and S. W. Opie	Method employing laser ablating for providing a pattern on a substrate
5,505,780	M. Dalvie and R. S. Hamaguchi	High-density plasma-processing tool with toroidal magnetic field
5,505,816	D. Podlesnik, J. D. Chapple-Sokol, J. H. Keller, M. S. Barnes, T. Cotler, and W. M. Holber	Etching of silicon dioxide selectively to silicon nitride and polysilicon
5,506,080	D. S. O'Grady, J. J. Lynch, J. T. Ohlson, W. C. Joyce, and W. J. Adair	Phase shift mask resist repair process
5,506,457	B. L. Krauter, L. A. Walls, P. J. Klim, S. E. Schuster, and T. H. Ning	Electronic switch for decoupling capacitor
5,506,520	D. J. Frank and P. M. Solomon	Energy conserving clock pulse generating circuits
5,506,528	B. L. Krauter, S. Dutta, T. A. Cao, T. D. Trinh, and T. Q. Nguyen	High-speed, off-chip receiver
5,506,691	C. Curley, D. Mazina, F. J. Yeskel, G. M. Bednar, L. P. Curley, P. L. Olson, and T. E. Carr	Method and apparatus for image processing at remote sites
5,506,753	C. L. Bertin, C. P. Miller, G. A. Kelley, and P. A. Farrar	Method and apparatus for a stress relieved electronic module
5,506,940	J. H. Bamford, J. J. Dimare, J. M. Goncalves, and M. C. Tarquinio	Font resolution method for a data processing system to convert a first font definition to a second font definition
5,506,947	G. Taubin	Curve and surface smoothing without shrinkage
5,506,955	J. C. Ross and J. N. Chen	System and method for monitoring and optimizing performance in a data processing system
5,506,957	L. E. Thatcher, R. E. Fry, and T. N. Hicks	Synchronization for out of order floating point data loads
5,506,961	B. A. Carlson, F. L. Huss, N. M. Schmucki, and R. E. Zelenski	Connection authorizer for controlling access to system resources
5,506,964	B. L. Beukema	System clustering via serially attached remote I/O buses
5,506,972	C. A. Heath, F. Strietelmeier, J. Nicholson, and J. Reid	Computer system having dynamically programmable linear/fairness priority arbitration scheme
5,506,979	J. M. Menon	Method and means for execution of commands accessing variable length records stored on fixed block formatted DASDs of an N+2 DAS
5,506,989	G. D. Boldt and S. D. Hanna	Arbitration system limiting high priority successive grants
5,506,993	B. Fitch, D. Joseph, and M. Giampapa	Message packet transmitter
5,507,028	L. Liu	History based branch prediction accessed via a history based earlier instruction address
5,507,029	C. Selinger, G. F. Miceli, M. A. Granato, V. L. Watts, and Y. Relis	Method for minimizing the time skew of electrical signals in very large scale integrated circuits

5,507,035	C. C. Huang, D. F. Bantz, F. Bauchot, and N. Moulton	Diversity transmission strategy in mobile/indoor cellular radio communications
5,507,668	R. F. Progovitz and W. T. Lambrinos	Cable assembly for multiple electronic components
5,507,872	A. Antenucci, B. Ventimiglia, D. Naugle, J. E. Lambert, J. M. Park, M. Berger, R. Weckesser, S. Olson, and T. Rednour	Contact sensor-based microdispensing tool
5,508,219	G. B. Bronner, J. DeBrosse, and J. A. Mandelman	SOI DRAM with field-shield isolation and body contact
5,508,234	E. J. White and M. Dusablon	Microcavity structures, fabrication processes, and applications thereof
5,508,518	R. A. Kendall	Lithography tool with vibration isolation
5,508,542	D. Kenney and P. J. Geiss	Porous silicon trench and capacitor structures
5,508,543	A. M. Hartstein, M. A. Tischler, and S. Tiwari	Low voltage memory
5,508,614	D. E. Horne, G. A. Garfunkel, and R. L. Smith	Non-contact method for testing for MR shield short circuits
5,508,641	D. Appenzeller and P. Wohl	Integrated circuit chip and pass gate logic family therefor
5,508,645	G. R. Castellucci and T. C. Coughlin, Jr.	Circuit for raising a minimum threshold of a signal detector
5,508,660	I. I. Novof and J. E. Gersbach	Charge pump circuit with symmetrical current output for phase-controlled loop system
5,508,664	R. P. Rizzo	Oscillator having charge/discharge circuits with adjustment to maintain desired duty cycles
5,508,732	F. R. Moore, H. D. Chadwick, J. F. Bottomley, J. M. Hall, J. T. Powers, M. A. Putterman, M. L. Schaszberger, R. Williams, and R. W. Withers	Data server, control server and gateway architecture system and method for broadcasting digital video on demand
5,508,734	G. A. Suarez, K. A. Kettler, K. A. Uplinger, and R. G. Baker	Method and apparatus for hemispheric imaging which emphasizes peripheral content
5,508,803	M. Hibbs and W. C. Joyce	Method and apparatus for monitoring lithographic exposure
5,508,829	D. D. Nolte, J. L. Freeouf, J. M. Woodall, M. R. Melloch, P. D. Kirchner, and R. T. Hodgson	LTG AlGaAs and GaAs non-linear optical material and devices fabricated therefrom
5,508,855	A. S. Patapoutain and R. A. Hutchins	Tracking threshold circuit for peak detection
5,508,859	D. W. Lloyd, J. C. Simkins, and P. Y. Hu	Gravity feed data storage library having magnetic picker
5,508,865	A. Panasiuk, A. Sanford, A. M. Guzman, J. L. Garcia, L. K. Whitted, and P. Y. Hu	Head guide assembly having improved resonance response