# Authors


### David M. Chess

*Research Division, Yorktown Heights, New York*


Mr. Chess joined IBM in July 1981 at the Thomas J. Watson Research Center, having worked there the previous summer as a student. Currently, he is working for the consulting, education, and performance management group on maintenance and development of performance tools in VM/370. He is involved particularly with RESLIM and PRISET (the VM Priority Changer). Mr. Chess received a B.A. in philosophy from Princeton University in 1981.


### Ken Christensen

*General Products Division, San Jose, California*


Mr. Christensen is manager of the Programming Development Process Department at the IBM Santa Teresa Laboratory. Since joining IBM in 1962, he has had various assignments in planning and developing software products. Since 1977, he has monitored programming development practices at the Santa Teresa Laboratory, with emphasis on installation of state-of-the-art processes. His interests have centered on measurement systems for statistical views of quality and productivity levels. He was granted an Outstanding Achievement Award for his work on formulation of programming development processes at the Santa Teresa Laboratory.


### George P. Fitsos

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Mr. Fitsos joined IBM in 1962 in Los Angeles. Prior to 1979, he worked on various assembler products in Los Angeles and San Jose, where he received an Outstanding Contribution Award for his work on the 1800 macro assembler. For the past two years he has been investigating practical usage of software science. His primary interest has been to decompose or translate various measurements as they relate to programming styles or disciplines. Mr. Fitsos has worked on several data management projects in both technical and management assignments. Most recently, he was part of an engineering group dealing with direct access storage architecture. He is a part-time instructor at San Jose University. Mr. Fitsos attended the University of Southern California, where he obtained his B.S. degree in mathematics.


### Philip H. Joslin

*Information Systems Group, Gaithersburg, Maryland*


Mr. Joslin joined IBM in 1960 and has worked in the design and development of software systems for military and commercial applications. Since 1973 he has concentrated in the area of display-oriented interactive programs. Mr. Joslin managed the programming group that developed the original Structured Programming Facility, and continues to play a lead role in the design and development of the System Productivity Facility. He received his A.B. degree in physics from Harvard College.

**Charles P. Smith**

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Mr. Smith is currently investigating software science measurement approaches and determining applicability to IBM programming products. He came to this assignment after establishing productivity tracking systems for the IBM Santa Teresa Laboratory. Mr. Smith joined IBM in 1965. He has had experience in education systems research, instructional systems development, and compiler and interpreter development, and has been both a manager and a programmer in support organizations for data management and control program development. He is a part-time instructor at San Jose State University, where he teaches BASIC, COBOL, PASCAL, and APL. He received his education at the University of San Francisco, San Jose State University (where he received a B.A. degree), and Stanford University.

**Arvind J. Thadhani**

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Mr. Thadhani is an advisory programmer in information systems in the General Products Division. He joined IBM in 1968 as a junior engineer in Poughkeepsie, where he was involved in the development of the System/360 Model 155. He held system design, system analysis, and technical planning positions for large systems, also in Poughkeepsie. In 1976, he was assigned to the San Jose Research Laboratory, where he was involved in the investigation of storage system architecture. Since 1978, he has been in the General Products Division headquarters, working in technical planning, and recently in systems productivity. Mr. Thadhani received a B.S. in electrical engineering from the Indian Institute of Technology, Bombay, in 1966. He received an M.S. in electrical engineering from Cornell University in 1968 and an M.S. in computer science from the University of Wisconsin, Madison, in 1972.

**Gerald Waldbaum**

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Dr. Waldbaum was a member of the technical staff at Bell Telephone Laboratories, working on the design and development of the Number 1 Electronic Switching System, from 1959 until 1966 when he joined IBM at the Thomas J. Watson Research Center. From 1966 to 1967, he worked as a systems programmer, measuring and improving TSS performance and helping to implement the first version of APL that ran under OS. Since that time, he has been active in system management areas dealing with performance, capacity, computer usage budgeting, planning, data processing security, consulting, and education. He is presently manager of consulting, education, and performance management as well as being the Research Division Program Manager of Systems Management Controls. Dr. Waldbaum has taught courses in computer science at Iona College Graduate School of Business and is coauthor of the book *Electronic Switching Theory and Circuits*. He received his B.A. and B.S.E.E. degrees from Columbia University and his M.S.E.E. and Ph.D. in operations research from New York University.