

- NOTES:
- CROSS REFERENCING—FOR SIGNAL SOURCES SEE SHEETS 39 TO 42
  - PIN NUMBERS USED MORE THAN ONCE ARE SHOWN BRACKETED
  - WITH BRCK SOURCES SUCH AS 18/4/37\* THE ASTERISK DENOTES THAT 37 APPLIES TO THE PIN NUMBER AND NOT THE MONITORING POINT

BLOCK DIAGRAM 1900 SCIENTIFIC UNIT

PROCESSOR

ISS.

1	P 445-10	10.66
3	1150 K6mk	1.67



1900 SCIENTIFIC UNIT LOGIC

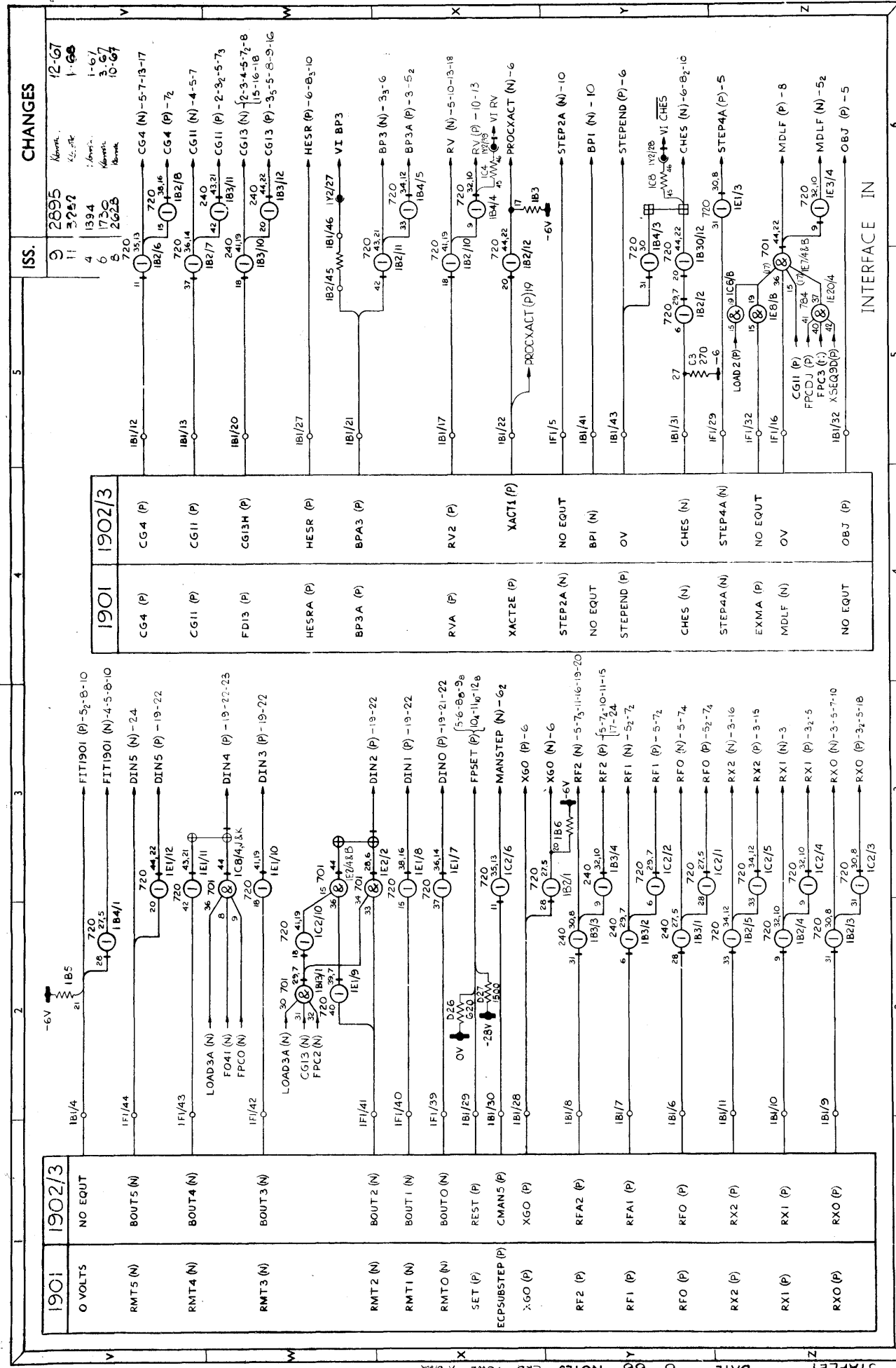
DIAGRAM

ISSUE

SHEET 1

ISSUE

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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ISS.	CHANGES
9	2895
11	3722
4	1394
6	1750
5	2625
720	

1900 SCIENTIFIC LOGIC UNIT  
 5016360 / 11 2  
 DIAGRAM SHEET  
 ISSUE SHEET  
 CAT D  
 ISSUE SHEET  
 ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.  
 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30  
 DATE 8-66  
 DRAWN BY STAPLEY  
 NOTES  
 CKD  
 POWER  
 X-track







1900  
SCIENTIFIC UNIT  
LOGIC

5016360  
DIAGRAM

ISSUE / 11

SHEET 5

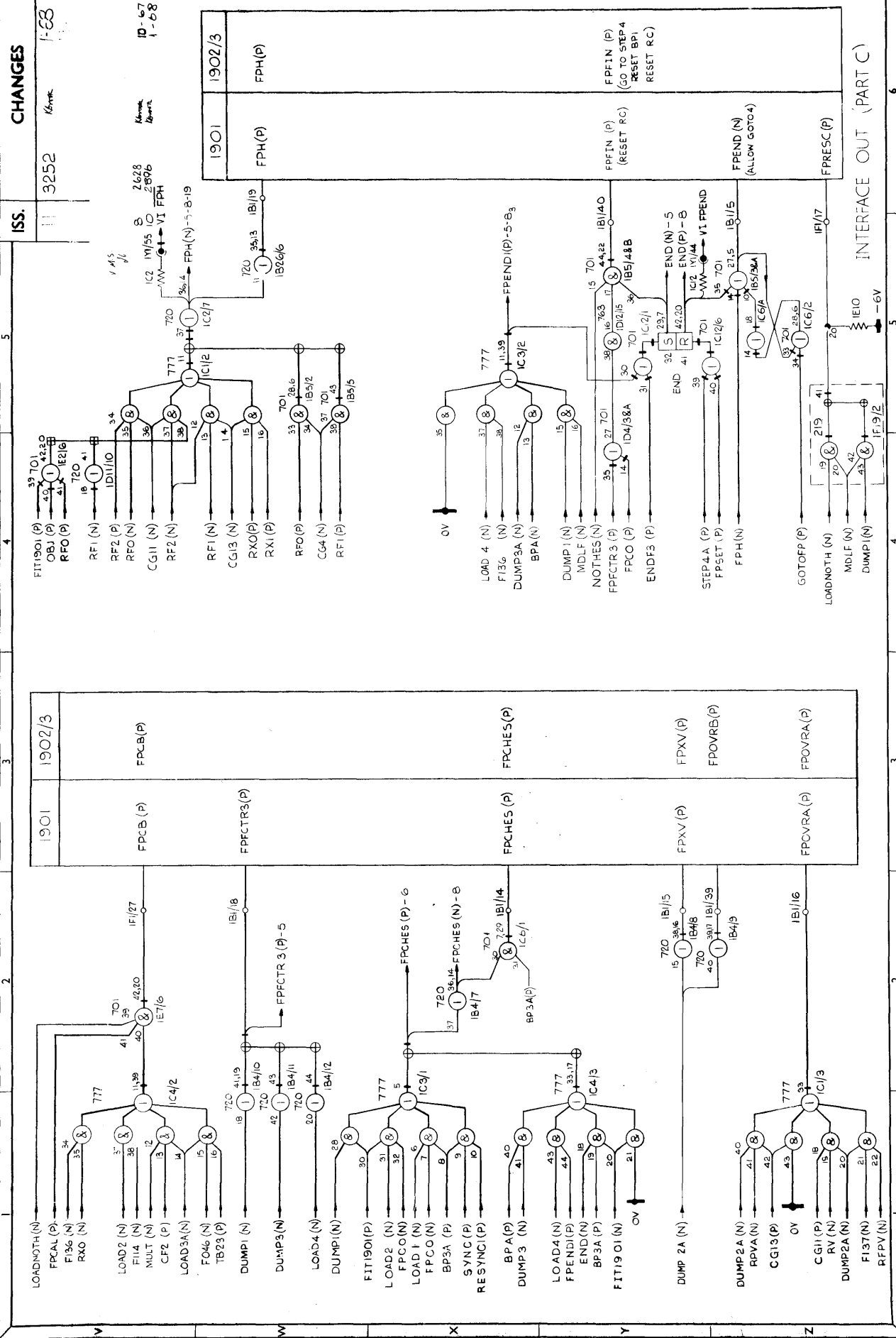
D CAT.

ISSUE

SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
-------	---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

ISS. 3252

CHANGES  
1-68



ISS. 3252

CHANGES  
1-68

DRAWN BY S A COOPER

DATE SEPT 66

NOTES CHK PLUCK

Keark

813128 E







1900  
SCIENTIFIC UNIT  
LOGIC

5016360  
DIAGRAM

ISSUE / 11

SHEET 8

D CAT

ISSUE

SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

DATE

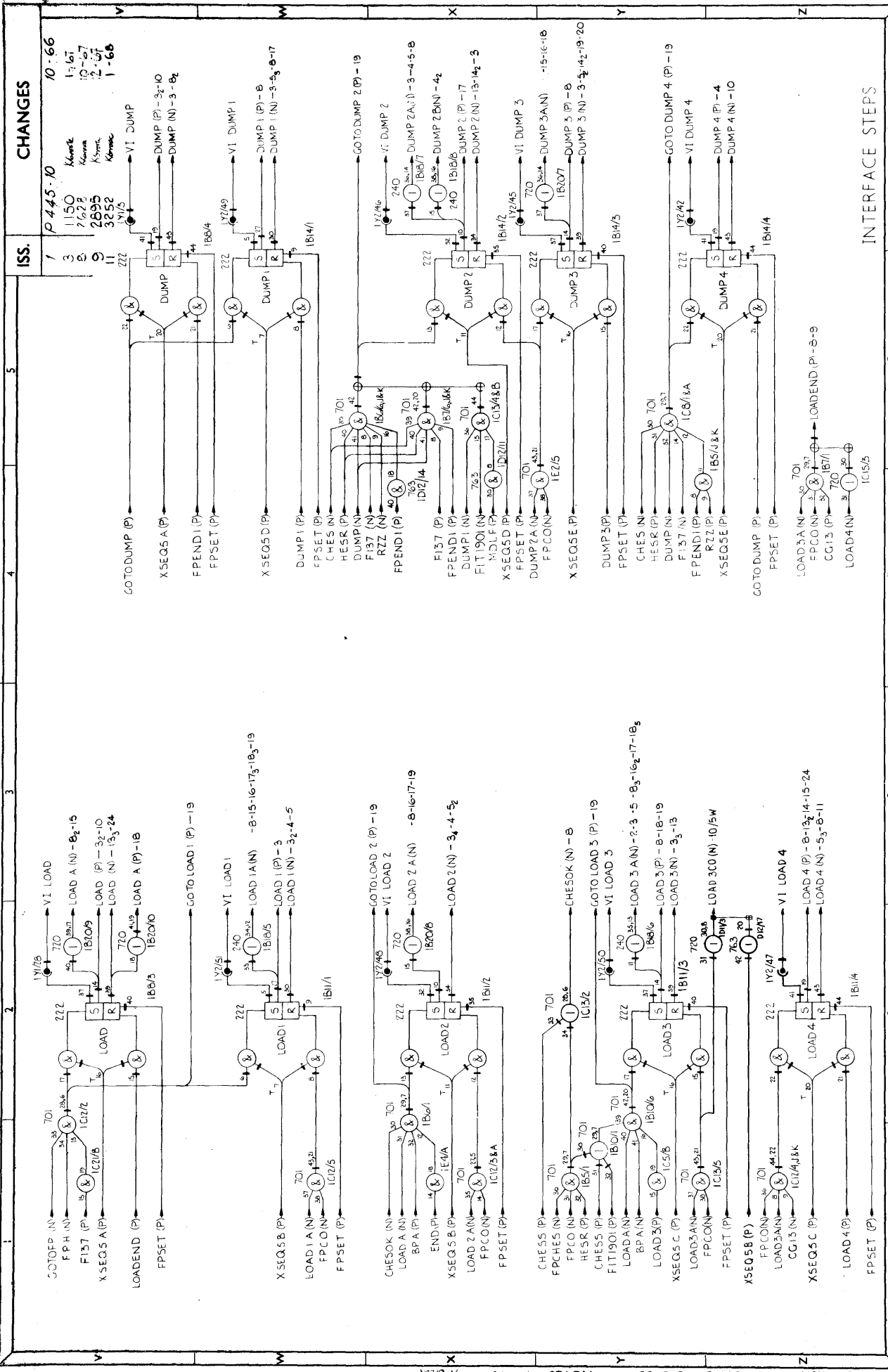
DRAWN BY R. J. OWEN

DATE 10-8-66

NOTES

CPD POWE

K-614



ISS.

CHANGES

7 P 4.45.10  
3 1150  
1-167  
2-622  
9 2895  
11 3252  
1-68

1-167  
1-67  
2-622  
1-68

VI DUMP  
DUMP (P) - 3-10  
DUMP (N) - 3 - 8<sub>2</sub>  
VI DUMP 1  
DUMP 1 (P) - 8  
DUMP 1 (N) - 3-5-8-17

GOTO DUMP 2 (P) - 19  
VI DUMP 2  
DUMP 2A (N) - 3-4-5-8  
DUMP 2B (N) - 4-2  
DUMP 2 (P) - 17  
DUMP 2 (N) - 13-14-5

VI DUMP 3  
DUMP 3A (N) - 15-16-18  
DUMP 3 (P) - 8  
DUMP 3 (N) - 3-5-4-2-9-20

VI DUMP 4  
DUMP 4 (P) - 4  
DUMP 4 (N) - 10

INTERFACE STEPS

ISSUE





1900  
SCIENTIFIC UNIT  
LOGIC

5016360  
DIAGRAM

ISSUE  
8

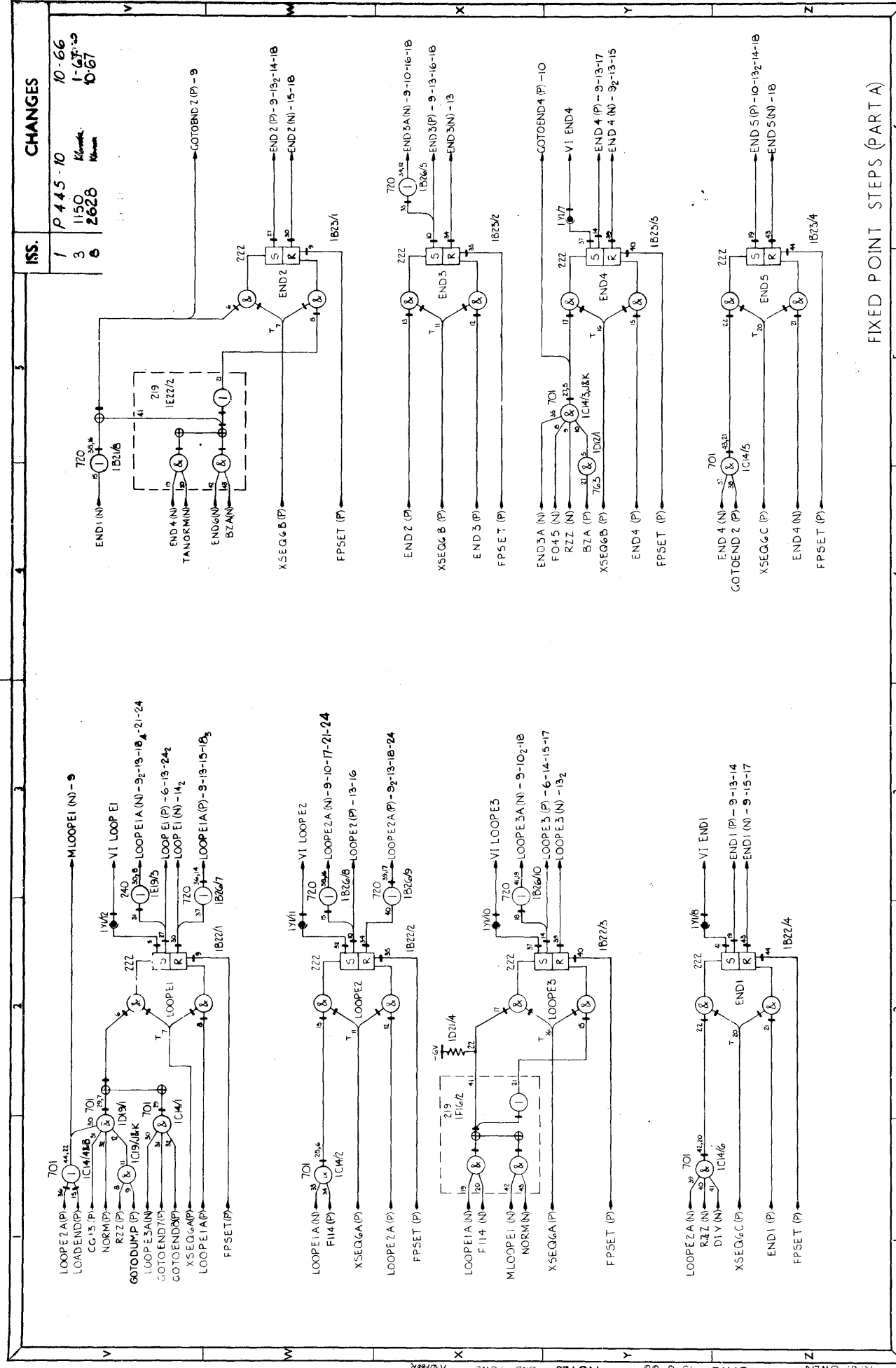
CAT

SHEET  
9

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30		

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

FIXED POINT STEPS (PART A)



ISS.	CHANGES
1	P 445-10 10-66
3	1150 1-67
6	2628 10-67



1900  
SCIENTIFIC UNIT  
LOGIC

DIAGRAM

5016360 / 11

ISSUE

D

10

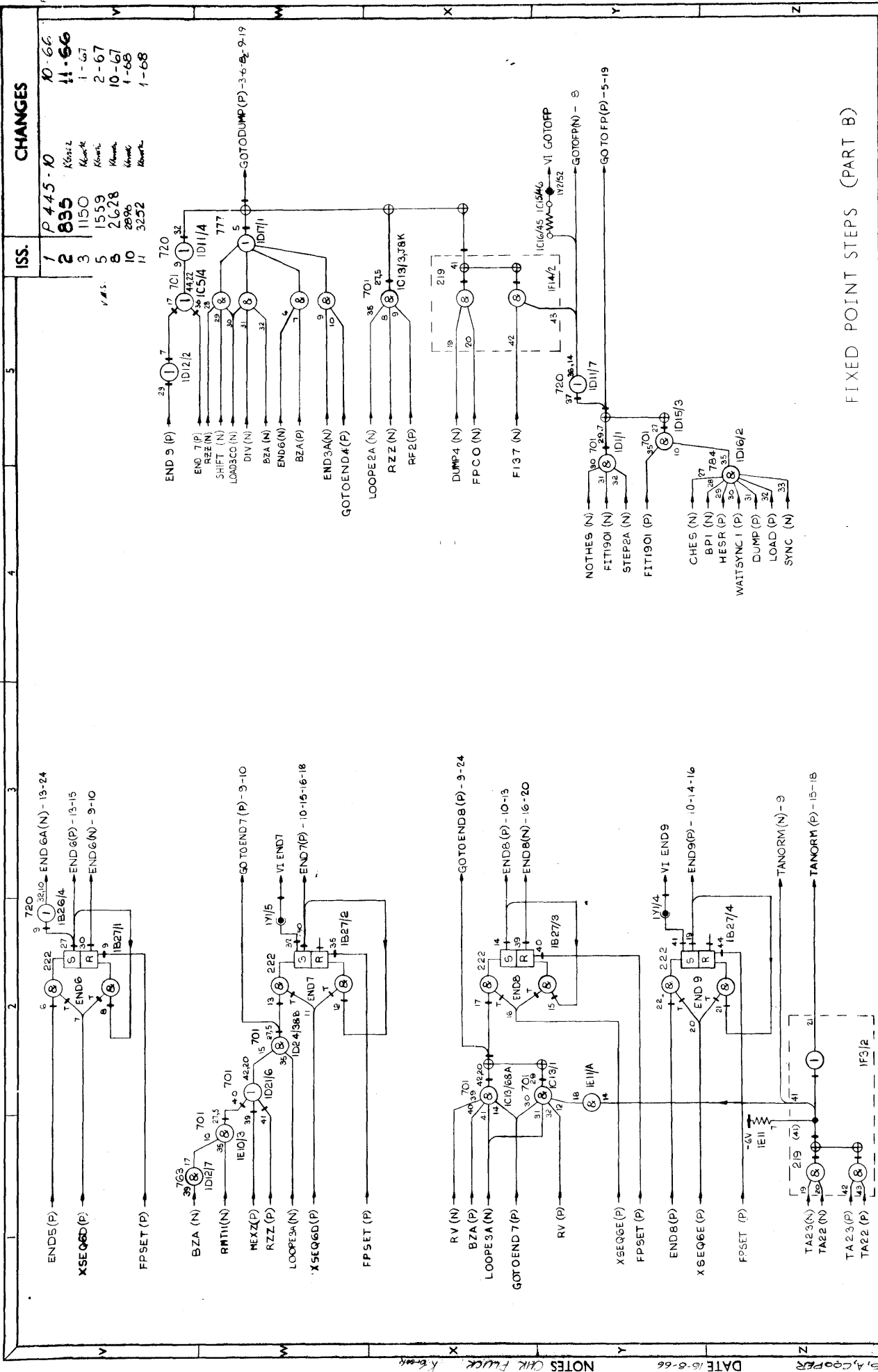
CAT

ISSUE SHEET

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

81328 E



ISS.

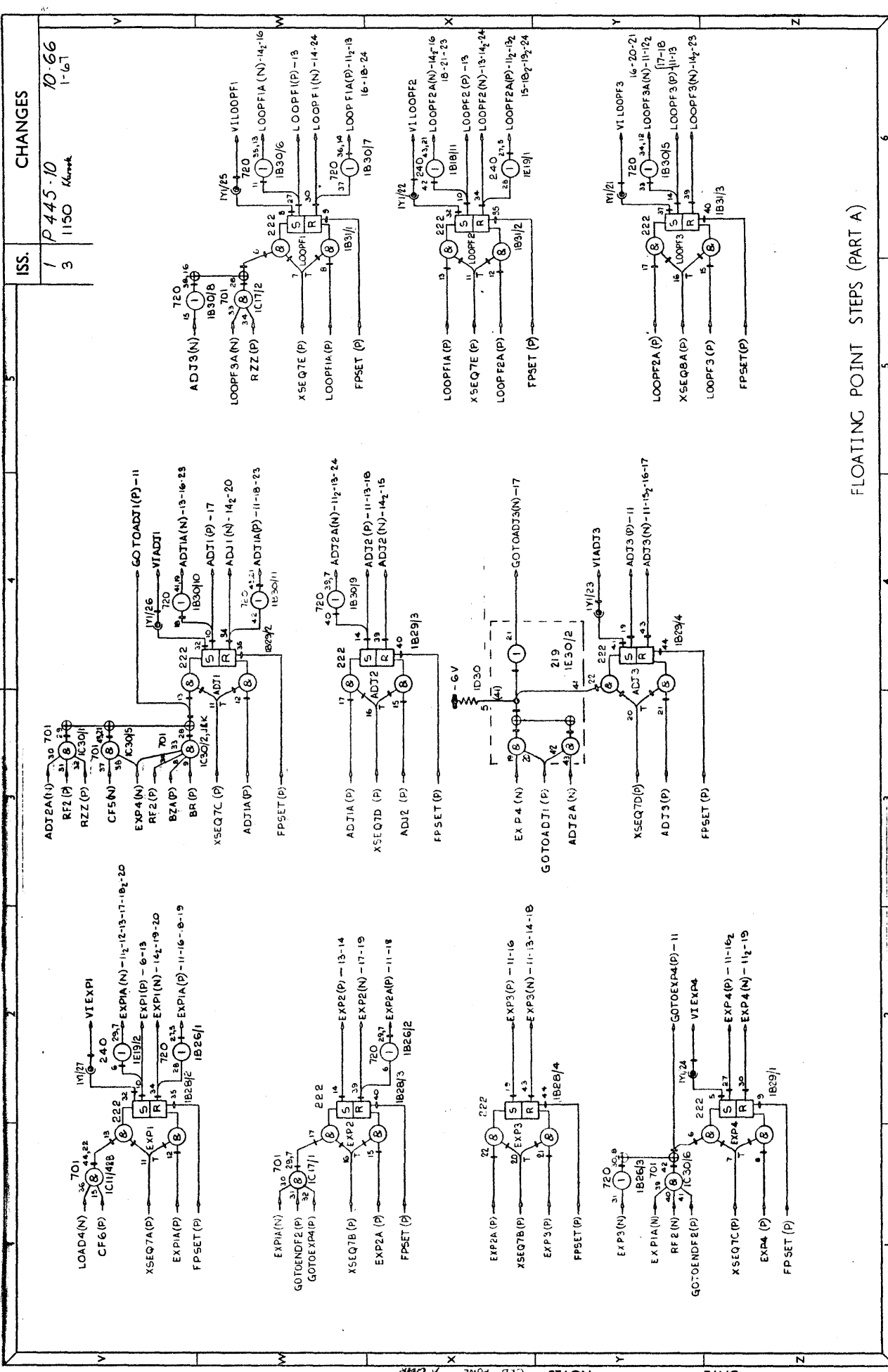
1	P 445-10	10-66
2	895	11-66
3	1150	1-67
5	1559	2-67
8	2628	10-67
10	2896	1-68
11	3252	1-68

CHANGES

1	Kennel
2	Kennel
3	Kennel
5	Kennel
8	Kennel
10	Kennel
11	Kennel

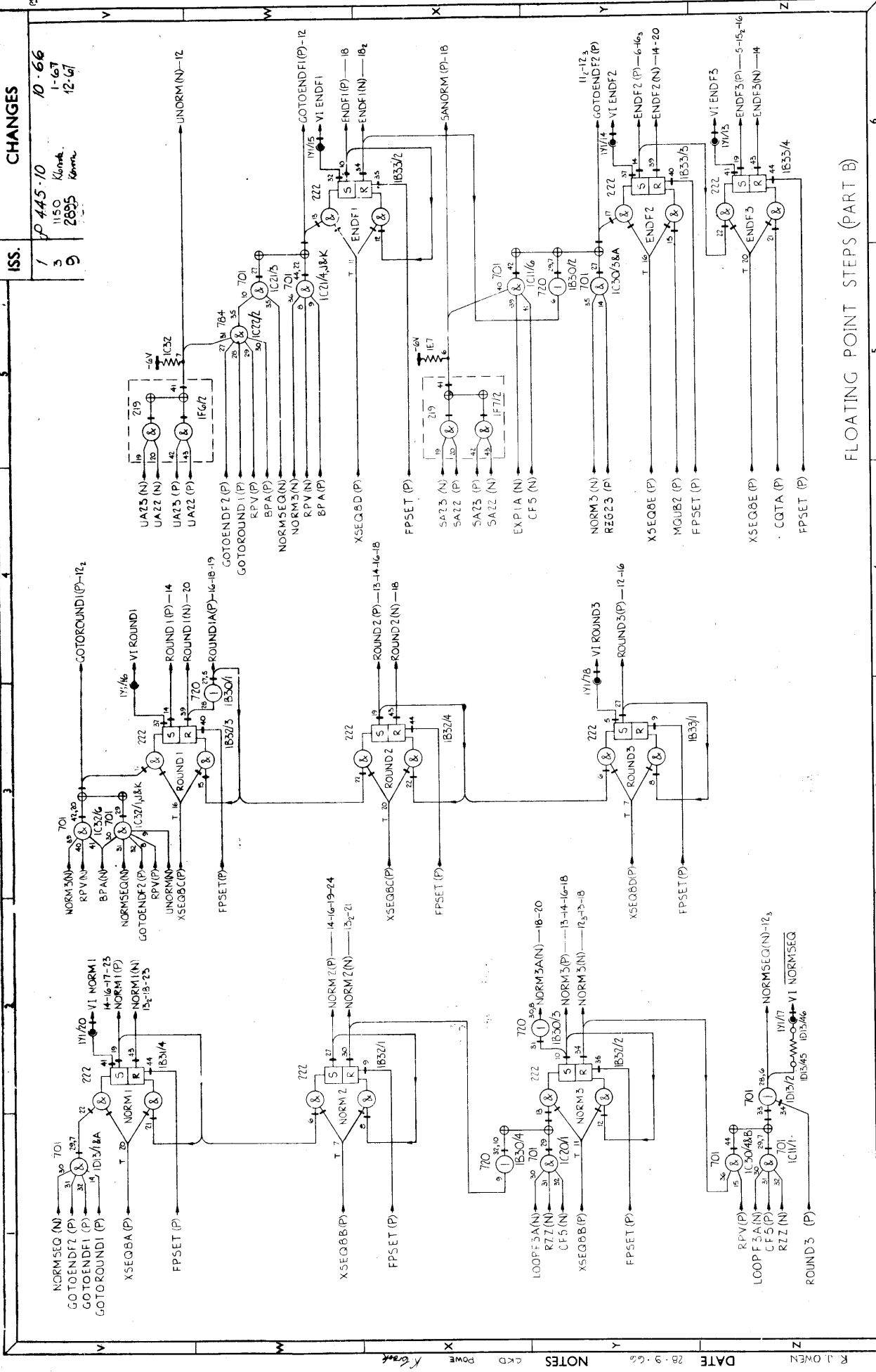
NOTES: CHIR FLICK DATE: 10-8-66 DRAWN BY: S.A. COOPER

FIXED POINT STEPS (PART B)



DRAWN BY S. A. COOPER	DATE 11-8-66	NOTES	CRD PONE K. CROOK	ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.																																		
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30					
1900 SCIENTIFIC UNIT LOGIC	5016360	DIAGRAM	3	ISSUE	11	SHEET	D	CAT	ISSUE																													
									FLOATING POINT STEPS (PART A)																													
CHANGES		ISS.																																				
1 P 445-10 10-66		3 1150 K. CROOK 1-67																																				





ISS.

1	P 445-10	10-66
3	1150	1-67
9	2895	12-67

CHANGES

1	P 445-10	10-66
3	1150	1-67
9	2895	12-67

FLOATING POINT STEPS (PART B)

1900	5016360/9	12	D	ISSUE	4	5	6
SCIENTIFIC UNIT		ISSUE	CAT	SHEET			
LOGIC		9	12	9	12	13	14
		15	16	17	18	19	20
		21	22	23	24	25	26
		27	28	29	30		
		ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.					
		DIAGRAM					
		DRAWN BY R. J. OWEN					
		DATE 28-9-66					
		NOTES					
		CKD					
		R. G. B. G.					



1900  
SCIENTIFIC UNIT  
LOGIC

5016360/9

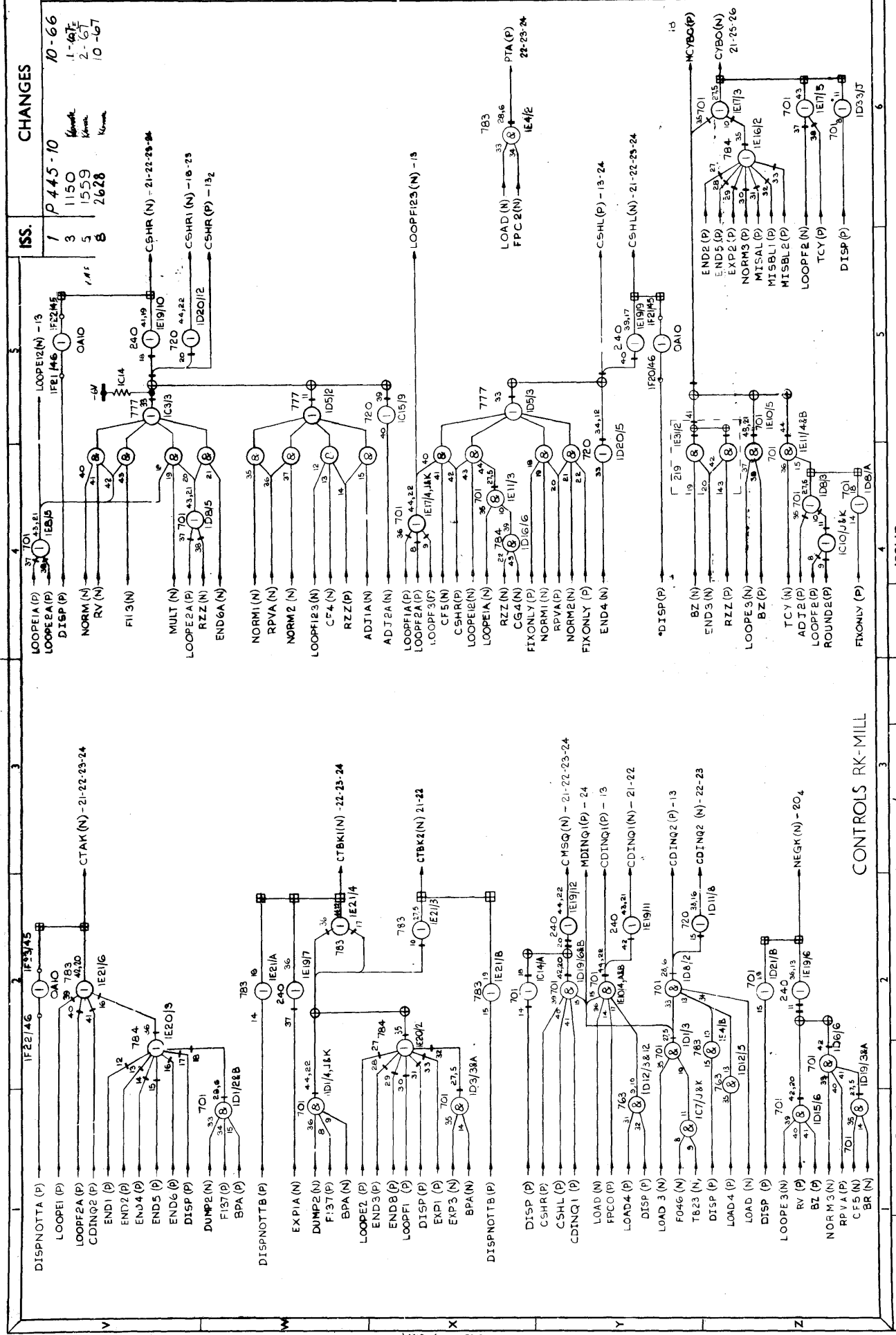
12

D

ISSUE

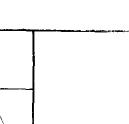
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

ADJ2(N) BP(N) SPNOTSA(P) ENDS(P) DISP(P) LOOPE3(P) ENDI(P) LOOPE2(N) CF4(N) RZZ(P) UB9(P) RZG23(P) UAO(P) QBIT(P) DIV(N) LOOPE(N) RZZ(P) CF2(N) LOOPF2(N) CF5(N) QBIT(P) QBIT(N) DIV(N) LOOPEI(N) RZZ(N) CF3(N) LOOPF2(N) CF5(N) QBIT(N) MULT(F) TBO(P) LOOPF2(I) CF4(N) UAO(N) ENDS(P) ENDS(P) DISP(P)



ISS. CHANGES

1	P 445-10	10-66
3	1150	1-67
5	1559	2-67
8	2628	10-67

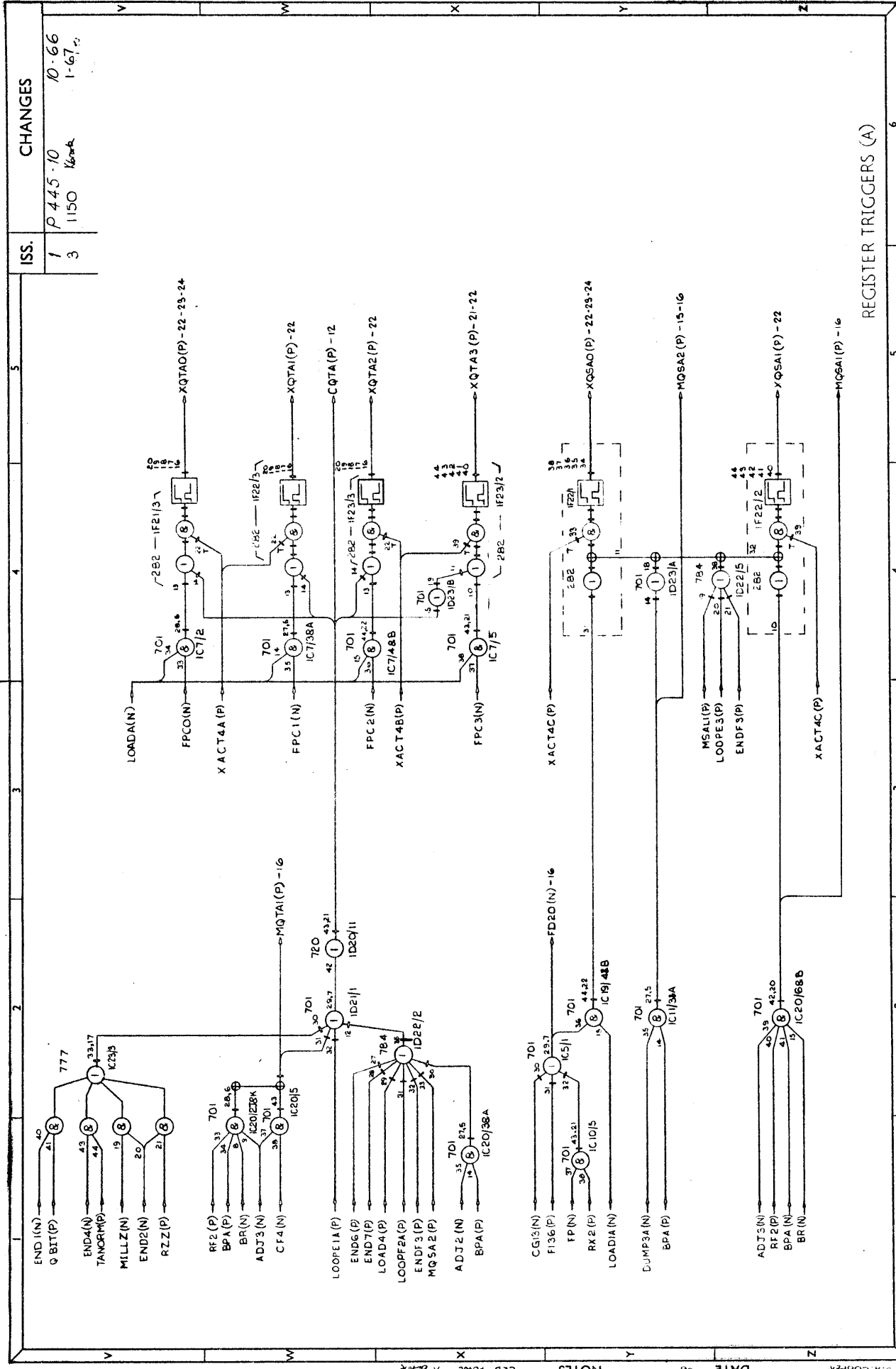

  
 1900 SCIENTIFIC UNIT LOGIC

5016360 / 8 13  
 ISSUE SHEET  
 DIAGRAM SHEET

ISSUE SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30  
 ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

DRAWN BY S.A. COFFEY  
 DATE 8.66  
 NOTES  
 CKD POWE

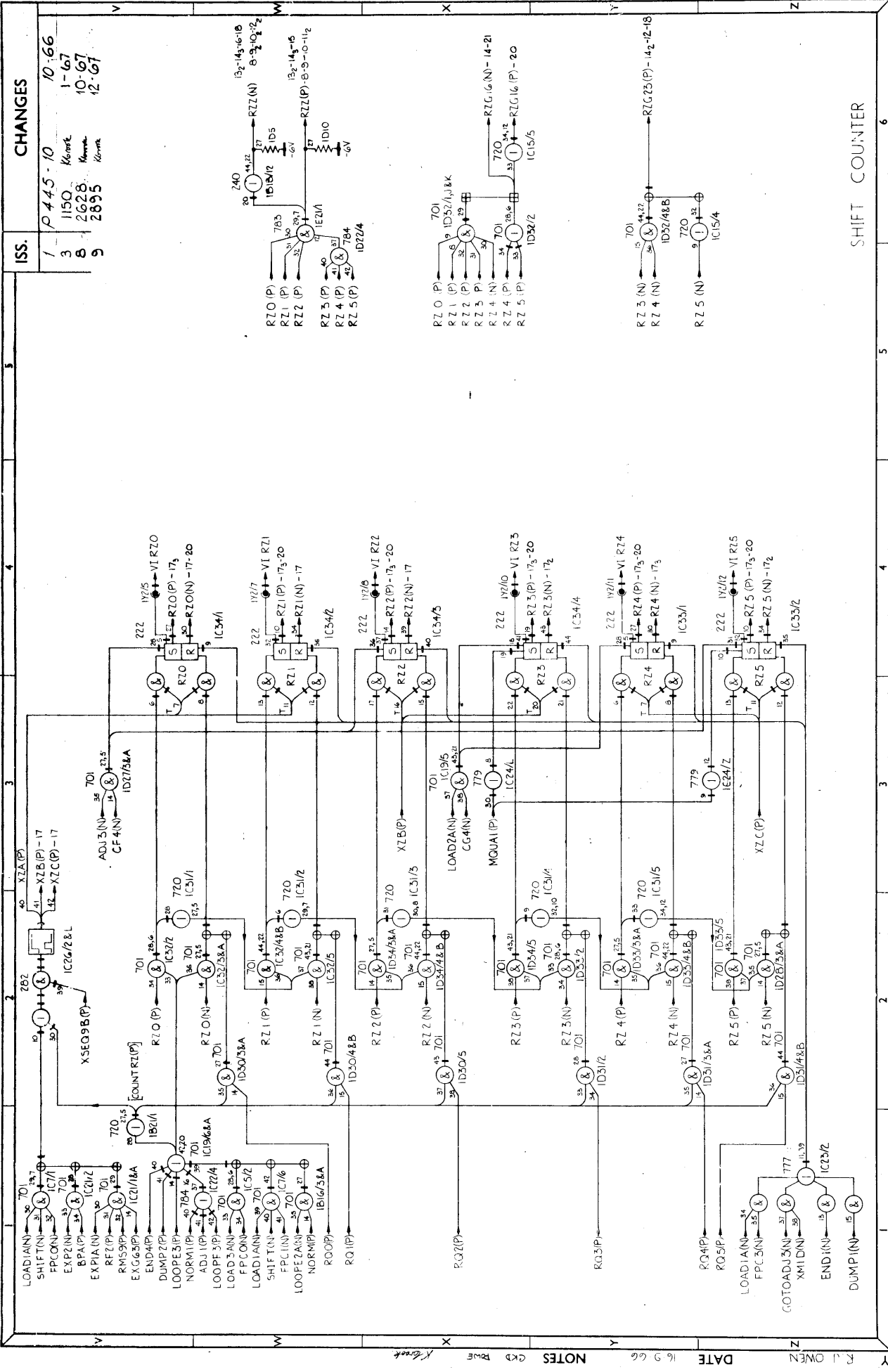




ISS.	1	2	3	4	5	6
	10-66					
CHANGES	P 445-10					
	1150					
	1-67					
ISSUE	1	2	3	4	5	6
SHEET	1	2	3	4	5	6
CAT	D					
SHEET	15					
ISSUE	3					
DIAGRAM	5016360					
SCIENTIFIC UNIT	LOGIC					
REGISTER TRIGGERS (A)						
DRAWN BY: S.A. COOPER						
DATE: R-66						
NOTES: CRD Powe R-66						
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.						





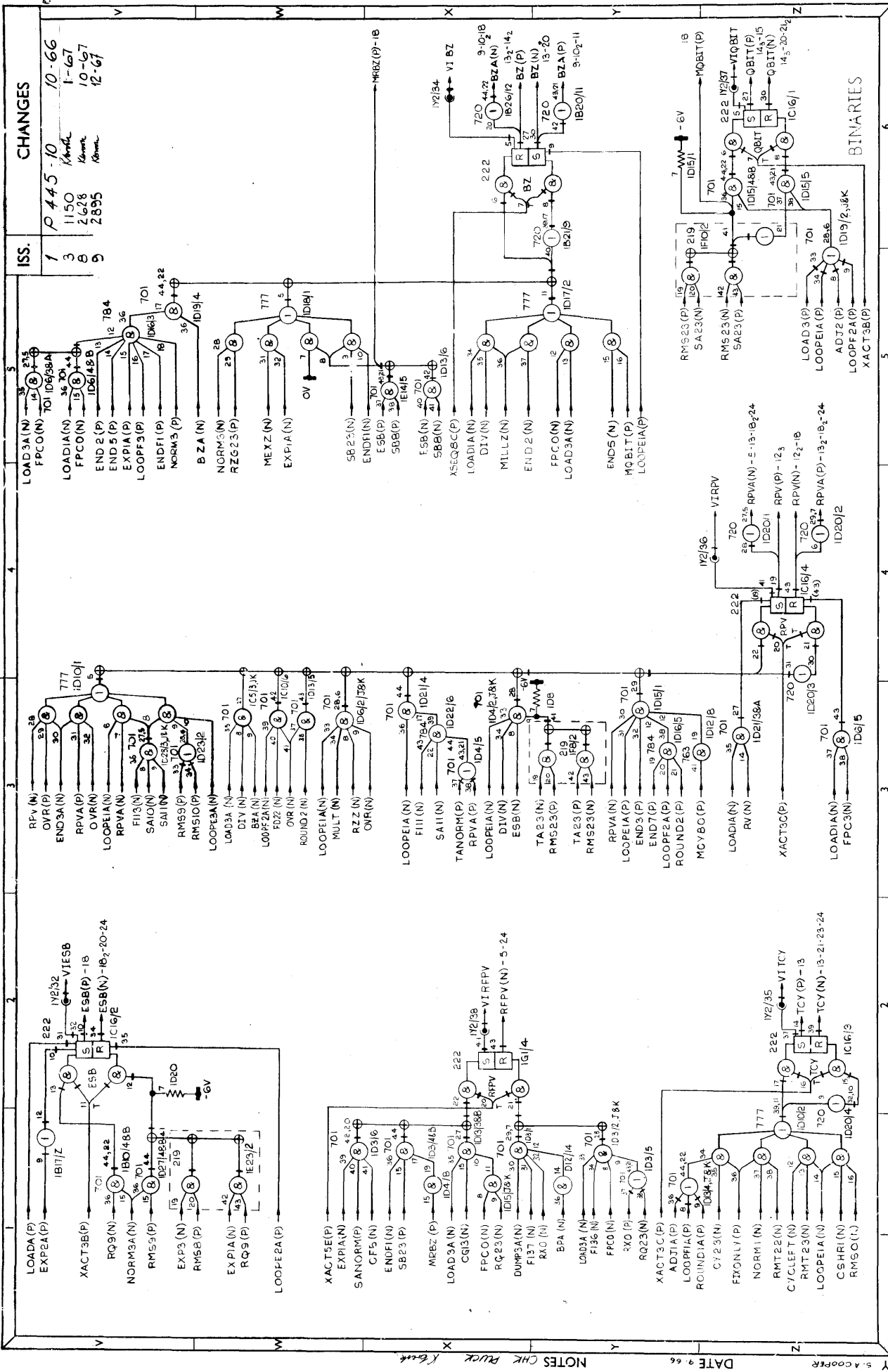


	1900 SCIENTIFIC UNIT LOGIC		5016360 / 9		ISSUE 17	D	ISSUE SHEET													
	DRAWN BY R. J. OWEN		DATE 16 9 66		NOTES CKD BWF		X-track		1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.											

SHIFT COUNTER

CHANGES

ISS.	1	P 445 - 10	10-66
	3	1150 Korman	1-67
	8	2628 Korman	10-67
	9	2855 Korman	12-67



ISS

1	P 445-10	10-66
3	1150	1-67
8	2628	10-67
9	2895	12-67

ISSUE

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

DIAGRAM

5016360 / 9 18

ISSUE SHEET CAT

1900 SCIENTIFIC UNIT LOGIC

NOTES: CHECK PUCK K644

DATE: 9-66

DRAWN BY: S.A. COOPER

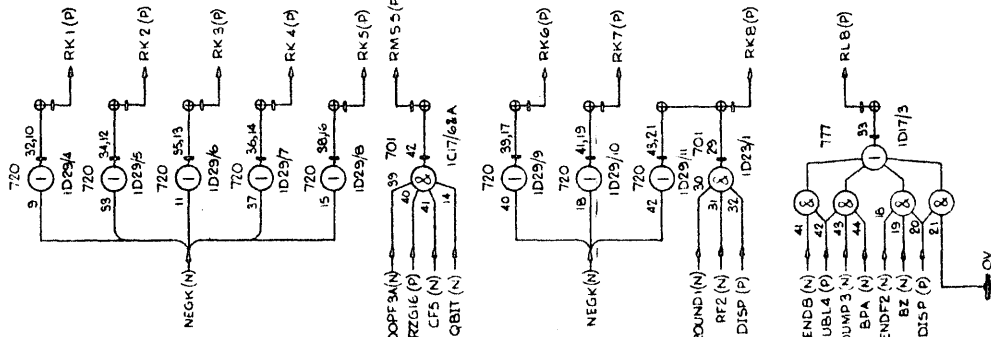


MILL BRICK LOCATION TABLE

BIT	Z19	SA	SB	TA	TB	UA	UB	A'	B'	C'	NY/D'
0	IE34	IF34/1	IG34/1	IF32/1	IG33/1	IG33/1	IG32/1	ID34/6	IG31/1	IF33	IV/29
1	IE33	IF33/1	IG33/1	IF32/1	IG32/1	IG31/2	IG31/2	NA	IG31/2	IE33	32
2	IE32	IF32/1	IG32/1	IF31/1	IG31/1	IG31/3	IG31/3	NA	IG31/3	IE32	30
3	IE31	IF31/1	IG31/1	IF30/1	IG30/1	IG30/1	IG30/1	NA	IG30/1	IE31	33
4	IE30	IF30/1	IG30/1	IF29/1	IG29/1	IG27/1	IG27/1	NA	IG30/2	IE30	34
5	IE29	IF29/1	IG29/1	IF28/1	IG28/1	IG27/1	IG27/1	NA	IG30/3	IE29	37
6	IE28	IF28/1	IG28/1	IF27/1	IG27/1	IG26/1	IG26/1	NA	IG26/1	IE28	35
7	IE27	IF27/1	IG27/1	IF26/1	IG26/1	IG25/1	IG25/1	NA	IG26/2	IE27	38
8	IE26	IF26/1	IG26/1	IF25/1	IG25/1	IG24/1	IG24/1	NA	IG26/3	IE26	36
9	IE24	IF24/1	IG24/1	IF23/1	IG23/1	IG22/1	IG22/1	NA	IG21/1	IE24	39
10	IE23	IF23/1	IG23/1	IF22/1	IG22/1	IG21/1	IG21/1	ID24/20	IG21/2	IE23	40
11	IE22	IF22/1	IG22/1	IF21/1	IG21/1	IG20/1	IG20/1	ID24/21	IG21/3	IE22	43
12	IF20	IG16/1	IG15/1	IF18/1	IG17/1	IG14/1	IG13/1	IF24/22	IG17/1	IF20	41
13	IF19	IG16/1	IG15/1	IF17/1	IG17/1	IG16/1	IG16/1	IF24/21	IG17/2	IF19	44
14	IF16	IG15/1	IG14/1	IF16/1	IG15/1	IG14/1	IG14/1	IE15/17	IG17/3	IF16	42
15	IF15	IG14/1	IG13/1	IF15/1	IG14/1	IG13/1	IG13/1	IE15/15	IG12/1	IF15	45
16	IF14	IG11/1	IG10/1	IF12/1	IG11/1	IG8/1	IG7/1	IE15/14	IG2/2	IF14	46
17	IF13	IG11/1	IG10/1	IF11/1	IG11/1	IG10/1	IG10/1	IE15/13	IG2/3	IF13	49
18	IF10	IG10/1	IG9/1	IF10/1	IG10/1	IG9/1	IG9/1	IE11/21	IG6/1	IF10	47
19	IF8	IG10/1	IG9/1	IF9/1	IG10/1	IG9/1	IG9/1	IE11/20	IG6/2	IF8	50
20	IF7	IG4/1	IG3/1	IF5/1	IG4/1	IG1/1	IG1/1	IE3/16	IG6/3	IF7	48
21	IF6	IG4/1	IG3/1	IF4/1	IG4/1	IG3/1	IG3/1	IE5/17	IG5/1	IF6	51
22	IF3	IG3/1	IG2/1	IF3/1	IG3/1	IG2/1	IG2/1	IE3/19	IG5/2	IF3	52
23	IF2	IG3/1	IG2/1	IF2/1	IG3/1	IG2/1	IG2/1	NA	IG5/3	IF2	55

BOARD 219 MONITOR POINTS

- 1 RG (P)
- 2 RG (N)
- 3 RL (P)
- 4 RK (P)
- 5 RMG (N)
- 6 RMT (N)
- 7 OUTPUT EL.2
- 8 CY (P)
- 9 RMS (P)



THESE ARE ALL ATTACHMENTS TO THE MILL. TRUE OUTPUTS ARE ON THE MILL DRAWING SHEET 22.

NOTE WITH BRICK SOURCES SUCH AS ID34/6, THE ASTERISK DENOTES THAT 6 APPLIES TO THE PIN NF NOT THE MONITORING POINT ON THE BRICK.

MILL BRICK LOCATION TABLE  
MILL ATTACHMENTS

ISS.

1	P 445-10	10-66
2	855	11-66
3	1150	1-67

CHANGES

DRAWN BY R.J. POWE

DATE SEPT 66

NOTES *Chk. Russ. Chk. Russ.*

1900 SCIENTIFIC UNIT LOGIC

5016360

DIAGRAM

ISSUE 3

SHEET 20

CAT D

ISSUE 6

SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

I.C.T.



CHANGES

ISS.	1	P 445-10	10-66
	3	1150	1-67

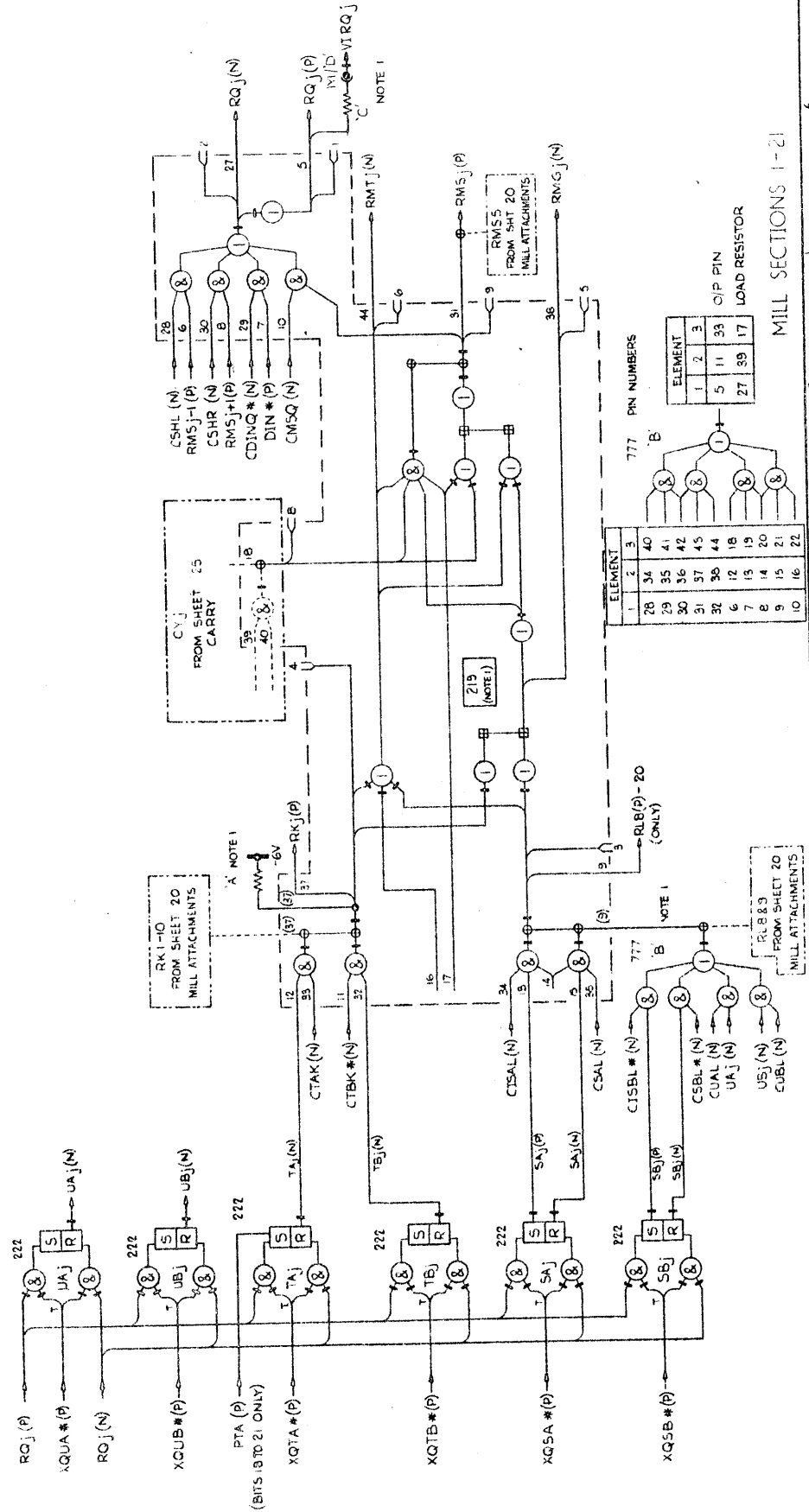
- NOTES.
- FOR LOCATION OF CARDS AND ELEMENT USED ON EACH MILL SLICE REFER TO TABLE ON SHEET 20
  - FOR J SUBSTITUTE MILL BIT №
  - PIN NUMBERS USED MORE THAN ONCE ARE SHOWN BRACKETED

222 PIN NUMBERS

ELEMENT	1	2	3	4
28	31	36	18	
6	13	17	22	
7	11	16	20	
8	12	15	21	
9	35	40	44	

SIGNAL VERSIONS

SIGNAL	#1-O	#1-1	#2	#3	#4	#5
CDINQ*	17-0	22-18				
C1SBL*	23-9	8-0				
CSBL*	23-9	8-0				
CTBK*	23-9	8-0				
XQSA*	23-12	11-0				
XQSB*	23-16	15-3	8-0			
XQTA*	23-18	17-12	11-6	5-0		
XQTB*	23-16	15-9	8-0			
XQUA*	23-12	11-0				
XQUB*	22-16	15-9	8-0			
DIN*	0,6,12,18,24,30,36,42,48,54,60,66,72,78,84,90,96,102,108,114,120,126,132,138,144					



777 PIN NUMBERS

ELEMENT	1	2	3
28	34	40	
29	35	41	
30	36	42	
31	37	43	
32	38	44	
6	12	18	
7	13	19	
8	14	20	
9	15	21	
10	16	22	

OIP PIN LOAD RESISTOR

ELEMENT	1	2	3
27	39	17	

MILL SECTIONS 1-21



1900 SCIENTIFIC UNIT LOGIC

5016360 / 3

DIAGRAM

ISSUE SHEET

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE SHEET

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

DRAWN BY R. J. POWELL

DATE

NOTES

CPD

811/10E





1900 SCIENTIFIC UNIT LOGIC

DIAGRAM

5016360 / 3

ISSUE SHEET 24 CAT

ISSUE SHEET 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

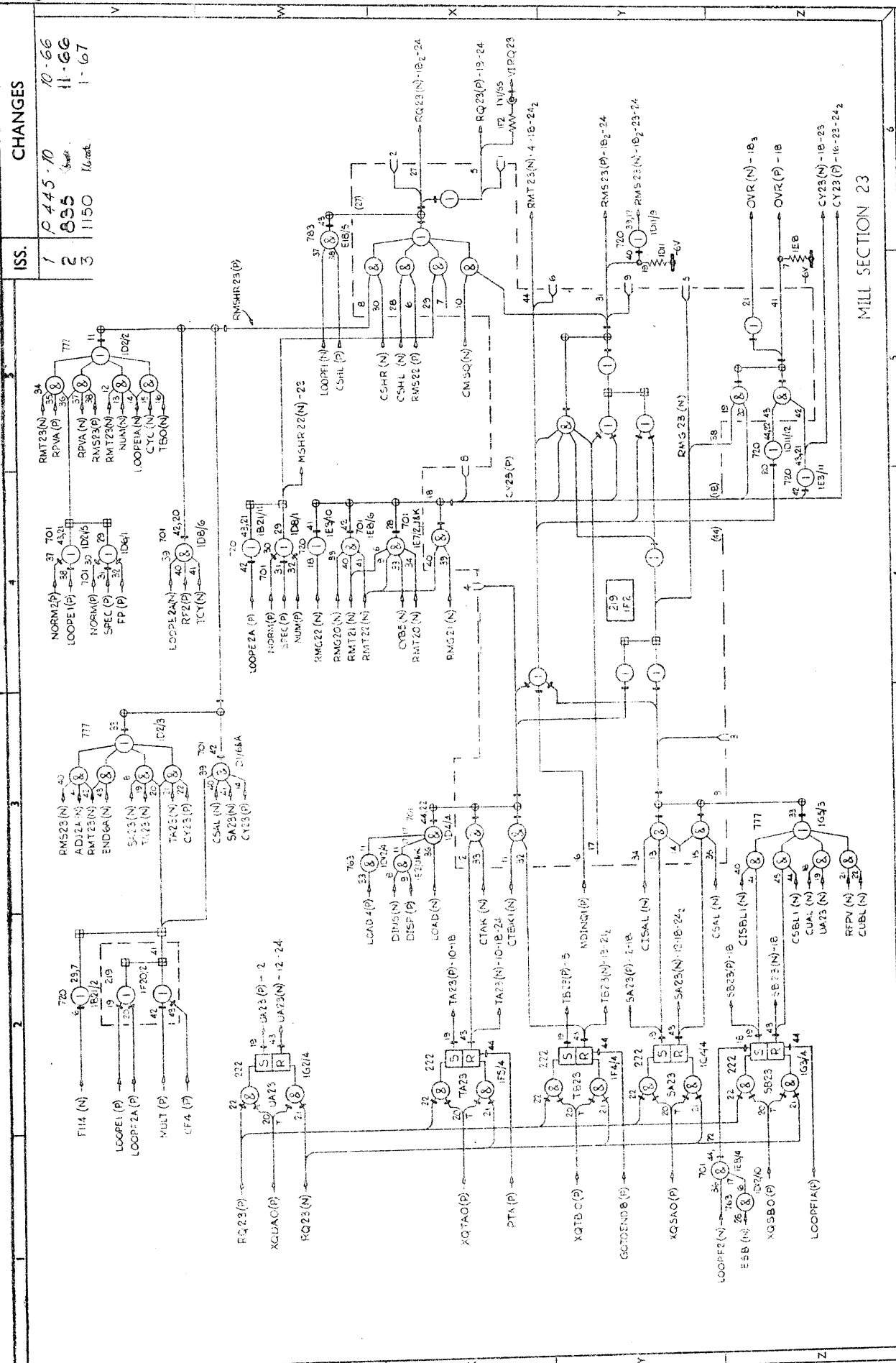
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.

MILL SECTION 23

DRAWN BY R. J. POWE

DATE SEPT 66

NOTES ON REVISION



CHANGES

ISS.

1	P 445-10	10-66
2	835	11-66
3	1150	1-67





1900  
SCIENTIFIC UNIT  
LOGIC

5016360  
DIAGRAM

ISSUE 3 / SHEET 25  
CAT D

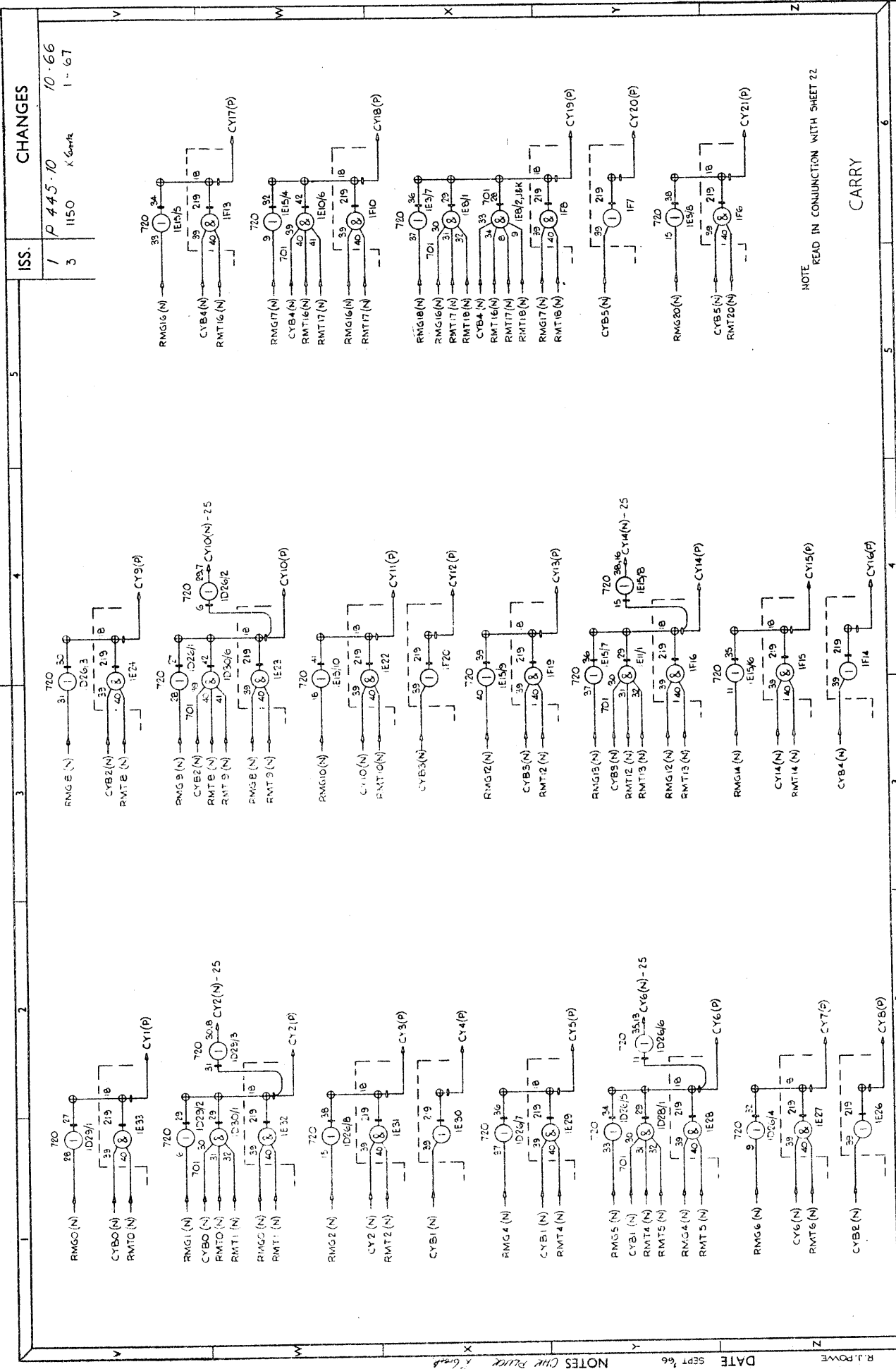
ISSUE SHEET

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

813129 E

NOTE  
READ IN CONJUNCTION WITH SHEET 22

CARRY



CHANGES

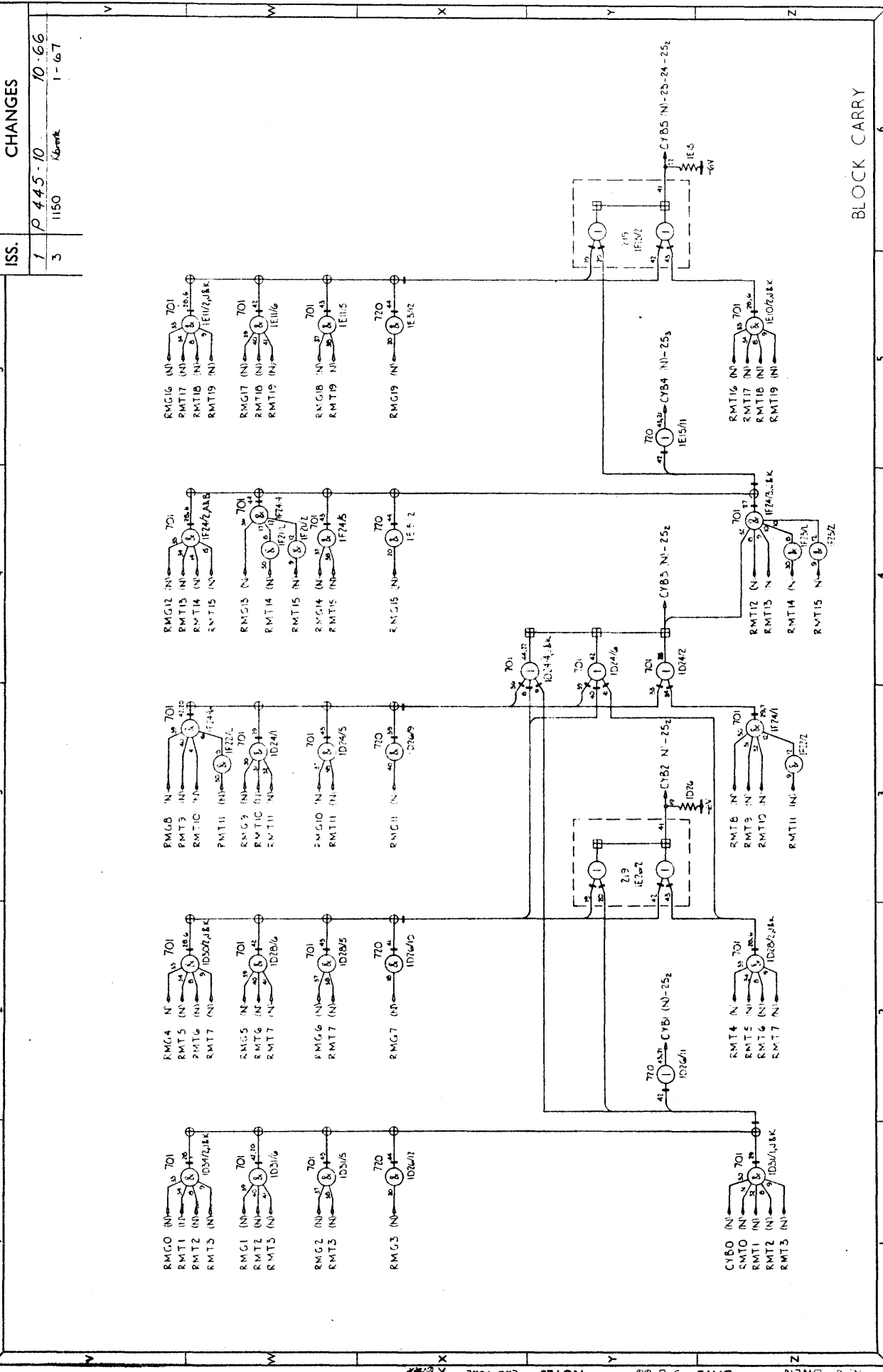
ISS.

1	P 445.10	10.66
3	1150	K. Garth

DRAWN BY R.J. POWE

DATE SEPT 66

NOTES CHE PLUCK



BLOCK CARRY

CHANGES

ISS.	1	0 445 - 10	10-66
	3	1150	1-67



1900  
SCIENTIFIC UNIT  
LOGIC

5016360  
DIAGRAM

ISSUE / 3

SHEET 26

ISSUE D

ISSUE SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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ISSUE SHEET 26 ISSUE D SHEET 26 ISSUE SHEET 26

DATE 9 8 66 NOTES CRD Powe

DRAWN BY R J OWEN

ISS. 1 P 445-10 10-66

5

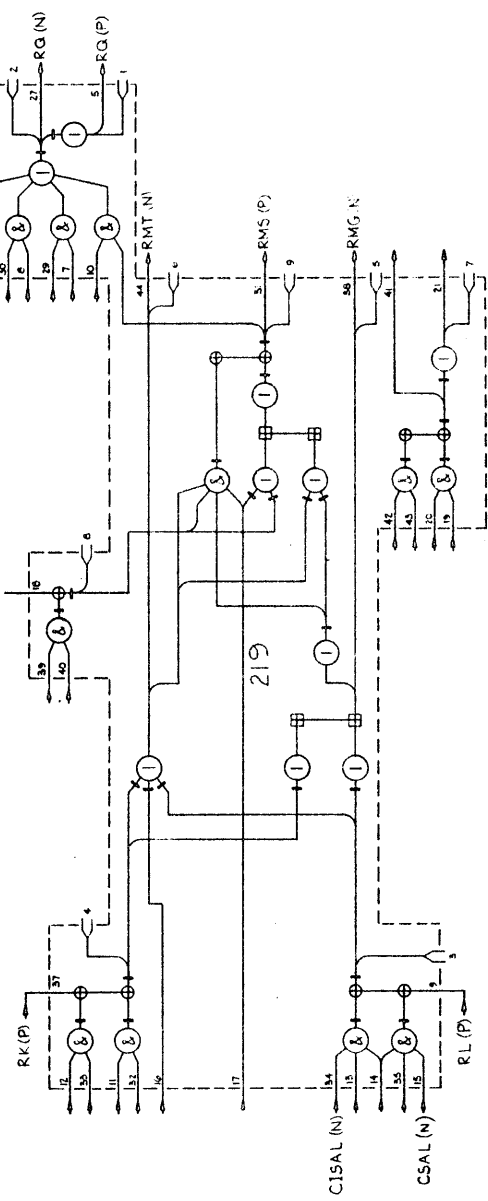
4

3

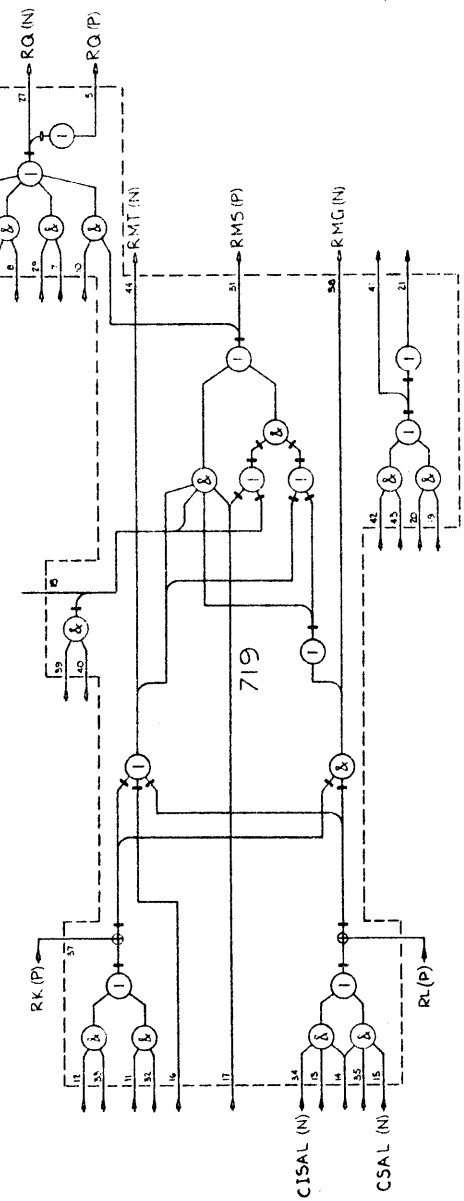
2

1

219 MILL PACKAGE



719 MILL PACKAGE



NOTES

ABOVE IS THE INTERNAL LOGIC ARRANGEMENT OF THE 219 SILICON MILL SLICE CARD. IT IS EXTERNALLY BOTH FUNCTIONALLY AND ELECTRICALLY IDENTICAL WITH THE 719 GERMANIUM MILL SLICE CARD WHICH USES THE DOUBLE-DIODE LOGIC AS SYMBOLISED BELOW.  
NOTE THAT THE CARD IS ALSO USED AS A GENERAL GATING CARD, P.9, ON SHEET 4, AND IS ALWAYS MONITOR POINTS AS SHOWN ABOVE ARE PROVIDED ON 219 ONLY.  
THE TWO CARDS ARE FULLY INTERCHANGEABLE.

⊖<sup>n</sup> DENOTES MONITOR POINT No. n FROM TOP OF PACKAGE.

MILL PACKAGE

6

ISSUE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
SHEET																														
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.																														

ISSUE D CAT

SHEET 27

ISSUE / 1

5016360 / 1

DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



DRAWN BY R. J. OWEN

DATE 3-10-66

NOTES CHK PACE K

NOTES

5016360 SL 28

CHANGES

ISS.

1	P 445-10	10-66
3	115Q K. Brock	1-67

# INTERFACE PROCEDURES.

Notes:

Avoids blocking RQ at C0  
 Incoming char. is spread over RQ  
 Last char. + 18 bits of TA → RQ

Necessary to test sign of divisor  
 First division loop flag

Keeps RADC in step with RY  
 Data word or N on 1902-3  
 Data word on 1901  
 N on 1901  
 Adds datum to X on RJ

- + Represents logical OR
- Represents logical AND

LOADING

The loading sequence is entered by GOTOFP from the Processor.  
 A single binary LOAD is on during the whole sequence even when hesitations are in progress.

LOAD does:

SET TA23-18 to 011111 at C2  
 CDINQ1, INHIBIT CMSQ at C3, C2, C1  
 CDINQ2, CMSQ at C0  
 TA → RK  
 DIN5 → RK23 [Only applies at C0]  
 SET ESB

LOAD•BP3 [i. e. Load not in hesitation] does:

CONTINUE CYCLE  
 TRIGGER TA CHARACTER-WISE  
 ALLOW RY to RADC on 1902-3  
 CBCH if 1902-3  
 CB if 1901•(LOAD1•CG11)  
 CAL if 1901•LOAD1•CG11  
 CDJ if LOAD1•CG13

DATUM is added to N as appropriate in STEP 1 by CP control  
 RESC 1902-3 allow MDLF for CG4 and CG13 (not 130, 131)  
 1901 Set FPRESC if MDLF•(OBJ + RMB)•C2

GOTOFP is CHES•HESR•BP1 on 1902-3  
 STEP2 on 1901

LOAD1 loads n or N  
 LOAD2 loads x or x\* or does A + 1 → N  
 LOAD3 loads x\* or x or n\*  
 LOAD4 restores TA

ISSUE SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET																															

ISSUE SHEET **D** CAT.

**28** SHEET

**3** ISSUE

**5016360 / 3** DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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5016360 SL29

CHANGES

P 445-10 10-66  
1150 1-67  
2628 K6-10-67

# INTERFACE PROCEDURES LOAD1 TO LOAD4

### Notes:

F<sub>PAR</sub> is F132-F135

RZ must be 63  
before RQ → RZ

Clear FP accumulator  
Zero divisor. Finish at LOAD3

BP is set off by entry GOTOFFP

Clear l.s. half of operand  
Reduces to 23 in LOAD3  
Hesitation return flag  
Hesitation corrupts RN on 1901

Clear RQ  
All 1's to RQ & RMS23 for QBIT  
setting  
Rounding constant  
Exponent sign is inverted in program

Overflow if divisor zero  
Compare dividend/divisor sign  
for first mill operation  
Floating point overflow if indicated  
in n\*  
"First time" flag for normalise  
F114/5  
No function to do.

Begin loop if multiply, divide or  
shift  
Test exponent first if F114/5

Function completed  
Begin function if F132-F135

+ Represents logical OR  
• Represents logical AND

### LOAD1 (LOAD n OR N)

LOAD (N) to SA if (CG4 + F136 + F<sub>PAR</sub>•RX2)  
LOAD (N) to UA if (F136 + (F<sub>PAR</sub>•RX2))  
TRANSFER N to SA if CG11  
CLEAR RZ at C3  
COUNT RZ if SHIFT  
RQ to RZ at C0 if SHIFT  
INHIBIT DIN if (F136•RX0)  
SET BZ if (MILLZ•DIV) at C0  
CLEAR RPV at C3. COPY RV to RPV  
CHES at C0  
GOTO LOAD2 [CHES•HESR•LOAD•BP]

### LOAD2 [LOAD (X) OR (X + 1) OR DO A + 1 → N]

LOAD (X) TO TB if MULT  
LOAD (X\*) to TB if MULT•CG13  
A + (1 at C3) → A, N if CG13  
INHIBIT DIN if F114  
SET RZ to 24 if CG4  
SET BP at C1  
CHES if 1902-3  
GOTO LOAD3 [((CHES•HESR) + 1901)•LOAD•BP]

### LOAD3 [LOAD OTHER HALF]

LOAD (X) if CG11 + F044 + F045 } to TA (LOAD REGISTER)  
LOAD (X\*) if F042 }  
LOAD (N) if CG13 to:  
UBF, SB if F136 + F<sub>PAR</sub>•RX2)  
UBF, TB if F136 + (F<sub>PAR</sub>•RX2)  
INH. DIN if F040 + F041 + (F046•TB23) + (F136•RX0)  
CLOSE ALL RQ GATES if (F046•TB23) & FORCE RMS23

FORCE DIN4 at C0 if F041  
INVERT DIN2 at C2 if CG13  
COUNT RZ at C0  
SET RPV if BZ  
SET QBIT if (RMS23 = SA23)

SET RFPV if CG13•RQ23•C0 RESET IF F136•C0•(RX0 + RQ23)  
RESET BP  
SET BZ at C0

GO TO DUMP if (DIV•BZ) + (SHIFT RZZ)  
LOADEND if CG13•C0  
GOTO LOOPE1 if (LOADEND•CG13•NORM•FPEND)

GOTO LOOPE3 if (LOADEND•NORM)  
GOTO LOAD4 if CG13  
LOAD4 [RESTORE TA]

UA → TA  
STEPEND [PROCESSOR]  
FPEND if F136  
LOADEND if F136  
GOTO EXP1 if F136

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ISSUE SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30  
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET

D CAT.

29 SHEET

8 ISSUE

5016360 / 8  
DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



CHANGES

ISS.	1	P445-10	10-66
	3	1150 K600h	1-67
	4	1394 K600h	1-67
	6	1730 K600h	3-67

# INTERFACE PROCEDURES.

## STORING

The writing of results to the store is identified by a single binary DUMP which is on during the whole sequence even when hesitations are in progress. The DUMP sequence is entered by GOTODUMP in the fixed point orders which also does RESYNC, and by GOTOFP•F137. DUMP is switched off by FPEND.

Notes:  
Add datum to X on RJ

Hesitation re-entry flag  
Clears address binaries in 1901 for a hesitation  
Inhibits all store cycles  
Hesitation corrupts RN on 1901  
MDLF is harmless on 1902-3

BP is off first time round, on second time round  
Exponent sign is inverted in program  
Floating point overflow is set in n<sup>2</sup>

Function has produced overflow  
Hesitation re-entry flag

F137•RX0. Set FP acc. to FP zero

BP is off first time round, on second time round

Wholly processor operation

- + Represents logical OR
- Represents logical AND

## DUMPI TO DUMP4

DUMP does CDJ and INHIBIT MDLF if  $\overline{CG13} \cdot \overline{BP3}$

DUMP 1 [ENTERED BY GOTODUMP; TEST RESC]

CLEAR RZ  
CONTINUE CYCLE  
FCTR3 [PROCESSOR]  
ALLOW MDLF on 1902-3  
CHES if 1902-3  
FPEND if 1901•MDLF  
GOTO DUMP2 if (1901•MDLF) or (1902-3 from CHES)

DUMP 2 [TRANSFER DATA]

{ If F137, TA if  $\overline{BP}$ , TB if BP } to BIN and to RB  
 { If F137, UA if  $\overline{BP}$ , UB if BP }  
 INVERT BIN2 if F137•BP•C2  
 RFPV → RQ23 WHEN UB → RQ  
 READ; DESTRUCTIVE IF 1901 & BP  
 CNJ if F137  
 CXJ if F137• $\overline{BP}$   
 CNJ if F137•BP } CONTROLLED BY DUMP•BP3  
 SET RV if (F137•RFPV) + ( $\overline{F137}$ •RPV)  
 COUNT RZ  
 GOTO DUMP3 at C3

DUMP 3 [WRITE STORE. UPDATE REGISTERS FP]

WRITE, CONTINUE CYCLE  
 UA if  $\overline{BP} \cdot (F137 \cdot \overline{RX0})$   
 UB if  $\overline{BP} \cdot (F137 \cdot \overline{RX0})$   
 2<sup>B</sup> if  $\overline{BP} \cdot \overline{NOT} \cdot \overline{UB}$  } to UA, SA, TA if  $\overline{BP}$   
 UB, SB, TB if BP  
 RESET RFPV if (F137•RX0)  
 SET BP at XSEQ  
 FCTR3  
 FPEND if BP  
 CHES if BP  
 GOTO DUMP4 if F137 [CHES• $\overline{HESR}$ •F137•FPEND  
 •RZZ] OR  
 GOTO DUMP2 if  $\overline{F137}$  [CHES• $\overline{HESR}$ • $\overline{F137}$ •FPEND]

DUMP 4 [COUNT RN]

RA +[1 at C3] → RA, RN  
 GOTO DUMP1

ISSUE SHEET

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30 SHEET

6 ISSUE

5016360/6 DIAGRAM

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5016360 SL31

CHANGES

P 445-10 10-66  
1150 K6002 1-67

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Notes: TA ± SA in multiply/divide. TA if shift/normalise

No shift occurs in last time round multiply/divide

Shift left into slice 0  
Shift right into slice 22  
Shift right into slice 23 in normalise  
In multiply the equivalent of RMS24  
End effects of right shift - m.s. half  
End effects of right shift - m.s. half  
TCY acts as a buffer for what falls off the end  
Quotient bit = 1 if divisor "goes"  
In division this first overflow may be phoney  
Overflow in numeric left shift

"First time" flag for normalise F114/5

Shifts multiplier, quotient or other half of x:

TCY acts as a buffer for what falls off the end

Forces sign of l.s. half to zero  
Division 'first time flag'  
Loop count

Normalise uses LOOPE1-3, others LOOPE1 & 2 only  
Function completed  
Division end effects

If not first time, +1 if RV, -1 if  $\overline{RV}$   
Loop count  
Exponent overflow  
Exponent is negative or reaches zero or arg. is zero  
Argument normalised

+ Represents logical OR  
Represents logical AND

# FIXED POINT FUNCTIONS

## LOOPE1 TO LOOPE3

### LOOPE1 [ARITHMETIC OPERATION]

TA → TA  
+ SA if (MULT • TB0 • RZZ) + (DIV • QBIT)  
+ INVA, INPC if (MULT • TB0 • RZZ) + (DIV • QBIT)  
SHIFT LEFT if F111 + (NORM • RV) + (DIV • RZZ)  
SHIFT RIGHT if F113 + (NORM • RV) + (MULT • RZZ)  
RMSHLO = TB23 if (F111 • SA11) OTHERWISE TB22  
RMSHR22 = RMS23  
RMSHR23 = (NORM • RPV • TA23) + (NORM • RPV • TA23)  
+ (MULT • ((TA23 • CY23) + (TA23 • SA23) + (CSAL • SA23 • CY23)))  
+ (CYCLIC • TB0) + (NUM • TA23) + (RPV • TA23 • SPEC)  
+ (SPEC • RPV • TA23)  
TCY = (F111 • CYCLIC • TA23) + (CSHR • RMS0)  
QBIT = DIV • (SA23 = RMS23)  
RPV = (MULT • RZZ • OVR) + (DIV • ESB • (TA23 = RMS23)) + (F111 • SA11 • (TA23 / RMS23)) + (SPEC • RPV)  
RESET BZ  
GOTO LOOPE3 if F114 ELSE GOTO LOOPE2

### LOOPE2 [SHIFT OPERATION]

TB → TB  
SHIFT LEFT if F111 + (NORM • RV) + DIV  
SHIFT RIGHT if F113 + (NORM • RV) + MULT  
RMSHLO = TCY if DIV, QBIT if DIV  
RMSHR22 = TCY if (NORM + (SHIFT • SA11)); RMS23 if not  
RMSHR23 = TCY if (MULT • RZZ) + (SHIFT • SA11); 0 if not  
SUPPRESS RQ23 if NUM + NORM + SPEC  
RESET ESB  
COUNT RZ if  $\overline{NORM}$   
GOTO LOOPE3 if NORM ELSE GOTO LOOPE1 if (NORM • RZZ)  
ELSE GOTO DUMP if (MULT + SHIFT) • RZZ ELSE GOTO END1 if DIV • RZZ

### LOOPE3 [ADJUST EXPONENT IN NORMALIZE]

SA → SA, INPC if BZ, +2046 if (BZ • RV)  
COUNT RZ  
SET RPV if (RMS9 + RMS10) • BZ  
GOTO END7 if (RMS0 - 9 = 0 + (RQ11 • BZ) + RZZ) ELSE  
GOTO END8 if (RV • BZ) + (RV • (TA22 ≠ TA23)) ELSE  
GOTO LOOPE1

ISSUE SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE SHEET CAT. D

31 SHEET

3 ISSUE

5016360 / 3 DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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5016360 SL 32

CHANGES

1 P445.10 10-66  
3 1150 K<sup>6</sup>mk 1-67

# FIXED POINT FUNCTIONS.

## END1 TO END9

### Notes:

Add divisor back if last QBIT was 0 TA + SA → TA if QBIT

Flag for F046 (rounded division)

END1 [LAST QUOTIENT BIT ZERO]

RESET RZ  
GOTO END2

END2 [REMAINDER = DIVISOR]

Set remainder zero if remainder = divisor

TA + INVSA + INPC → TA if (MILLZ + RZZ)  
SET BZ if MILLZ  
GOTO END3

END3 [ROUND QUOTIENT]

Add 1 to quotient if remainder = divisor or F045  
Overflow here is as phoney as the one in LOOPE1  
Rounding procedure if first time and non zero remainder

TB + [INPC if (BZ + RZZ)] → TB

REVERSE RPV if OVR

GOTO END4 if (F045 • RZZ • BZ) ELSE GOTO DUMP

END4 [SHIFT REMAINDER]

We now test to see if remainder > divisor  
Flag for F045 (rounded division)  
If TA23 ≠ TA22 remainder must be greater than 1/2 divisor

TA SHIFT LEFT → TA if (TA23 = TA22).  
RMSHLO = 0  
COUNT RZ  
GOTO END2 if (TA23 ≠ TA22) ELSE GOTO END5

END5 [COMPARE DIVISOR & TWICE REMAINDER]

Set BZ if rounding required

TA + INVSA + INPC → MILL  
SET BZ if SA23 = RMS23  
GOTO END6

END6 [RESTORE REMAINDER]

Finish if rounding not required

TA SHIFT RIGHT → TA. RMSHR23 = RMS23  
GOTO END2 if BZ ELSE GOTO DUMP

END7 [NORMALIZE, ZERO RESULT]

0 → TA, TB. CLEAR RPV  
GOTO DUMP

END8 [ROUND IN NORMALISE FN.]

Rounding F115 must not carry to m. s. half  
Suppresses sign in l. s. half

TB + 2<sup>8</sup> → TB if (F115 • CY23)

RESET TB23 WITH GOTO END8  
GOTO END9

END9 [PLACE EXPONENT]

SA → TBE  
GOTO DUMP

+ Represents logical OR  
• Represents logical AND

ISSUE SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30  
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET

D CAT.

32 SHEET

3 ISSUE

5016360 / 3  
DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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5016360 SL 33

ISS.	1	3
CHANGES	P 445-10 10-66	1150 Kbrk 1-67

FIXED POINT FUNCTIONS

Begin with register contents as tabled

FUNCTION	ACTION	SA	TA	TB
040	$x:1 = x.n$	n	0	x
041	$x:1 = x.n + 2^{-24}$	n	$2^{22}$	x
042	$x:1 = x.n + x^*$	n	$x^*$	x
044	$x^*:1 = x: \div n$	n	x	$x^*$
045	$x^*:1 = x: \div n$ rounded	n	x	$x^*$
046	$x^*:1 = x^* \div n$	n	Sign $x^*$	$x^*$
111	Shift x: left	N (RZ)	x	$x^*$
113	Shift x: right	N (RZ)	x	$x^*$
114	Normalise x	N	x	0
115	Normalise x:	N	x	$x^*$

MULTIPLY

Multiply SA by TB, product to T. No end effects.

DIVIDE

Divide T by SA forming quotient in TB, End effects.

SHIFT

Shift T. No end effects.

NORMALISE

Normalise T, adjust SA. End effects.

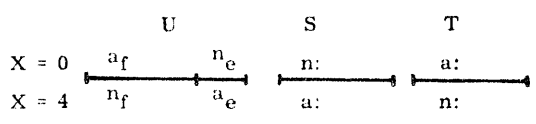
LOOPE1, 2 do arithmetic and shift operations  
 LOOPE3 adjusts exponent in F114/5  
 END1 to 6 do division end effects  
 END7 to 9 do normalise end effects

FLOATING POINT FUNCTIONS

F	X = 0	X = 4
132	$a:1 = a: + n:$	$a:1 = n: + a:$
133	$a:1 = a: - n:$	$a:1 = n: - a:$
134	$a:1 = a: \times n:$	$a:1 = n: \times a:$
135	$a:1 = a: \div n:$	$a:1 = n: \div a:$

F	X = 0	X = 1
136	$a:1 = n:$	$a:1 = 0$
137	$n:1 = a:$	$n:1 = a:, a:1 = 0$

Begin with a: in U, S, T  
 If X = 0 load n: to S and UE  
 If X = 4 load n: to T and UF



LOAD/STORE

F136 uses only LOAD1 to LOAD4  
 F137 uses only DUMP1 to DUMP4

ADD/SUBTRACT [ADSU]

If TE - SE IS +, TE → SE, Shift & round S  
 If TE - SE IS -, UE → SE, Shift & round U, T  
 } T ± S → U  
 } Normalise U, SE

MULTIPLY [MY]

TE + SE → SE. Clear T. Multiply U by S, product to T. (and to U last time round). Normalise U, SE.

DIVIDE [DV]

TE - SE → SE. Shift T right. Clear U. Divide T by S, quotient to U. Normalise U, SE

EXP1 to 4 do all exponent computations  
 ADJ1 to 3 adjust the operand  
 LOOPE1 to 3 do the function  
 NORM1 to 3 normalise the argument  
 ROUND1 to 3 round the argument  
 ENDF1 to 3 test exponent and place the result

ISSUE SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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33 SHEET

3 ISSUE

5016360 / DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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5016360 SL.34

CHANGES

P. 445-10 10-66  
1150 Klock 1-67

# FLOATING POINT FUNCTIONS

## EXPI TO EXP4

Notes:

EXP1 [ALL FUNCTIONS COMP/ADD/SUB EXPs]

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Compare/add/sub exponents  
10 bits wide

$\left. \begin{array}{l} TBE \rightarrow RK, TB8 \rightarrow RK9 \\ \text{If } \overline{MY} : \overline{SBE} \rightarrow RL \\ \text{If } MY : \overline{SBE} \rightarrow RL, INPC \end{array} \right\} RL8 \rightarrow RL9$

Store difference in RZ if positive and  $\leq 63$   
BP stores sign of difference  
No adjustment required if expts. equal  
BR says exp. difference is out of range  
ESB (exp. sign bit) acts as 10th bit  
Dividing by zero

RQ  $\rightarrow$  SBE  
RQ  $\rightarrow$  RZ if  $(\overline{ADSU} \cdot \overline{M9} \cdot \overline{EX} \leq 63)$   
SET BP FROM M9  
SET BZ if EXZERO  
SET BR if  $(\overline{M9} \cdot \overline{EX} > 63)$   
M9  $\rightarrow$  ESB  
IF  $(\overline{DV} \cdot \overline{SANORM})$  SET RFPV and GOTO  
ENDF2 ELSE  
IF  $(\overline{MY} + \overline{DV})$  GOTO EXP4 ELSE GOTO EXP2

BR says exp. difference is out of range

EXP2 [CORRECT NEGATIVE DIFFERENCE.  
A/S ONLY]

$\overline{SBE}, \overline{INPC} \rightarrow RZ$  if  $\overline{BP}$   
SET BR if  $(\overline{BP} \cdot \overline{EX} > 63)$   
SET ESB [READY FOR EXP3]  
GOTO EXP3

ESB (exp. sign bit) acts as 10th bit

EXP3 [SELECT FINAL EXPONENT.  
A/S ONLY]

TBE if BP, UB if  $\overline{BP} \rightarrow$  SBE  
M8  $\rightarrow$  ESB  
GOTO EXP4

Saves splitting the argument  
Ensures correct operand shift in division  
Operand adjustment required

EXP4  
0  $\rightarrow$  TBE, UBE  
RESET BP if  $(\overline{MY} + \overline{DV})$   
GOTO ADJ1 if  $(\overline{DV} + (\overline{ADSU} \cdot \overline{BZ} \cdot \overline{BR}))$  ELSE  
GOTO ADJ3 and CLEAR RZ

- + Represents logical OR
- o Represents logical AND

ISSUE SHEET

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34 SHEET

3 ISSUE

5016360 / 3  
DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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DATE

NOTES

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5016360 35

CHANGES

P 445-10 10-66  
1150 Klock 1-67

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Shift dividend right or 'low exp' operand  
Round in A/S penultimate shift

Computes 'carried down' bit

Shift divided right or 'low exp' operand

Round in A/S penultimate shift

Sign can change by rounding

Clear product register  
Clear quotient register  
Clear T if worthless in A/S  
Clear SF if worthless in A/S

+ Represents logical OR  
● Represents logical AND

# FLOATING POINT FUNCTIONS

## ADJ1 TO ADJ3

Notes:

ADJ1 [ADJUST LEAST SIG. HALF]

{ If  $\overline{BP}$ ,  $UB \rightarrow UBF$ ,  $TBF$ ; and  $TBE$  if DV  
If  $\overline{BP}$ ,  $SB \rightarrow SBF$   
 $+2^9$  if  $RZ=1$ .  $CY23 \rightarrow TCY$   
COUNT RZ  
SHIFT RIGHT  
 $RMSHR23=0$   
 $RMSHR22 = [CY23 \neq (UAO \text{ if } \overline{BP}, SAO \text{ if } BP)]$   
GOTO ADJ2

ADJ2 [ADJUST MOST SIG. HALF]

{ If  $\overline{BP}$ ,  $UA \rightarrow UA$ ,  $TA$ . SET QBIT if ( $RMS23 = SA23$ )  
If  $\overline{BP}$ ,  $SA \rightarrow SA$   
 $INPC = TCY$   
SHIFT RIGHT  
 $RMSHR23 = RMS23 \bullet RMT23$   
 $RMSHR22 = RMS23$   
GOTO ADJ1 if ( $ADSU \bullet \overline{RZZ}$ ) ELSE GOTO ADJ3  
AND CLEAR RZ

ADJ3 [CLEAR REGISTERS]

$0 \rightarrow U$  if DV. SET RZ to 40  
 $0 \rightarrow T$  if MY. SET RZ to 37  
 $0 \rightarrow T$  if ( $ADSU \bullet \overline{BP} \bullet BR$ )  
 $0 \rightarrow SA, SBF$  if ( $ADSU \bullet BP \bullet BR$ )  
GOTO LOOPF1

ISSUE SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30  
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET

D CAT.

35 SHEET

3 ISSUE

5016360 / 3  
DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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5016360 SL 36

CHANGES

ISS.	1	P445-10	10-66
	3	1150	Krook 1-67

# FLOATING POINT FUNCTIONS

## LOOPF1 TO LOOPF3

Notes:

TB ± SB → TB  
 Final produce in multiply or sum or difference in A/S → UB

Maintains sign of l. s. half zero  
 Only one half of multiplier (U) is shifted

TA ± SA → TA. Final product in multiply or sum or difference in A/S → UA

Store initial exp. sign in SB23  
 Overflow in multiply/add/subtract

See LOOPE1  
 Done by bi-phase collector forcing  
 BP avoids rounding after re-normalising  
 Division exp. is 1 out due to dividend shift

Shift appropriate half of multiplier/quotient

See logic of MBIT  
 Enters QBIT into correct place in U

Divide → NORM3 to adjust exp. for dividend shift

- + Represents logical OR
- Represents logical AND

LOOPF1 [ALL FUNCTIONS. OPERATE ON L. S. HALF]

TB → TB; AND UB if ((MY•RZZ) + ADSU)  
 +SBF if (MY•MBIT•RZZ) + (DV•QBIT) + AD  
 +INVSBF, INPC if (MY•MBIT•RZZ) + (DV•QBIT) + SU  
 CY23 → TCY  
 RESET SB23  
 SHIFT RIGHT if (MY•RZZ), LEFT if DV  
 RMSHL0 = 0  
 RQ23 FORCED ZERO if NOT LEFT SHIFT  
 RMSHR22 = RMS23 (0)  
 MBIT IS UB9 WHILE RZ > 23, UA0 WHILE RZ < 23  
 GOTO LOOPF2

LOOPF2 [ALL FUNCTIONS. OPERATE ON M. S. HALF]

TA → TA; AND UA if ((MY•RZZ) + ADSU)  
 +SA if (MY•MBIT•RZZ) + (DV•QBIT) + AD  
 +INVSA if (MY•MBIT•RZZ) + (DV•QBIT) + SU  
 INPC = TCY  
 COPY ESB INTO SB23  
 SET QBIT if RMS23 = SA23  
 SET RPV if ("TR. UA"•OVR)  
 SHIFT AS IN LOOPF1  
 RMSHL0 = TB23  
 RMSHR22 = RMS23. RMSHR23 AS FIXED POINT MPY  
 RMS0 → TB22 if MY•RZZ  
 SET BP [ROUNDING FLAG]  
 SET BR [DIV. EXP. FLAG]

GOTO LOOPF3

LOOPF3 [SHIFT MULTIPLIER/QUOTIENT]

UA → UA if ((MY•RZ < 23) + (DV•RZ > 16))  
 OTHERWISE UB → UB  
 SHIFT AS IN LOOPF1  
 COUNT RZ  
 RMSHR22 = 1 if 'UB'  
 RMSHL0 = QBIT•RZ > 16  
 RMS5 = QBIT•RZ < 16•DIV  
 GOTO LOOPF1 if RZZ ELSE NORMSEQ if DV  
 ELSE NORM3

ISSUE SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET																															

D CAT.

36 SHEET

3 ISSUE

5016360 / 3 DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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DATE

NOTES CHK PLUCK. Krook

5016360 54.37

817858 B

# FLOATING POINT FUNCTIONS. NORMALISE SEQUENCE

ISS.	CHANGES
1	P 445-10 10-66
3	1150 <del>Norm</del> 1-67
9	2895 <del>Norm</del> 12-67

Notes:  
 Argument in U. Exponent in SBE, ESB, SB23      NORMSEQ [NORMALISE ROUTINE ENTRY SWITCHING]

40 Shifts without norm. Therefore argument zero  
 Argument normalised. Rounding required  
 Argument renormalised after rounding  
 Normalising required

If (NORM3•RZ = 23) GOTO ENDF2 ELSE  
 If (RPV•UNORM•BP) GOTO ROUND1 ELSE  
 If (RPV•UNORM•3P) GOTO ENDF1 ELSE  
 GOTO NORM1

NORM1 [NORMALISE L.S. HALF]

UB → UB  
 SHIFT LEFT if  $\overline{RPV}$ , RIGHT if RPV  
 RMSHL0 = 0. RMS22 → TCY  
 RMSHR22 = UA0  
 COUNT RZ  
 GOTO NORM2

Monitors for zero argument

NORM2 [NORMALISE M.S. HALF]

UA → UA. RESET BR  
 SHIFT LEFT if  $\overline{RPV}$ , RIGHT if RPV  
 RMSHL0 = TCY  
 RMSHR22 = RMS23  
 RMSHR23 = RMS23  
 GOTO NORM3

Correct for overflow in right normalise

NORM3 [ADJUST EXPONENT IN SB]

{ SBE → RL, ESB → RL9  
 +1 (INPC); 2046 → RK if  $\overline{RPV} \cdot (DV \cdot BR)$  } → SBE  
 SET BZ if RZ = 23      RQ9 → ESP

Adjust exponent (±1) ± 10 bit opd.  
 40 shifts without norm. Therefore argument zero

GOTO ROUND1 if RPV•BP  
 GOTO ENDF1 if RPV• $\overline{BP}$   
 NORMSEQ if  $\overline{RPV}$

- + Represents logical OR
- Represents logical AND

ISSUE SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30		
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET																																

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37 SHEET

9 ISSUE

5016360 / 9  
DIAGRAM

1900 SCIENTIFIC UNIT LOGIC



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5016360 SL 38

CHANGES

P 445-10 10-66  
1150 Rev 1-67

ISS.

1  
3

# FLOATING POINT FUNCTIONS ROUNDING & FINISHING.

Notes:

Rounding in A/S is before add/sub  
Notes that rounding has occurred

## ROUND 1 [ROUND L.S. HALF]

UB + [2<sup>8</sup> if NOT ADSU] → UB. CY23 → TCY  
RESET BP.  
GOTO ROUND2

## ROUND 2 [ROUND M.S. HALF]

UA + TCY → UA  
SET RPV if OVR  
GOTO ROUND3

## ROUND 3 [IDLE]

0 → UBE. NORMSEQ

## ENDF1 [TEST EXP.OVERFLOW/UNDERFLOW]

SET RFPV if (ESB ≠ SB8) • SB23  
SET BZ if (ESB ≠ SB8) • SB23  
GOTO ENDF2

## ENDF2 [L.S. HALF RESULT]

UB, SBE if  $\overline{BZ}$ ; 2<sup>8</sup> if BZ → UB, SB, TB  
RESYNC  
GOTO ENDF3

## ENDF3 [M.S. HALF RESULT]

UA if  $\overline{BZ}$ ; 0 if BZ → UA, SA, TA  
FPEND

Overflow causes re-normalising

Test normalising required after rounding

Exponent overflow i.e. > +255  
Exponent underflow i.e. < -256. Result zero

Result or FP zero to all registers  
Resynchronise clocks

Function complete

- + Represents logical OR
- Represents logical AND

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
ISSUE SHEET																													
ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET																													

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38 SHEET

3 ISSUE

5016360 / 3  
DIAGRAM

1900  
SCIENTIFIC UNIT  
LOGIC



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DATE

NOTES CHR PLUCK.

H. GORR

417856 B

NAME	SOURCE	REF	NAME	SOURCE	REF	NAME	SOURCE	REF	SOURCE REFERENCE LIST 'A'		REF	CHANGES
									NAME	SOURCE		
SLICE 0	E34	21	BZ(P)	B28/1	18/6Y	C5H11(N)	D20/12	13/5V	DUMP2B(N)	B18/8	8/6X	
SLICE 1	E33		BZ(N)	B28/1	18/6X	C7AK(N)	E21/6	13/3V	DUMP3(P)	B14/3	8/6Y	
SLICE 2	E32		BZA(P)	B20/11	18/6Y	C7BK1(N)	E21/4	13/3W	DUMP3(N)	B14/3	8/6Y	
SLICE 3	E31		BZA(N)	B26/12	18/6X	C7BK2(N)	E21/3	13/3X	DUMP4(N)	B20/7	8/6Y	
SLICE 4	E30					CUAL(N)	E18/6	14/6X	DUMP4(P)	B14/4	8/6Z	
SLICE 5	E29		CDINQ1(P)	E10/4	13/3Y	CUBL(N)	E18/3	14/6Y	DUMPNOTH(N)	D11/1	3/3Z	
SLICE 6	E28		CDINQ1(N)	E19/11	13/3Y	CYB0(N)	E17/3	13/6Z	END(P)	C12/6	5/5Y	
SLICE 7	E27		CDINQ2(P)	D8/2	13/3Y	CYB1(N)	D26/11	26/2Y	END(N)	C12/1	5/5Y	
SLICE 8	E26		CDINQ2(N)	D11/8	13/3Y	CYB2(N)	E26/2	26/3X	END1(P)	B22/4	9/3Z	
SLICE 9	E24		CF0(P)	B16/1	7/2V	CYB3(N)	D24/2	26/4Y	END1(N)	B22/4	9/3Z	
SLICE 10	E23		CF1(P)	B16/2	7/2V	CYB4(N)	E15/11	26/5Y	END2(P)	B23/1	9/6W	
SLICE 11	E22		CF2(P)	B16/4	7/2W	CYB5(N)	F13/2	26/6Y	END3(P)	B23/2	9/6X	
SLICE 12	F20		CF2(N)	B21/3	7/2W	CYC(N)	C31/9	7/6W	END3(N)	B23/2	9/6X	
SLICE 13	F19		CF3(P)	B16/6	7/2W	CYCLEFT(N)	D11/5	7/6V	END3A(N)	B26/5	9/6X	
SLICE 14	F16		CF3(N)	B21/4	7/2X	CY6(N)	D29/3	25/2W	END4(P)	B23/3	9/6Y	
SLICE 15	F15		CF4(P)	B15/1	7/2X	CY10(N)	D26/6	25/2Y	END4(N)	B23/3	9/6Y	
SLICE 16	F14		CF4(N)	B18/2	7/2X	CY14(N)	D26/2	25/4V	END5(P)	B23/4	9/6Z	
SLICE 17	F13		CF5(P)	B15/2	7/2X	CY23(P)	E15/8	25/4Y	END5(N)	B23/4	9/6Z	
SLICE 18	F10		CF5(N)	B18/3	7/2X	CY23(N)	1F2/18*	24/6Z	END6(P)	B27/1	10/3V	
SLICE 19	F8		CF6(P)	B15/4	7/2Y		1E3/11	2/4/6Z	END6(N)	B27/1	10/3V	
SLICE 20	F7		CF6(N)	B21/5	7/2Y				END6A(N)	B26/4	10/3V	
SLICE 21	F6		CF7(P)	B15/6	7/2Y				END7(P)	B27/2	10/3W	
SLICE 22	F3	23	CF7(N)	B21/6	7/2Z				END7(N)	B27/3	10/3X	
SLICE 23	F2	24	CG1(N)	B3/11	2/6W				END8(P)	B27/3	10/3X	
AD11(P)	B29/2	11/4W	CG11(P)	B2/7	2/6V	DIN0(P)	E1/7	2/3X	END9(P)	B27/4	10/3Y	
AD11(N)	B29/2	11/4W	CG11(N)	B3/12	2/6W	DIN1(P)	E1/8	2/3X	END9(N)	B27/4	10/3Z	
AD11A(P)	B30/11	11/4W	CG13(P)	B3/10	2/6W	DIN2(P)	E2/2	2/3W	ENDF1(P)	B33/2	12/6W	
AD11A(N)	B30/10	11/4W	CG13(N)	B2/8	2/6V	DIN3(P)	E1/10	2/3W	ENDF1(N)	B33/2	12/6X	
AD12(P)	B29/3	11/4X	CG4(P)	B2/6	2/6V	DIN4(P)	E1/11	2/3W	ENDF2(P)	B33/3	12/6Y	
AD12(N)	B29/3	11/4X	CG4(N)	B2/6	2/6Y	DIN5(P)	E1/12	2/3V	ENDF2(N)	B33/3	12/6Y	
AD12A(N)	B30/9	11/4W	CHES0K(N)	C13/2	8/2Y	DIN5(N)	ZF44	2/3V	ENDF3(P)	B33/4	12/6Z	
AD13(P)	B29/4	11/4Y	CHES(P)	B5/6	6/2V	DISP(P)	B34/17*	19/6Z	ENDF3(N)	B33/4	12/6Z	
AD13(N)	B29/4	11/4Y	CHES(N)	B4/3	2/6Y	DISP(N)	B34/18*	19/6X	ESB(P)	C16/2	18/2V	
BP(P)	C34/4	19/5V	CISAL(N)	E19/8	14/4Y	DISPNOTTA(P)	B34/13*	19/6X	ES3(N)	C16/2	18/2A	
BP(N)	C34/4	19/5V	CISAL(N)	E19/12	14/4Z	DISPNOTTB(P)	B34/14*	19/6Y				
BPA(N)	B21/1	2/6Y	CISAL(N)	E18/1	14/4Y	DISPNOTSA(P)	B34/11*	19/6W				
BPA(N)	B41/5	2/6X	CISB1.1(N)	E18/2	14/4X	DISPNOTSR(P)	B34/12*	19/6X				
BPA(N)	B18/10	19/5W	CISB1.2(N)	D5/3	13/5Y	DISPNOTTUA(P)	B34/15*	19/6Y				
BR(P)	C33/3	19/5X	CMSQ(N)	C3/3	13/5W	DISPNOTUB(P)	B34/16*	19/6Y				
BR(N)	C33/3	19/5X	CMSQ(N)	E19/10	13/5V	DIV(P)	B15/3	7/6X				
			CQTA(P)	D20/11	15/5W	DIV(N)	B18/1	7/6X				
			CSAL(N)	E21/2	14/2V	DUMP(P)	B38/4	8/6V				
			CSB1.1(N)	E18/4	14/4X	DUMPI(N)	B38/4	8/6V				
			CSB1.2(N)	E18/2	14/4X	DUMPI(P)	B14/1	8/6W				
			CSSL(N)	B18/9	19/5V	DUMPI(N)	B14/1	8/6W				
			CSSL(N)	B19/9	13/5Y	DUMPI(P)	B14/1	8/6W				
			CSSR(P)	C33/3	19/5X	DUMPI(N)	B14/2	8/6X				
			CSSR(N)	C33/3	19/5X	DUMPI(N)	B14/2	8/6X				

NAME		SOURCE	REF	NAME	SOURCE	REF	NAME	SOURCE	REF	SOURCE REFERENCE LIST 'B'	ISS.	CHANGES
EXG63(P)	C15/6	19/5W	FPCHE(S)	B4/7	5/2X	LOADEND(P)	B7/1	8/5Z				
EXP1(P)	B28/2	11/2V	FPEND1(P)	C3/2	5/5X	LOADNOOTH(N)	E19/4	3/5X				
EXP1(N)	B28/2	11/2V	FPH(N)	C2/7	5/5V	LOOPE1(P)	B22/1	9/3V				
			FPSET(P)	I	2/3X	LOOPE1(N)	B22/1	9/3V				
EXP1A(P)	B26/1	11/2V	<b>FPFCTR3(P)</b>	<b>B4/10</b>	<b>5/8W</b>	LOOPE12(N)	E8/5	13/5V				
EXP1A(N)	E19/2	11/2V	FPCDJ(P)	E7/1	3/5W	LOOPE1A(P)	B26/7	9/3W				
FX P2(P)	B28/3	11/2W	GOTOAD11(P)	C30/5	11/4V	LOOPE1A(N)	E19/3	9/3V				
FX P2(N)	B28/3	11/2W	GOTOAD13(N)	E30/7	11/4X	LOOPE2(P)	B22/2	9/3W				
EX P2A(P)	B26/2	11/2X	GOTODUMP(P)	D17/1	10/6W	LOOPE2A(P)	B26/9	9/3X				
EX P3(P)	B28/4	11/2X	GOTODUMP2(P)	B6/6	8/6W	LOOPE2A(N)	B26/8	9/3W				
EX P3(N)	B28/4	11/2X	GOTODUMP4(P)	C8/1	8/6Y	LOOPE3(P)	B22/3	9/3X				
EX P4(P)	B29/1	11/2Z	GOTOEND2(P)	B21/8	9/6V	LOOPE3(N)	B22/3	9/3Y				
EX P4(N)	B29/1	11/2Z	GOTOEND4(P)	C14/3	9/6X	LOOPE3A(N)	B26/10	9/3X				
			GOTOEND7(P)	D24/3	10/3W	LOOPE1(P)	B31/1	14/6W				
F137X1(P)	C6/6	7/2Z	GOTOEND8(P)	C13/6	10/3X	LOOPE1(N)	B31/1	11/6W				
F041(N)	B20/5	7/4V	GOTOENDF1(P)	C21/4	12/6W	LOOPE123(N)	E17/4	13/5X				
F045(N)	B20/1	7/4V	GOTOENDF2(P)	B30/2	12/6Y	LOOPE1A(P)	B30/7	11/6W				
F046(N)	B19/10	7/4W	GOTOEX P4(P)	B26/3	11/2Y	LOOPE1A(N)	B30/6	11/6W				
F111(N)	B19/3	7/4X	GOTOFP(P)	D1/1	10/6Y	LOOPE2(P)	B31/2	11/6X				
F113(N)	B20/3	7/4X	GOTOFP(N)	D11/7	10/6Y	LOOPE2(N)	B31/2	11/6X				
F114(P)	B15/5	7/4Y	GOTOLOAD1(P)	C12/2	8/2V	LOOPE2A(P)	E19/1	11/6X				
F114(N)	B19/2	7/4Y	GOTOLOAD2(P)	B6/1	8/2N	LOOPE2A(N)	B18/11	11/6X				
F115(N)	B19/12	7/4Y	GOTOLOAD3(P)	B10/6	8/2Y	LOOPE3(P)	B31/3	11/6Y				
F136(P)	B15/5	7/4Z	GOTOROUND1(P)	C32/6	12/4V	LOOPE3A(N)	B31/3	11/6Y				
F136(N)	C31/11	7/4Z				LOAD3CO(N)	B30/5	11/6Y				
F137(P)	C8/3	7/4Z	HESR(P)	I	2/6W		D11/5	8/2Z				
F137(N)	B18/4	7/4Z				MANSTEP(N)	C2/6	2/3X				
FD20(N)	C5/1	15/2Y				MBIT(N)	C15/10	14/2W				
FD21(N)	B13/2	16/6V				MCYBO(P)	E19/5	13/6Y				
FD22(N)	B13/4	16/6X				MDINQ1(P)	D1/3	13/3Y				
FIXONLY(P)	I	2/3V	LOAD(P)	B8/3	8/2V	MDLF(P)	E7/4	2/6Z				
FIXONLY(N)	B4/1	2/3V	LOAD(N)	B8/3	8/2V	MDLF(N)	E7/4	2/6Z				
FP(P)	D13/6*	4/5Z	LOAD1(P)	B11/1	8/2W	MEXZ(P)	D23/6	19/2V				
FP(N)	B13/6	7/6Z	LOAD1(N)	B11/1	8/2W	MEXZ(N)	D23/6	19/2V				
FPC0(P)	C15/1	7/6Z	LOAD2(N)	B11/2	3/2X	MILLZ(N)	B21/12	19/2V				
FPC0(N)	B7/2	19/3Y	LOAD2A(N)	B20/8	8/2X	MISAL(P)	E15/1	19/2V				
FPC1(P)	B3/5	19/3Y	LOAD3(P)	B11/3	8/2Y	MISAL(N)	D5/1	14/2Z				
FPC1(N)	B3/6	19/3Y	LOAD3A(N)	B18/6	8/2Y	MISB1(P)	D7/2	14/4Z				
FPC2(N)	B3/7	19/3Z	LOAD4(P)	B11/4	8/2Z	MISB12(P)	D19/5	14/4Z				
FPC3(N)	B3/9	19/3Z	LOAD4(N)	B11/4	3/2Z	MLOOPE1(N)	C14/4	9/3V				
FPCAL(P)	E1/2	4/5Z	LOADA(P)	B20/10	8/2V	MQBIT(P)	F10/2	18/6Y				
FPCHE(P)	C3/1	5/2X	LOADA(N)	B20/9	8/2V	MQSA1(P)	C20/6	15/5Z				
						MQSA2(P)	C11/3	15/5Y				

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	1900 SCIENTIFIC UNIT LOGIC	5016360/8	40	DIAGRAM	D	ISSUE SHEET	ISSUE SHEET
	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30	ISSUE	ISSUE	CAT.	ISSUE	ISSUE	ISSUE

NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET





1900 SCIENTIFIC UNIT LOGIC

DIAGRAM

5016360/11

ISSUE SHEET

41

D

CAT.

ISSUE SHEET

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET

CHANGES

ISS.

2 835 11-66  
11 3252 K6 1-68

SOURCE REFERENCE LIST 'C'

NAME	SOURCE	REF	NAME	SOURCE	REF	NAME	SOURCE	REF
ORJ(P)	I	2/6Z	RPV(N)	C16/4	18/4Z	SA0(N)	F34/1	21/2Y
OVR(P)	F2/41*	24/6Z	RPVA(P)	D20/2	18/4Z	SA10(P)	F28/3	22/2Y
OVR(N)	F2/7	24/6Z	RPVA(N)	D20/1	18/4Z	SA10(N)	F28/3	22/2Y
PROXACT(P)	I	2/5X	RQO(P)	E34/1	21/6X	SA11(P)	F28/4	22/2Y
PROXACT(N)	B2/12	2/6X	RQO(N)	E34/2	21/6X	SA11(N)	F28/4	22/2Y
PTA(P)	E4/2	13/6X	RQj(P)	- / 1	22/6X	SA22(P)	C4/3	23/2Y
			RQj(N)	- / 2	22/6X	SA22(N)	C4/3	23/2Y
			RQ22(P)	F3/1	23/6X	SA23(P)	C4/4	24/2Y
			RQ22(N)	F3/2	23/6X	SA23(N)	C4/4	24/2Y
QBIT(P)	C16/1	18/6Z	RQ23(P)	F2/1	24/6X	SB8(P)	C24/1	22/2Z
QBIT(N)	C16/1	18/6Z	RQ23(N)	F2/2	24/6X	SB8(N)	C24/1	22/2Z
			RV(P)	B4/4	2/6X	SB23(P)	G3/4	24/2Z
			RV(N)	B2/10	2/6X	SB23(N)	G3/4	24/2Z
RESYNC(N)	C19/2	6/2X	RX0(P)	C2/3	2/3Z	SANORM(P)	F7/P	12/6X
RESYNC1(P)	E28/7	6/2X	RX0(N)	B2/3	2/3Z	SHIFT(N)	B19/8	7/6Y
RF0(P)	C2/1	2/3Y	RX1(P)	C2/4	2/3Z	SPEC(P)	D33/6	7/6W
RF0(N)	B3/1	2/3Y	RX1(N)	B2/4	2/3Z	STEP2A(N)	I	2/6Y
RF1(P)	C2/2	2/3Y	RX2(P)	C2/5	2/3Z	STEP4A(P)	F1/3	2/6Z
RF1(N)	B3/2	2/3Y	RX2(N)	B2/5	2/3Z	STEPEND(P)	J	2/6Y
RF2(P)	B3/4	2/3Y	RY0(P)	B8/1	19/3Y	SYNC(P)	E29/7	6/5Y
RF2(N)	B3/3	2/3Y	RY0(N)	B8/1	19/3Y	SYNC(N)	F29/P	6/5Y
RFVP(N)	G1/4	18/2X	RY1(P)	B8/2	19/3Z	SPECFPC3(N)	B12/1	19/2X
RFVP(N)	D17/3	22/3Z	RY1(N)	B8/2	19/3Z			
RMGO(N)	E34/5	21/6Y	RZ0(P)	C34/1	17/4V	TA22(P)	F5/3	23/2X
RMGj(N)	- / 5	22/6Y	RZ0(N)	C34/1	17/4W	TA22(N)	F5/3	23/2X
RMG22(N)	F3/5	23/6Y	RZ1(P)	C34/2	17/4W	TA23(P)	F5/4	24/2X
RMS0(N)	D20/9	21/6Y	RZ1(N)	C34/2	17/4W	TA23(N)	F5/4	24/2X
RMS0(P)	E34/9	21/6Y	RZ2(P)	C34/3	17/4X	TANORM(P)	F3/7	10/3Z
RMSj(P)	- / 9	22/6Y	RZ2(N)	C34/3	17/4X	TB0(P)	F32/1	21/2X
RMS22(P)	F3/9	23/6Y	RZ3(P)	C34/4	17/4X	TB0(N)	F32/1	21/2X
RMS23(P)	F2/9	24/6Y	RZ3(N)	C34/4	17/4X	TB22(N)	F4/3	23/2X
RMS23(N)	D11/9	24/6Y	RZ4(P)	C33/1	17/4Y	TB23(P)	F4/4	24/2X
RMT0(N)	E34/6	21/6X	RZ4(N)	C33/1	17/4Y	TB23(N)	F4/4	24/2X
RMTj(N)	- / 6	22/6X	RZ5(P)	C33/2	17/4Z	TCY(P)	C16/3	18/2Z
RMT22(N)	F3/6	23/6X	RZ5(N)	C33/2	17/4Z	TCY(N)	C16/3	18/2Z
RMT23(N)	F2/6	24/6X	RZG16(P)	C15/5	17/6X	UA0(P)	C33/1	21/2W
ROUND1(P)	B32/3	12/4V	RZG16(N)	D32/2	17/6X	UA0(N)	C33/1	21/2W
ROUND1(N)	B32/3	12/4V	RZG23(P)	D32/4	17/6X	UA1-3(N)	C33	
ROUND1A(P)	B30/1	12/4W	RZZ(P)	E21/1	17/6W	UA4-7(N)	C28	
ROUND2(P)	B32/4	12/4X	RZZ(N)	B18/2	17/6W	UA8-11(N)	C23	
ROUND2(N)	B32/4	12/4X				UA12-15(N)	C14	
ROUND3(P)	B33/1	12/4Y	SA0(P)	F34/1	21/2Y			
RPV(P)	C16/4	18/4Z						

NAME	SOURCE	REF
UA16-19(N)	G8	22/2W
UA20-21(N)	G2	23/2W
UA22(P)	G2/3	23/2W
UA22(N)	G2/3	24/2W
UA23(P)	G2/4	24/2W
UA23(N)	G2/4	21/2W
UB0(N)	G32/1	
UB1-3(N)	G32	
UB4-7(N)	G27	
UB8-11(N)	G22	
UB9(P)	G22/2	22/2W
UB12-15(N)	G13	
UB16-19(N)	G7	
UB20-21(N)	G1	
UB22(N)	G1/3	23/2W
UNORM(N)	F6/P	12/6V
WAITSYNC1(P)	D33/1	6/2X
XACT(N)	C31/6	6/5V
XACT3A(P)		6/6W
XACT3B(P)		6/6X
XACT3C(P)	C24/2	
XACT4A(P)		
XACT4B(P)	C24/3	
XACT4C(P)		
XACT4D(P)		
XACT4E(P)		
XACT5A(P)		
XACT5B(P)		
XACT5C(P)	G19/2	
XACT5D(P)		
XACT5E(P)		
XGO(P)	I	2/3X
XGO(N)	B2/1	2/3Y
XMID(N)	C31/7	6/6X
XQSA0(P)	F22/1	15/5Y

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SOURCE REFERENCE LIST 'D'		ISS.	CHANGES
		2	835 11-66
NAME	SOURCE	REF	
XQSA1(P)	F22/2	15/5Z	
XQSB0(P)	G18/3	16/3Y	
XQSB1(P)	G19/1	16/3Y	
XQSB2(P)	G20/1	16/3Y	
XQTA0(P)	F21/3	15/5V	
XQTA1(P)	F22/3	15/5W	
XQTA2(P)	F23/3	15/5W	
XQTA3(P)	F23/2	15/X	
XQTB0(P)	F21/1	16/3V	
XQTB1(P)	F21/2	16/3W	
XQTB2(P)	F23/1	16/3W	
XQUA0(P)	G18/2	16/6V	
XQUA1(P)	G20/2	16/6W	
XQUB0(P)	G18/1	16/6X	
XQUB1(P)	G19/3	16/6Y	
XQUB2(P)	G20/3	16/6Z	
XSEQ5A(P)	B17/1	6/6Z	
XSEQ5B(P)		6/6Z	
XSEQ5C(P)	B17/2	6/6Y	
XSEQ5D(P)		6/6Y	
XSEQ5E(P)	B17/3	6/6Z	
XSEQ6A(P)		6/6Z	
XSEQ6B(P)	C24/1	6/6Y	
XSEQ6C(P)		6/6Y	
XSEQ6D(P)	C26/3	6/6X	
XSEQ6E(P)		6/6X	
XSEQ7A(P)	C26/2	6/5W	
XSEQ7B(P)		6/5W	
XSEQ7C(P)	C26/2	17/3V	
XSEQ7D(P)		17/3V	
XSEQ7E(P)			
XSEQ8A(P)			
XSEQ8B(P)			
XSEQ8C(P)			
XSEQ8D(P)			
XSEQ8E(P)			
XSEQ9A(P)			
XSEQ9B(P)			
XSEQ9D(P)			
XZB(P)			
XZC(P)			

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ISSUE SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30

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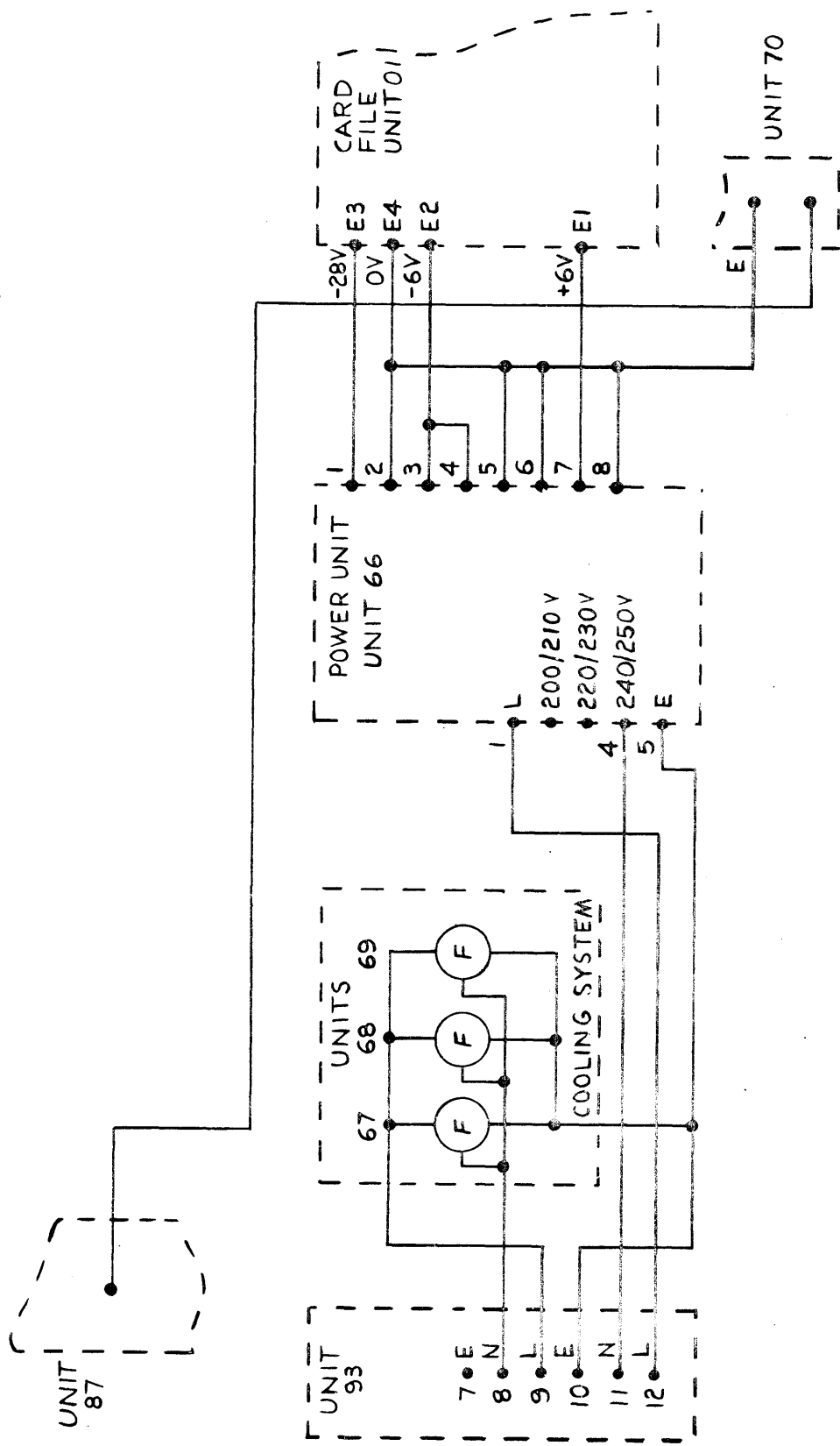
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FIG 25

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CHANGES								
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835				11-66				

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# 1900 SCIENTIFIC UNIT INDEX

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2 835 11-66  
3 1150 No 1-67

CHANGES

SHT NO.	LOGIC GROUP
1	BLOCK DIAGRAM
2	INTERFACE IN
3	INTERFACE OUT 'A'
4	INTERFACE OUT 'B'
5	INTERFACE OUT 'C'
6	TIMING CONTROL
7	FUNCTION DECODE
8	INTERFACE STEPS
9	FIXED POINT STEPS 'A'
10	FIXED POINT STEPS 'B'
11	FLOATING POINT STEPS 'A'
12	FLOATING POINT STEPS 'B'
13	CONTROLS RK - MILL
14	CONTROLS RL
15	REGISTER TRIGGERS 'A'
16	REGISTER TRIGGERS 'B'
17	SHEET COUNTER
18	BINARIES
19	CHARACTER COUNTER
	MILL TEST
	ENGINEER'S CONTROLS
20	MILL ATTACHMENTS
	MILL BRICK LOCATION TABLE
21	MILL SECTION O

SHT NO.	LOGIC GROUP
22	MILL SECTIONS 1-21
23	MILL SECTION 22
24	MILL SECTION 23
25	CARRY
26	BLACK CARRY
27	MILL PACKAGE
28	INTERFACE PROCEDURES - LOADING
29	INTERFACE PROCEDURES - LOAD1 TO LOAD4
30	INTERFACE PROCEDURES - DUMP1 TO DUMP4
31	FIXED POINT FUNCTIONS - LOOP1 TO LOOP3
32	FIXED POINT FUNCTIONS - END1 TO END9
33	FIXED POINT FUNCTIONS
34	FLOATING POINT FUNCTIONS
35	FLOATING POINT FUNCTIONS - EXPI TO EXP4
36	FLOATING POINT FUNCTIONS - ADJ1 TO ADJ8
37	FLOATING POINT FUNCTIONS - LOOP1 TO LOOP3
38	FLOATING POINT FUNCTIONS - NORMALISE SEQUENCE
39	FLOATING POINT FUNCTIONS - ROUNDING & FINISHING
40	CROSS REFERENCE SOURCE LIST 'A'
41	CROSS REFERENCE SOURCE LIST 'B'
42	CROSS REFERENCE SOURCE LIST 'C'
43	CROSS REFERENCE SOURCE LIST 'D'
44	POWER SUPPLIES SCHEMATIC INDEX



NOTES *CHK PULL. K. K. K.*

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1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
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