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Memorandum M-2651

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By: R. L. Everett
Date: 2-15-60

Division 6 - Lincoln Laboratory
Massachusetts Institute of Technology
Cambridge 39, Massachusetts

SUBJECT: GROUP 62 SECTION LEADERS MEETING 25 January 1954

To: Division 6 Group Leaders; Group 62 Staff Members

From: A. P. Kromer

Abstract: Decision on the display system and basic circuits for the instruction control frame were reached during the week. A revised basis for release of design and construction by IBM in advance of Lincoln approval has been proposed by Project High. Initial negotiations leading to an Air Force contract for two duplex centrals (in addition to the XD-1 and XD-2 systems) have been started.

1. Display

Consideration of the display system has led to agreement to use an inscribed square (19" diagonal) on the 19" round Charactron tube for the Situation display. Also, digital expansion method will be used providing expansion ratios of 2, 4 and 8.

IBM has proposed a magnetic core display selection system (using approximately 3500 mo-permalloy cores for each of the two systems in a duplex central).

A basic deflection amplifier with high gain will be designed for all consoles. An IBM plugboard unit will be used to patch in one of four levels of feedback and thus provide flexibility and standardization.

A meeting will be held on Tuesday to discuss and settle the physical layout and arrangement for manual input switches and other features of the display consoles.

2. Memory

The 64 x 64 memory will be installed in MTC on Monday or Tuesday of this week.

LIN. LAB. DIV. 6
DOCUMENT ROOM
3. Power

DO NOT REMOVE
FROM
THIS ROOM

F. Sandy will spend full time in Room 62 starting 2-1-54. He will work on power controls and distribution for the FSQ-7 along with IBM personnel.

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4. Circuits

Final release for 10 basic circuits for the instruction control frame was reached at a meeting in Poughkeepsie last week. The circuits involved were the following:

Model A Flip-Flop	Model B Register Driver
Model A Gate Tube	DC Diode Circuits
Model A Pulse Amplifier	Pulse Diode Circuits
Model B Pulse Amplifier	Standard 5965 Cathode Follower
Model A Register Driver	Power Cathode Follower

(Note: This agreement also covers the use of these circuits in all other authorized applications in the system.)

Three additional circuits for the instruction control frame, namely, a delay line, push-button pulse generator, and relay pull-in circuit, remain to be agreed upon.

5. IBM-MIT Concurrence

IBM has reconsidered the proposed procedure for joint concurrence and Lincoln release on specifications for design and for prototype construction. (Refer to Minutes of Section Leaders Meeting of January 11, M-2620.) They have now proposed that Lincoln's activity be principally advisory and handled as confirmation of action already taken by IBM. This latest proposal is advanced in the interest of saving time. It is to be reviewed further in joint discussions here during the current week.

6. Drums

The drum is now operating with MTC in a limited manner. It is planned to complete sufficient work to permit evaluation of diode switching of read-write circuits as compared to magnetic switching at a meeting planned for February 2 at Cambridge. The diode switching arrangement will be demonstrated at that time.

7. Production After the Prototypes

IBM received a better contract proposal for two duplex centrals and an advance authorization of 5 million dollars which they can expend against this contract pending negotiation of a final definitive contract.

In connection with IBM's plans to erect a plant in Kingston, N.Y., they have rented space in that city to start operations which will permit hiring and training of operators on work to be transferred from Plant 2.

Signed


A. P. Kromer

Approved


N. H. Taylor

APK/mmt

CC: J. W. Forrester, R. R. Everett

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