

6M-3938

Division 6 - Lincoln Laboratory  
Massachusetts Institute of Technology  
Lexington 73, Massachusetts

SUBJECT: THE LOGICAL STRUCTURE OF DIGITAL COMPUTERS  
The Turing Machine

To: Class Registrants

Abstracts to: All Lincoln Division Heads and Group Leaders

From: W. A. Clark

Approved: W. A. Clark  
W. A. Clark

Date: 5 October 1955

ABSTRACT: The logic of the Turing machine as a symbol manipulator is described and examples of counting and sorting are explained. A set of problems is included.

## INTRODUCTION

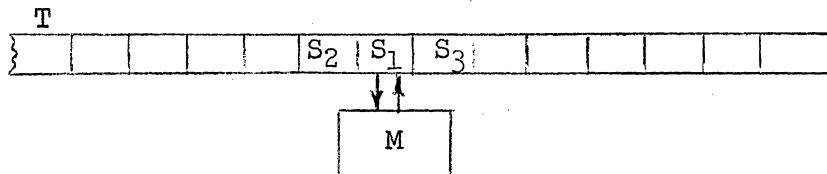
The subject of this course is the Logical Structure of Digital Computers. By "computer logic" one means the set of rules which the computer follows in carrying out its operations. Logical structure is to be distinguished from physical structure. The electronic components, wires, motors, and other hardware, comprising the physical structure of the computer, do no more than mechanize the operating rules defining the logical structure of the computer. It is completely irrelevant to the logic that the computer is built of vacuum tubes, or relays, or paper, as long as the rules are properly represented and followed.

The digital computer is essentially a symbol-manipulating machine. It accepts a set of symbols defining a problem to be solved and the data on which to operate. It then performs various operations on these symbols according to the rules defining its logical structure, and thereby produces a new set of symbols which comprise the solution to the problem. The rules thus take the form of a set of statements describing the manner in which certain symbols are to be replaced with new symbols. (Consider, for example, a particular sequence of five symbols. One useful rule in a computer dealing with this sequence would be: "If the first symbol is a '1', the second '+', the third '1', the fourth '=', and the fifth 'x', then replace the symbol 'x' with the symbol '2'.")

## THE TURING MACHINE

A simple abstract model of the general symbol-manipulation process (and, therefore, of digital computer logical structure) was formulated by the British mathematician, Turing<sup>1</sup> as a conceptual aid in proving certain results in mathematical logic. He defined a class of symbol processing mechanisms which he called simply "automatic machines," but which are now generally known as "Turing machines."

The elements of the Turing machine are illustrated below:



<sup>1</sup>A. M. Turing: On Computable Numbers, with an Application to the Entscheidungs Problem, Proc. Lond. Math. Soc., series 2, V24, pp. 230-265, 1936.

A machine,  $M$ , having a finite number of internal configurations or states operates on an infinitely long tape,  $T$ , which is divided into cells. Each cell is capable of bearing one symbol from a specified, finite set of symbols,  $S_0, S_1, S_2 \dots S_n$ , e.g., the alphabet, the digits, etc. The machine deals with one cell at a time (called the scanned cell) and can read the symbol in this cell and write a new symbol in its place, or move to the next cell to the right or to the left.

An operation is carried out concurrently with a jump from one machine state to another. This action is completely determined by the current state of the machine and the currently scanned symbol. Each move results in a new configuration of machine and tape in which the scanned symbol and machine state determine the next move, and so on.

A notation will now be described and some examples of Turing machines presented. This material will differ from that in Turing's original presentation, but the essential features are retained.

The operations to be discussed are:

- 1) Replace the scanned symbol,  $S_i$ , with the symbol  $S_j$ , abbreviated:

$$S_i : S_j$$

where  $i$  and  $j$  may have any particular values  $0, 1, 2, \dots n$ . The symbol  $S_0$  will represent blank tape to complete the description.

- 2) If the scanned symbol is  $S_i$ , move to the next cell on the right:

$$S_i : R$$

- 3) If the scanned symbol is  $S_i$ , move to the next cell on the left:

$$S_i : L$$

These operations can be abbreviated:

$$S_i : T_k \quad \left\{ \begin{array}{ll} T_k = \text{print } S_k & k = 0, 1, \dots n \\ T_k = \text{move "R"} & k = n + 1 \\ T_k = \text{move "L"} & k = n + 2 \end{array} \right.$$

The rules by which the machine operates are then formulated in terms of these operations and the internal states of the machine. Each rule will be of the form:

<p><u>If</u> the machine is in state <math>p</math> (<math>p = 1, 2, \dots, M</math>)</p> <p><u>and</u> the scanned symbol is <math>S_i</math> (<math>i = 0, 1, \dots, N</math>)</p> <p><u>then</u> carry out operation <math>T_k</math> (<math>k = 0, 1, \dots, N + 2</math>)</p> <p><u>and</u> jump to state <math>q</math> (<math>q = 1, 2, \dots, M</math>)</p>	<p>(<math>p = 1, 2, \dots, M</math>)</p> <p>(<math>i = 0, 1, \dots, N</math>)</p> <p>(<math>k = 0, 1, \dots, N + 2</math>)</p> <p>(<math>q = 1, 2, \dots, M</math>)</p>
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which will be abbreviated to the quadruple:

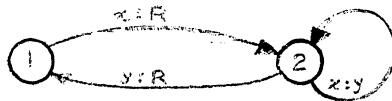
$$(p, S_i : T_k, q)$$

For example, the quadruple  $(3, x : R, 14)$  means "If the machine is in state 3 and the scanned symbol is  $x$ , move to the next cell on the right and jump to state 14."

The logical structure of the machine is thus specified by a finite set of quadruples of the above form.

The ordered pair of symbols  $p, S_i$  will be called a determinant, since it determines the subsequent move of the machine according to the remaining terms in the quadruple. To be consistent, the requirement is imposed that no two quadruples describing a given machine can have the same determinant.

The logical structure of a Turing machine may be represented conveniently as a network in which each node corresponds to a state of the machine and each directed branch between nodes corresponds to a jump between states. The branch is labeled with the operation which occurs during the jump. For example, the network



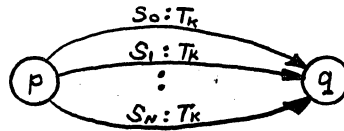
corresponds to the set of three quadruples

$$\begin{aligned} &(1, x : R, 2) \\ &(2, x : y, 2) \\ &(2, y : R, 1) \end{aligned}$$

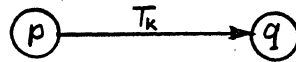
A drawing of the network for a given machine is variously called a state diagram or transition diagram. Two conventions which simplify

the drawing of transition diagrams for processes involving a large number of symbols  $S_0, S_1, S_2, \dots, S_n$  are the following:

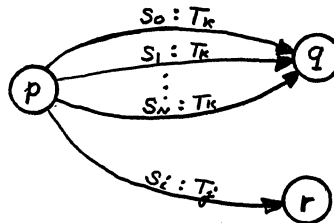
- a) If all branches from a given state,  $p$ , lead to state  $q$ , and involve the same operation,  $T_k$



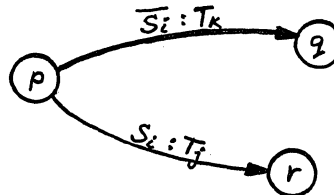
then the diagram may be abbreviated to



- b) If all branches except the one for a particular scanned symbol,  $S_i$ , lead from state  $p$  to state  $q$ , and involve the same operation,  $T_k$



then the diagram may be abbreviated to

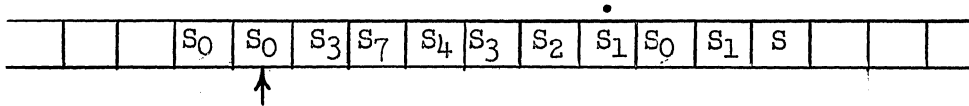


where  $\overline{S_i}$  is read "not  $S_i$ ."

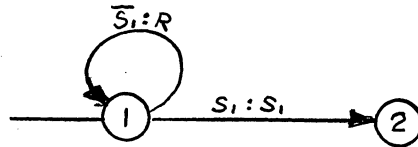
A few examples of Turing machines will now be given to illustrate the preceding definitions and concepts.

Example 1

Given a tape on which the symbols  $S_0, S_1, S_2, \dots, S_n$  appear in any order and number. The machine starts in state 1 scanning any cell to the left of a cell holding the symbol,  $S_1$



The machine is to "hunt" for the first cell to the right which holds  $S_1$  and jump to state 2 when it is scanning this cell. The transition diagram is



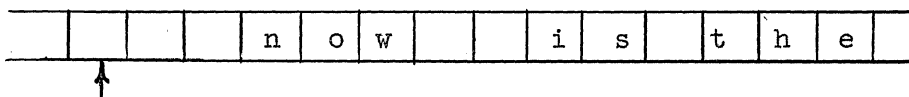
The machine remains in state 1 and scans the next cell on the right until  $S_1$  is found, whereupon it jumps to state 2 with no change of symbol on the scanned cell.

Example 2

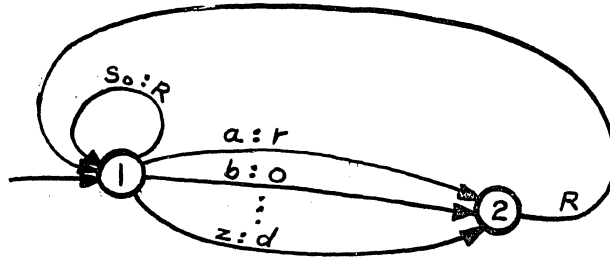
Consider a process of simple cryptographic encoding. Letters of the alphabet are to be scrambled according to the code

- a → o
- b → r
- c → p
- 
- 
- 
- z → d

e.g., the word "cab" becomes "por", etc. The message to be encoded is printed on the tape with an arbitrary number of spaces between words.



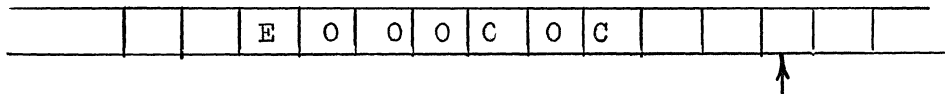
The machine starts in state 1, scanning the indicated cell. Its diagram is



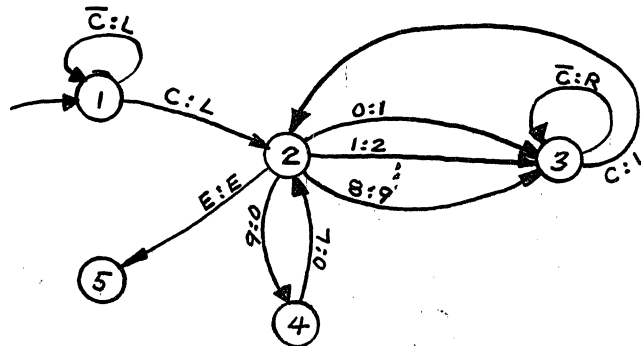
The symbol  $S_0$  represents blank tape. The machine continues indefinitely, changing letters of the message to their equivalent code value.

Example 3

A block of five cells, each holding the digit 0, is separated from the rest of the tape by the symbol E on the left and C on the right.



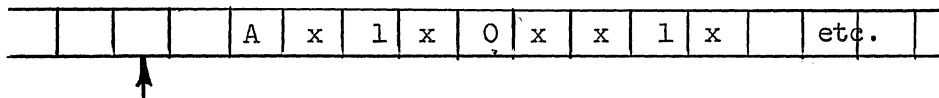
The machine starts in state 1, scanning a cell to the right of C. It is to print in succession the 5-digit decimal numbers from 0 to 99,999 on the marked block of cells, reset them to 0, and then jump to state 5. Its diagram is



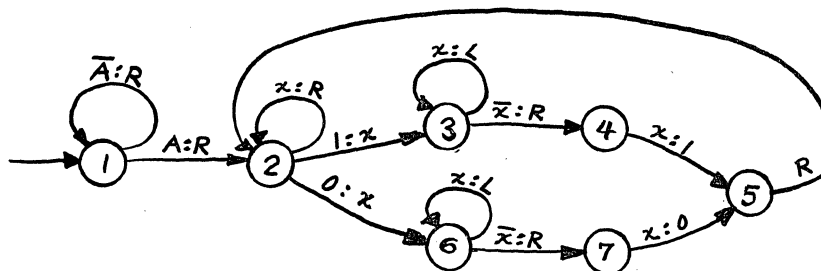
The machine finds the first digit following symbol C and jumps to state 2. If this digit is not 9, it is replaced with the next larger digit and the machine jumps to state 3. In state 3, the machine moves right until it finds C and backs up one cell, jumping back to state 2. When the digit is 9, it is replaced with 0 and the machine jumps to state 4 and moves left one cell (corresponding to a "carry" from one digit position to the next), returning to state 2 again. Clearly, this process results in the printing of the required sequence of numbers up to 99,999. At this point, the repeated sequence of transitions (2, 9 : 0, 4) (4, 0 : L, 2) resets the five cells to 0 and the process terminates with (2, E : E, 5).

#### Example 4

Consider a tape marked with A, 1, 0, and x in the following manner:



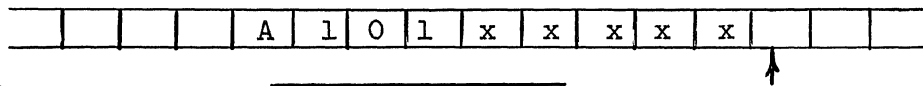
The 1's and 0's are intermixed with x's to the right of A in an arbitrary way. The machine starts in state 1, scanning a cell to the left of A, and is to compact the sequence of 1's and 0's into a block following A. The order of the 1's and 0's is to be retained. The diagram is



In state 1, the machine finds the first symbol to the right of A and jumps to 2. In state 2, the machine skips over cells holding x's and finds the nearest 0 or 1, replaces it with an x and jumps to state 6 or 3, respectively. In the



case illustrated, the nearest non-x symbol is a 1, and the machine will go to state 3. In state 3, the machine finds the leftmost x, jumps to 4, prints a 1, and moves to the right, returning to state 2 via 5. Note that in taking either the upper or lower branch,  $(2, 1 : x, 3)$  or  $(2, 0 : x, 6)$ , the machine in effect "remembers" that the last symbol scanned was a 1 or 0, respectively. The reader should verify that the tape illustrated becomes:



It is possible to restrict the Turing machine to two symbols, 0 and 1, without loss of generality.

Consider a problem which is expressed in terms of four symbols:  $S_0, S_1, S_2, S_3$ . These can be encoded into groups of 1's and 0's in many ways. For example:

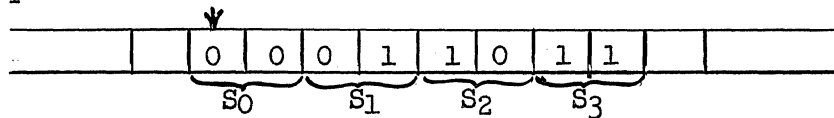
- $S_0 = 000$
- $S_1 = 001$
- $S_2 = 010$
- $S_3 = 100$

(1)

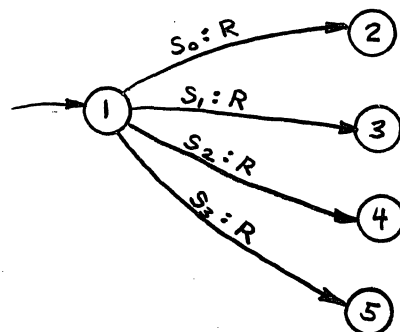
- $S_0 = 00$
- $S_1 = 01$
- $S_2 = 10$
- $S_3 = 11$

(2)

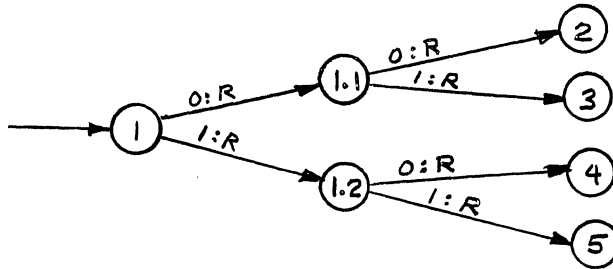
Case (2) will be discussed in more detail. The tape is divided into groups of two cells each:



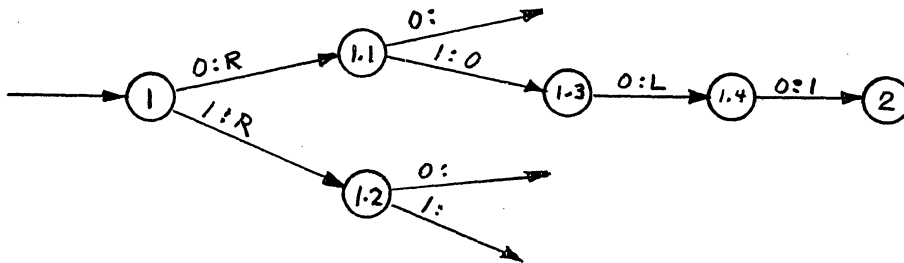
Then consider a section of the transition diagram of a machine dealing with  $S_0, S_1, S_2, S_3$  which is of the form



This is equivalent to the following in which only 0 and 1 are used:



Similarly, a change of symbol, say  $(1, S_1 : S_2, 2)$  can be represented as:



Note that the interpretation of the sequence of 1's and 0's depends on the direction of travel. This dependence could be eliminated by using a symmetrical code:

$$\begin{cases} S_0 = 000 \\ S_1 = 010 \\ S_2 = 101 \\ S_3 = 111 \end{cases}$$

We have now shown that the operations of any Turing machine can be reduced to the set

$$\begin{cases} \text{print 0} \\ \text{print 1} \\ \text{move right one cell} \\ \text{move left one cell} \end{cases}$$

This can be reduced still further. The only possible symbol-printing situations are:

$$\left. \begin{array}{l} 0 : 1 \\ 1 : 0 \end{array} \right\}$$

$$\left. \begin{array}{l} 0 : 0 \\ 1 : 1 \end{array} \right\}$$

The first two cases involve a change of information on the tape; the second two do not. We can define an operation "complement," abbreviated "C", to replace the two print operations. The cases involving no change of symbol are replaced with two complement operations done in sequence:



Thus, the set of Turing machine operations reduces to:

$$\left. \begin{array}{l} C \\ L \\ R \end{array} \right\}$$

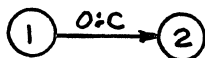
This can be reduced again to any of the three sets:

- (1)  $\left\{ \begin{array}{l} CL \quad \text{complement and move left} \\ R \quad \text{move right} \end{array} \right.$
- (2)  $\left\{ \begin{array}{l} L \quad \text{move left} \\ CR \quad \text{complement and move right} \end{array} \right.$
- (3)  $\left\{ \begin{array}{l} CL \quad \text{complement and move left} \\ CR \quad \text{complement and move right} \end{array} \right.$

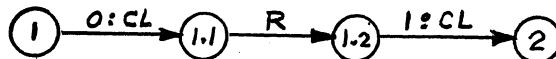
The proofs for (1) and (2) reduce to showing that "CL" can be broken into "C" and "L":



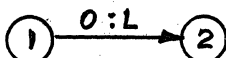
is the equivalent of



and

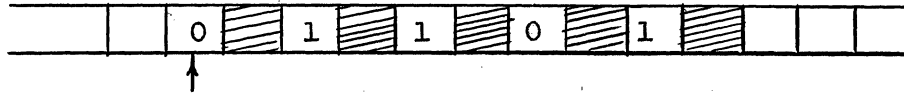


is the equivalent of



The other possible actions on 0 and 1 for cases (1) and (2) are proved in a similar manner.

Case (3) requires a different arrangement of symbols on the tape. Alternate cells are used to hold the symbols of the problem. The cells in between aid in "phasing" the complementing, but hold no significant information:



The equivalent forms are:



The other forms can be obtained directly from these.

PROBLEMS

- 1.1) A Turing machine tape is marked in the following manner:

			P	$d_i$	$d_j$	$d_k$	$d_l$	$d_m$		
--	--	--	---	-------	-------	-------	-------	-------	--	--

Symbol "P" marks the beginning of a block of 5 different decimal digits,  $d_i, d_j, d_k, d_l, d_m$ . (e.g., 7 0 9 3 4).

Describe (draw a state-and-transition diagram of) a machine which will rearrange the digits in descending order on the 5 cells following "P" and move on to the right when finished. Any additional symbols and cells may be used in the process providing they are erased upon completion. The machine is to start on any cell to the left of "P".

- 1.2) Restate problem 1.1 in terms of a tape on which only the symbols " $S_0$ " and " $S_1$ " appear. (Invent a suitable code for the symbols "P", blank, 0, 1, . . . 9, etc., and describe the initial tape configuration). Redraw the state-and-transition diagram accordingly, using only the operations "complement" (change  $S_0$  to  $S_1$  or  $S_1$  to  $S_0$ ), "move right", and "move left", abbreviated "C", "R", and "L", respectively.
- 1.3) Non-erasable tape can be defined as tape on which it is possible to write a symbol in a given cell only if the cell is blank. Show that it is possible for any Turing machine to use non-erasable tape.

6M-3938, Supplement 1

Division 6 - Lincoln Laboratory  
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SUBJECT: THE LOGICAL STRUCTURE OF DIGITAL COMPUTERS  
The Universal Turing Machine

To: Class Registrants

Abstract to: J. C. Proctor, C. W. Farr

From: W. A. Clark

Date: 20 October 1955

Abstract: The complexity of a Turing machine can be measured by the number of quadruples defining its logical structure. At a certain level of complexity, it becomes possible to design a Turing machine which is universal in the sense that it can perform any calculation which any other Turing machine can perform. The Universal Turing Machine achieves this generality by having the ability to simulate other Turing machines, even those which are more complex. This simulation process is described in terms of the manipulation of the quadruples themselves. An example of a Universal Turing Machine is presented in detail, and a set of related problems is included.

  
W. A. Clark

WAC/jhk

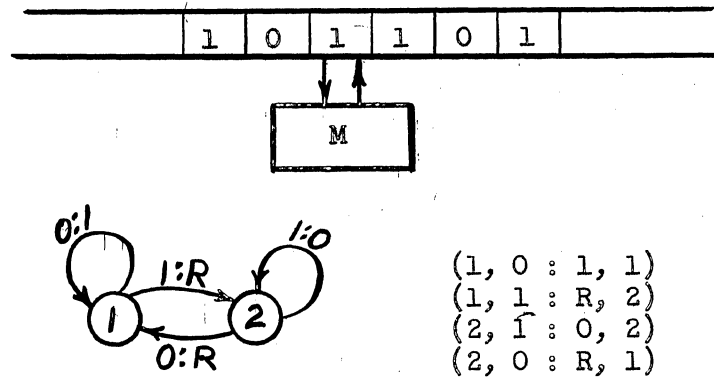
THE UNIVERSAL TURING MACHINEPreliminary Remarks

We have seen that a very general class of Turing machines can be defined which use a single tape on which only the symbols "0" or "1" appear in any cell ("0" corresponding to blank tape) and which perform only the operations "print 0," "print 1," "move right one cell," and "move left one cell." For convenience in subsequent reference, we will call these machines "Class A Turing machines" to distinguish them from other machines which use more symbols, different operations, more tapes, etc. Within a given class one might measure the complexity of a particular machine by the number of its internal states or, perhaps better, by the number of quadruples required to describe its logical structure. Thus, a 100-quadruple Class A Turing machine would be more complicated than a 10-quadruple Class A Turing machine.

It seems reasonable to attempt to relate a machine's complexity to its capability. For example, it is possible to combine a machine,  $M_1$ , which is capable of counting, with a machine,  $M_2$ , which is capable of ordering a set of numbers, and thereby obtain a more complicated machine,  $M_3$ , which is capable of both ordering and counting. It might be supposed that it is always possible to increase the generality of a machine by increasing its complexity in this way. The fact is, however, that there is a critical complexity beyond which no further increase in generality can be guaranteed! That is, at a certain level of complexity it becomes possible to design a Turing machine which is universal in the sense that it can perform any calculation which any other Turing machine can perform, even if the other machine is more complicated than the universal machine. The universal Turing machine achieves this generality by having the ability to simulate any machine whose calculation it is required to duplicate. The tape of the simulated machine appears as a designated sequence of cells on the tape of the universal machine. We will consider these points in more detail later.

Quadruple Manipulation

The simulation is itself a symbol manipulation process in which the symbols represent the set of quadruples describing the simulated machine. As an example of the manipulation of quadruples, consider the following simple Class A machine and the set of quadruples describing its logical structure:



This machine, starting in state 1, will print the sequence 10101010..  
 ..... Its operation will now be described in terms of the quadruples  
 and scanned symbols:

Define active quadruple to be that quadruple which describes the action of the machine at any given point in the process. The active determinant is then the determinant found in the active quadruple (see page 3). The first term of the active quadruple will be called the initial state, and the last term, the final state. The second term of the active quadruple is, of course, the scanned symbol, and the third term is the specified operation.

In the illustration, if the machine is in state 1, the active quadruple is the second one in the list, namely:

$$(1, 1 : R, 2)$$

The machine will move one cell to the right and jump to the final state 2. State 2 thus becomes the initial state of the next machine action and the new scanned symbol is a "1". Therefore, the next active determinant will be:

$$\underline{2, 1}$$

The next active quadruple can be found by again examining the list of quadruples and finding the quadruple which starts with the determinant 2, 1. In this case, it is the third determinant:

$$(2, 1 : 0, 2)$$

The machine will print a "0" and jump to the final state 2. The scanned symbol is now "0" and the next initial state is 2;



thus, the next active determinant will be

2, 0

and the next active quadruple is found to be

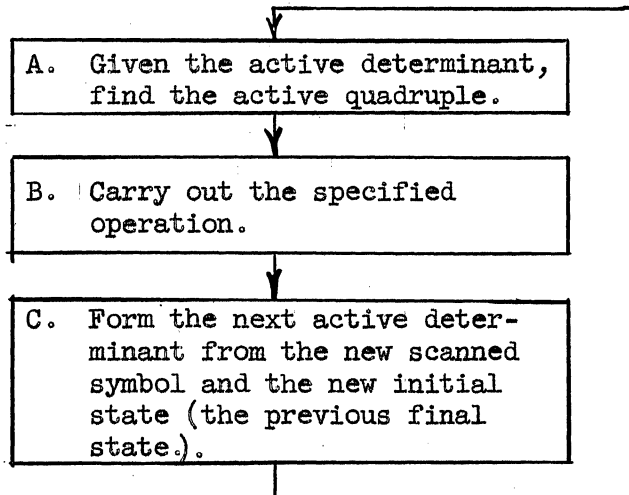
(2, 0 : R, 1)

etc.

This example illustrates the use of the set of quadruples in describing a sequence of machine actions.

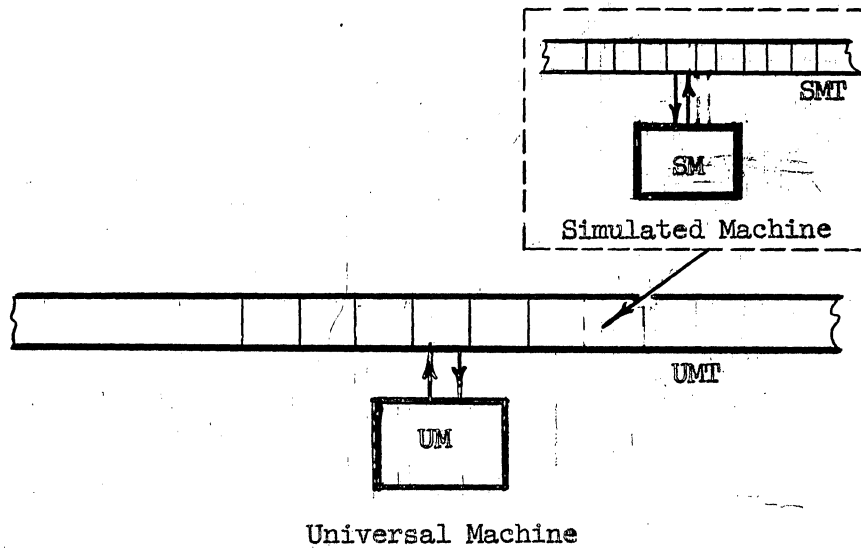
### General Description of the Universal Turing Machine

The basic simulation process can be represented in the following way:



It is, of course, necessary to start this sequence with the first active determinant at A.

The Universal Turing Machine, UM, will be designed to carry out the above steps A, B, and C for any list of quadruples describing a given simulated machine, SM, which will be encoded in a suitable manner and printed on the universal machine tape, UMT. As we mentioned earlier, this tape will also hold a sequence of cells which correspond to the cells of the simulated machine tape, SMT. It will also require cells on which to print the active determinant symbols, and cells to mark significant points on the tape, e.g., the SMT scanned cell.



Before UM begins its calculation, the quadruples defining SM are printed on UMT, the cells on UMT corresponding to SMT are marked to match the initial configuration of SMT, and the first active determinant is printed on UMT. UM is started in a specified initial state scanning a specified cell on UMT. It then carries out steps A, B, and C without end and prints the results of SM's calculation on the designated UMT cells corresponding to SMT. It is assumed that the set of symbols used by SM is included within the set of symbols used by UM.

#### Detailed Description of a Class A Universal Turing Machine

The general description of the previous section will now be related to a particular Class A Universal Turing Machine. It is seen that the first problem is that of finding a suitable code using the symbols 0 and 1 to represent quadruples, determinants, etc. The coding scheme presented here is the work of E. F. Moore who employed it in a description of a 3-tape universal machine<sup>2</sup>. The second problem is that of finding a suitable arrangement of the symbols on the universal machine tape. Finally, a description of the universal machine itself must be developed.

Consider first the coding of a Class A machine quadruple  $(r, S_i : T_k, s)$ . Each determinant must be one of the two forms:

<sup>2</sup>E. F. Moore: A Simplified Universal Turing Machine, Bell Telephone System Monograph 2098, presented at the Meeting of the Association for Computing Machinery, Toronto, Canada, Sept. 8, 1952.

or

$r, 0$

$r, 1$

where  $r$  takes on any integral value  $1, 2, \dots, M$  (for an  $M$ -state machine). The specified operation,  $T_k$ , is any one of the four forms:

O  
1  
R  
L

and finally, the final state,  $s$ , is an integer from 1 to  $M$ .

The scheme proposed by Moore is the following:

Determinant:

Code	$r, 0$	as	a block of	$3r + 1$	successive	$1$ 's
"	$r, 1$	"	"	"	"	$3r + 2$
						"
						$1$ 's

Operation:

Code	O	as	O	immediately	following	determinant.
"	1	as	00	"	"	"
"	R	as	000	"	"	"
"	L	as	0000	"	"	"

Final State:

Code  $s$  as a block of  $3s$  successive  $1$ 's immediately following the operation.

For example, the quadruple  $(1, 0 : 1, 1)$  would become:

111100111

and the quadruple  $(1, 1 : R, 2)$  would become:

11111000111111

A list of quadruples is coded by stringing together in any order the codes of member quadruples, separating one quadruple from the next by at least one 0. For example, the machine described on page 14

(1, 0 : 1, 1)  
(1, 1 : R, 2)  
(2, 0 : R, 1)  
(2, 1 : 0, 2)

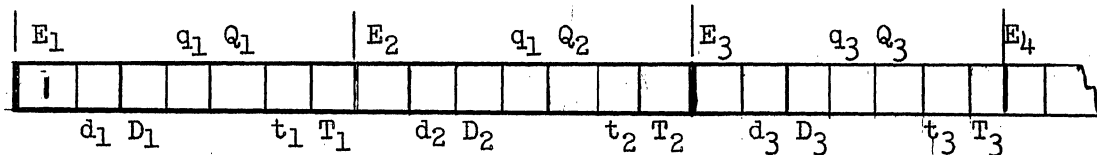
is completely (although not uniquely) coded by the sequence:

..00001111001110111111111011111100011111110001110011111100011111100..

Notice that any block of ones in this sequence has a unique interpretation, e.g., a block of N ones represents a final state only if N is divisible by 3, etc. A block of zeros following a determinant represents an operation; a block of zeros following a final state is simply a separation.

We now proceed to describe the arrangement of symbols on the universal machine tape, UMT. UMT will need to be endless only on the right. The class of machines which the universal machine will simulate will also use tapes which are endless only on the right. This causes no restriction in the generality of these machines over that of doubly endless tape machines (see problem 2.1).

UMT will be divided into groups of 7 cells each. The quadruple list, active determinant, SMT cells, and various marking cells will be interleaved on UMT in the following manner:



UMT

The E cells are used to mark the end of the tape (only E<sub>1</sub> holds a "1"; all other E cells hold "0". These are not changed.)

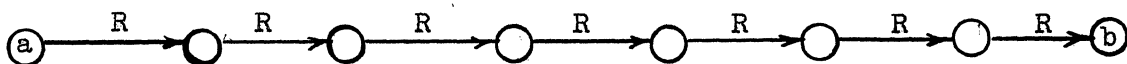
The D cells hold the active determinant.

The Q cells hold the list of quadruples.

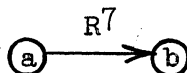
The T cells correspond to the cells of the simulated machine tape.

The cells labeled d, q, and t are used to mark the following D, Q, and T cells, respectively. Only one cell of each will hold a "1" at any given point in the calculation. For example, a "1" in t<sub>3</sub> indicates that T<sub>3</sub> would be the scanned cell on SMT. The use of these marker cells will become clear later.

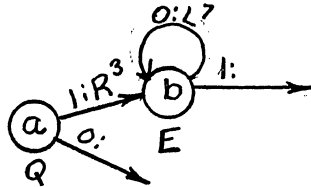
To go from cell Q<sub>i</sub> to Q<sub>i+1</sub> for example, it is necessary to slip over the intervening 6 cells. This process is diagrammed:



and will be abbreviated:



The process of locating the end of the tape will now be described. Suppose that UM is scanning some Q cell and is to find the cell  $E_1$  if the scanned symbol is "1". The diagram is:



The letters Q and E below the state node in this diagram indicate which "phase" of the 7 tape phases the machine will end in after the transition to that state. The machine starts in state "a" scanning a Q cell, (i.e., in Q-phase). If the scanned symbol is a "1", the machine moves 3 cells to the right (to the nearest E cell) and jumps to state "b". Now in E phase, the machine scans successive E cells to the left until a "1" is found, which occurs on  $E_1$  at the end of the tape.

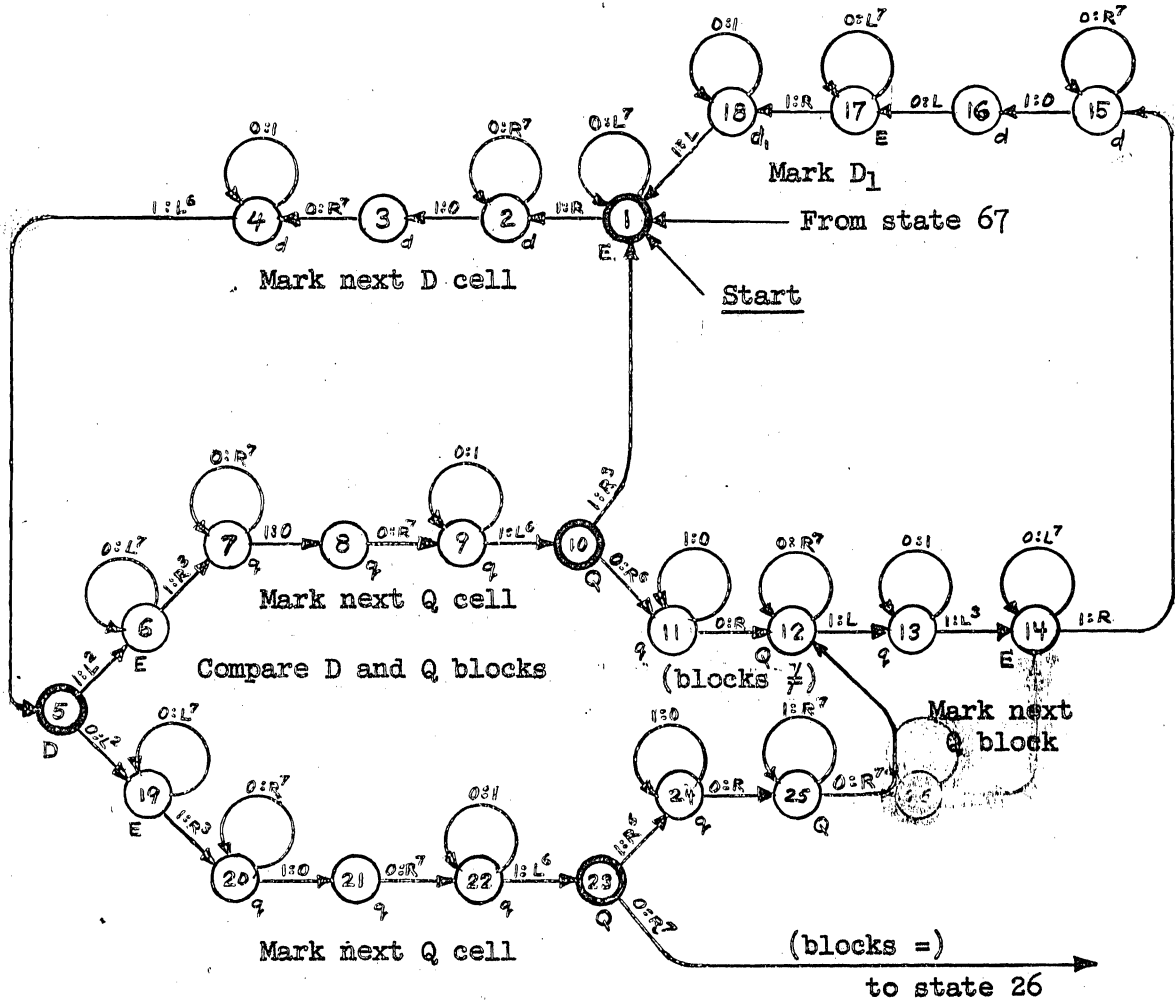
A. ...

UM will start in state 1 scanning  $E_1$ . The quadruple list for SM is printed on the Q cells beginning with  $Q_1$  and  $q_1$  holds a "1" ( $q_1$  marks  $Q_1$ ). The first active determinant is printed on  $D_1, D_2, D_3$ , etc., and  $d_1$  holds a "1" ( $d_1$  marks  $D_1$ ). Finally, the T cells are marked according to SMT and the initial simulated scanned cell  $T_s$  will be marked ( $t_s$  holds a "1").

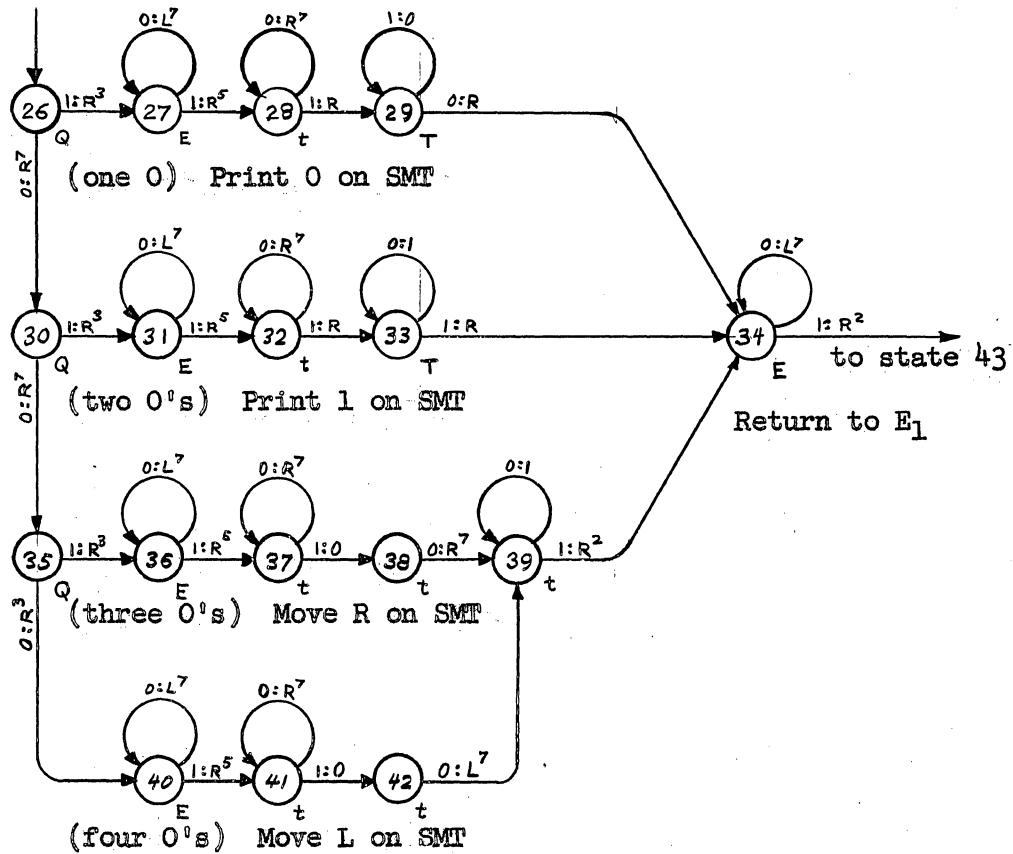
It will be noted that UM must move to the end of UMT before searching for a "1" on any marker cell in order to guarantee that the marked cell will not be missed.

Parts A, B, and C of the operation of one particular Class A Universal Turing Machine will be described separately on the following pages.

A. Given the active determinant, find the active quadruple.

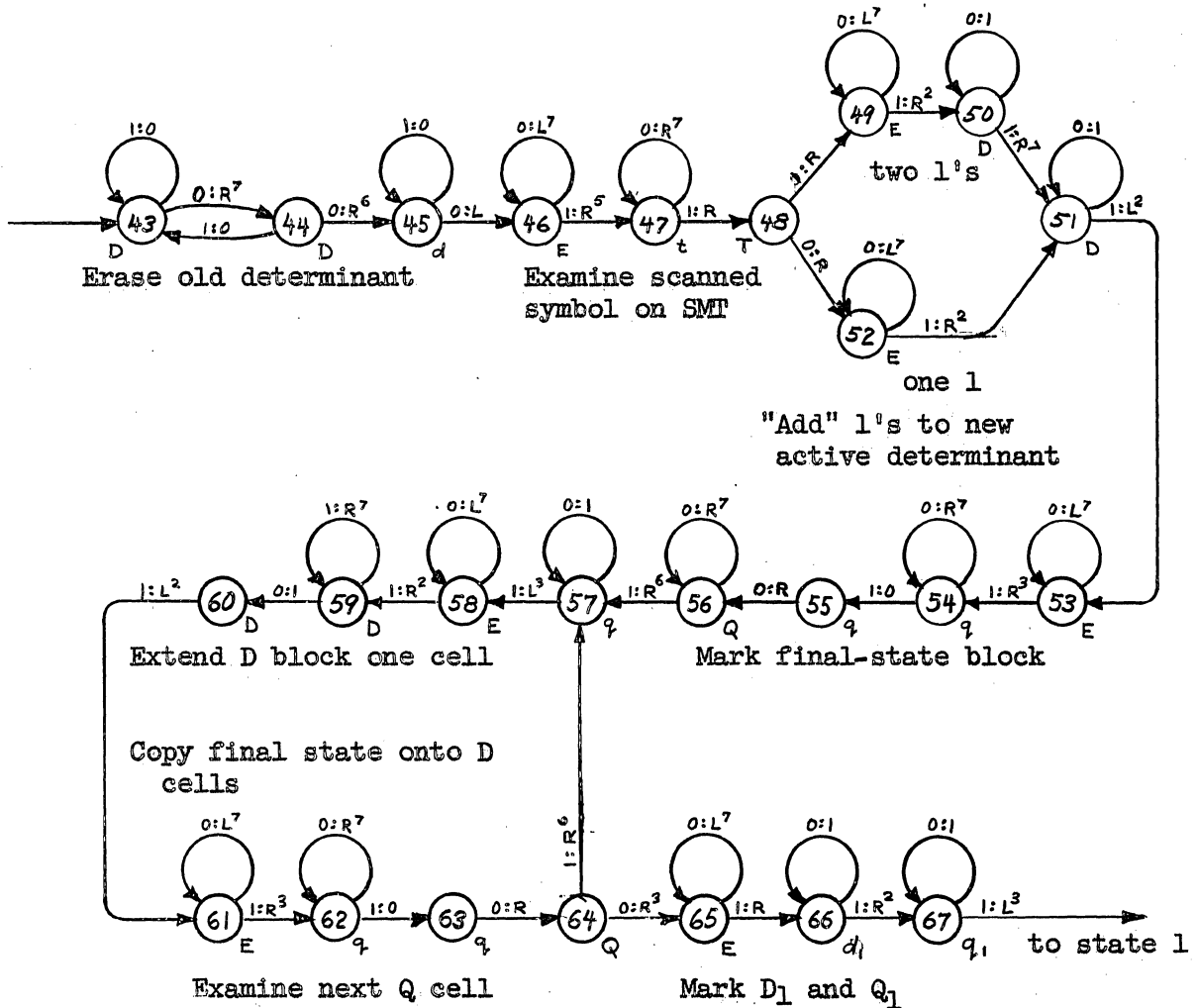


The next D cell is marked (a "1" is printed in the associated d cell) in states 2 to 4 and the previously marked D cell is examined (state 5). If it holds a "1", UM checks the currently marked Q cell (and marks the next Q cell) in state 10 and if it holds a "1", prepares to compare the next D and Q cells by returning to state 1. If the D and Q blocks are of unequal length, UM marks the next Q block of 1's, marks the beginning of the active determinant again, and starts the comparison with the new Q block by returning to state 1. When both D and Q cells hold 0's concurrently, the active quadruple has been found and UM jumps to state 26 to begin part B.

B. Carry out the specified operation.

The active quadruple has been located and UM determines which SM operation is specified by counting the number of successive 0's in the operation code. If one 0, UM finds the scanned cell on SMT and prints a "0"; if two 0's, a "1"; if three 0's, UM marks the T cell on the right of the currently marked T cell; if four 0's, UM marks the T cell on the left of the currently marked T cell. UM then returns to the end of the tape and moves on to part C.

C. Form the next active determinant from the new scanned symbol and the new initial state (the previous final state).



The old active determinant is erased (states 43 to 46) and the current scanned symbol on SMT is examined. If it is a "0", a "1" is printed on  $D_1$  to start the next active determinant; if the scanned cell on SMT holds a "1", then a "1" is printed on both  $D_1$  and  $D_2$ . UM then combines the block of "1's" which code the final state of the active quadruple with the one or two 1's now on the initial D cells, thus forming the new active determinant.  $D_1$  and  $Q_1$  are marked (1's printed on  $d_1$  and  $q_1$ ) and UM returns to state 1 to repeat A, B, and C for the next simulated transition.



PROBLEMS

2.1 Show that any problem which can be solved by a Turing machine using doubly infinite tape (infinitely long in both directions) can also be solved by a Turing machine using singly infinite tape.

2.2 Consider the class of Turing machines which use only the symbols "0" and "1" and which perform only the operations "print 0", "print 1", "move right one", "move left one", abbreviated O, 1, R, L, respectively.

How many one-state machines of this class are there? Two-state? N-state?

2.3 A Turing machine calculation which never uses more than a finite length of tape might be called limited; otherwise non-limited. (For example, the machine  $(1,0:0,1) (1,1:R,1)$  performs a limited calculation if the tape holds a "0" to the right of the scanned cell.)

Write the set of quadruples for each one-state machine of the class defined in 2.2 which is non-limited for all possible arrangements of symbols on the tape.

2.4 Design a Universal Turing Machine using as few internal states as possible. The design may use any finite number of symbols to aid in reducing the number of states required. Make a count of the number of states and the number of quadruples.

6M-3938, Supplement 2

Division 6 - Lincoln Laboratory  
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SUBJECT: THE LOGICAL STRUCTURE OF DIGITAL COMPUTERS  
Boolean Algebra

To: Class Registrants

Abstracts to: J. C. Proctor, C. W. Farr

From: W. A. Clark

Date: 2 November 1955

Abstract: The symbol-printing operations in a Class A Turing machine can be described in terms of the tape cells themselves. For example, a machine which performs the sequence "If cell A holds "1" or if cell B holds "0", print "1" on cell C" is described by the statement:

$$(A^1 \text{ or } B^0):C^1$$

The manipulative aspects of this notation can be exploited in demonstrating that the rules for printing symbols define a Boolean Algebra.

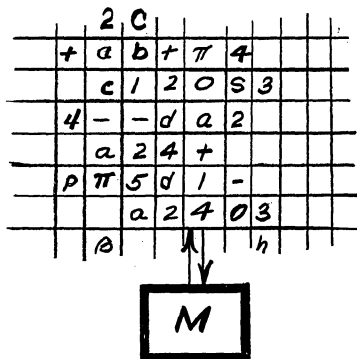
  
W. A. Clark

WAC/jhk

The Class A Universal Turing Machine described on the preceding pages provides us with one value for the critical complexity discussed earlier. A count of transitions shows that about 800 quadruples are required to describe this machine. Other universal machines using more symbols and fewer internal states have been designed (see also problem 2.4); Shannon has shown<sup>3</sup> that it is possible to design a two-state universal machine (and impossible to design a one-state universal machine) by using a large enough number of symbols.

#### Variations on the Turing Machine Theme

There are many possible variations to the Turing machine concept. In the example presented earlier, the machine operated on a linear array of symbols printed on an infinitely long tape. Another class of machine might be defined which operates on a two-dimensional array of symbols printed on an infinite plane.



The operations of this machine would include "move right," "move left," "move up," and "move down." Extension of these ideas to n-dimensional arrays readily follows.

A three-tape Turing machine was mentioned earlier<sup>4</sup>. In this case, the machine deals with three separate linear arrays scanning three cells simultaneously but operating on only one tape at a time. The transitions are described as sets of sextuples rather than quadruples, each determinant consisting of the initial state and the three scanned symbols. Again, extension to machines using more tapes or several n-dimensional arrays is possible.

Von Neumann has suggested a parts-manipulating machine analogous to

<sup>3</sup>C. E. Shannon and others: "Automata Studies," Princeton University Press (to be published shortly.)

<sup>4</sup>E. F. Moore: Op. cit.

to the Turing symbol-manipulating machine. This machine would operate in an environment containing hardware of various kinds such as nuts, bolts, wire, vacuum tubes, etc., and would construct another machine from these parts. Again, it is possible to design a universal constructing machine capable of constructing anything that any other constructing machine can construct. A case of particular interest is that of a machine which constructs a copy of itself. With the current trend toward automation, some of these ideas are being applied in practical situations.

### Summary Remarks

Before moving on to further discussion of the Turing machine and its relation to other topics, let us list some of the items which have been introduced in the preceding pages:

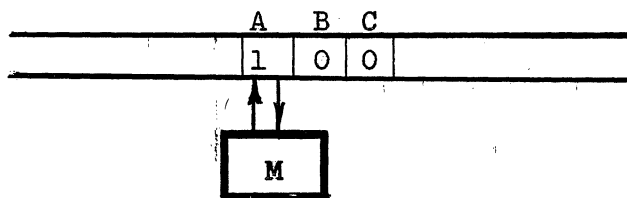
- |                        |                       |
|------------------------|-----------------------|
| 1. Logical structure   | 6. Coding             |
| 2. Operating rules     | 7. Machine complexity |
| 3. Stable states       | 8. Simulation         |
| 4. Transitions         | 9. Universality       |
| 5. Symbol manipulation |                       |

These are all items which will be discussed in more detail during the remainder of the course. It is interesting that the conceptually simple Turing machine serves to introduce so many of the basic ideas in the subject of digital computers.

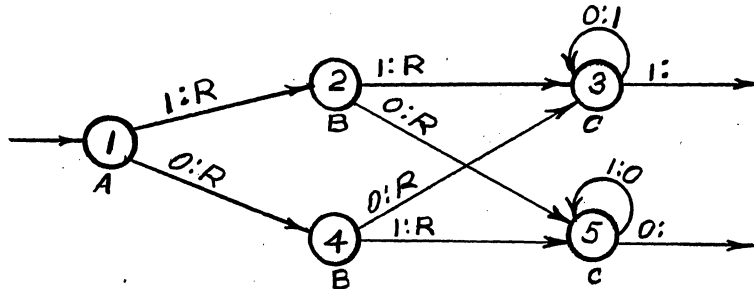
### BOOLEAN ALGEBRA

We now proceed to develop a manipulative notation which enables us to describe the action of two-symbol machines in terms of the cells themselves. This will lead to a kind of symbol-printing algebra which will be shown to have the properties of Boolean algebra.

Consider the following simple Class A Turing machine which operates on cells labeled A, B, and C.



for which the transition diagram is



The machine prints "1" on C if A and B hold the same symbol and prints a "0" on C if A and B hold different symbols. The symbol finally appearing in cell C depends on the symbols in A and B. There are, of course, several kinds of dependence relations possible, and the machine illustrated mechanizes only one of these relations. A word description of the illustrated process in terms of basic Turing machine operations would consist of the following pair of statements:

1. If cell A holds "1" and cell B holds "1",  
or if cell A holds "0" and cell B holds "0",  
then print "1" on cell C.
2. If cell A holds "1" and cell B holds "0",  
or if cell A holds "0" and cell B holds "1",  
then print "0" on cell C.

A notation which simplifies this description is one which employs superscripts to denote the symbol held in a given cell:

$A^0$  will mean "there is a '0' in cell A"  
and  $A^1$  will mean "there is a '1' in cell A"

Then we might agree that

$A^0$ : will mean "if there is a '0' in cell A"  
and  $:C^1$  will mean "print a '1' in cell C"

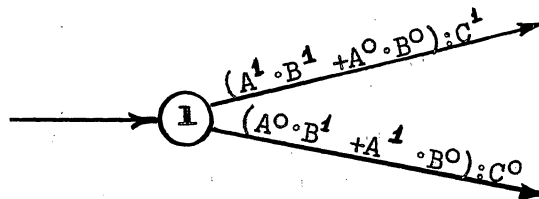
With these abbreviated forms, the statements describing the action of the illustrated machine become:

$$\left. \begin{array}{l} (A^1 \text{ and } B^1 \text{ or } A^0 \text{ and } B^0):C^1 \\ (A^0 \text{ and } B^1 \text{ or } A^1 \text{ and } B^0):C^0 \end{array} \right\}$$

The remaining simplification involves replacing "and" and "or" with shorter symbols. We will choose "." to replace "and" and "+" to replace "or." With these changes the statements become:

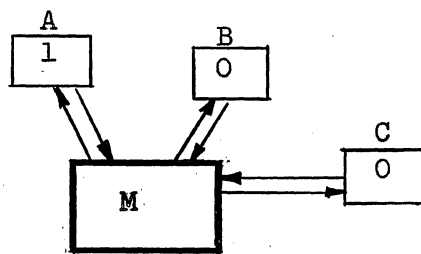
$$\left. \begin{array}{l} (A^1 \cdot B^1 + A^0 \cdot B^0):C^1 \\ (A^0 \cdot B^1 + A^1 \cdot B^0):C^0 \end{array} \right\}$$

and the transition diagram can be redrawn in the form:



which is considerably simpler and more compact than the original diagram.

It is interesting to note that in this new description the operations "R" and "L" do not appear. It is no longer a requirement that the cells A, B, and C be adjacent cells in a linear array. In fact, the new transition diagram equally well describes the action of a discrete-state machine which deals with several independent cells simultaneously:



We will return to this idea later.

#### Table of Combinations

The possible outcomes and the corresponding conditions of A and B can be represented conveniently in a table. On the left are listed all combinations of symbols found in A and B, and on the right the resulting symbol in C:

A	B	C
0	0	1
0	1	0
1	0	0
1	1	1

Thus, the first line of the table corresponds to  $A^0 \cdot B^0 : C^1$ , etc. The number of lines,  $k$ , in such a table is given by  $k=2^n$ , where  $n$  is the number of cells determining the symbol to be printed. The number of different tables is  $2^k$ , i.e.,

$$2^{2^n} = \text{number of functionally different machines} \\ \text{which print "1" or "0" depending on the} \\ \text{symbols held in } n \text{ cells.}$$

It should be noted that directions for printing only the 1's are sufficient to determine the complete table. Thus, it is sufficient to describe the illustrated machine by:

$$(A^1 \cdot B^1 + A^0 \cdot B^0) : C^1$$

from which the table is written:

A	B	C	→	A	B	C
0	0	1		0	0	1
0	1			0	1	0
1	0			1	0	0
1	1	1		1	1	1

0's appearing in all unfilled positions. Similarly, directions for the printing of 0's are also sufficient and may result in simpler descriptions in some cases. For example,

$$A^0 \cdot B^0 : C^0 \quad \text{and} \quad (A^1 \cdot B^0 + A^0 \cdot B^1 + A^1 \cdot B^1) : C^1$$

both describe the same machine.

We will say that two cells, C and D, are equivalent if C holds a "1" whenever D holds a "1" and C holds "0" whenever D holds "0". Thus, from a table, e.g.,

A	B	C	D	E
0	0	1	1	0
0	1	0	0	0
1	0	0	0	1
1	1	1	1	1

it is seen that C is equivalent to D and E is equivalent to A. These will be written  $C=D$  and  $E=A$ , respectively.

Thus, given

$$x^1:f^1 \quad \begin{array}{c|c} x & f \\ \hline 0 & 0 \\ 1 & 1 \end{array}$$

it is seen from the table that  $x=f$ . This could have been obtained from the statement  $x^1:f^1$  by dropping superscripts and replacing ":" with "=", and the converse is of course also true. Using this rule, we would also obtain  $x^0=f^0$  from  $x^0:f^0$ .

From  $x^0:f^1$  we would obtain  $f=x^0$ . The table is:

$$\begin{array}{c|c} x & f \\ \hline 0 & 1 \\ 1 & 0 \end{array}$$

Inspection of the table shows that the symbol in cell f is the complement of the symbol in cell x, i.e.,  $f=\text{compl\`e}ment\text{ of }x$ . Thus,  $x^0$  will be read "x complement" or "complement of x", the superscript "0" indicating the complementation.

From

$$\begin{array}{c} x^0:f^1 \\ f^0:g^1 \end{array} \quad \begin{array}{c|c|c} x & f & g \\ \hline 0 & 1 & 0 \\ 1 & 0 & 1 \end{array}$$

we conclude that  $x = (x^0)^0$ , the double-complement rule.

Now we define two cells 1 and 0 in the following way: Cell 1 always holds the symbol "1" (see, for example, cell  $E_1$  of the UM described earlier) and cell 0 always holds the symbol "0". Evidently:

$$1^0 = 0 \quad \text{and} \quad 0^0 = 1$$

Consider the following printing statements for cells  $f_1$  through  $f_6$ :

$$\begin{array}{l} (1^1+1^1):f_1^1 \\ (1^1+0^1):f_2^1 \\ (0^1+0^1):f_3^1 \\ 1^1 \cdot 1^1:f_4^1 \\ 1^1 \cdot 0^1:f_5^1 \\ 0^1 \cdot 0^1:f_6^1 \end{array}$$

The table of combinations is then



1	0	$f_1$	$f_2$	$f_3$	$f_4$	$f_5$	$f_6$
1	0	1	1	0	1	0	0
1	0	1	1	0	1	0	0
1	0	1	1	0	1	0	0
1	0	1	1	0	1	0	0

from which we conclude that

$$\begin{array}{ll} 1+1=1 & 1 \cdot 1=1 \\ 1+0=1 & 1 \cdot 0=0 \\ 0+0=0 & 0 \cdot 0=0 \end{array}$$

These results illustrate properties of an arithmetic which is like ordinary arithmetic for the "dot" operation (multiplication) but unlike ordinary arithmetic for the "plus" operation (addition). We have described the fundamental operations of Boolean Algebra.

Evidently these operations are commutative, i.e., "x or y" is the same as "y or x" and "x and y" is the same as "y and x". Symbolically

$$x + y = y + x \quad \text{and} \quad x \cdot y = y \cdot x$$

The "and" and "or" operations are also associative. Using the parenthesis to denote grouping, the following statements hold:

$$x + (y + z) = (x + y) + z \quad \text{and} \quad x \cdot (y \cdot z) = (x \cdot y) \cdot z$$

Consequently, the order and grouping of symbols in any Boolean expression is arbitrary. The distributive properties of the "and" and "or" operations can be established by constructing the table of combinations for the forms:

$$\begin{array}{ll} (x^1 \cdot y^1 + x^1 \cdot z^1): f_1^1 & (x^1 + y^1) \cdot (x^1 + z^1): f_3^1 \\ x^1 \cdot (y^1 + z^1): f_2^1 & (x^1 + y^1) \cdot z^1: f_4^1 \end{array}$$

x	y	z	$f_1$	$f_2$	$f_3$	$f_4$
0	0	0	0	0	0	0
0	0	1	0	0	0	0
0	1	0	0	0	0	0
0	1	1	0	0	1	1
1	0	0	0	0	1	1
1	0	1	1	1	1	1
1	1	0	1	1	1	1
1	1	1	1	1	1	1

Comparison of columns in the table shows that  $f_1 = f_2$  and  $f_3 = f_4$ , i.e.,

$$x \cdot y + x \cdot z = x \cdot (y + z) \quad \text{and} \quad (x + y) \cdot (x + z) = x + y \cdot z$$

As in ordinary algebra, then, one can "multiply" through a "sum" (recall the process of "factoring"). Unlike ordinary algebra, Boolean algebra permits one to "add" through a "product". These forms will occur quite often in subsequent work.