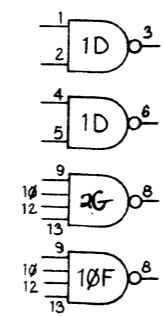
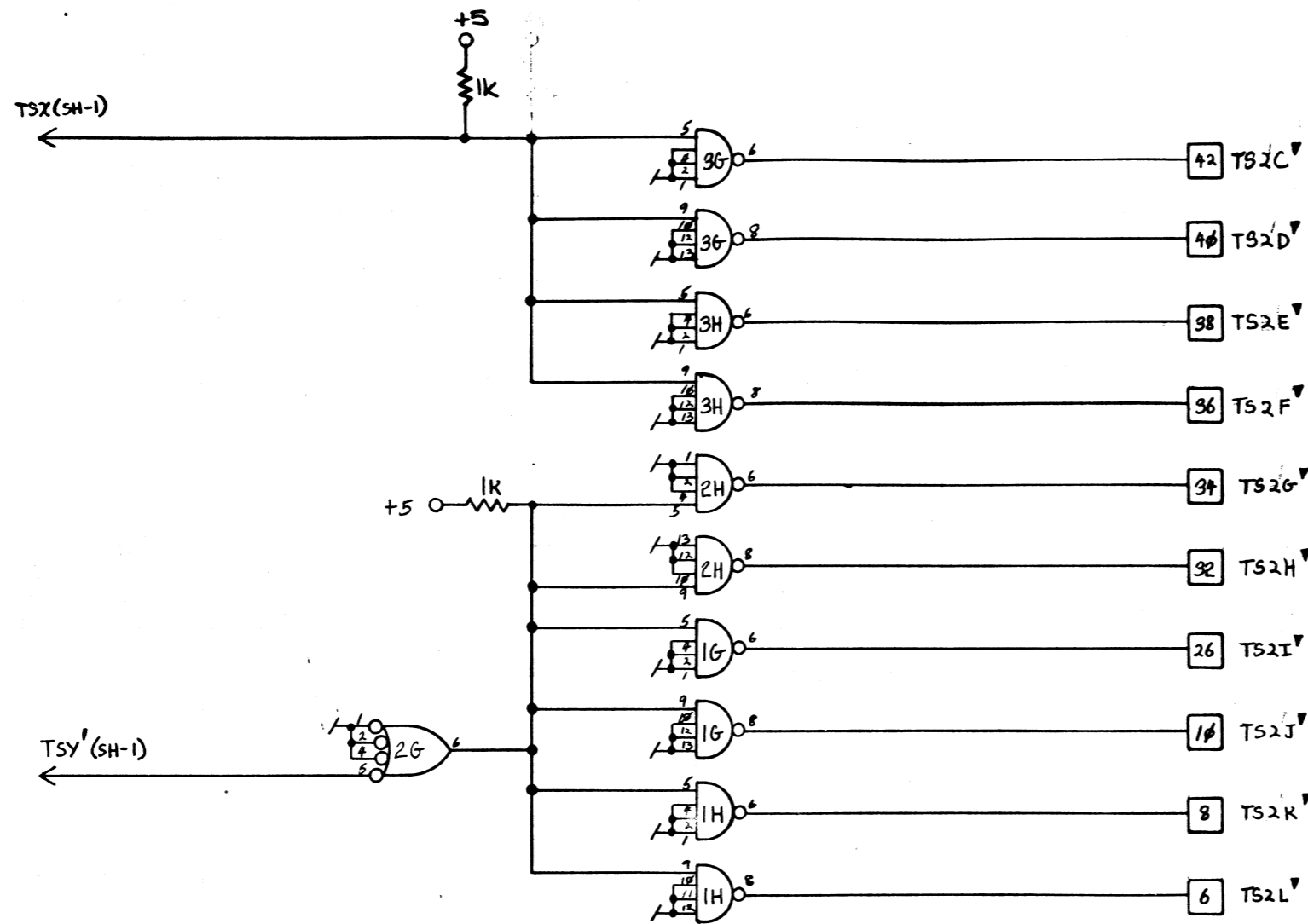



NOTES:
 1. UNLESS OTHERWISE SPECIFIED
 ALL RESISTORS ARE 1/8W 10% CARBON COMP.
 ALL CAPACITORS ARE CD10070
 ALL TRANSISTORS ARE QT93646
 ALL DELAY LINES ARE 50ns TYPE WITH VARIABLE TAP



RELEASED LATEST REVISIONS 11/26/73		DATE	DRAWN	APPROVED
NO.	DESCRIPTION			
REVISIONS				
1010107	DESIGNED D. DOOGUE	TITLE CLOCK CARD NO. 2 C18		
	DRAWN Q. ITOYAMA	PROJECT FIRST USED-M1 FAST MEMORY		
Berkeley Computer Corporation	SCALE	SHEET 1	OF 2	REV. L01001



.0 RELEASED LATEST REVISIONS 11/28/73		DATE	DRAWN	APPROVED
NO.	DESCRIPTION	DATE	DRAWN	APPROVED
REVISIONS				
 The Aloha System BCC-500	DESIGNED D. DODGE SPL. CHIEF	TITLE CLOCK CARD NO. 2 C18		
	DRAWN M. MATSUWAKI SPL. CHIEF	PROJECT FIRST USED-M1 FAST MEMORY		
	SCALE	SHEET 2	OF 2	NO. L01001 .2